According to the block diagram, the converter has AGC circuit that can lock the output at a constant level.

Simultaneously, when the input signal exceeds the dynamic range, the circuit's attenuator will automatically attenuate within the limit of the output level.

The Converter's TX is composed of PLL LO 3360MHz, plus Mixer, to convert CH1 2412MHz to 5772MHz.

The Converter's RX converts 5772MHz to 2412MHz, same for other channels.