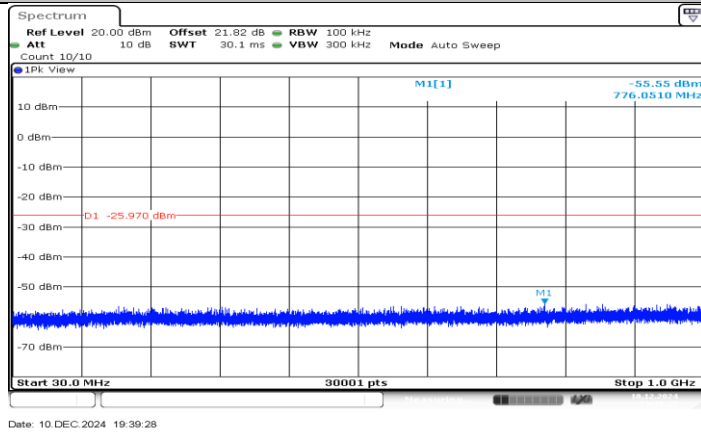
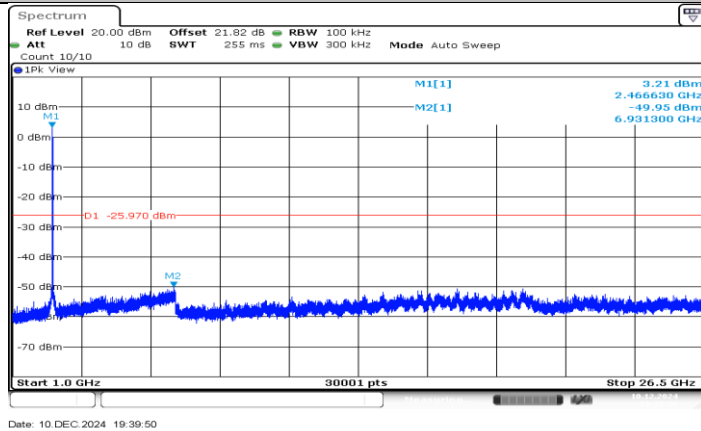


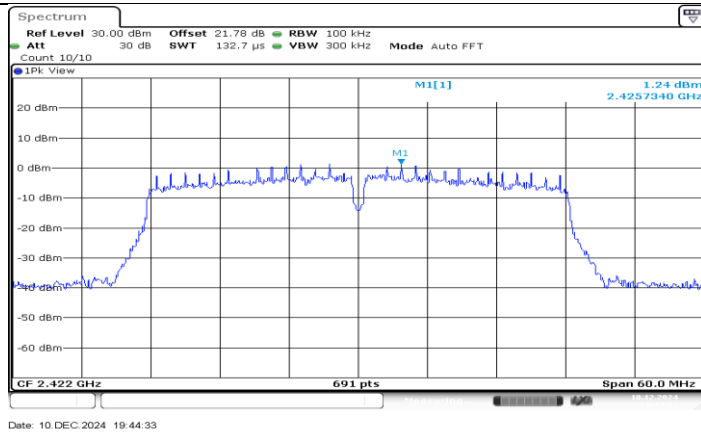
11N20MIMO_Ant2_2462_0~Reference



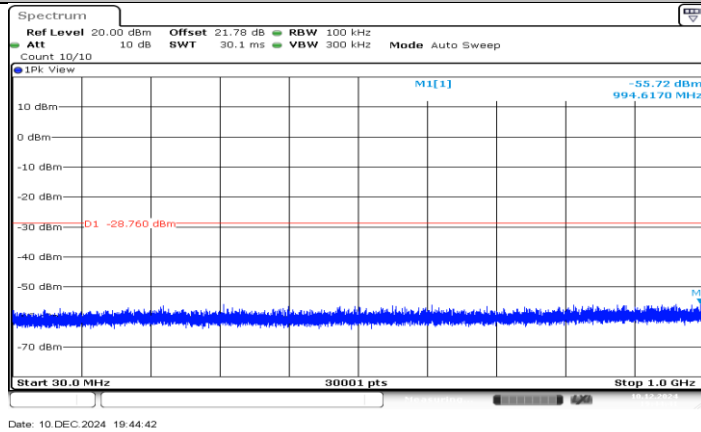
11N20MIMO_Ant2_2462_30~1000



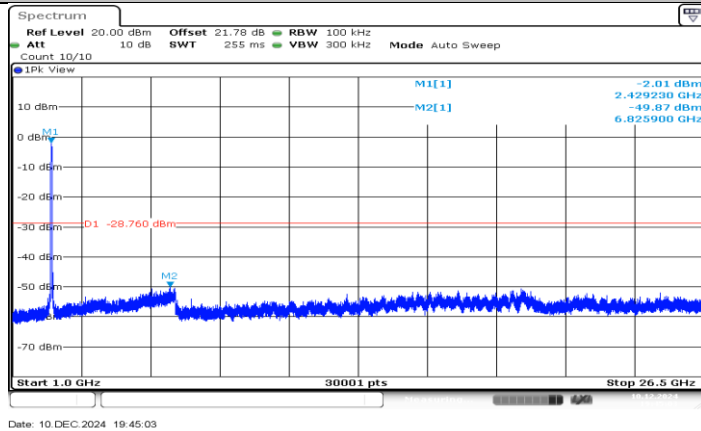
11N20MIMO_Ant2_2462_1000~26500



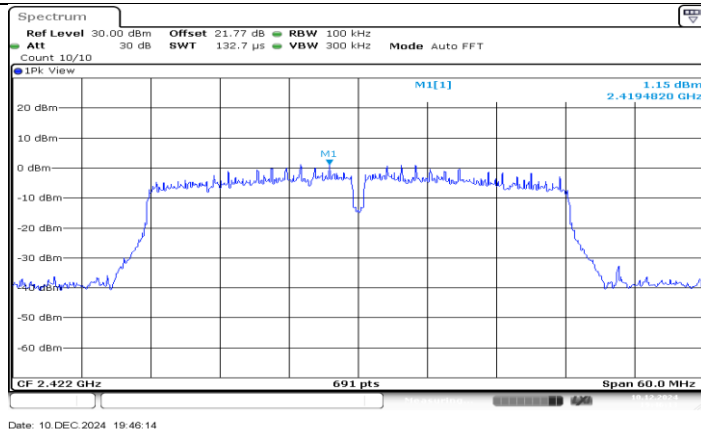
11N40MIMO_Ant1_2422_0~Reference



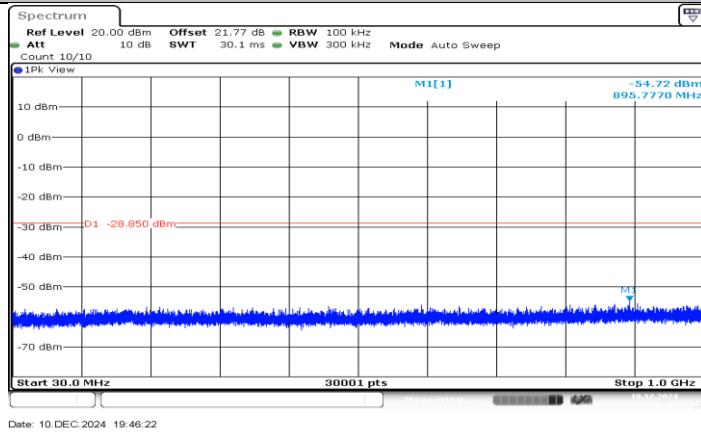
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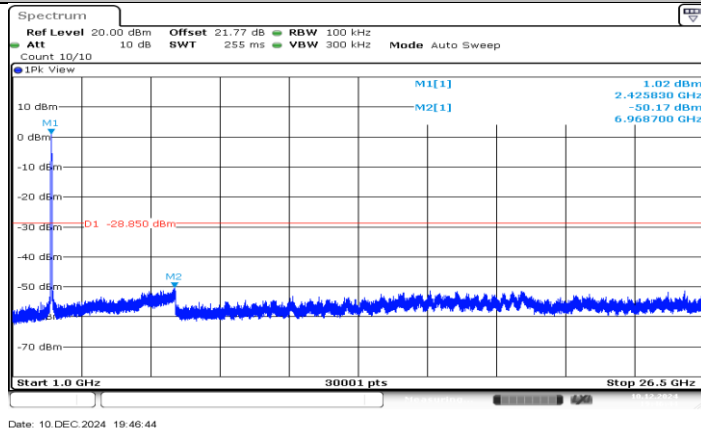
11N40MIMO_Ant1_2422_1000~26500



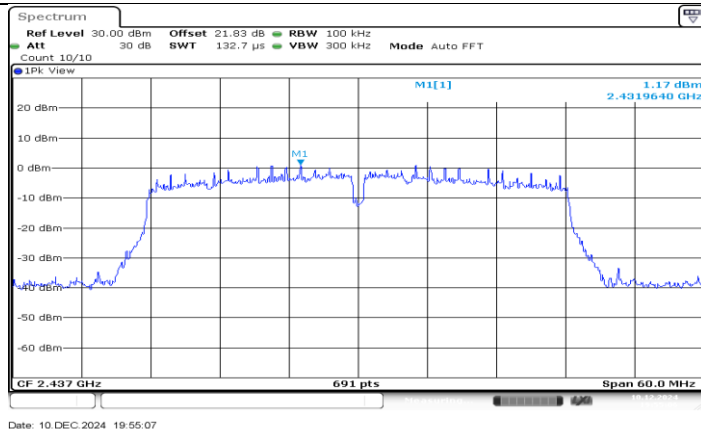
11N40MIMO_Ant2_2422_0~Reference



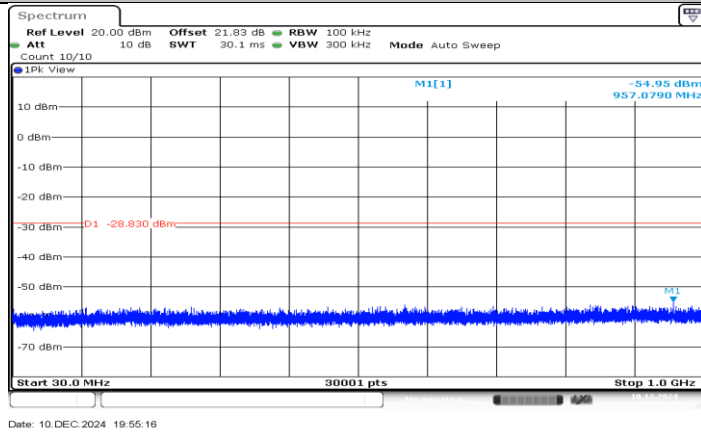
11N40MIMO_Ant2_2422_30~1000



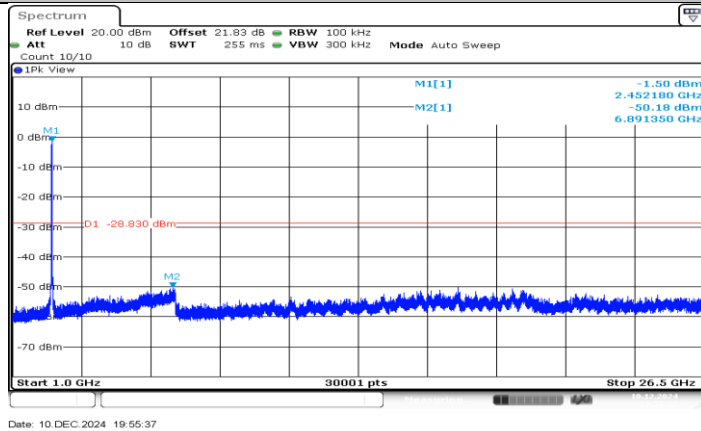
11N40MIMO_Ant2_2422_1000~26500



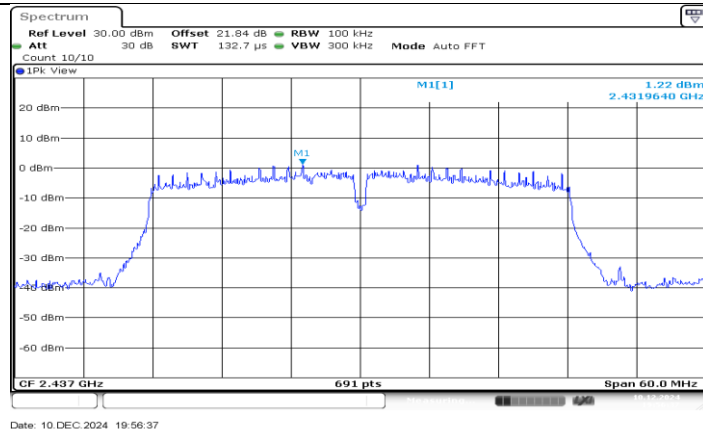
11N40MIMO_Ant1_2437_0~Reference



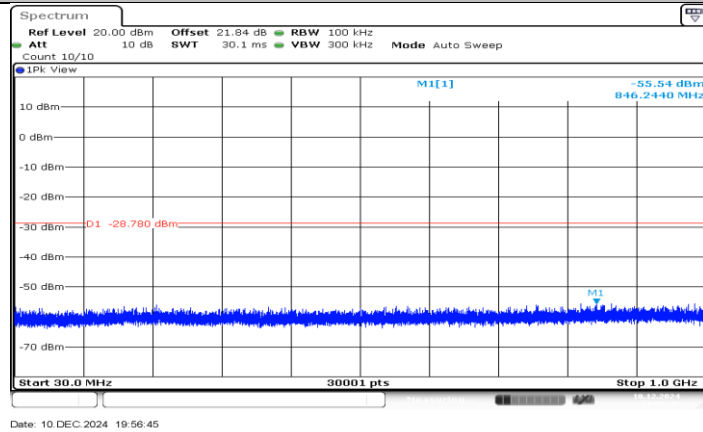
11N40MIMO_Ant1_2437_30~1000



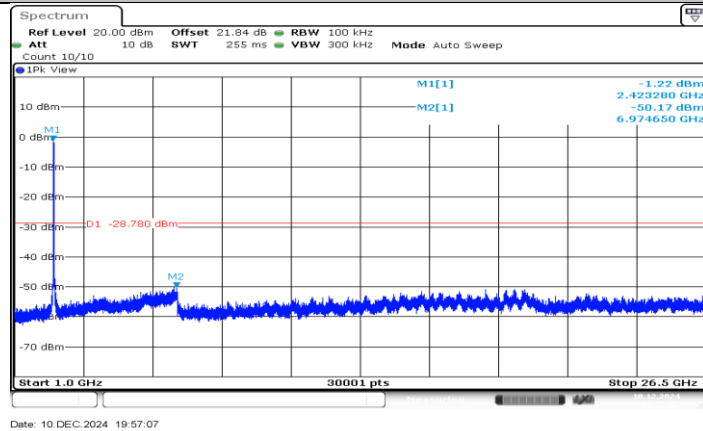
11N40MIMO_Ant1_2437_1000~26500



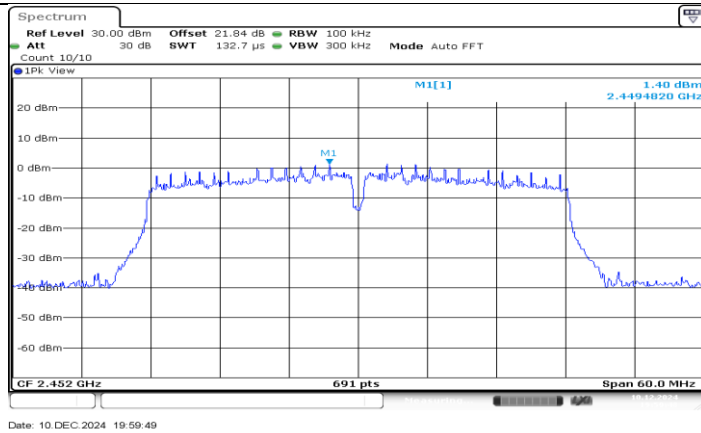
11N40MIMO_Ant2_2437_0~Reference



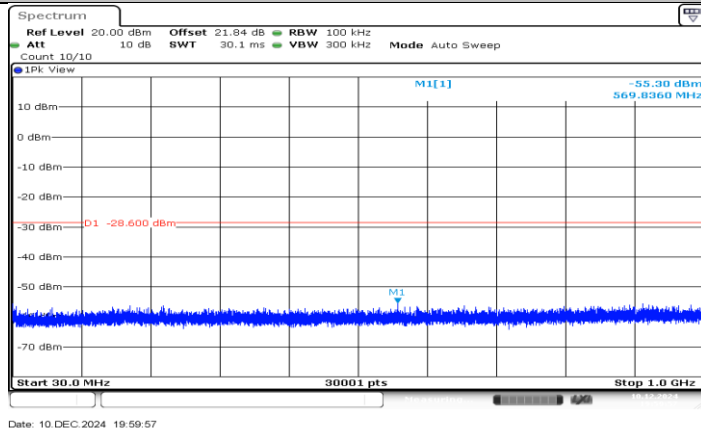
11N40MIMO_Ant2_2437_30~1000



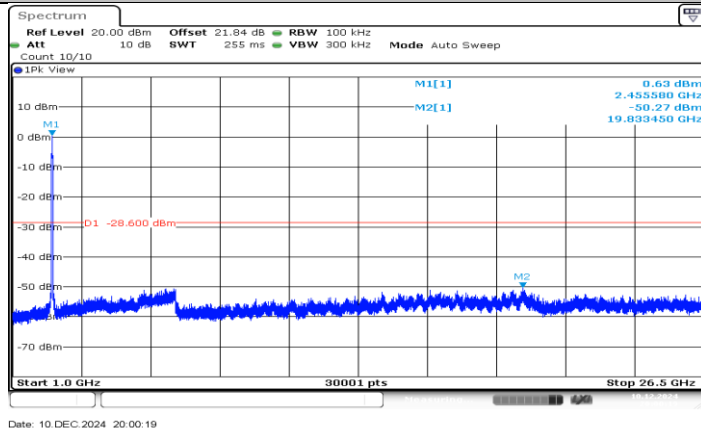
11N40MIMO_Ant2_2437_1000~26500



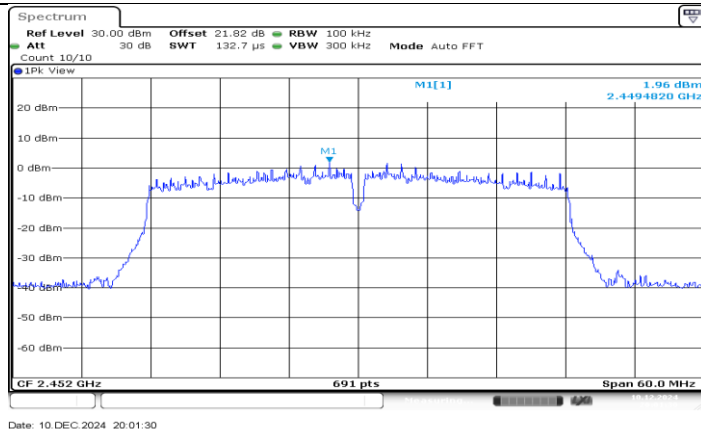
11N40MIMO_Ant1_2452_0~Reference



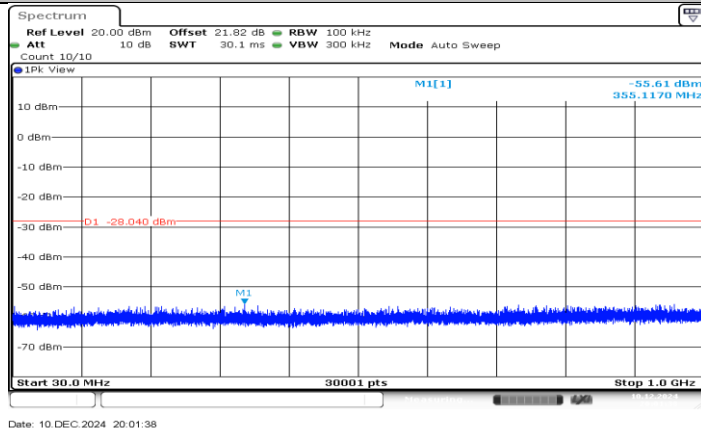
11N40MIMO_Ant1_2452_30~1000



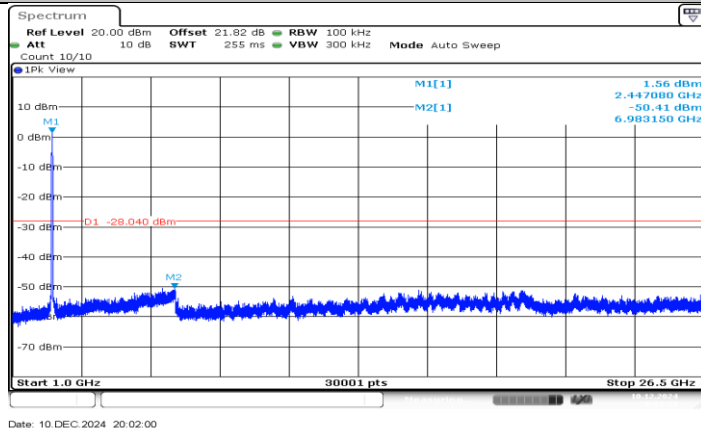
11N40MIMO_Ant1_2452_1000~26500



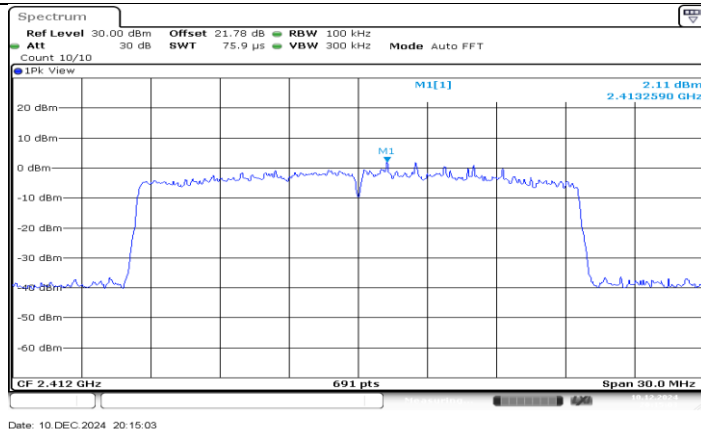
11N40MIMO_Ant2_2452_0~Reference



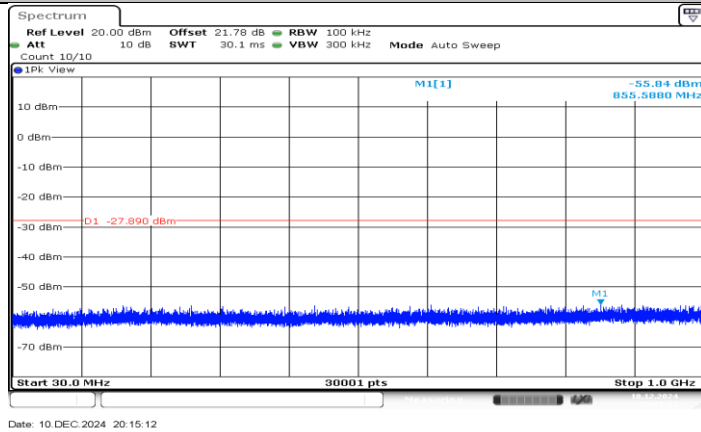
11N40MIMO_Ant2_2452_30~1000



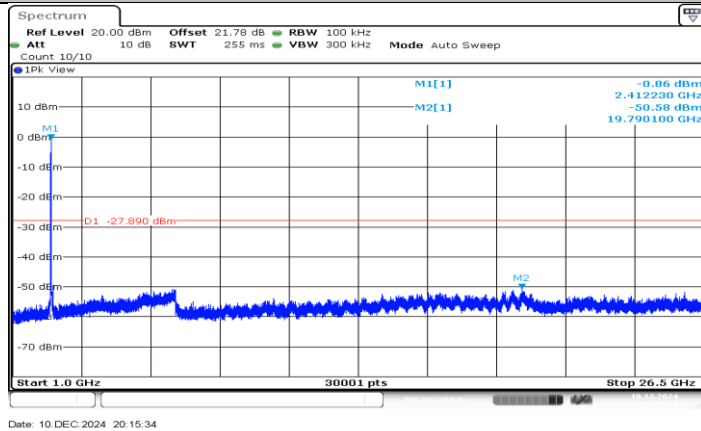
11N40MIMO_Ant2_2452_1000~26500



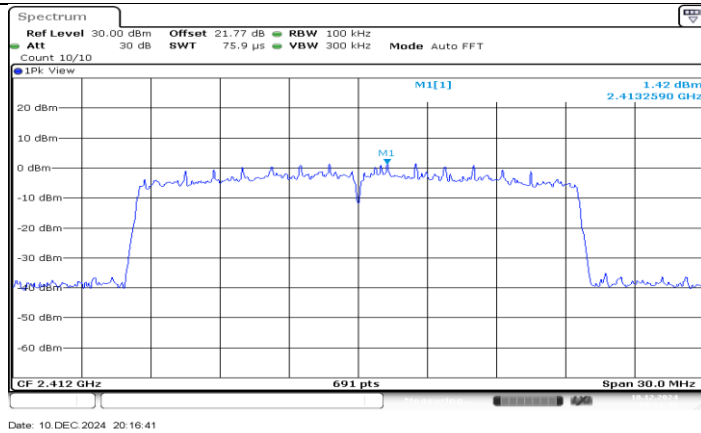
11AX20MIMO_Ant1_2412_0~Reference



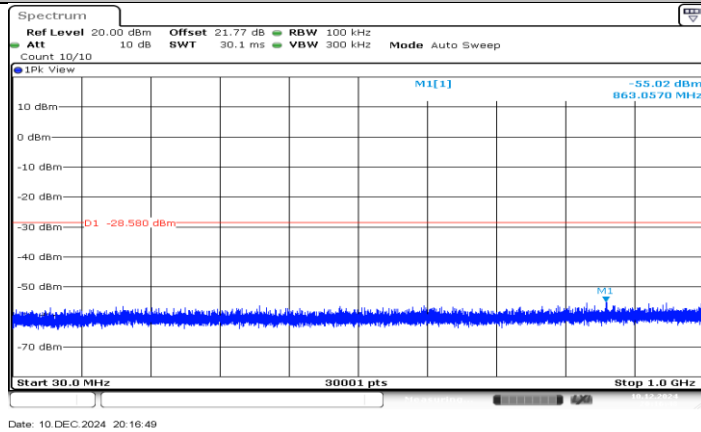
11AX20MIMO_Ant1_2412_30~1000



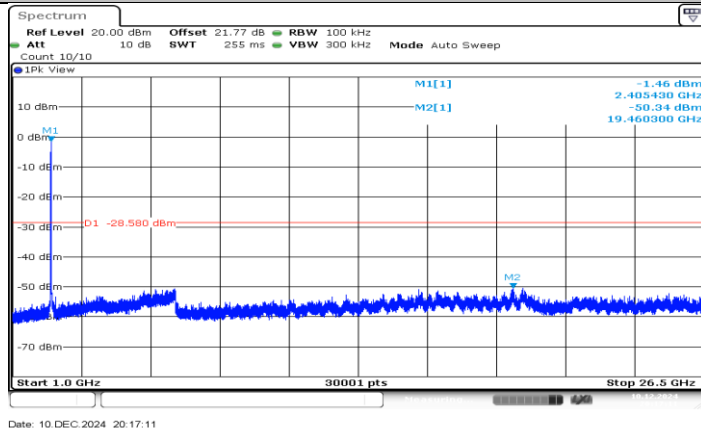
11AX20MIMO_Ant1_2412_1000~26500



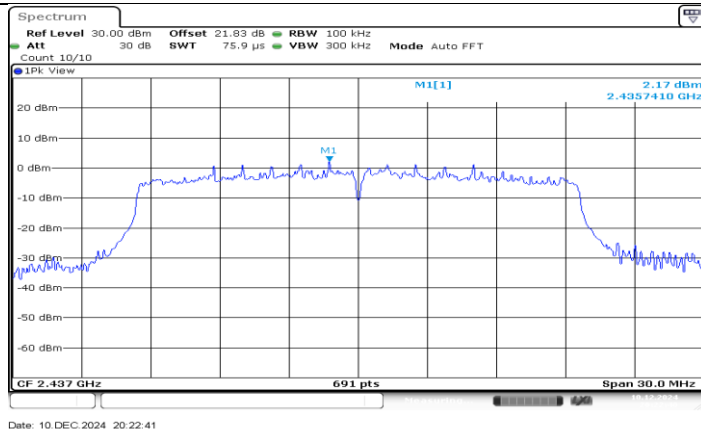
11AX20MIMO_Ant2_2412_0~Reference



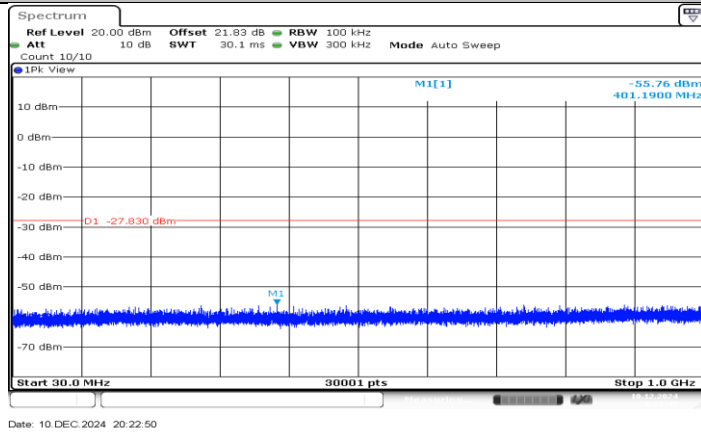
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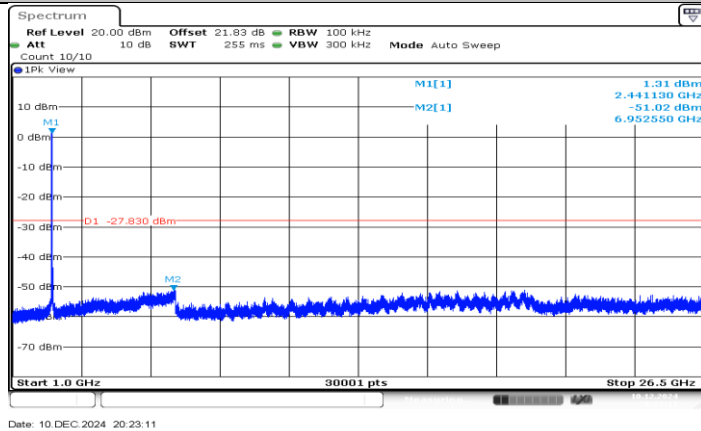
11AX20MIMO_Ant2_2412_1000~26500



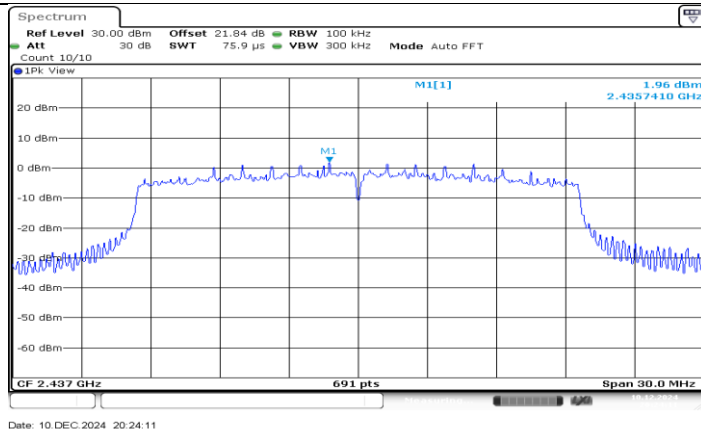
11AX20MIMO_Ant1_2437_0~Reference



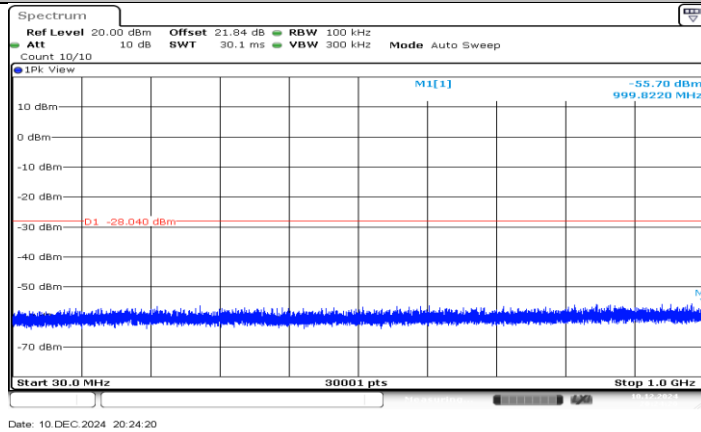
11AX20MIMO_Ant1_2437_30~1000



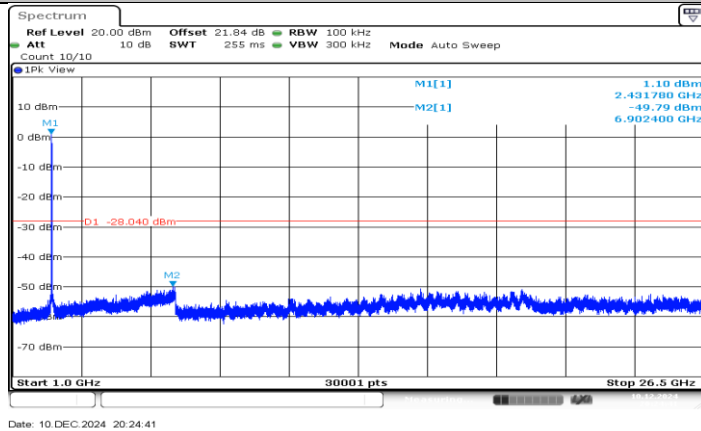
11AX20MIMO_Ant1_2437_1000~26500



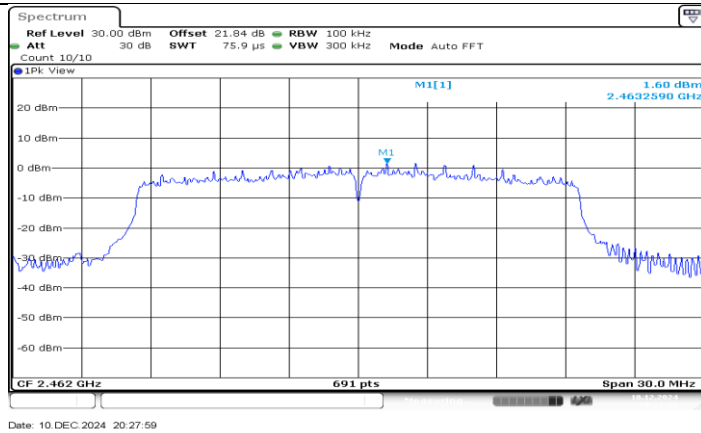
11AX20MIMO_Ant2_2437_0~Reference



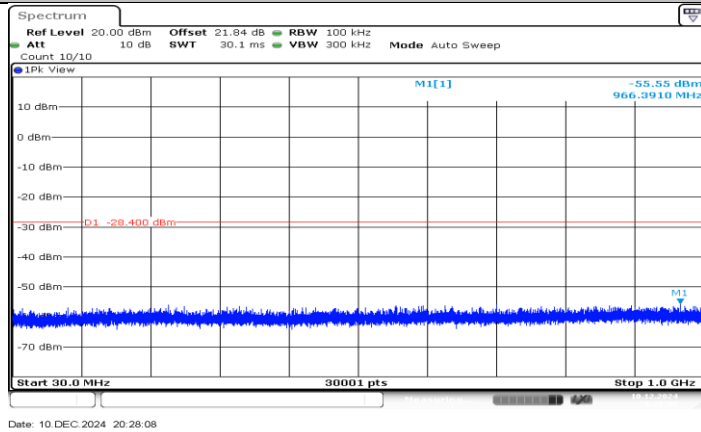
11AX20MIMO_Ant2_2437_30~1000



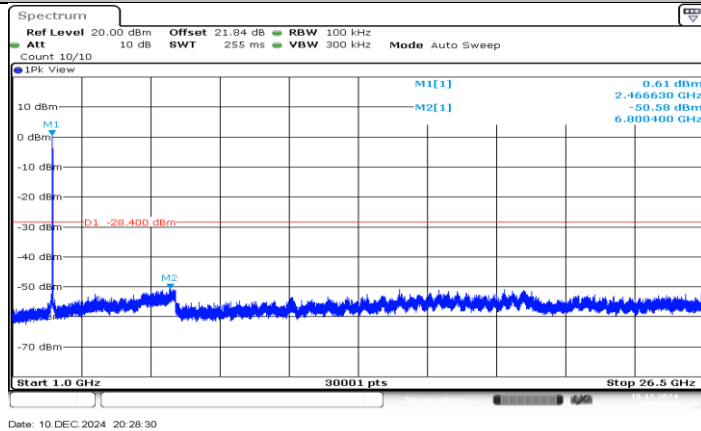
11AX20MIMO_Ant2_2437_1000~26500



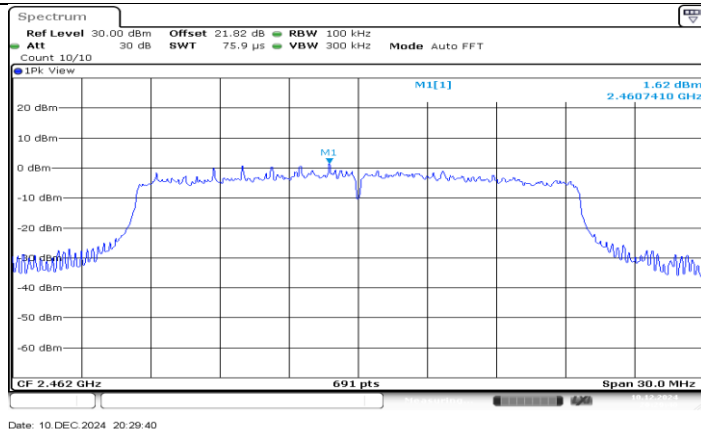
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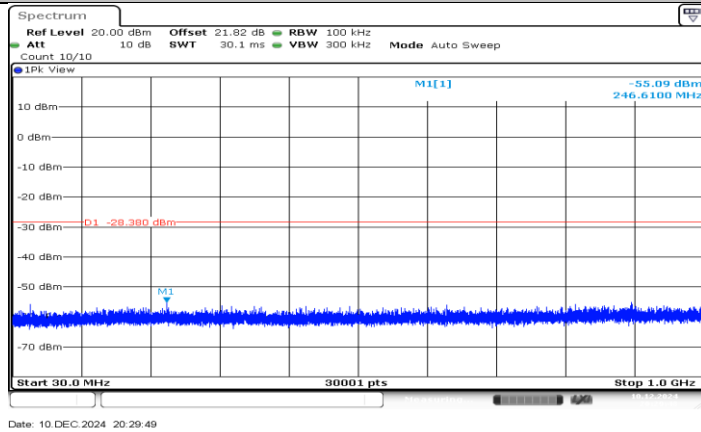
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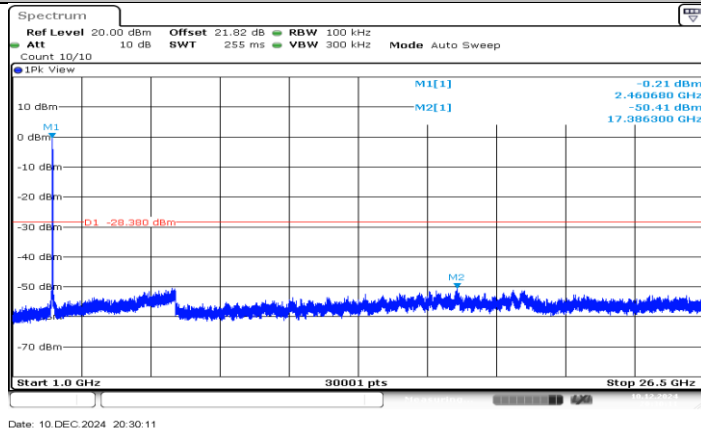
11AX20MIMO_Ant1_2462_1000~26500



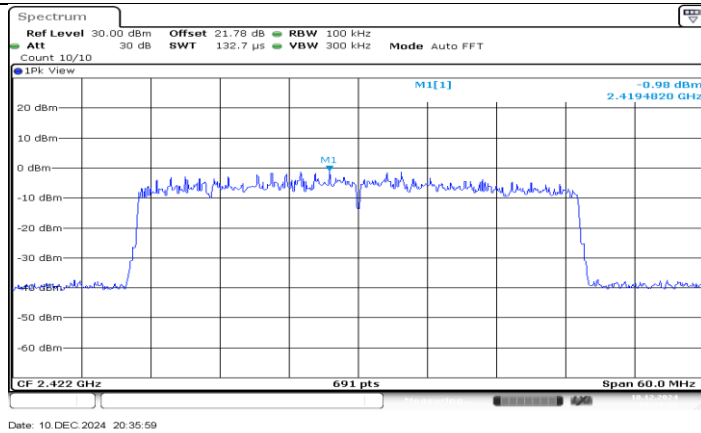
11AX20MIMO_Ant2_2462_0~Reference



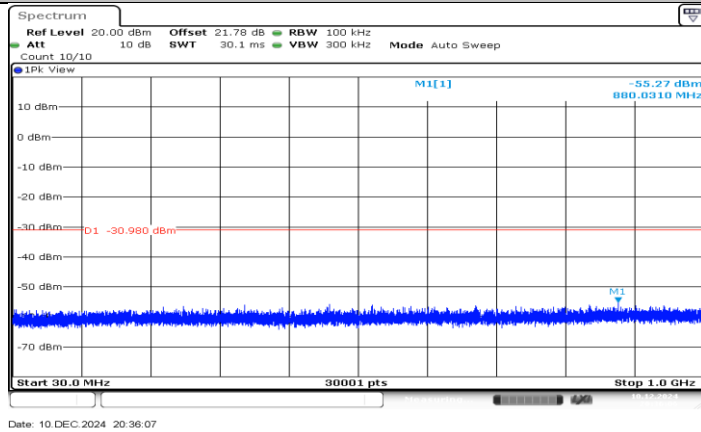
11AX20MIMO_Ant2_2462_30~1000



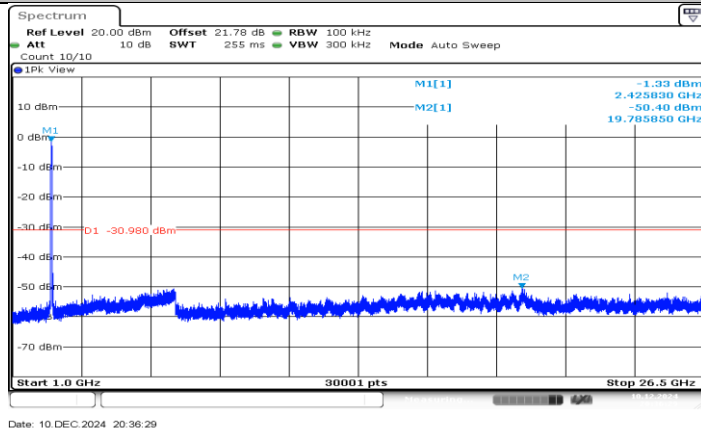
11AX20MIMO_Ant2_2462_1000~26500



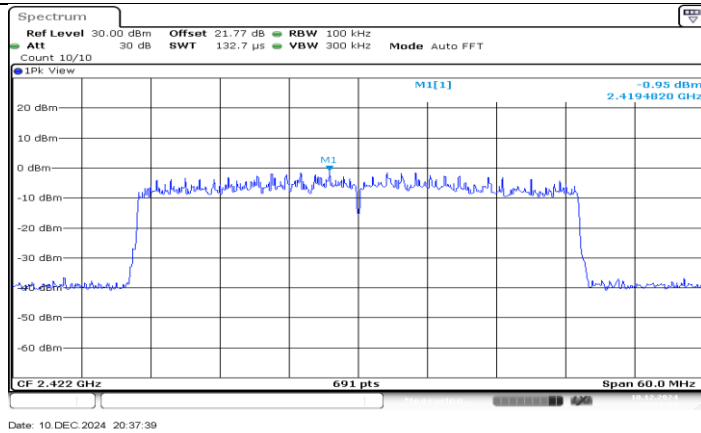
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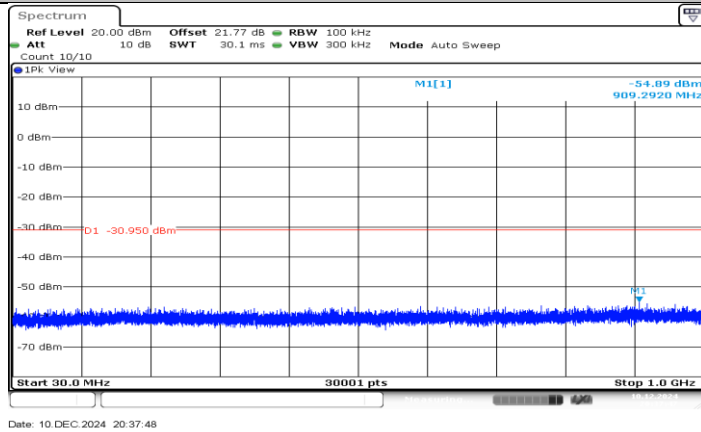
11AX40MIMO_Ant1_2422_30~1000



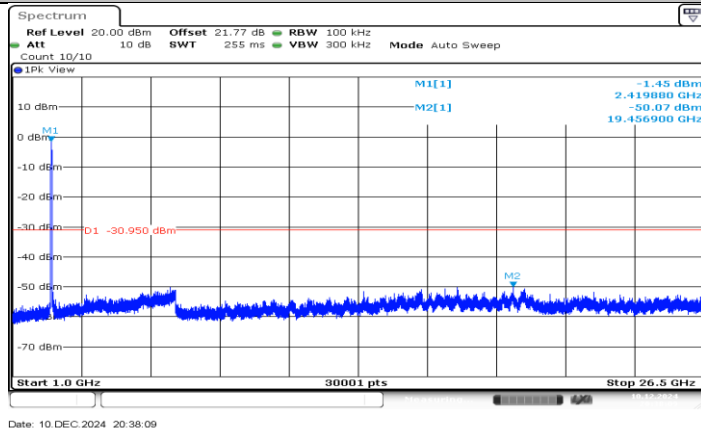
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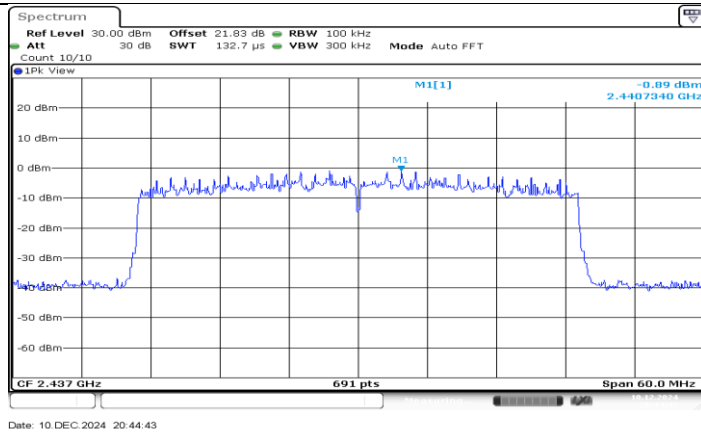
11AX40MIMO_Ant2_2422_0~Reference



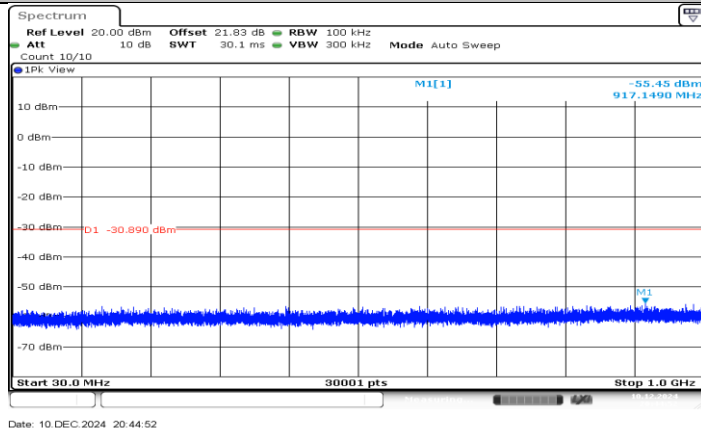
11AX40MIMO_Ant2_2422_30~1000



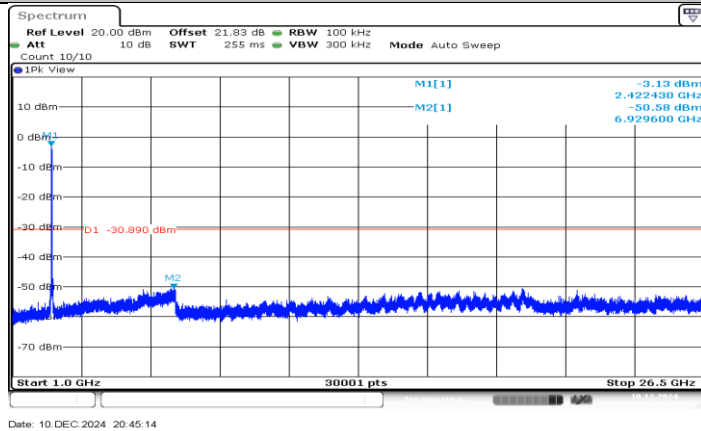
11AX40MIMO_Ant2_2422_1000~26500



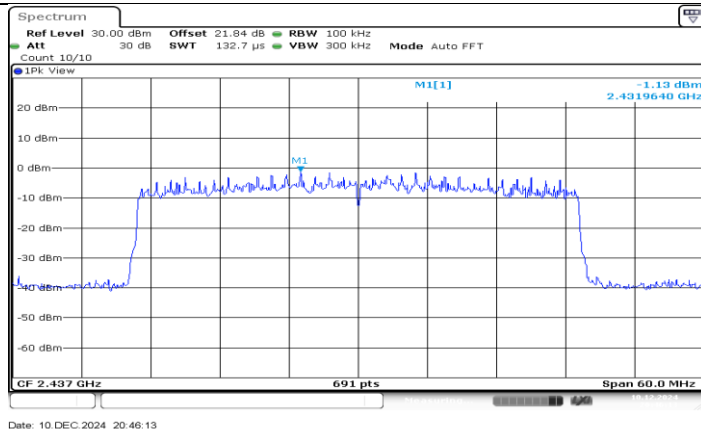
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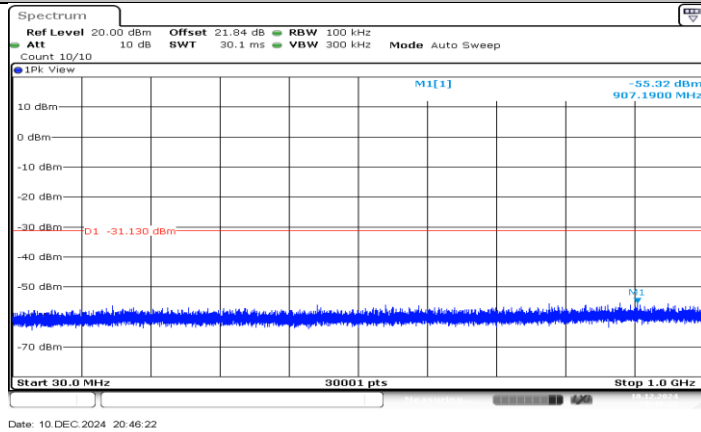
11AX40MIMO_Ant1_2437_30~1000



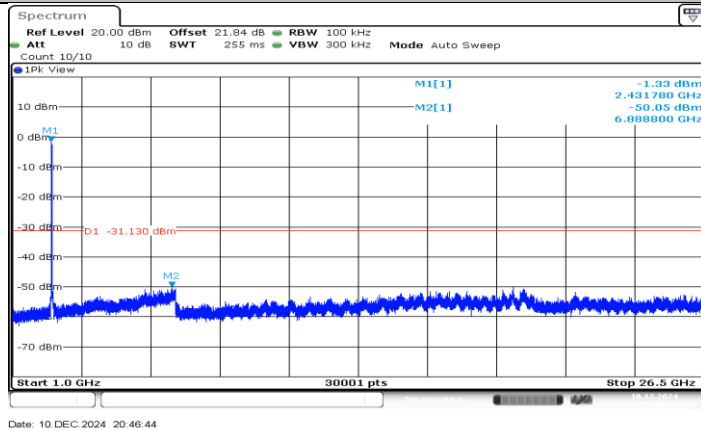
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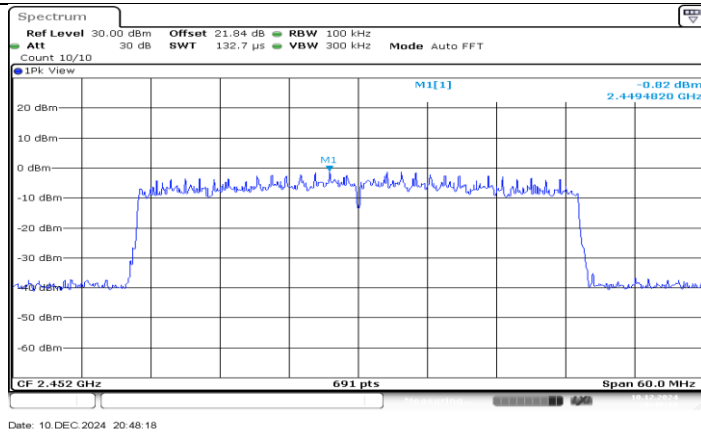
11AX40MIMO_Ant2_2437_0~Reference



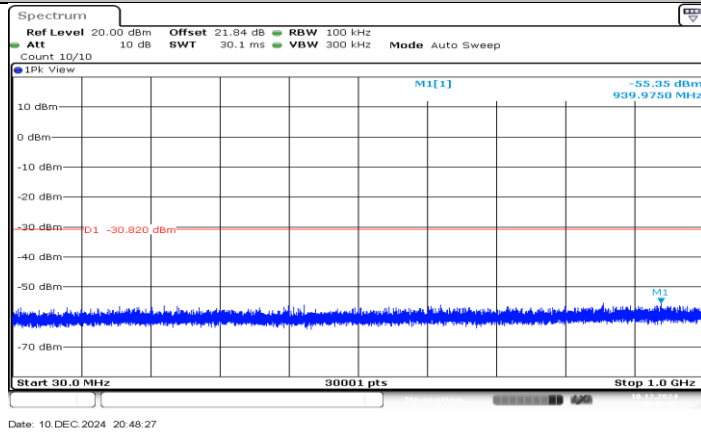
11AX40MIMO_Ant2_2437_30~1000



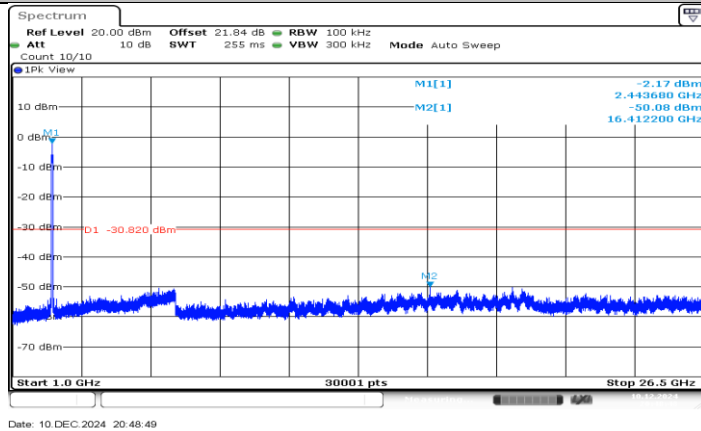
11AX40MIMO_Ant2_2437_1000~26500



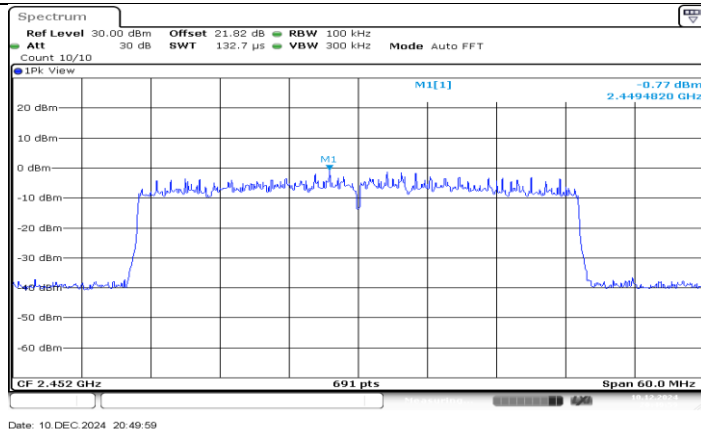
11AX40MIMO_Ant1_2452_0~Reference



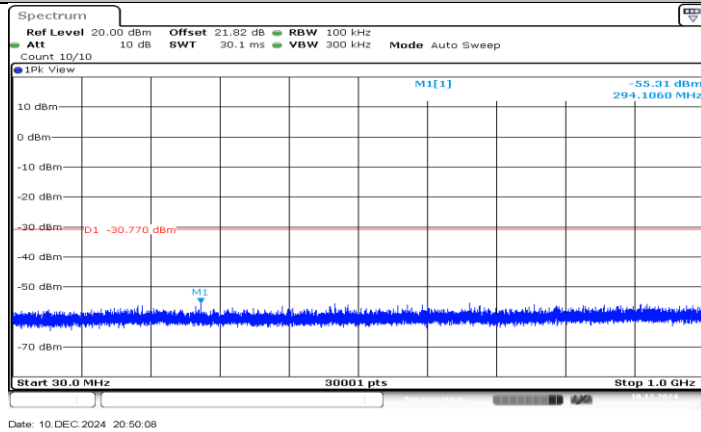
11AX40MIMO_Ant1_2452_30~1000



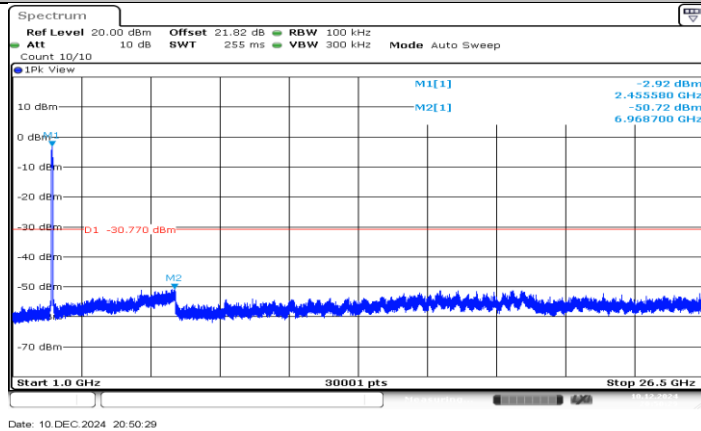
11AX40MIMO_Ant1_2452_1000~26500



11AX40MIMO_Ant2_2452_0~Reference



11AX40MIMO_Ant2_2452_30~1000



11AX40MIMO_Ant2_2452_1000~26500

11.7. APPENDIX G: DUTY CYCLE**11.7.1. Test Result**

| Test Mode | On Time (msec) | Period (msec) | Duty Cycle x (Linear) | Duty Cycle (%) | Duty Cycle Correction Factor (dB) | 1/T Minimum VBW (kHz) | Final setting For VBW (kHz) |
|------------|----------------|---------------|-----------------------|----------------|-----------------------------------|-----------------------|-----------------------------|
| 11B | 8.37 | 8.99 | 0.9310 | 93.10 | 0.31 | 0.12 | 1 |
| 11G | 1.39 | 2.03 | 0.6847 | 68.47 | 1.64 | 0.72 | 1 |
| 11N20MIMO | 1.30 | 1.94 | 0.6701 | 67.01 | 1.74 | 0.77 | 1 |
| 11N40MIMO | 0.65 | 1.29 | 0.5039 | 50.39 | 2.98 | 1.54 | 2 |
| 11AX20MIMO | 1.03 | 1.67 | 0.6168 | 61.68 | 2.10 | 0.97 | 1 |
| 11AX40MIMO | 0.31 | 0.92 | 0.3370 | 33.70 | 4.72 | 3.23 | 4 |

Note:

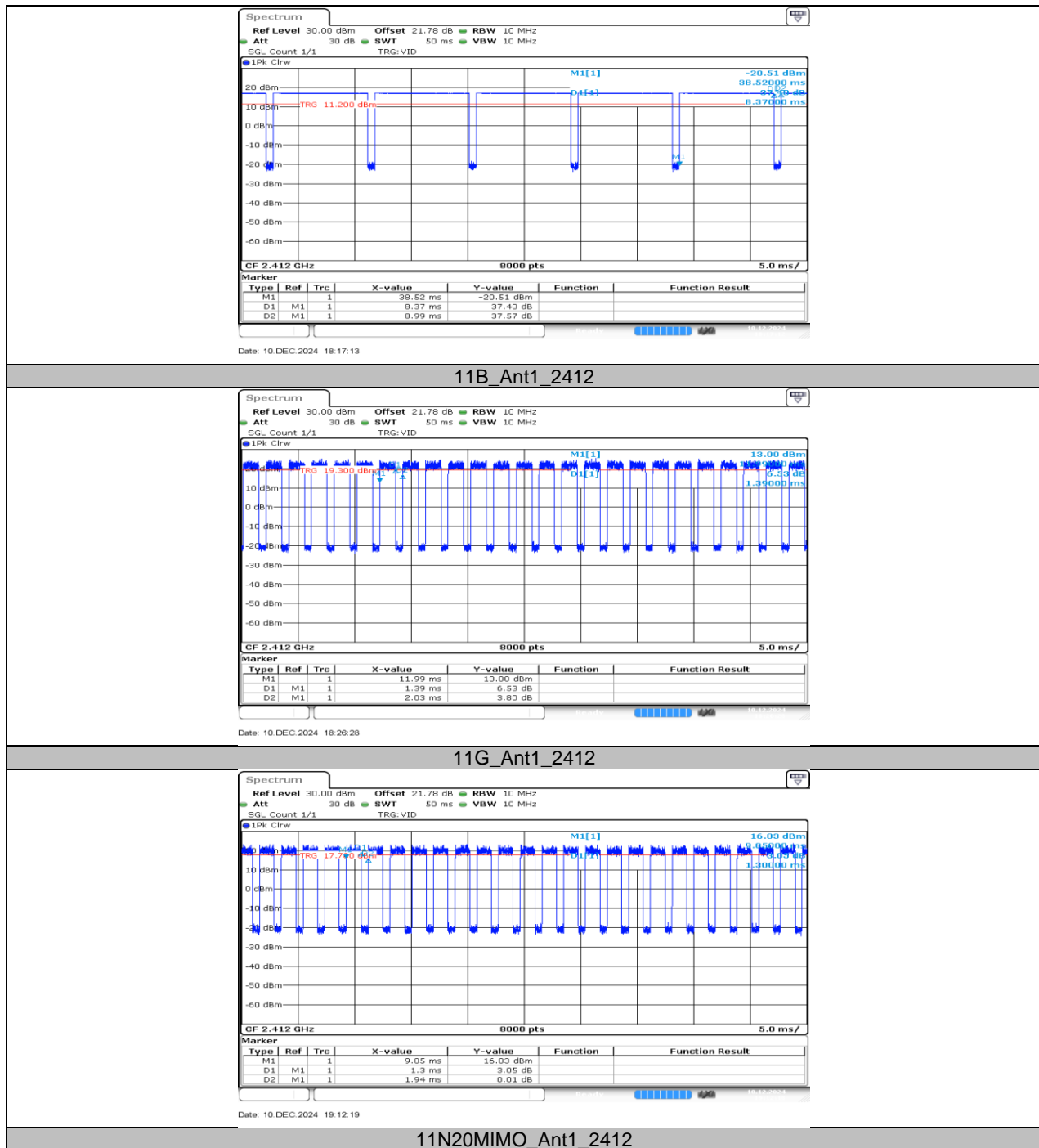
Duty Cycle Correction Factor= $10\log(1/x)$.

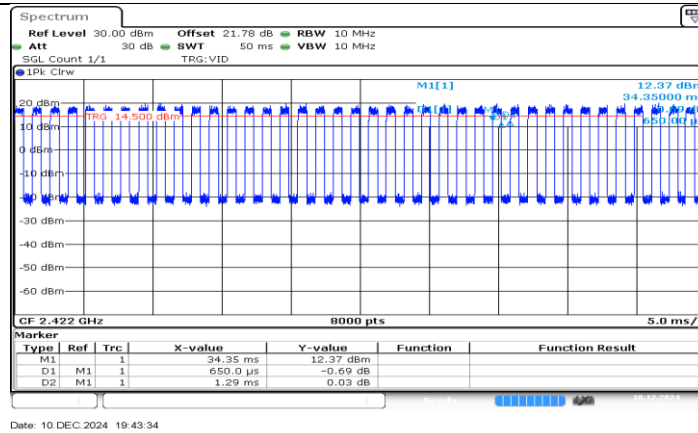
Where: x is Duty Cycle (Linear)

Where: T is On Time

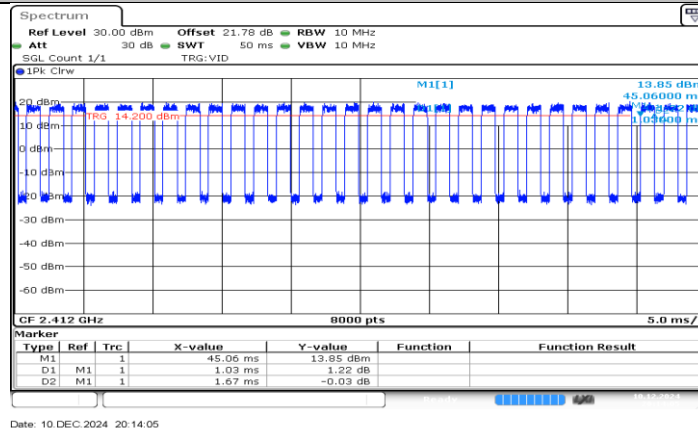
If that calculated VBW is not available on the analyzer then the next higher value should be used.

11.7.2. Test Graphs

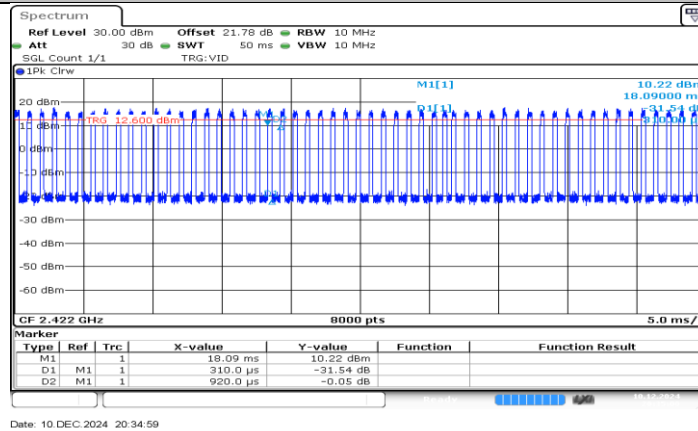




11N40MIMO_Ant1_2422



11AX20MIMO_Ant1_2412



11AX40MIMO_Ant1_2422

END OF REPORT