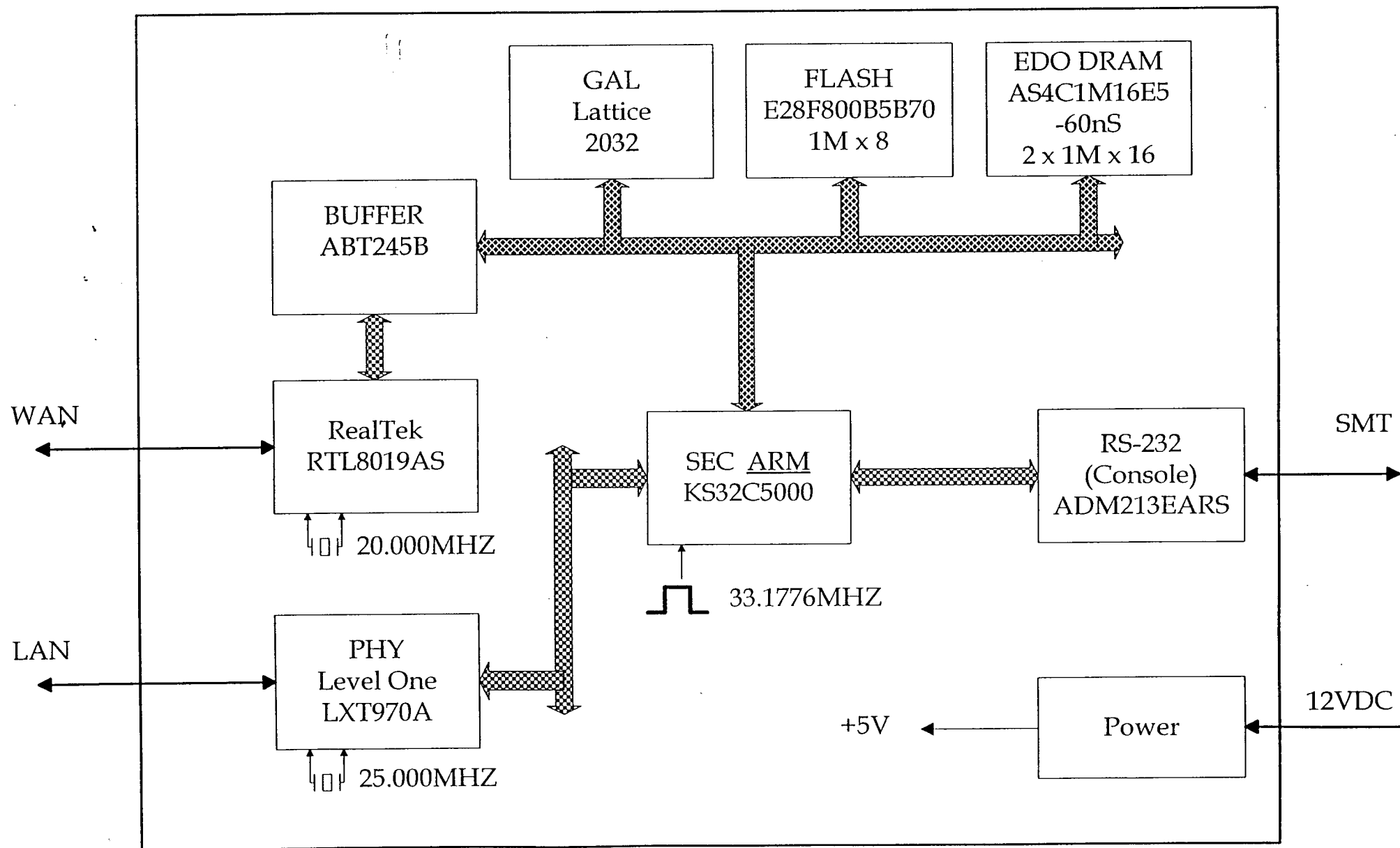
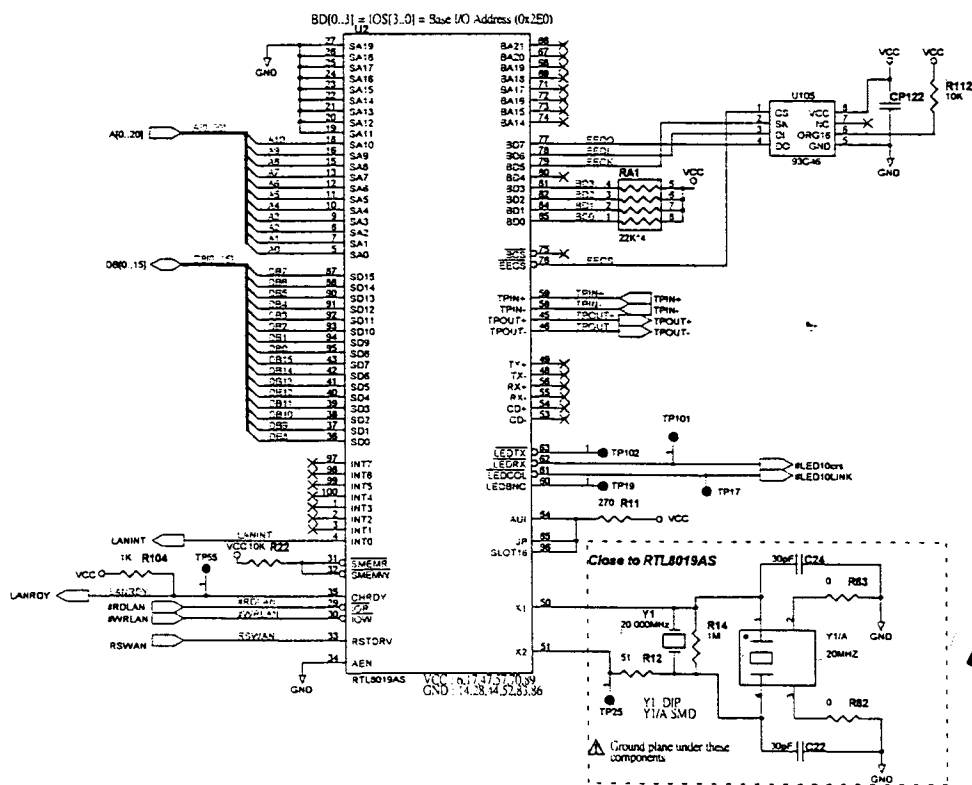
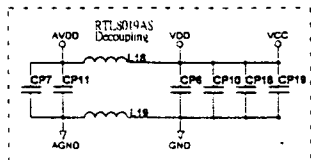


Hardware Block Diagram of Prestige 310





合勤科技股份有限公司

TITLE: P310

DRN: 89 97 DWG NO: P310-ck-000

CKD: 林紹安 REV: B0-1

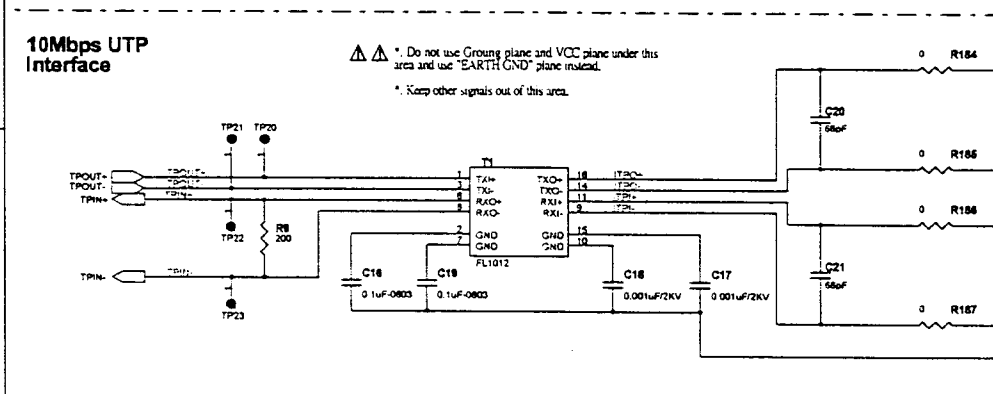
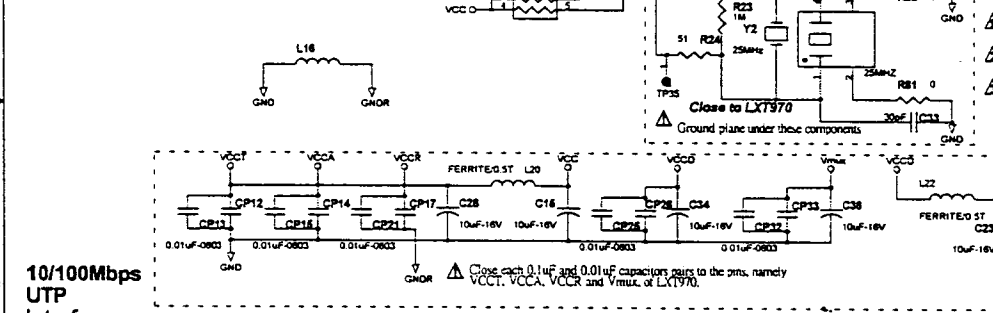
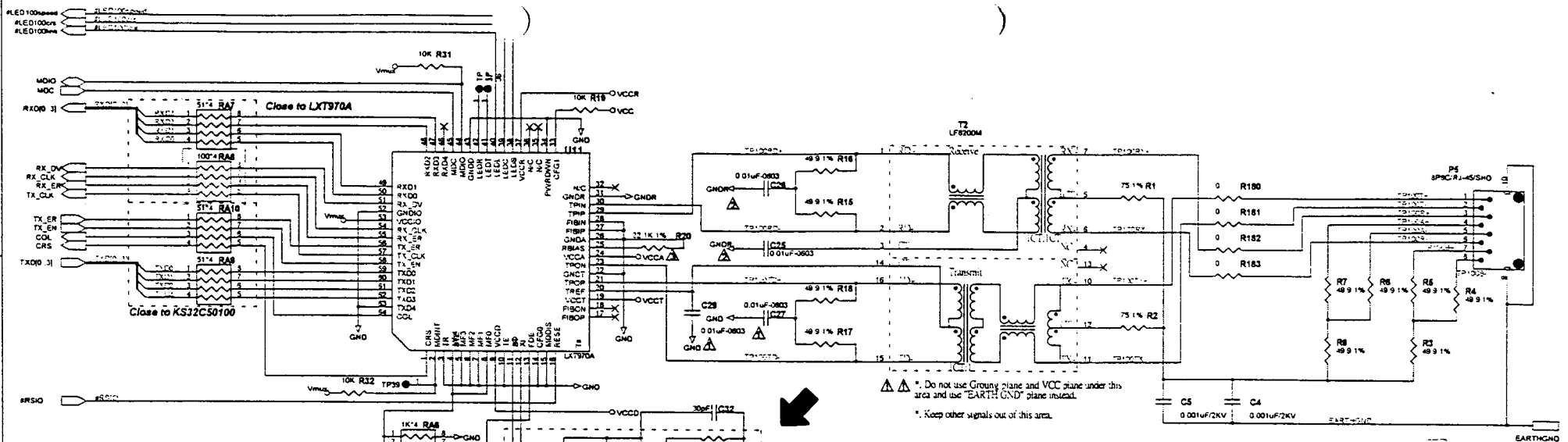
APPD: 林紹安 SHEET 4 OF 7

Amendment History

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Section wan circuits

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* Do not use Ground plane and VCC plane under this area and use "EARTH GND" plane instead.
* Keep other signals out of this area.

Ground close to pin 22 GNDT of LX7970
Ground close to pin 31 GNDT of LX7970
Resistor 22.1K 1% close to pin 25 & 26 of LX7970
Keep it away from other components or signal traces and do not run any signals under the resistor.

* Keep the LX7970, magnetics, and RJ45 connector as close together as possible.
* Route the transmit and receive pairs differentially and keep them as short as possible, e.g.
(1) TP100TD+ & TP100TD-
(2) TP100RD+ & TP100RD-
(3) TP100TX+ & TP100TX-
(4) TP100RX+ & TP100RX-
(5) TP100T+ & TP100T-
(6) TP100R+ & TP100R-
(7) TP100A+ & TP100A-
(8) TP100B+ & TP100B-
* Route traces over an unbroken ground plane. Do not route over breaks in the ground plane.
* Provide shielding by placing a ground plane under TP0P and TP0N and the magnetics. Place the shielding ground plane between the traces and the ground plane.

68pF & ML8201309 default open
* Keep the RTL8019AS, magnetics, and RJ45 connector as close together as possible.
* Route the transmit and receive pairs differentially and keep them as short as possible, e.g.
(1) TP0UT+ & TP0UT-
(2) TP0IN+ & TP0IN-
(3) TP0P+ & TP0P-
(4) TP0R+ & TP0R-
(5) TP0A+ & TP0A-
* Route traces over an unbroken ground plane. Do not route over breaks in the ground plane.
* Provide shielding by placing a ground plane under the area including RTL8019AS and magnetics. Place the shielding ground plane between the traces and the ground plane.

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TITLE: P310	
DRN: 89/99	DWG NO: P310-CK-005
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Amendment History

Date	Release	Description
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Section 10/100 LAN interface		
Size: C	Document Number: P310B0.dsn	Rev: B0
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