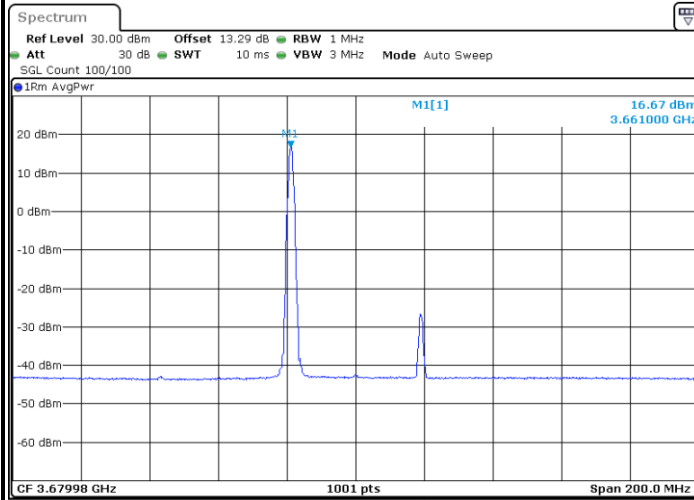




FR1 Part 96 n48 / 40MHz

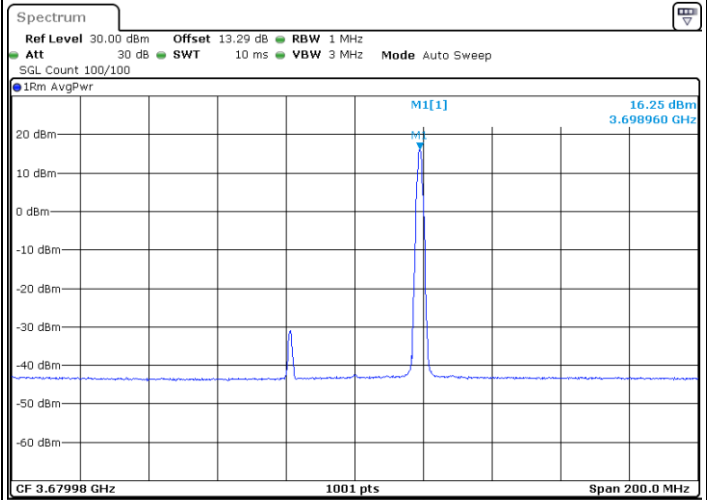
BPSK

Highest Channel / 1RB0



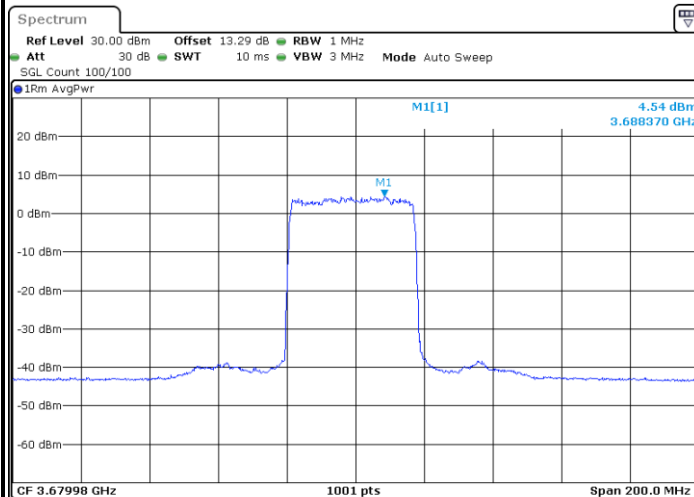
Date: 9.MAY.2023 00:02:42

Highest Channel / 1RBmax



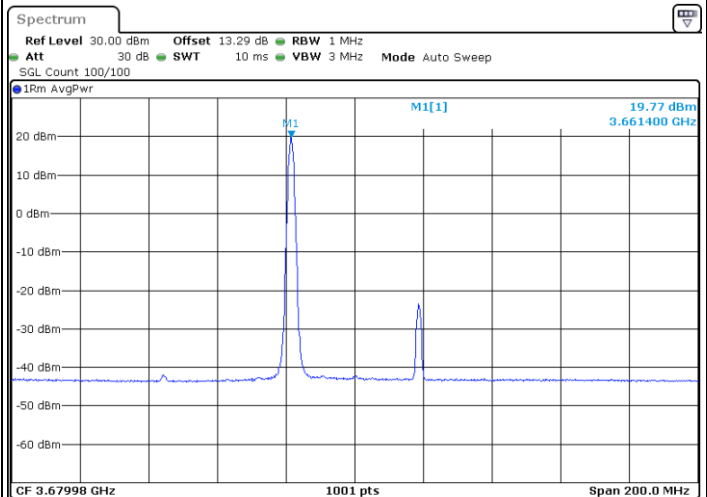
Date: 9.MAY.2023 00:04:07

Highest Channel / FullIRB



Date: 8.MAY.2023 23:57:06

Highest Channel / 1RB1



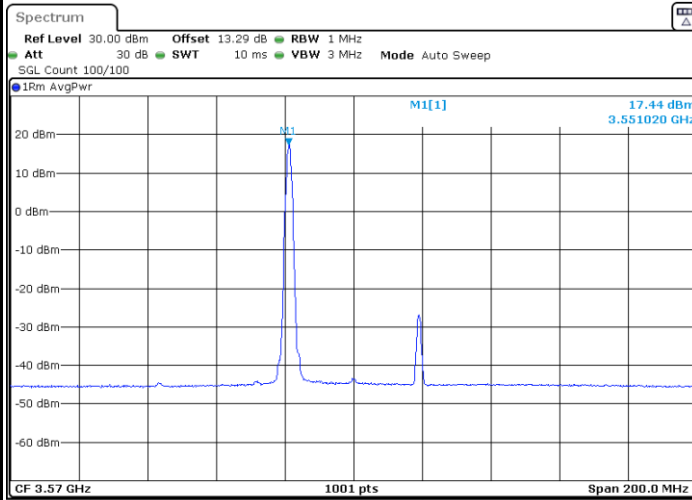
Date: 9.MAY.2023 00:08:31



FR1 Part 96 n48 / 40MHz

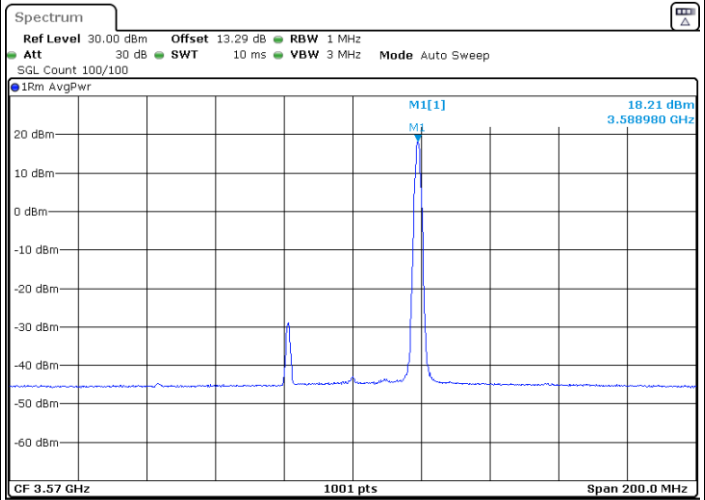
QPSK

Lowest Channel / 1RB0



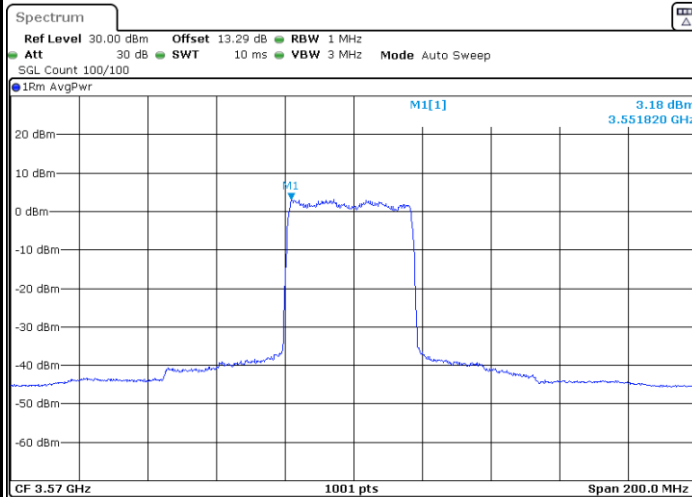
Date: 8.MAY.2023 10:25:33

Lowest Channel / 1RBmax



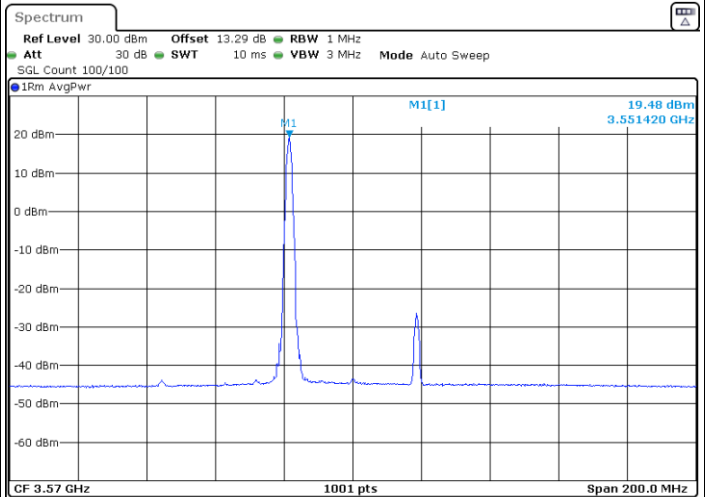
Date: 8.MAY.2023 10:31:39

Lowest Channel / FullRB



Date: 8.MAY.2023 10:21:17

Lowest Channel / 1RB1



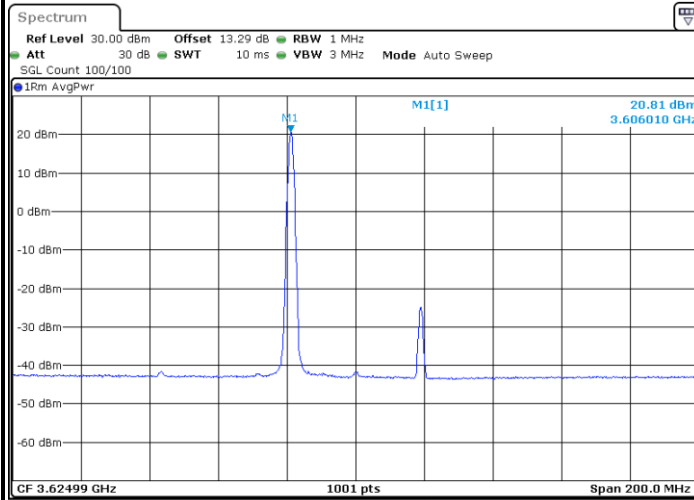
Date: 8.MAY.2023 10:28:29



FR1 Part 96 n48 / 40MHz

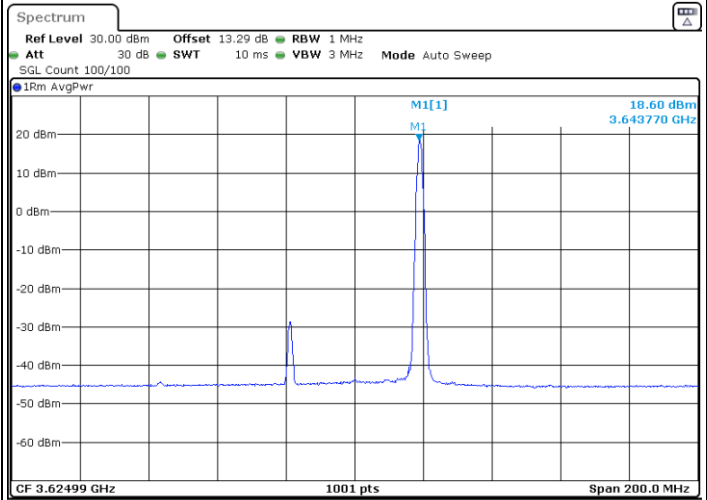
QPSK

Middle Channel / 1RB0



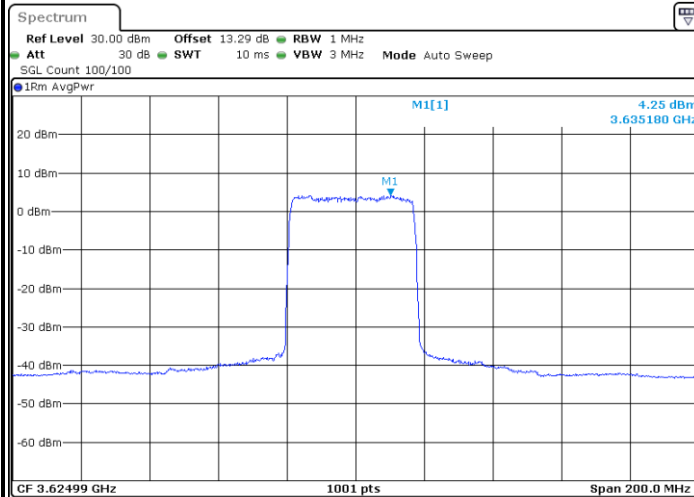
Date: 8.MAY.2023 23:49:28

Middle Channel / 1RBmax



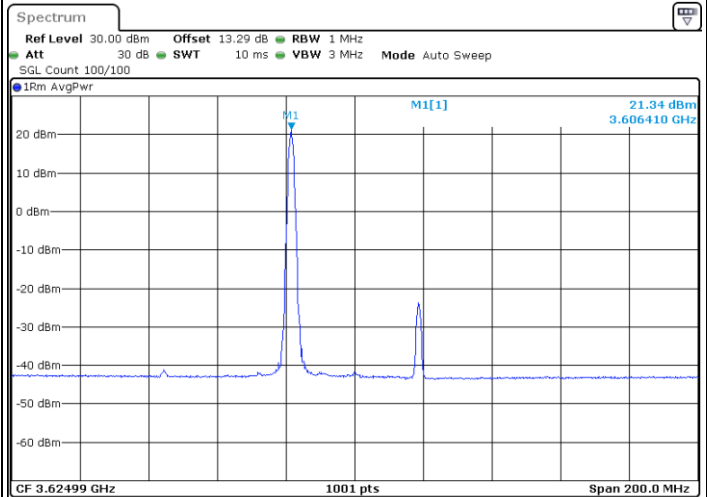
Date: 8.MAY.2023 10:51:50

Middle Channel / FullRB



Date: 8.MAY.2023 23:54:13

Middle Channel / 1RB1



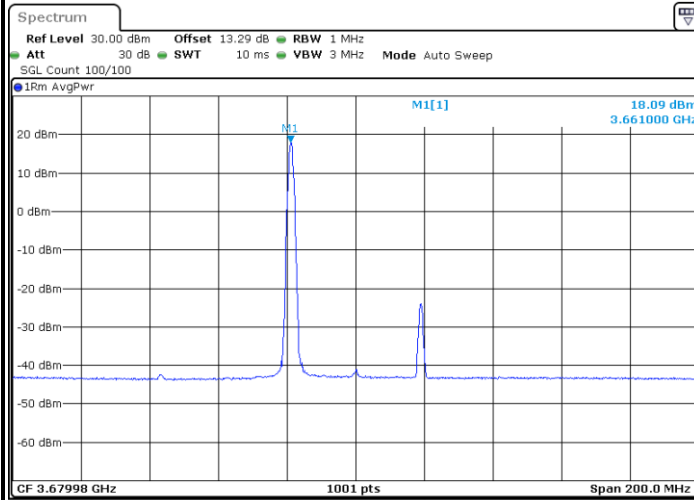
Date: 8.MAY.2023 23:51:05



FR1 Part 96 n48 / 40MHz

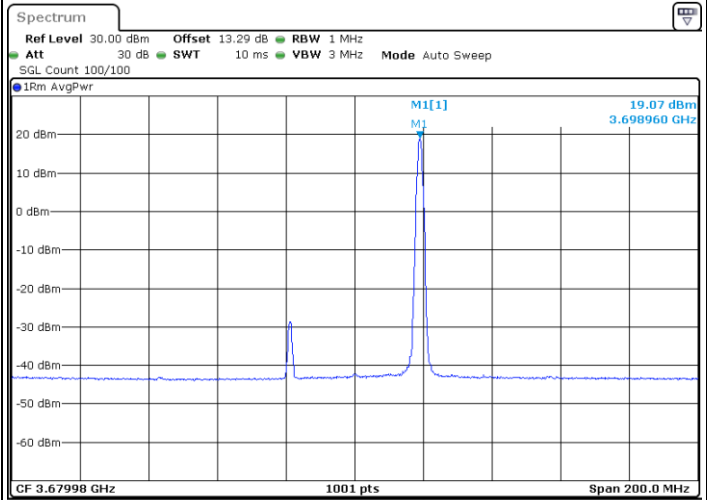
QPSK

Highest Channel / 1RB0



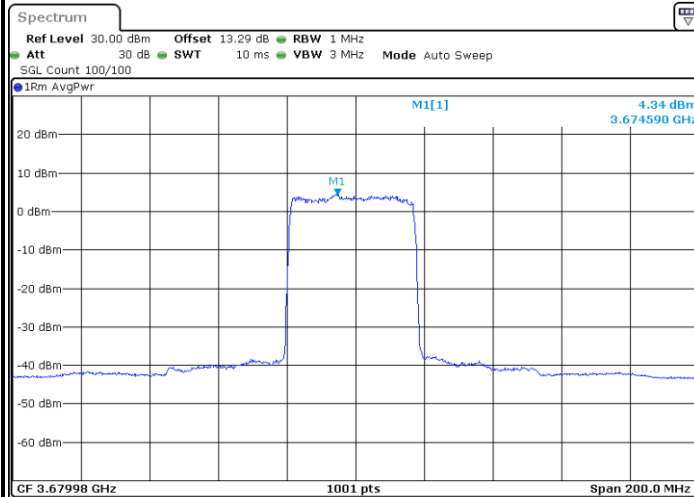
Date: 9.MAY.2023 00:02:02

Highest Channel / 1RBmax



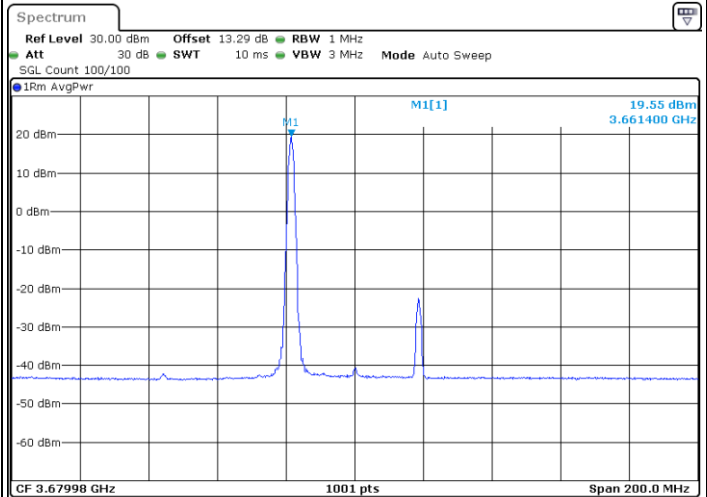
Date: 9.MAY.2023 00:04:28

Highest Channel / FullIRB



Date: 8.MAY.2023 23:57:30

Highest Channel / 1RB1



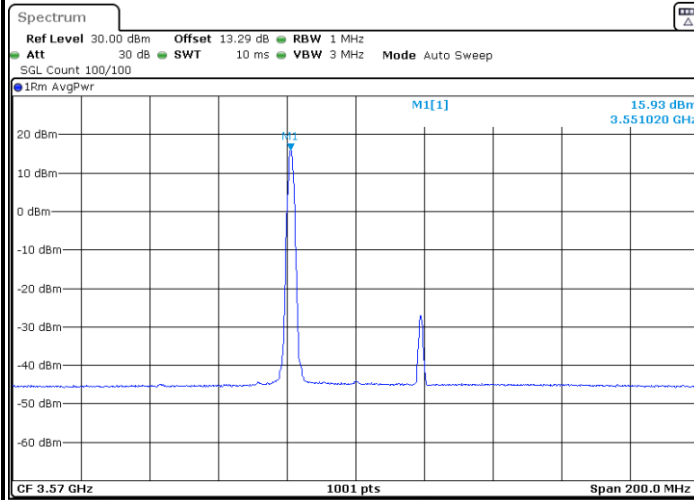
Date: 9.MAY.2023 00:08:03



FR1 Part 96 n48 / 40MHz

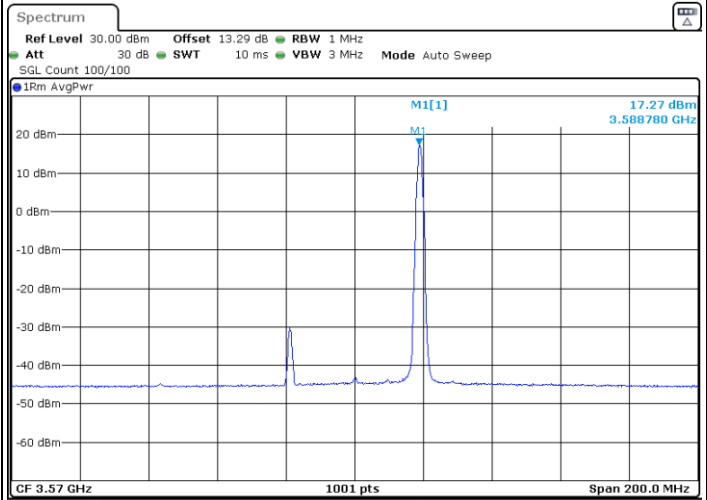
16QAM

Lowest Channel / 1RB0



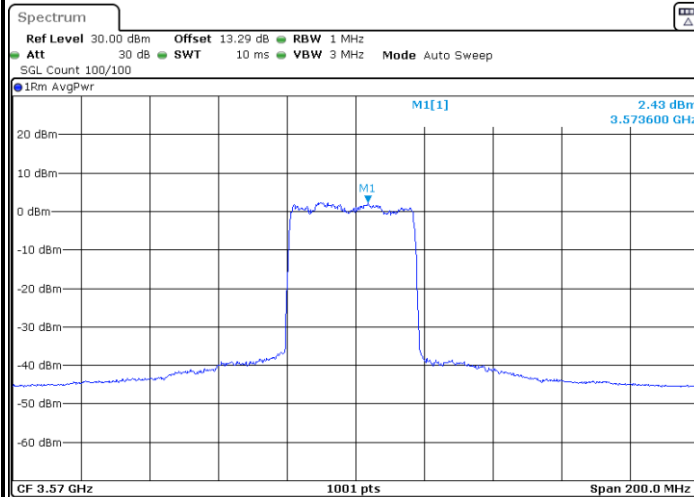
Date: 8.MAY.2023 10:25:04

Lowest Channel / 1RBmax



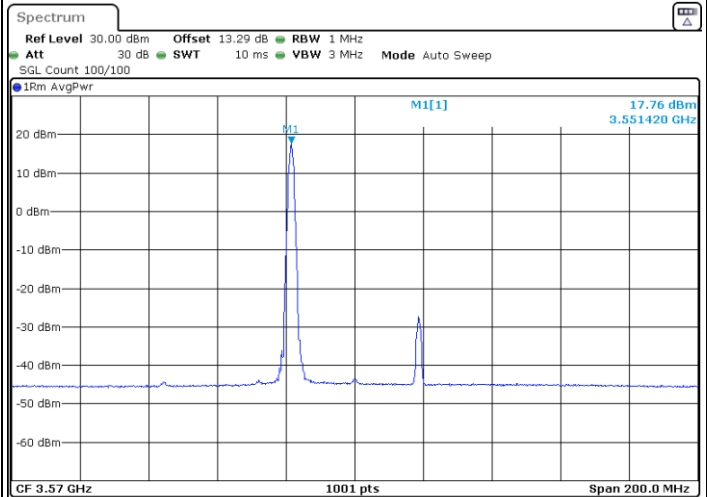
Date: 8.MAY.2023 10:31:07

Lowest Channel / FullRB



Date: 8.MAY.2023 10:22:33

Lowest Channel / 1RB1



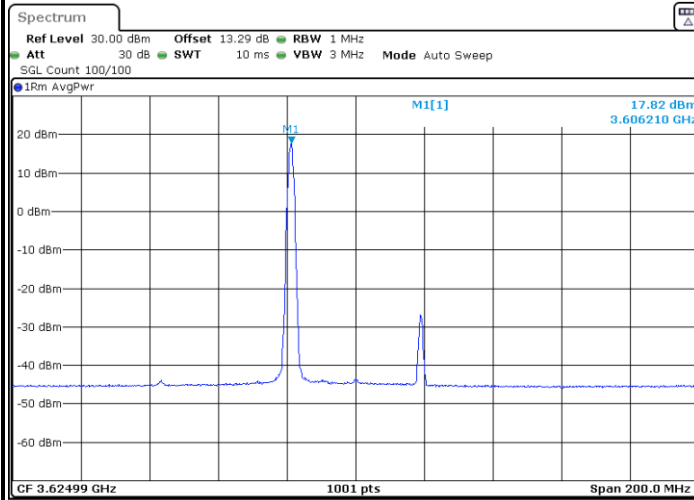
Date: 8.MAY.2023 10:28:52



FR1 Part 96 n48 / 40MHz

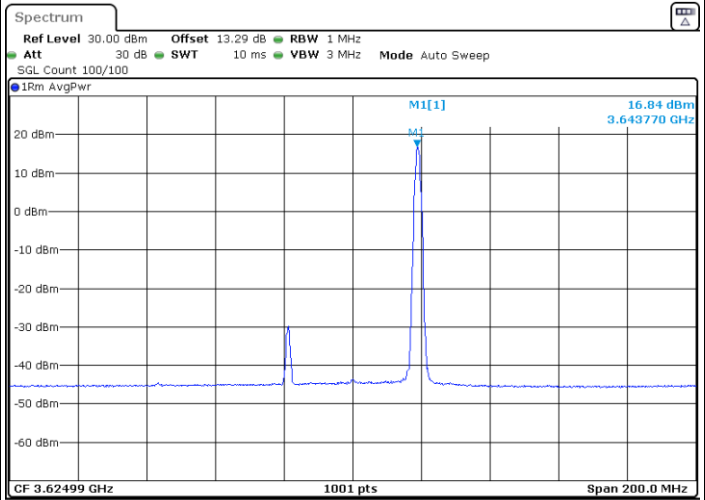
16QAM

Middle Channel / 1RB0



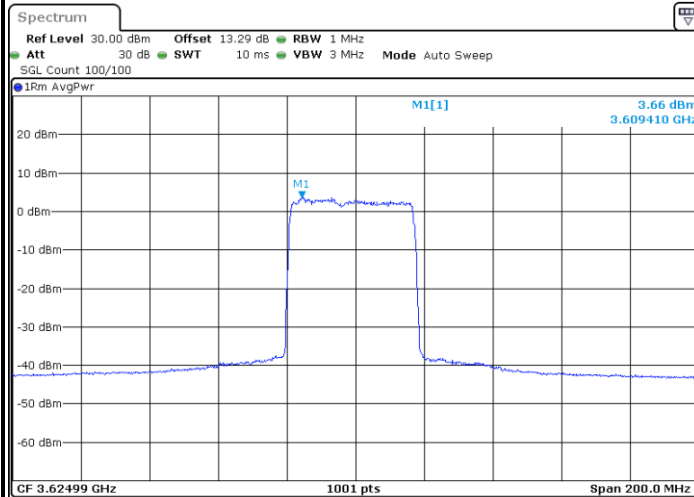
Date: 8.MAY.2023 10:55:22

Middle Channel / 1RBmax



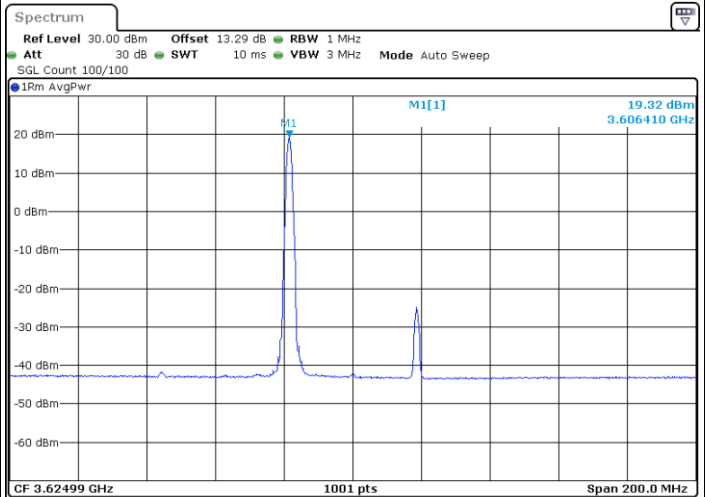
Date: 8.MAY.2023 10:52:13

Middle Channel / FullRB



Date: 8.MAY.2023 23:53:55

Middle Channel / 1RB1



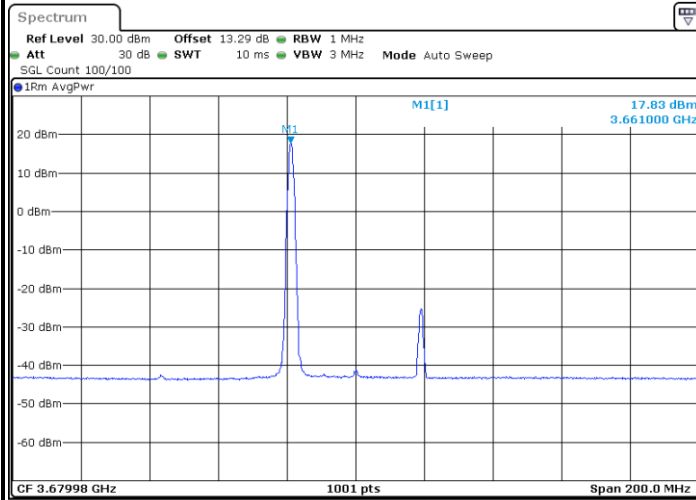
Date: 8.MAY.2023 23:51:32



FR1 Part 96 n48 / 40MHz

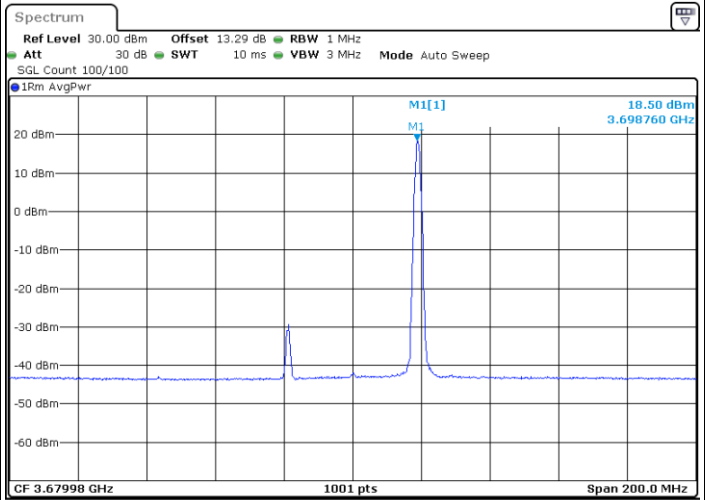
16QAM

Highest Channel / 1RB0



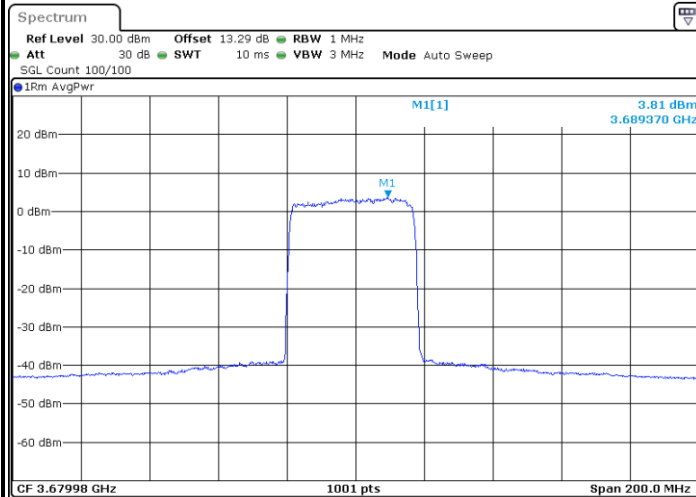
Date: 9.MAY.2023 00:01:31

Highest Channel / 1RBmax



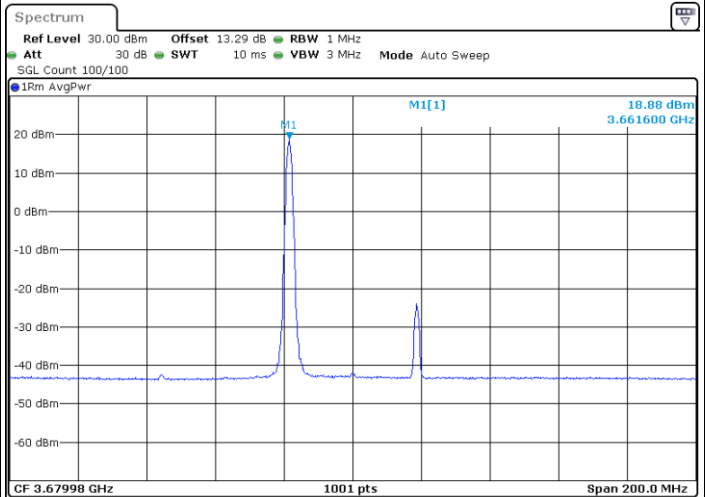
Date: 9.MAY.2023 00:04:45

Highest Channel / FullIRB



Date: 8.MAY.2023 23:58:58

Highest Channel / 1RB1



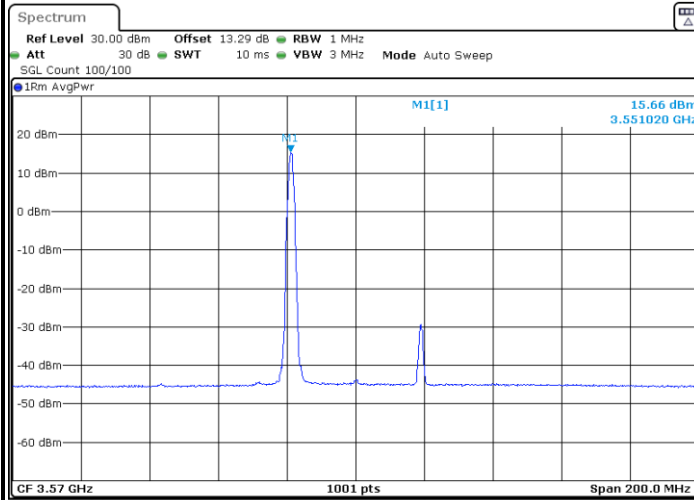
Date: 9.MAY.2023 00:07:43



FR1 Part 96 n48 / 40MHz

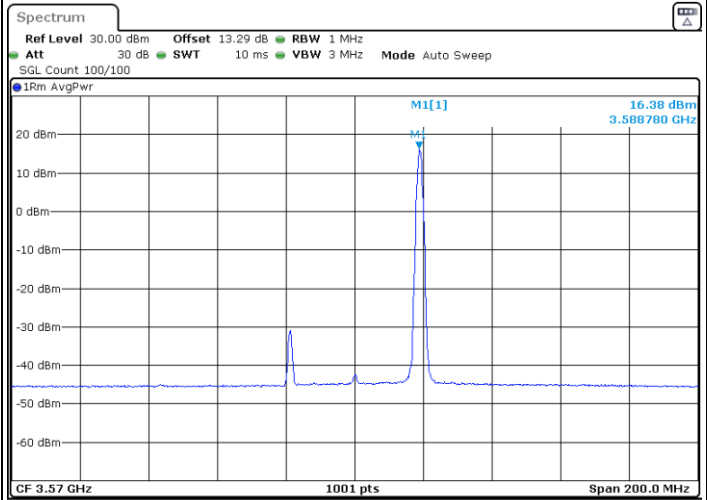
64QAM

Lowest Channel / 1RB0



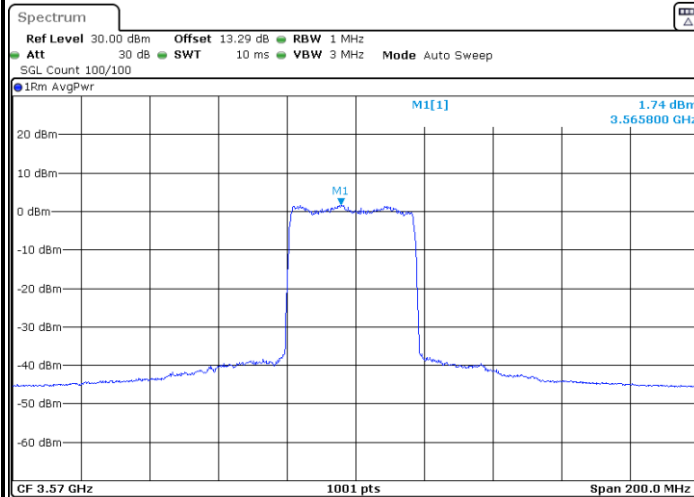
Date: 8.MAY.2023 10:24:39

Lowest Channel / 1RBmax



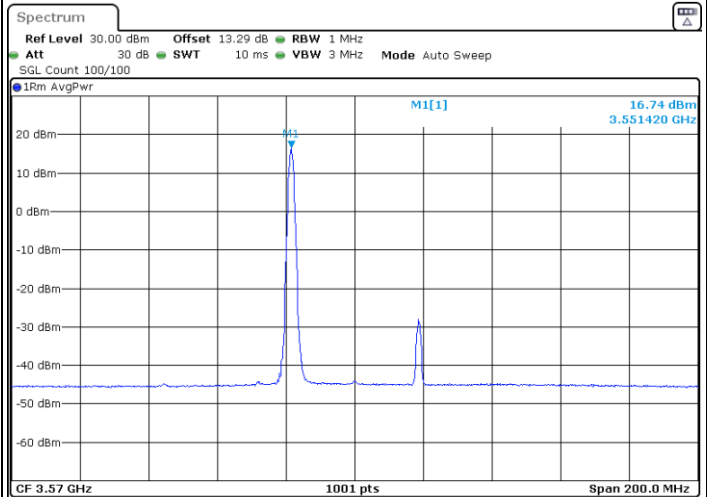
Date: 8.MAY.2023 10:30:44

Lowest Channel / FullRB



Date: 8.MAY.2023 10:23:01

Lowest Channel / 1RB1



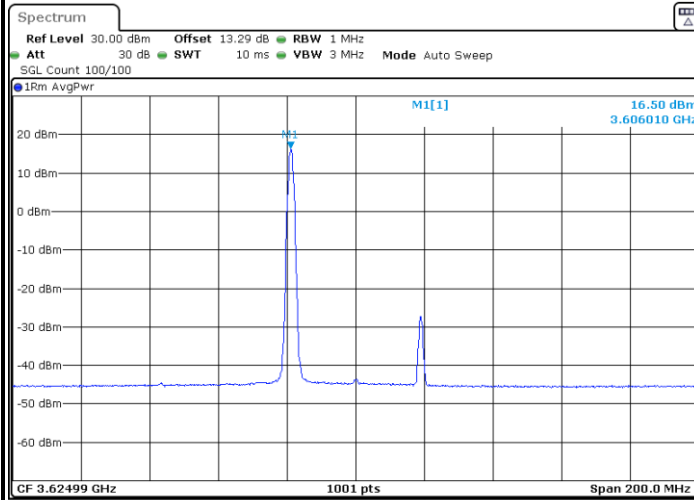
Date: 8.MAY.2023 10:29:16



FR1 Part 96 n48 / 40MHz

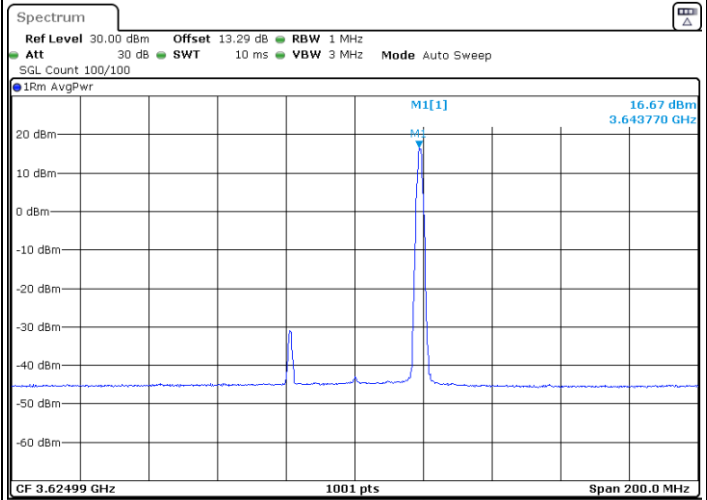
64QAM

Middle Channel / 1RB0



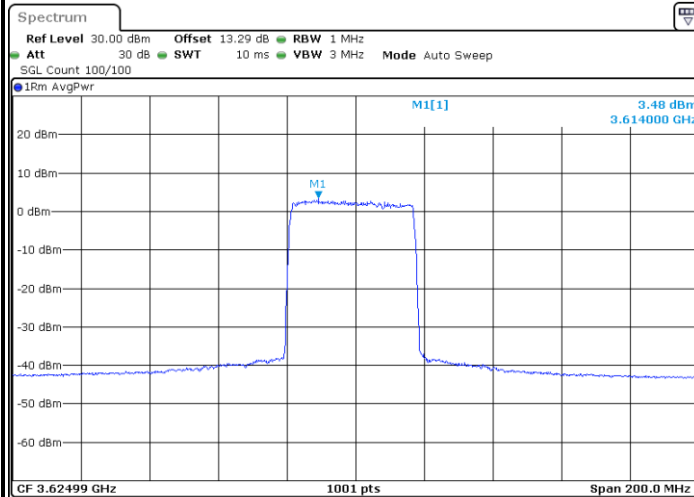
Date: 8.MAY.2023 10:54:37

Middle Channel / 1RBmax



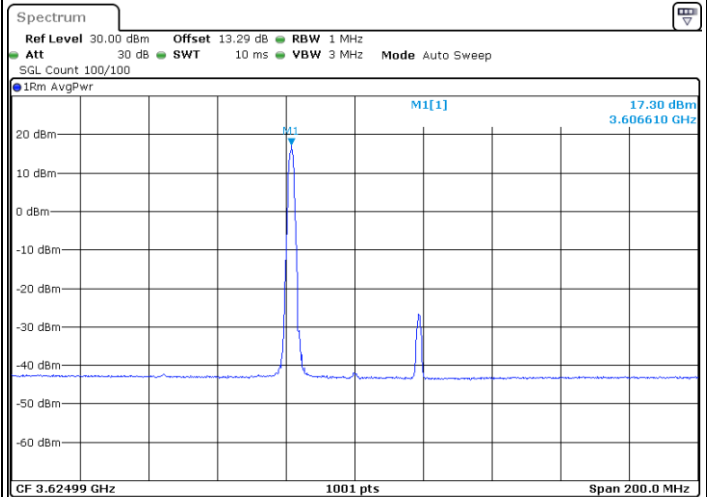
Date: 8.MAY.2023 10:53:07

Middle Channel / FullRB



Date: 8.MAY.2023 23:53:37

Middle Channel / 1RB1



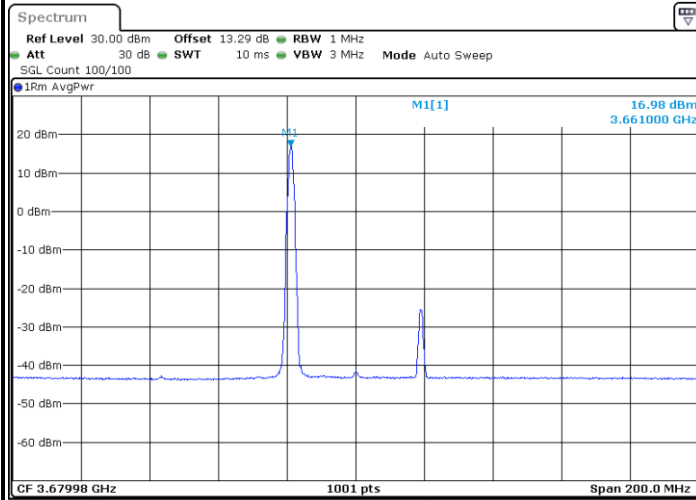
Date: 8.MAY.2023 23:52:13



FR1 Part 96 n48 / 40MHz

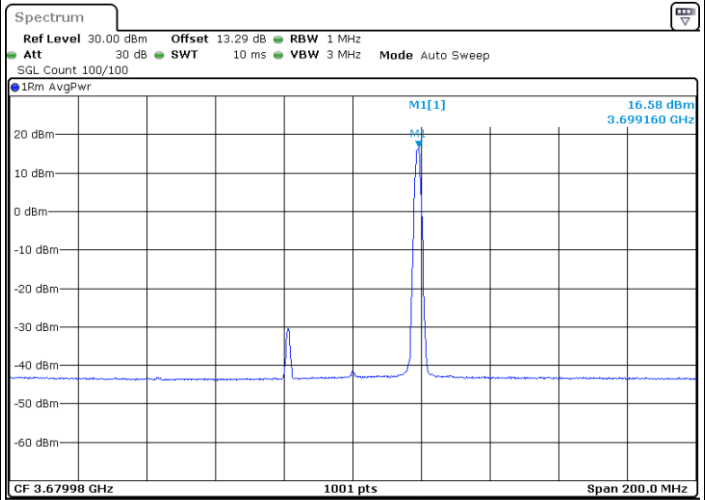
64QAM

Highest Channel / 1RB0



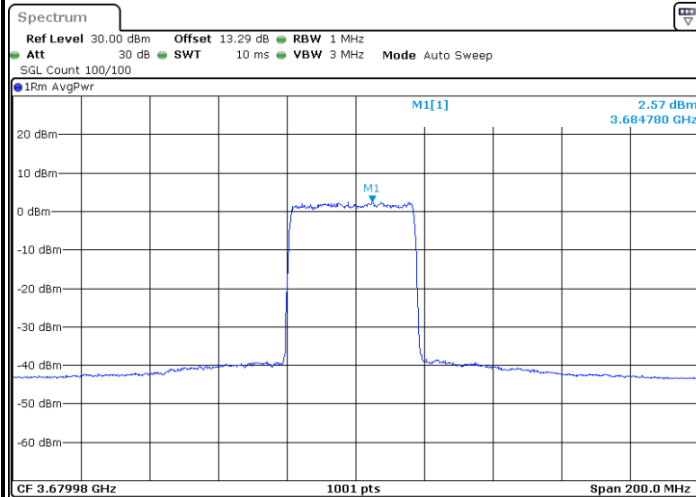
Date: 9.MAY.2023 00:01:12

Highest Channel / 1RBmax



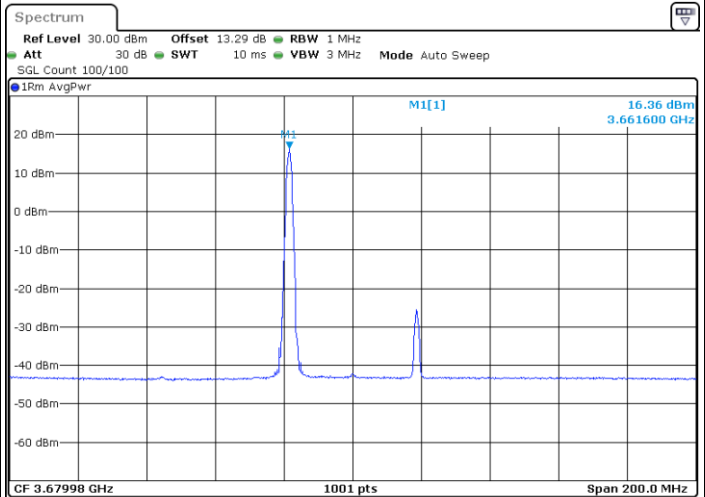
Date: 9.MAY.2023 00:05:09

Highest Channel / FullIRB



Date: 8.MAY.2023 23:59:22

Highest Channel / 1RB1



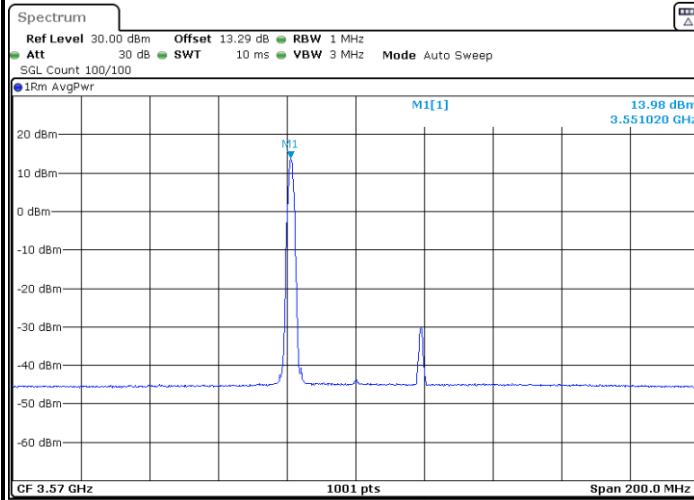
Date: 9.MAY.2023 00:06:57



FR1 Part 96 n48 / 40MHz

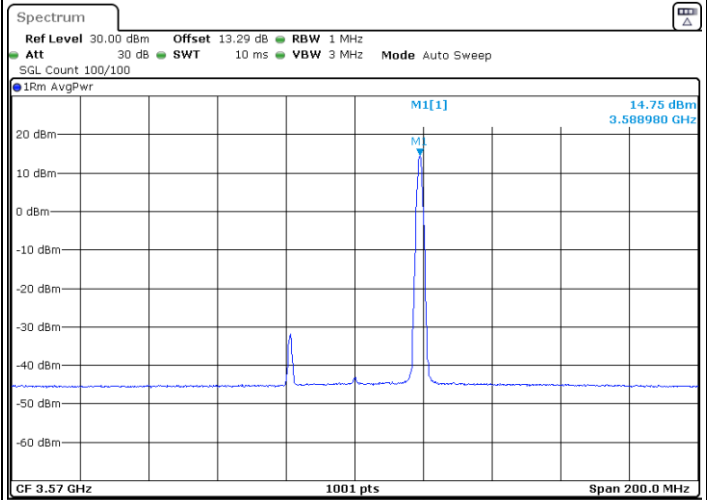
256QAM

Lowest Channel / 1RB0



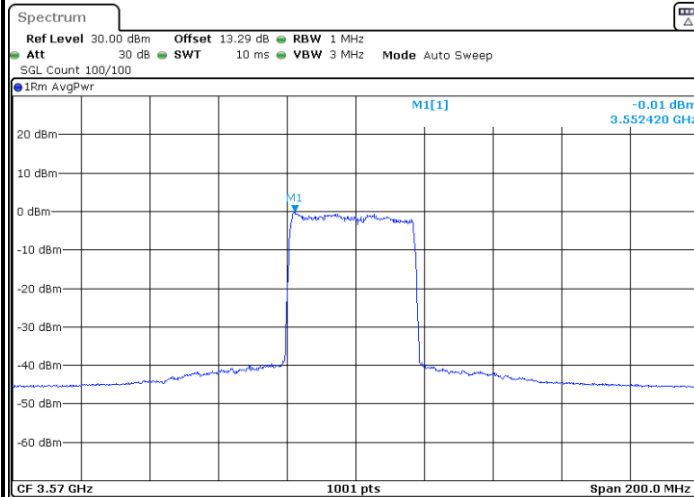
Date: 8.MAY.2023 10:24:05

Lowest Channel / 1RBmax



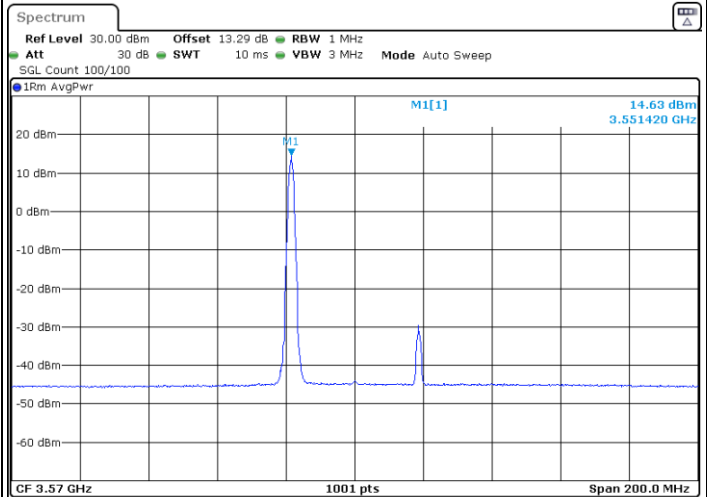
Date: 8.MAY.2023 10:30:21

Lowest Channel / FullRB



Date: 8.MAY.2023 10:23:38

Lowest Channel / 1RB1



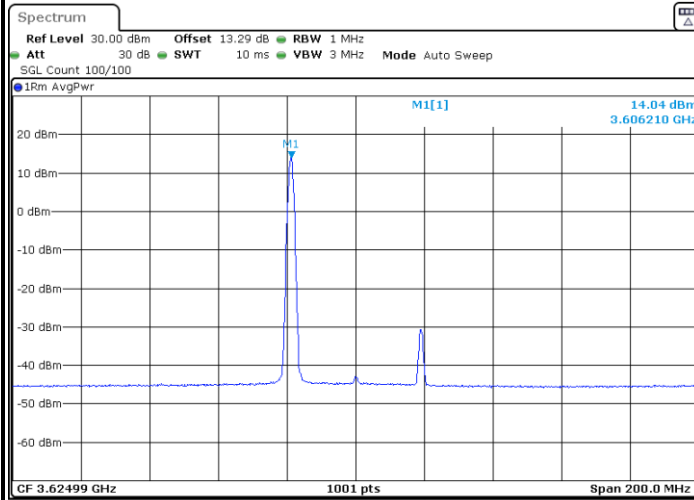
Date: 8.MAY.2023 10:29:49



FR1 Part 96 n48 / 40MHz

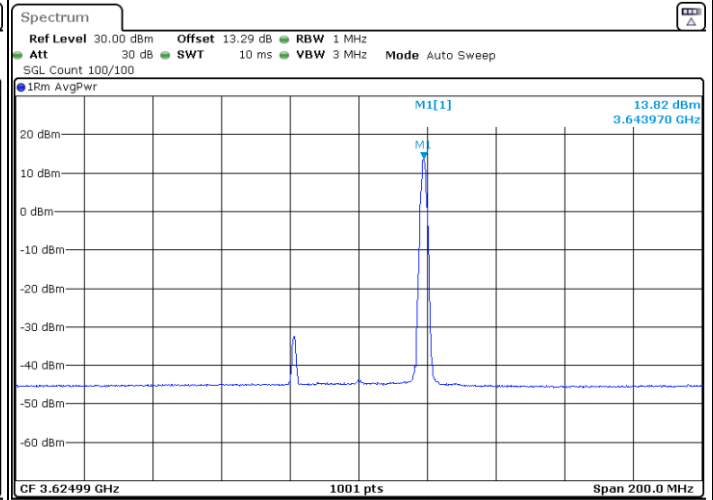
256QAM

Middle Channel / 1RB0



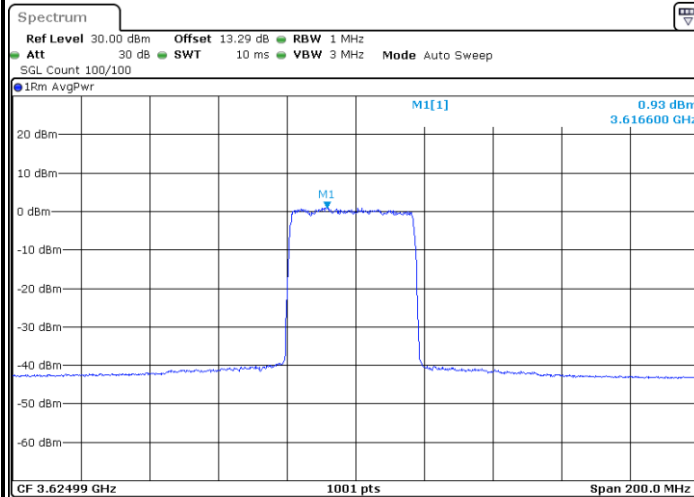
Date: 8.MAY.2023 10:54:10

Middle Channel / 1RBmax



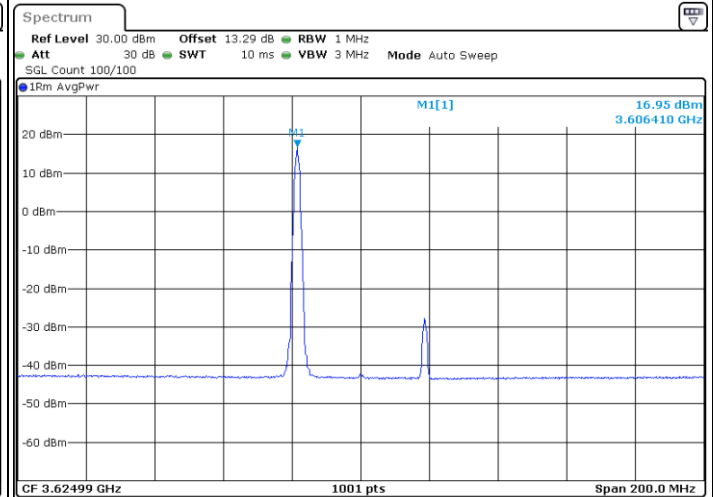
Date: 8.MAY.2023 10:53:42

Middle Channel / FullRB



Date: 8.MAY.2023 23:53:19

Middle Channel / 1RB1



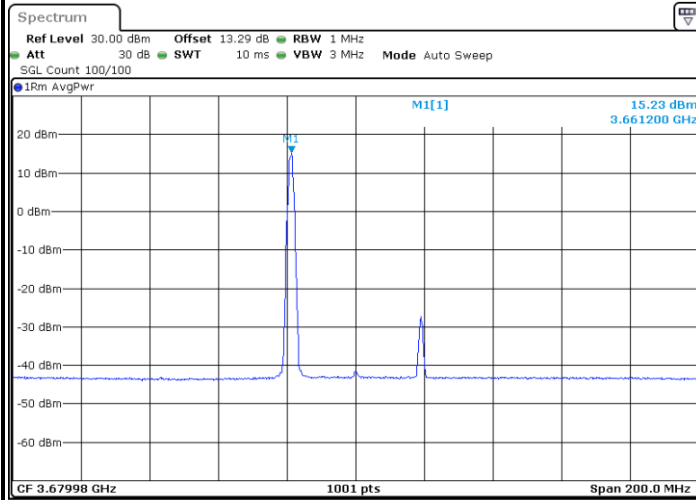
Date: 8.MAY.2023 23:52:44



FR1 Part 96 n48 / 40MHz

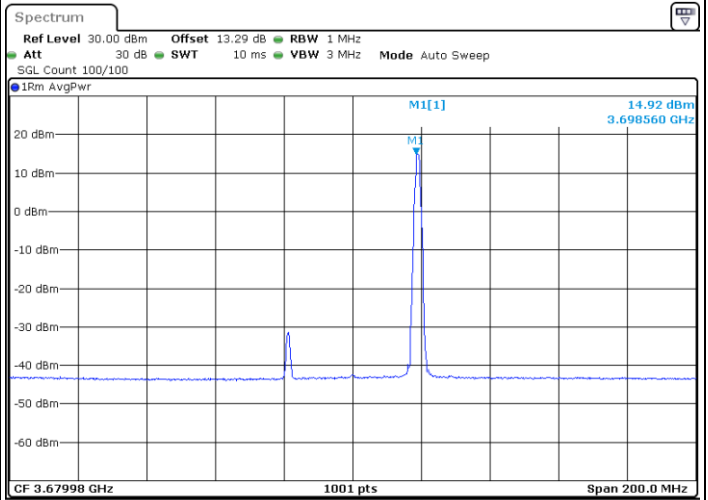
256QAM

Highest Channel / 1RB0



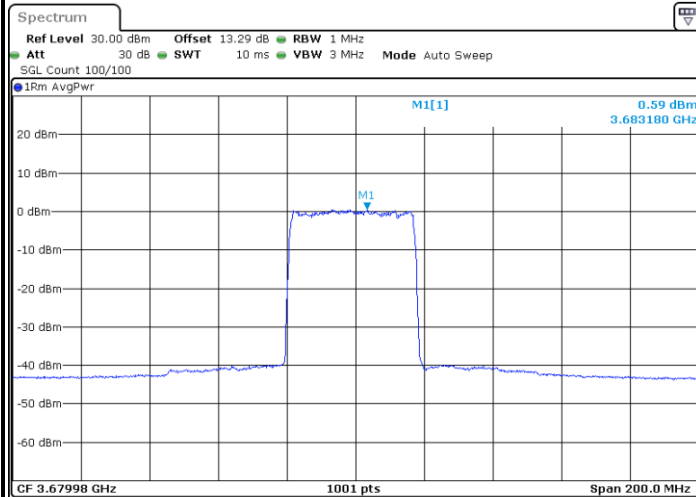
Date: 9.MAY.2023 00:00:44

Highest Channel / 1RBmax



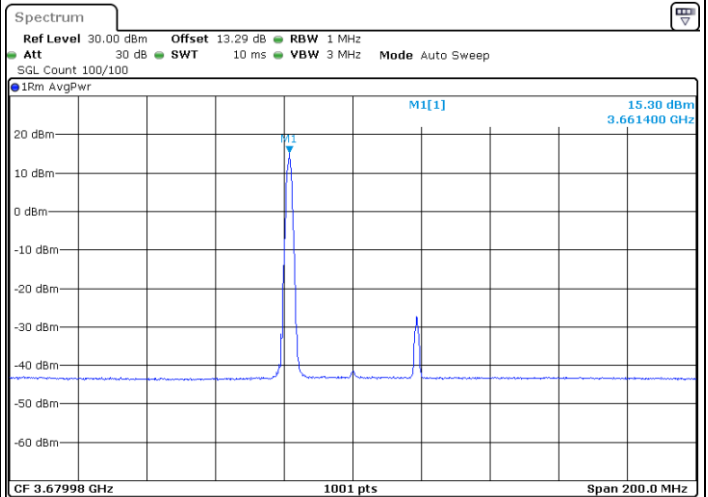
Date: 9.MAY.2023 00:05:43

Highest Channel / FullIRB



Date: 8.MAY.2023 23:59:47

Highest Channel / 1RB1



Date: 9.MAY.2023 00:06:33

5G NR n48 UL MIMO (Ant.0+3):

Mode	FR1 Part96 N48 : EIRP Power Density (dBm/1MHz)				
BW	10M (1RB1)				
Mod.		QPSK	16QAM	64QAM	256QAM
Lowest CH		25.99	26.35	24.44	21.97
Middle CH		26.69	26.42	24.85	22.02
Highest CH		26.08	26.54	24.60	21.97
BW	20M (1RB1)				
Mod.		QPSK	16QAM	64QAM	256QAM
Lowest CH		26.47	26.30	24.75	21.44
Middle CH		26.93	26.37	24.57	21.77
Highest CH		26.42	26.43	24.30	21.94
BW	40M (1RB1)				
Mod.		QPSK	16QAM	64QAM	256QAM
Lowest CH		26.42	26.10	24.21	21.67
Middle CH		26.43	25.50	24.96	21.15
Highest CH		26.48	25.78	24.04	21.90
Limit	37dBm /1MHz				
Gain	8.41				
Result	Pass				



BW	10M (1RB0)				
Mod.		QPSK	16QAM	64QAM	256QAM
Lowest CH		25.46	25.58	25.04	21.70
Middle CH		25.08	25.33	25.19	22.07
Highest CH		25.26	25.35	24.19	21.77
BW	20M (1RB0)				
Mod.		QPSK	16QAM	64QAM	256QAM
Lowest CH		24.89	25.31	24.54	21.98
Middle CH		25.26	25.22	24.51	21.79
Highest CH		25.36	25.55	24.38	22.04
BW	40M (1RB0)				
Mod.		QPSK	16QAM	64QAM	256QAM
Lowest CH		25.69	25.16	23.68	21.55
Middle CH		25.07	25.22	23.91	22.00
Highest CH		25.47	24.62	24.12	21.71
Limit	37dBm /1MHz				
Gain	8.41				
Result	Pass				
BW	10M (1RBMax)				
Mod.		QPSK	16QAM	64QAM	256QAM
Lowest CH		24.79	25.27	24.48	21.59
Middle CH		25.27	25.63	24.54	22.02
Highest CH		25.35	25.60	24.97	21.88
BW	20M (1RBMax)				
Mod.		QPSK	16QAM	64QAM	256QAM
Lowest CH		25.40	25.65	24.39	21.82
Middle CH		24.97	25.30	24.30	21.98
Highest CH		25.15	25.79	24.50	21.83
BW	40M (1RBMax)				
Mod.		QPSK	16QAM	64QAM	256QAM
Lowest CH		24.78	25.20	24.31	21.31
Middle CH		25.47	25.16	24.53	24.41
Highest CH		25.21	25.68	24.47	22.47
Limit	37dBm /1MHz				
Gain	8.41				
Result	Pass				

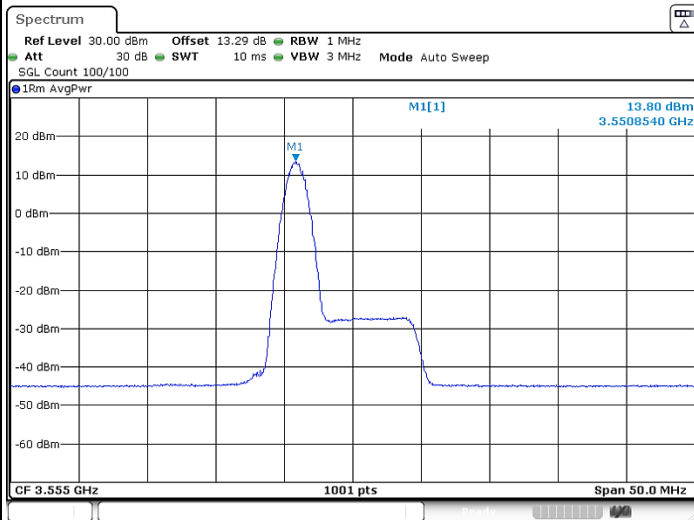
BW	10M (FULL)				
Mod.		QPSK	16QAM	64QAM	256QAM
Lowest CH		16.72	16.82	16.54	13.18
Middle CH		16.95	16.90	16.37	13.57
Highest CH		16.74	16.85	16.36	13.34
BW	20M (FULL)				
Mod.		QPSK	16QAM	64QAM	256QAM
Lowest CH		13.88	13.79	13.42	10.37
Middle CH		13.72	13.73	13.31	10.12
Highest CH		13.55	13.61	13.31	10.21
BW	40M (FULL)				
Mod.		QPSK	16QAM	64QAM	256QAM
Lowest CH		7.63	8.18	8.01	5.83
Middle CH		7.91	7.98	8.15	5.89
Highest CH		7.51	7.53	7.52	5.56
Limit	37dBm /1MHz				
Gain	8.41				
Result	Pass				

**Conducted PSD - ANT0**

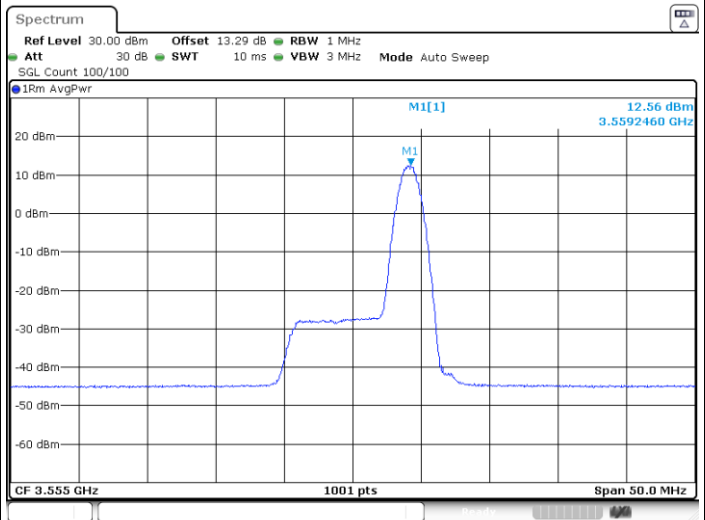
FR1 Part 96 n48 / 10MHz

QPSK

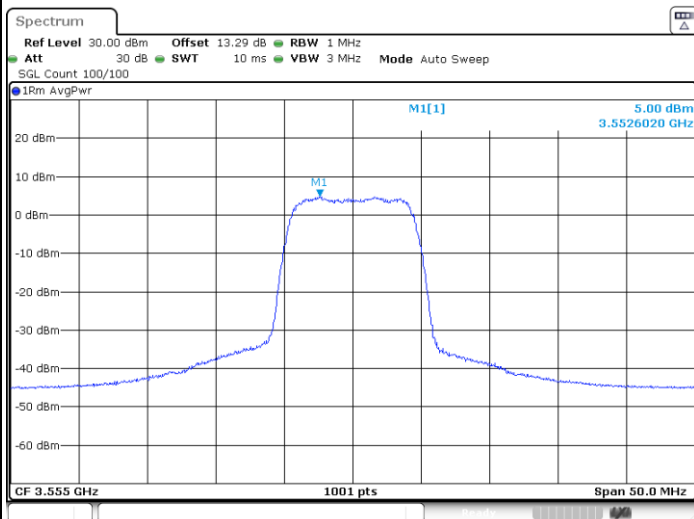
Lowest Channel / 1RB0



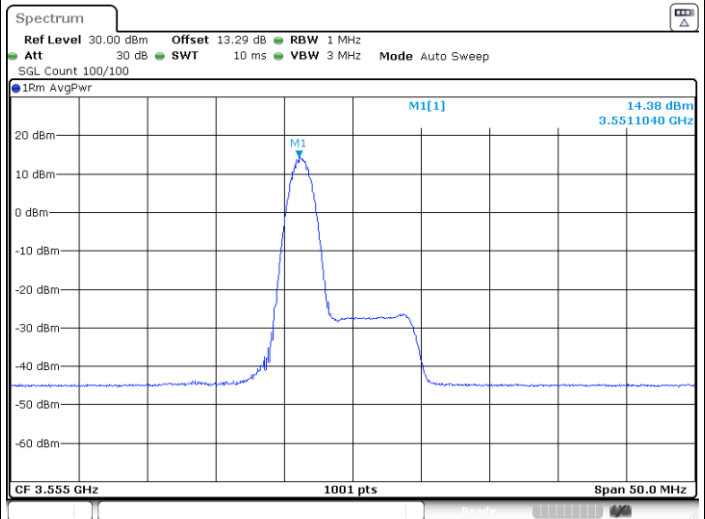
Lowest Channel / 1RBmax



Lowest Channel / FullRB



Lowest Channel / 1RB1

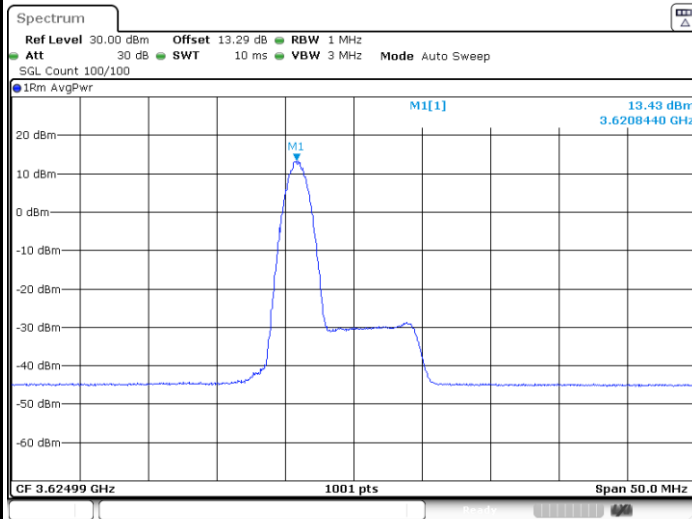




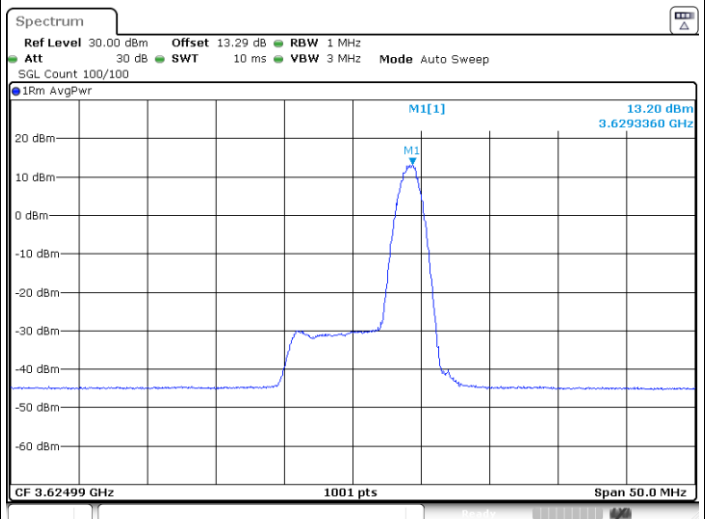
FR1 Part 96 n48 / 10MHz

QPSK

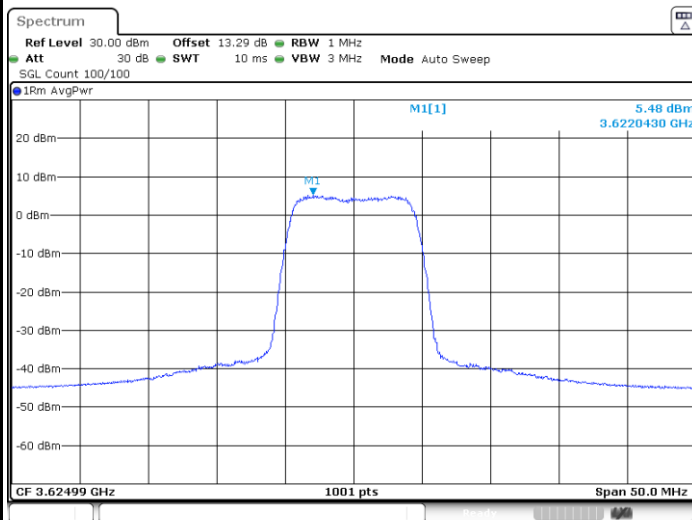
Middle Channel / 1RB0



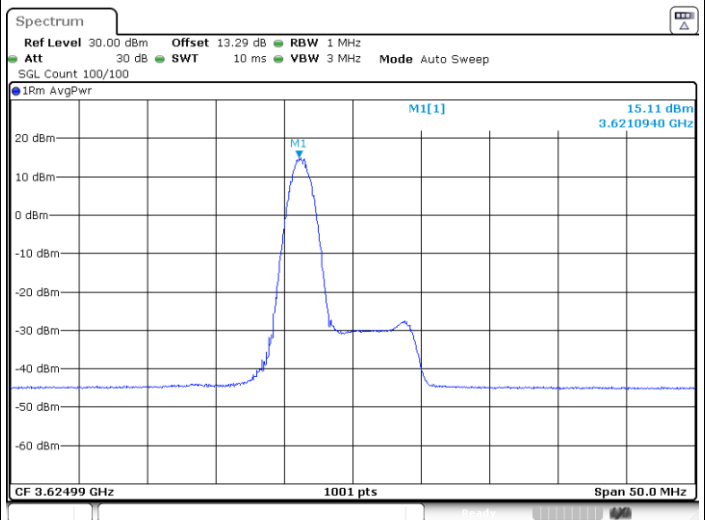
Middle Channel / 1RBmax



Middle Channel / FullRB



Middle Channel / 1RB1

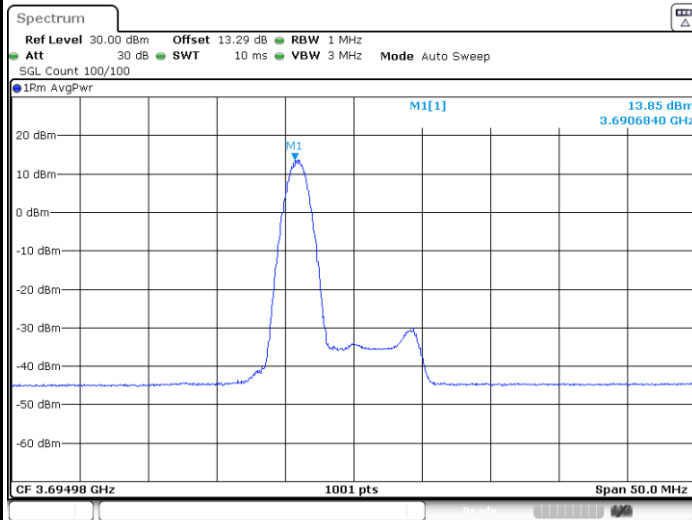




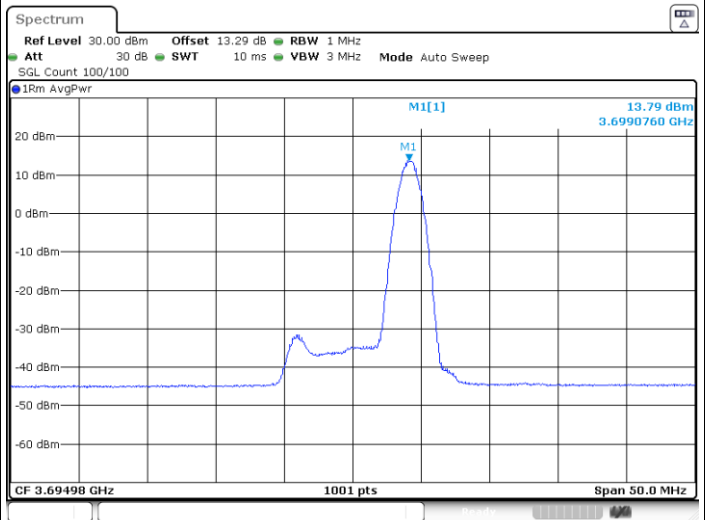
FR1 Part 96 n48 / 10MHz

QPSK

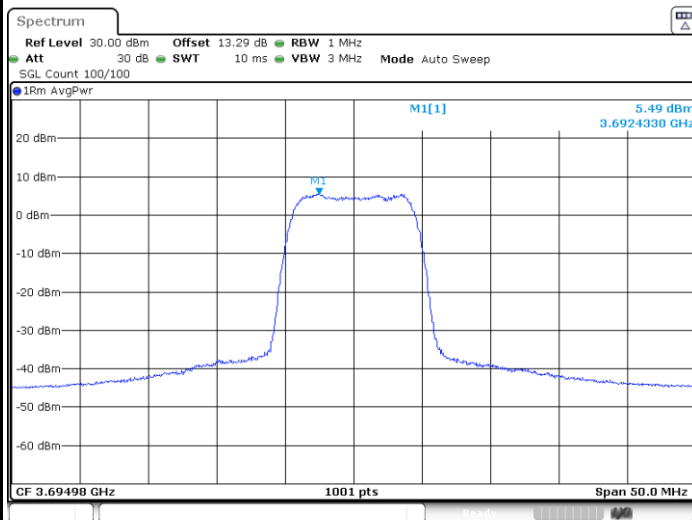
Highest Channel / 1RB0



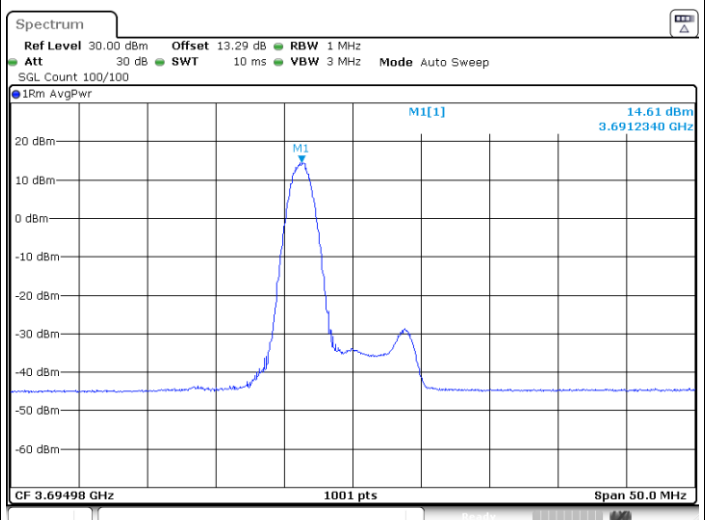
Highest Channel / 1RBmax



Highest Channel / FullIRB



Highest Channel / 1RB1

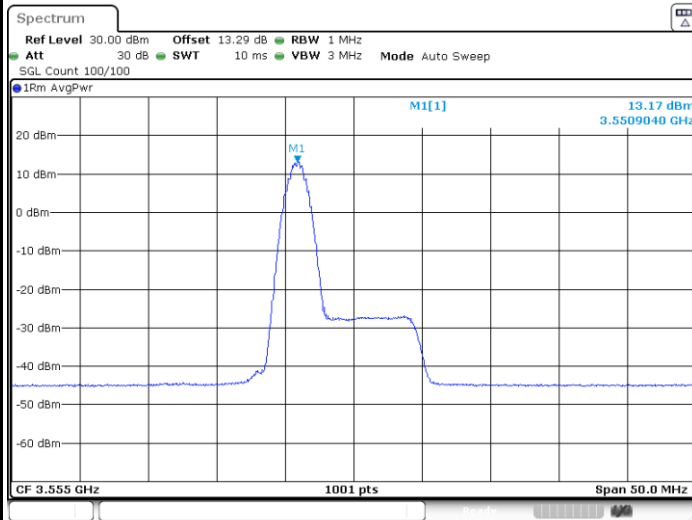




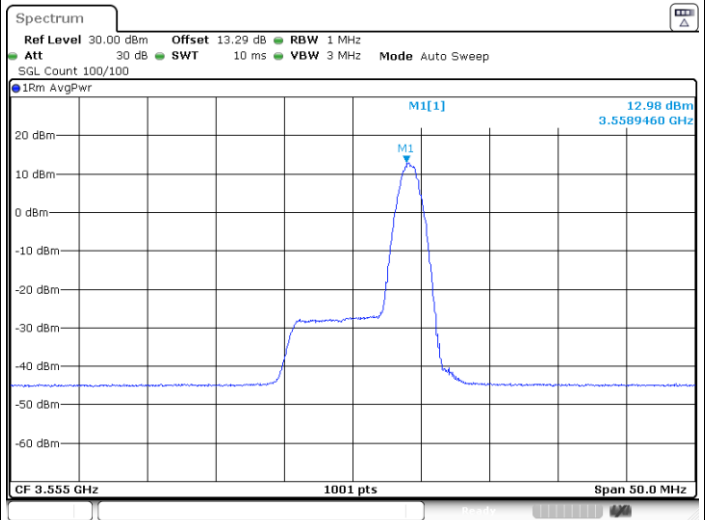
FR1 Part 96 n48 / 10MHz

16QAM

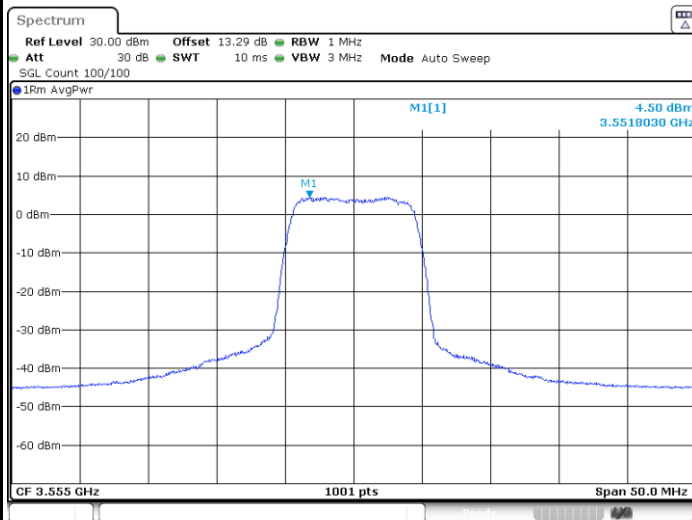
Lowest Channel / 1RB0



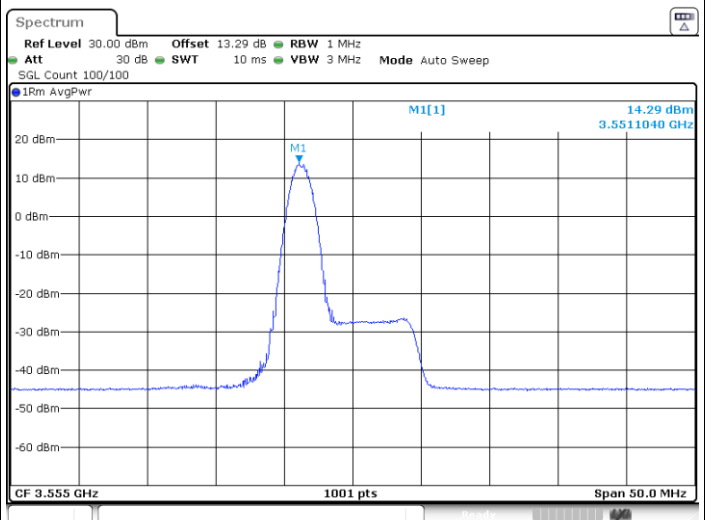
Lowest Channel / 1RBmax



Lowest Channel / FullRB



Lowest Channel / 1RB1

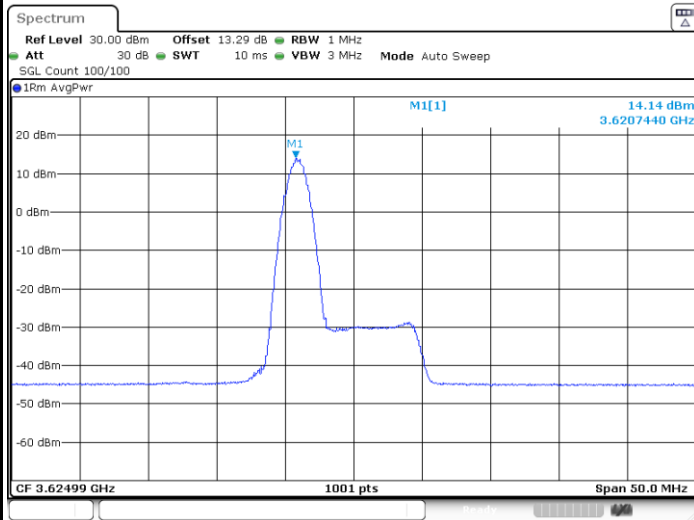




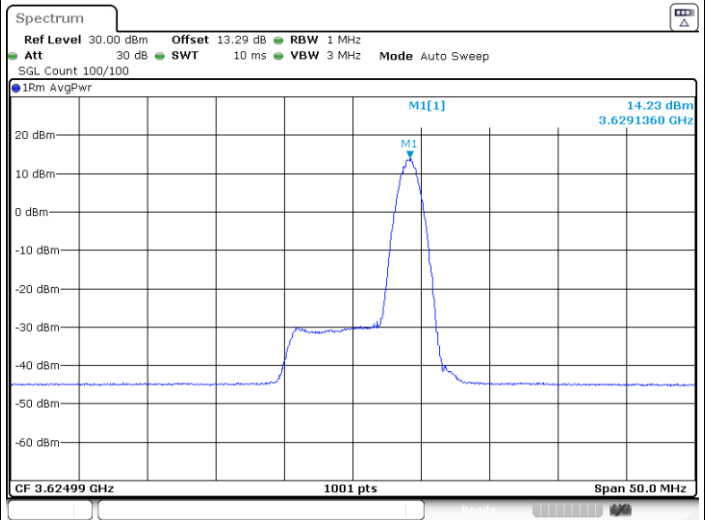
FR1 Part 96 n48 / 10MHz

16QAM

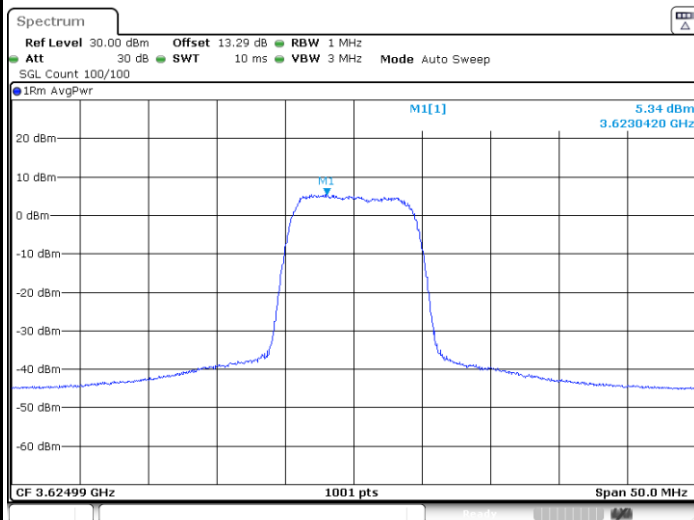
Middle Channel / 1RB0



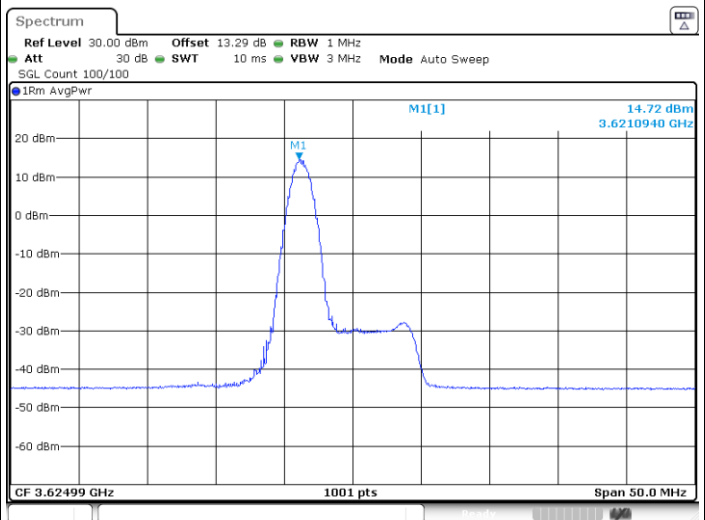
Middle Channel / 1RBmax



Middle Channel / FullRB



Middle Channel / 1RB1

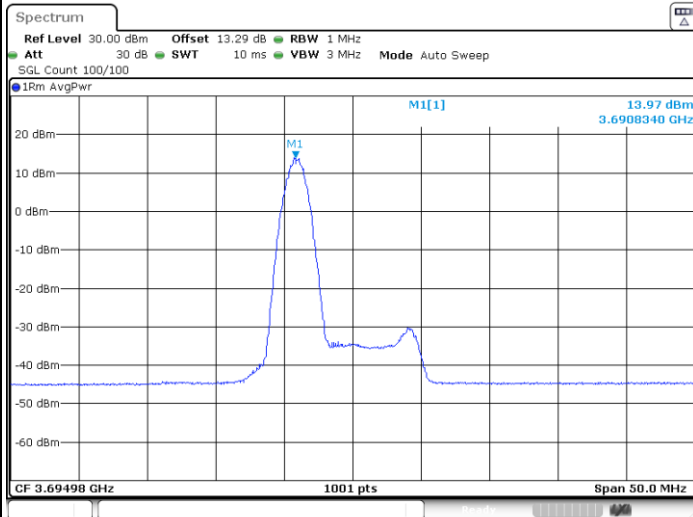




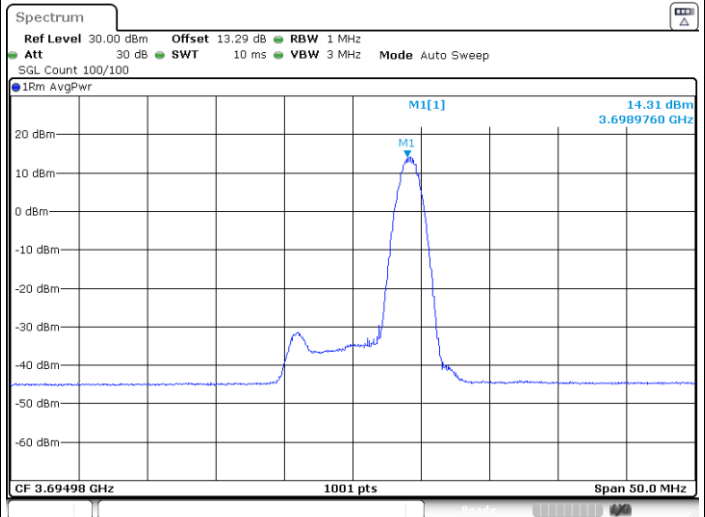
FR1 Part 96 n48 / 10MHz

16QAM

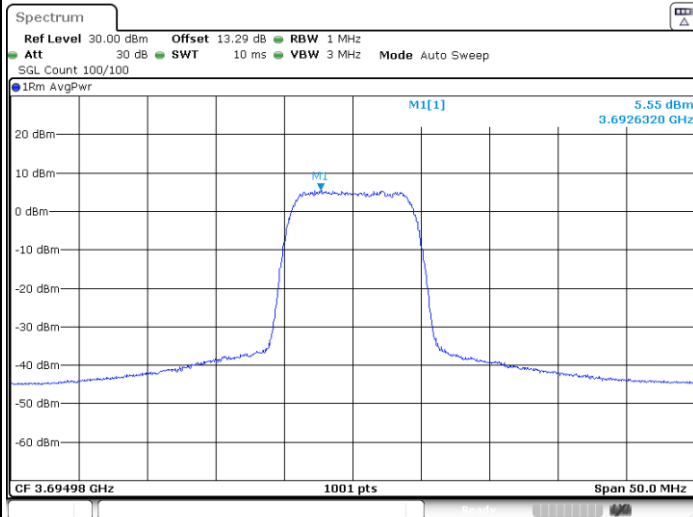
Highest Channel / 1RB0



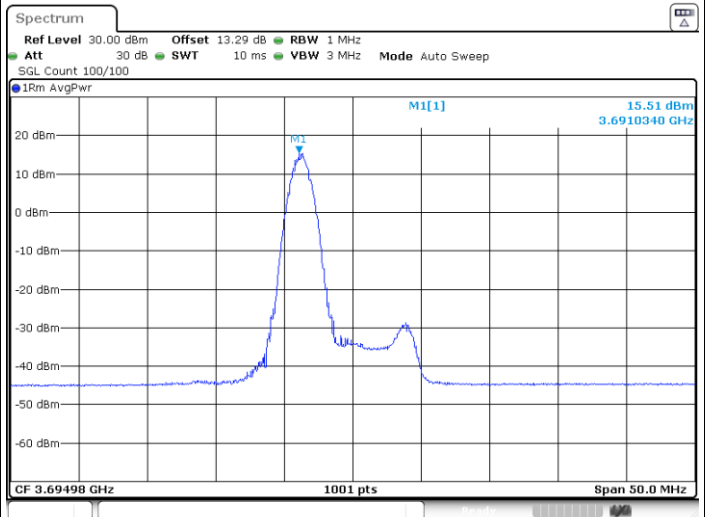
Highest Channel / 1RBmax



Highest Channel / FullRB



Highest Channel / 1RB1

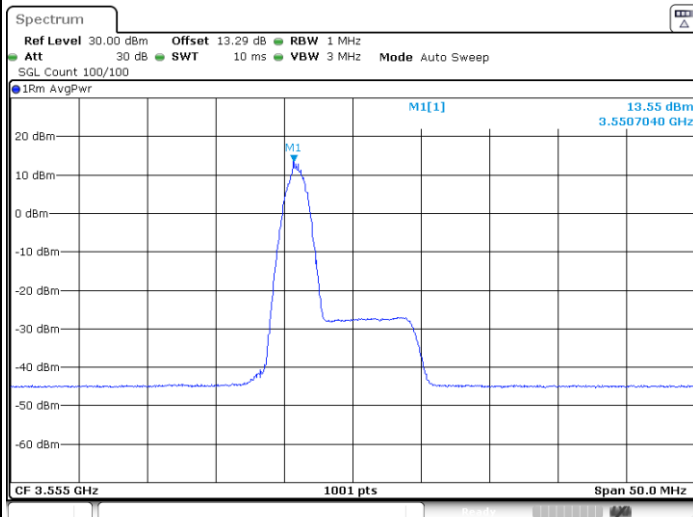




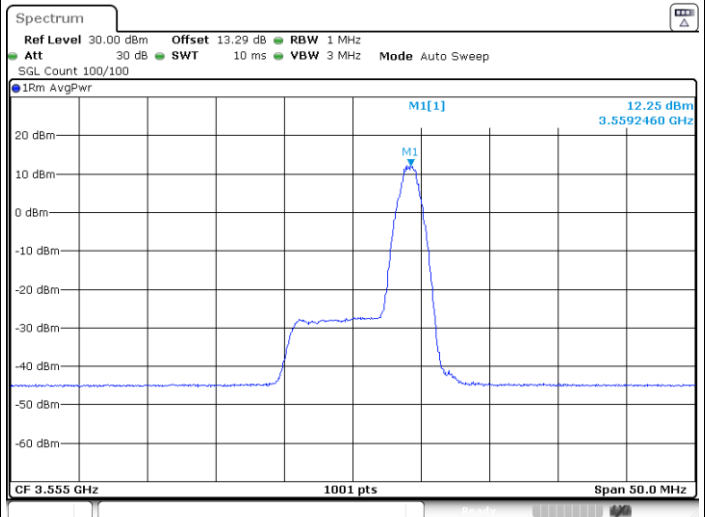
FR1 Part 96 n48 / 10MHz

64QAM

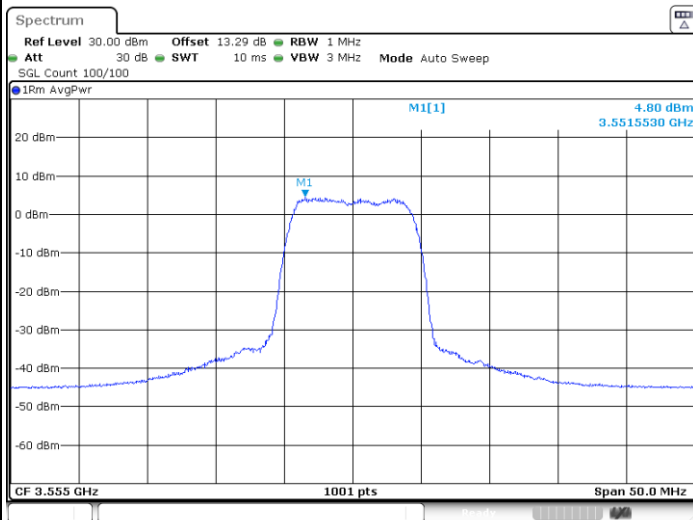
Lowest Channel / 1RB0



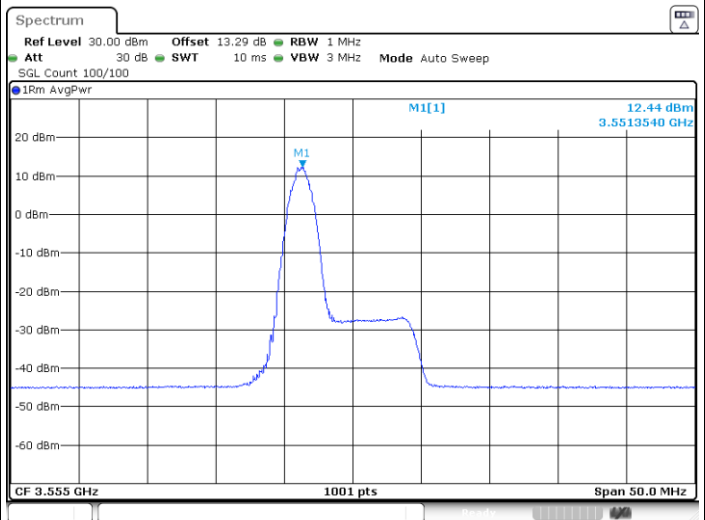
Lowest Channel / 1RBmax



Lowest Channel / FullRB



Lowest Channel / 1RB1

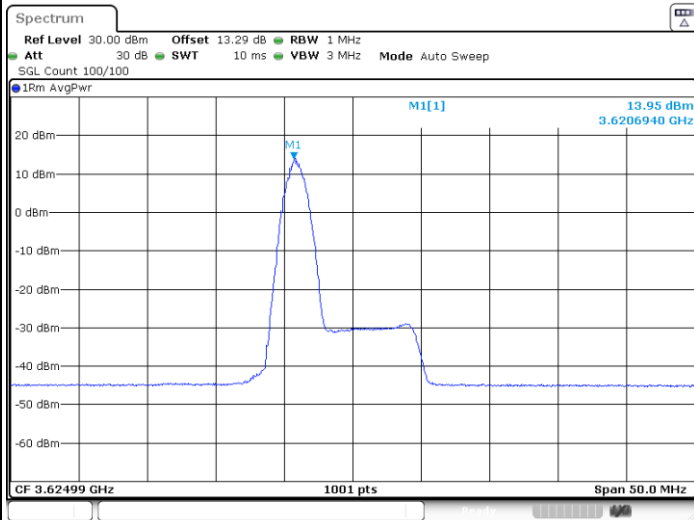




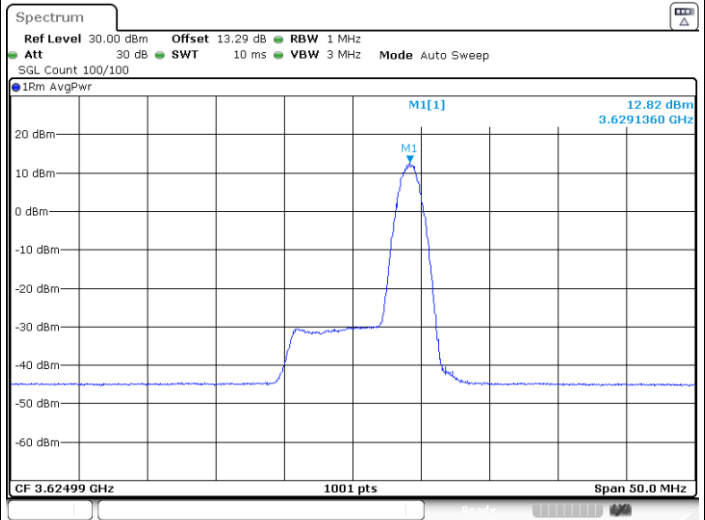
FR1 Part 96 n48 / 10MHz

64QAM

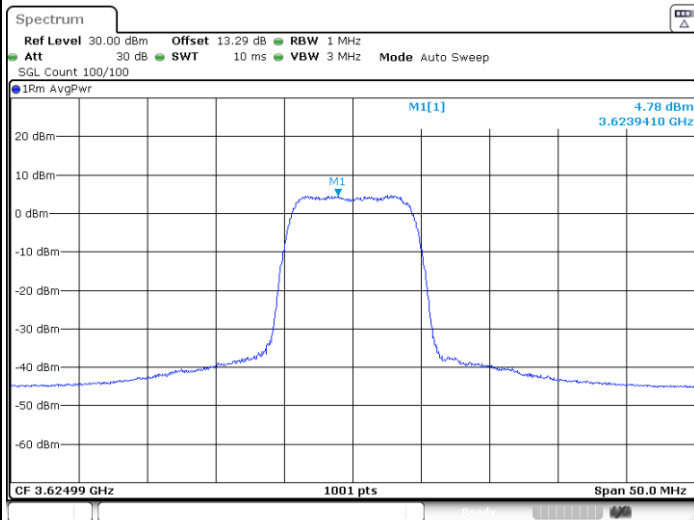
Middle Channel / 1RB0



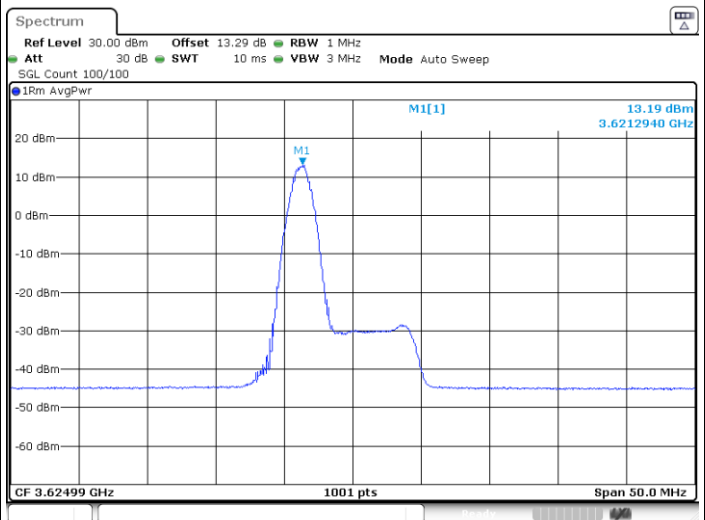
Middle Channel / 1RBmax



Middle Channel / FullRB



Middle Channel / 1RB1

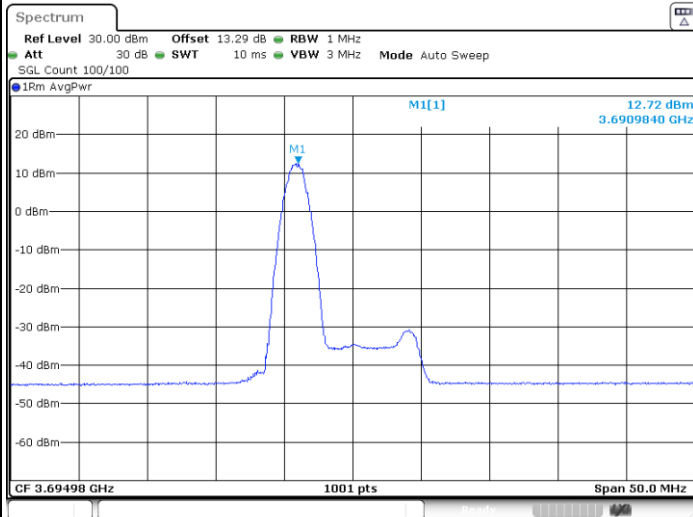




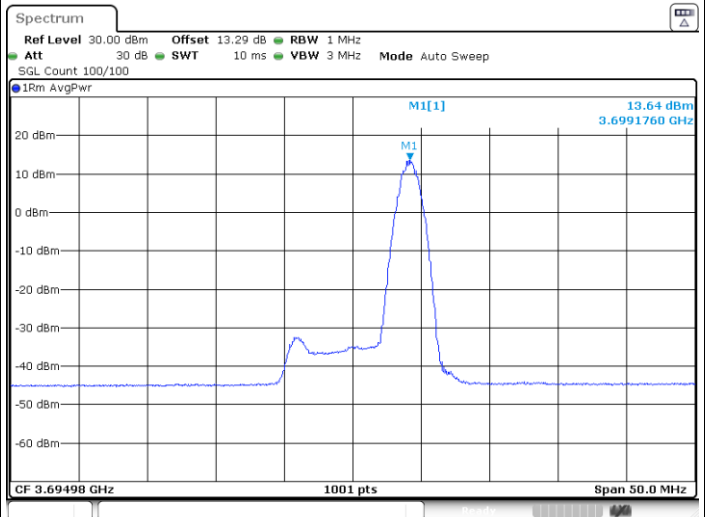
FR1 Part 96 n48 / 10MHz

64QAM

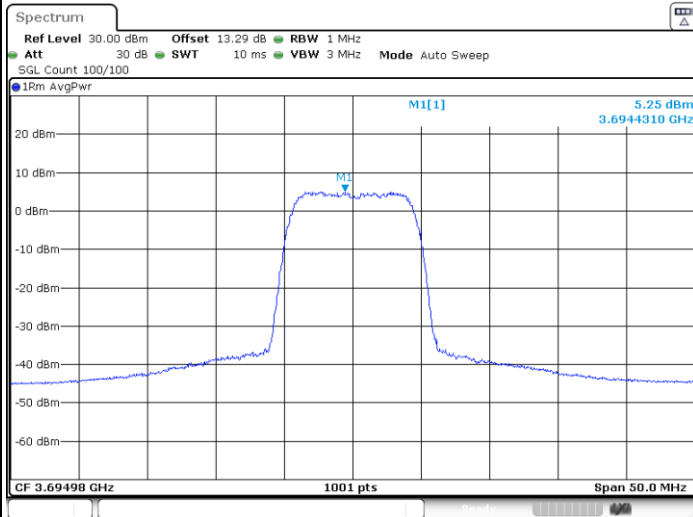
Highest Channel / 1RB0



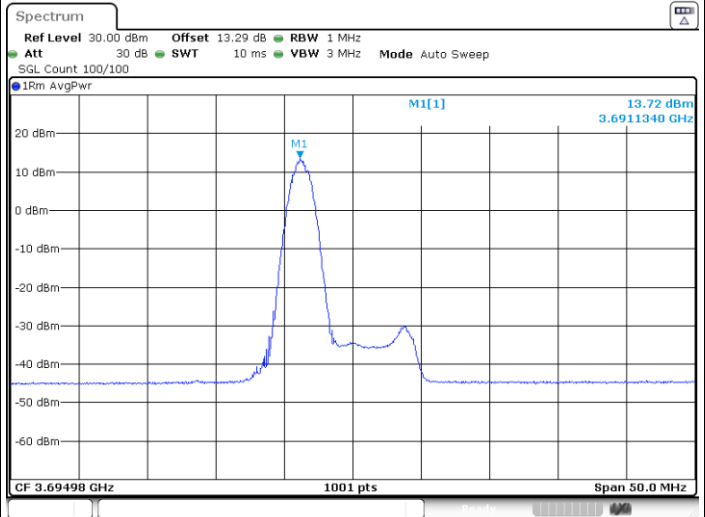
Highest Channel / 1RBmax



Highest Channel / FullRB



Highest Channel / 1RB1

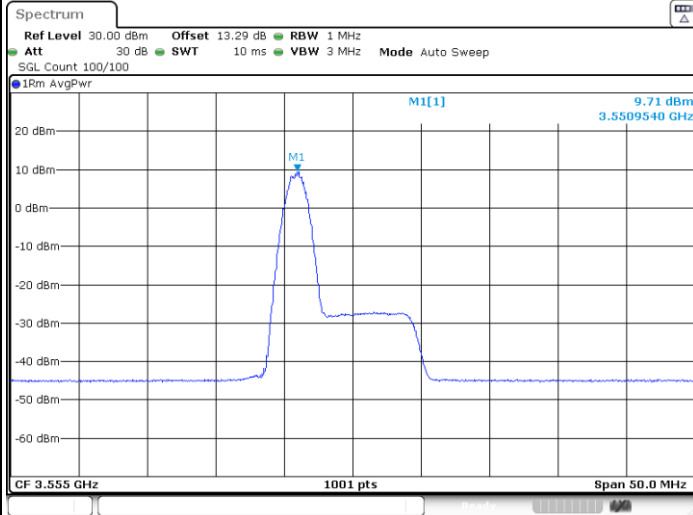




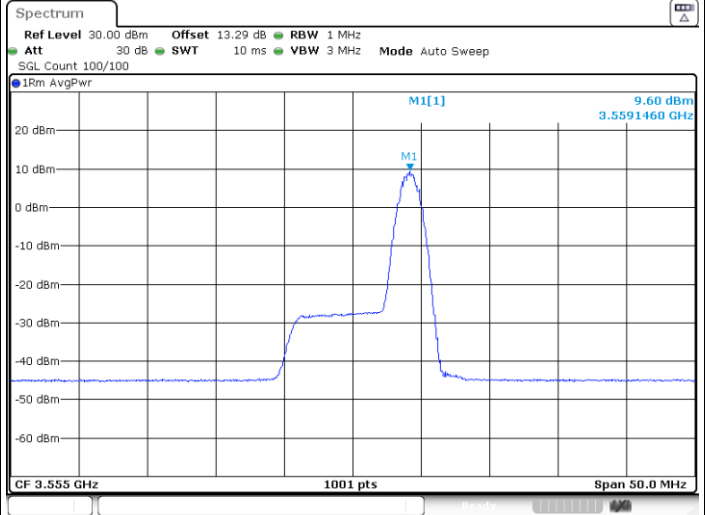
FR1 Part 96 n48 / 10MHz

256QAM

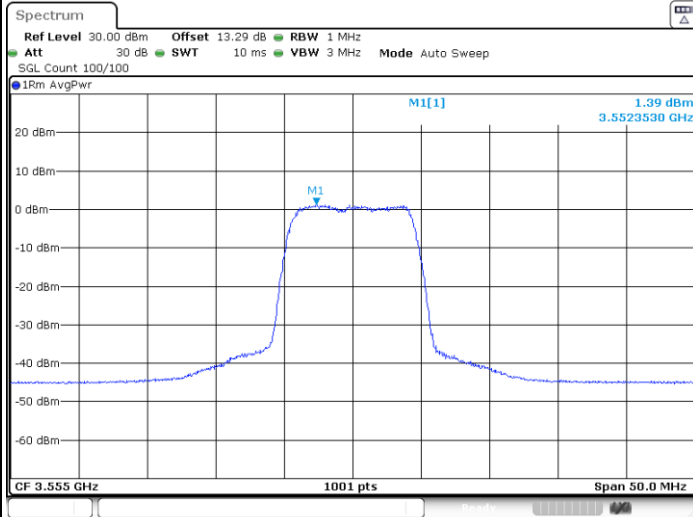
Lowest Channel / 1RB0



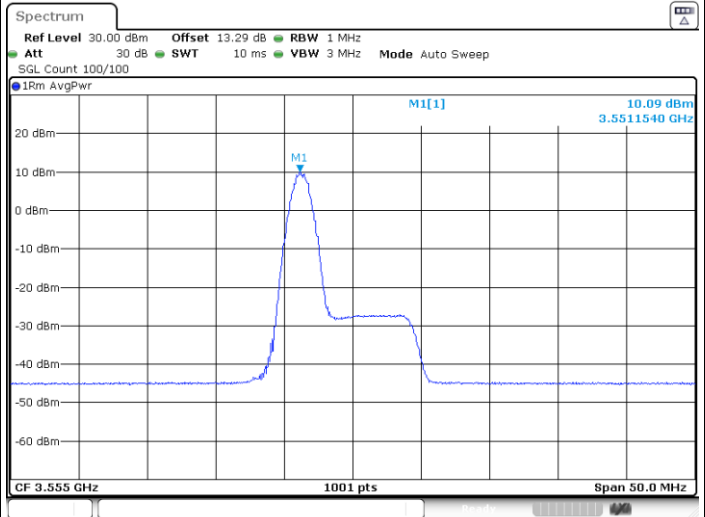
Lowest Channel / 1RBmax



Lowest Channel / FullRB



Lowest Channel / 1RB1

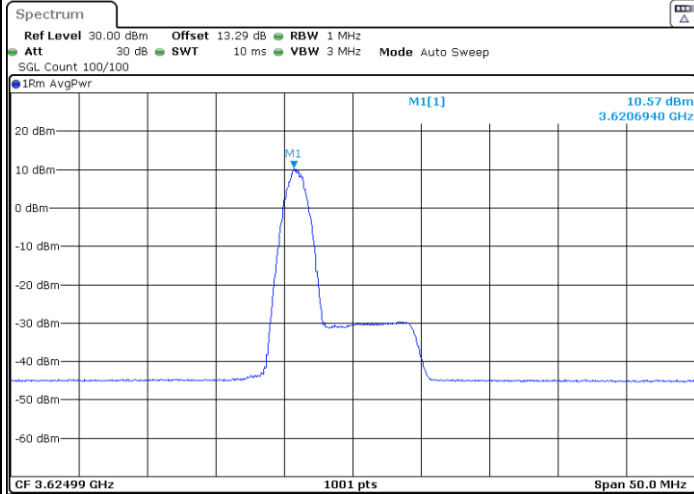




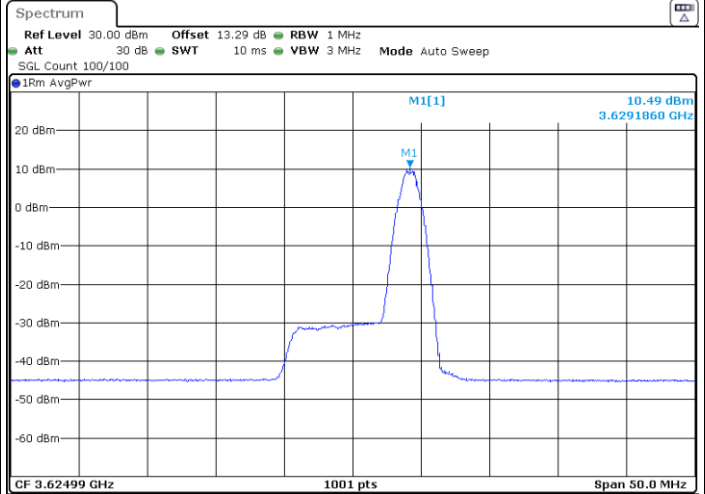
FR1 Part 96 n48 / 10MHz

256QAM

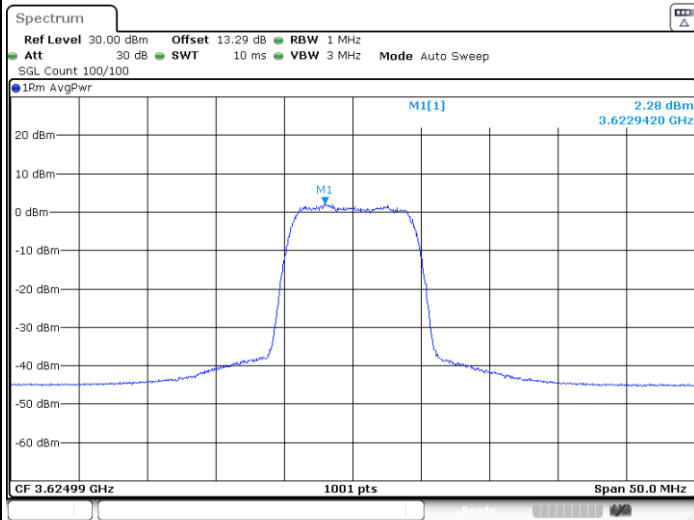
Middle Channel / 1RB0



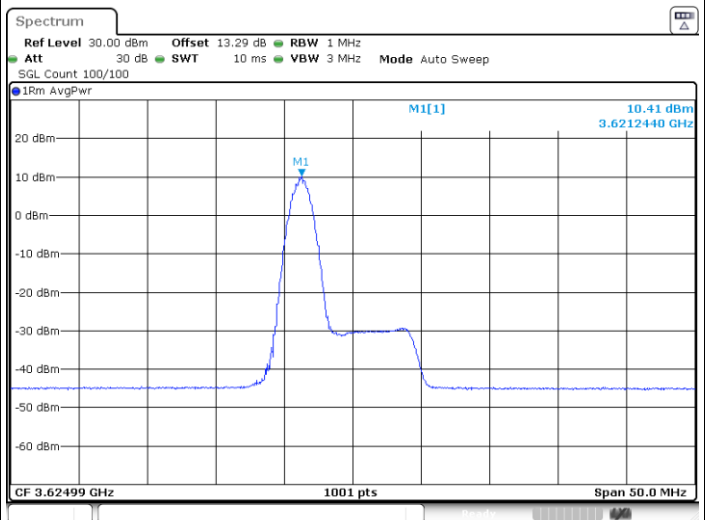
Middle Channel / 1RBmax



Middle Channel / FullRB



Middle Channel / 1RB1

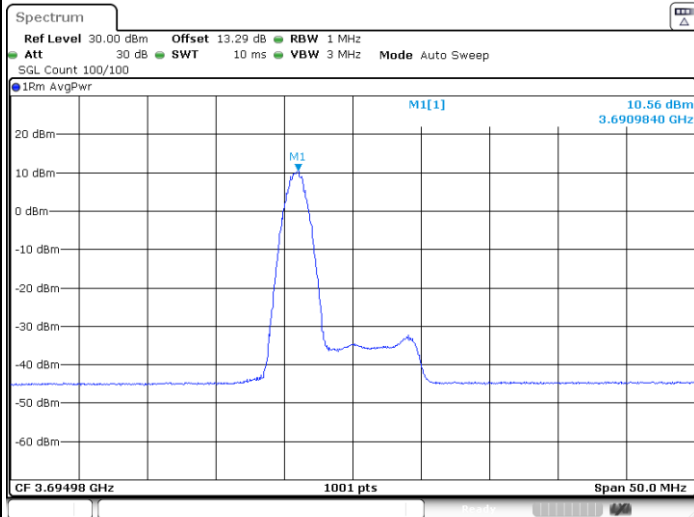




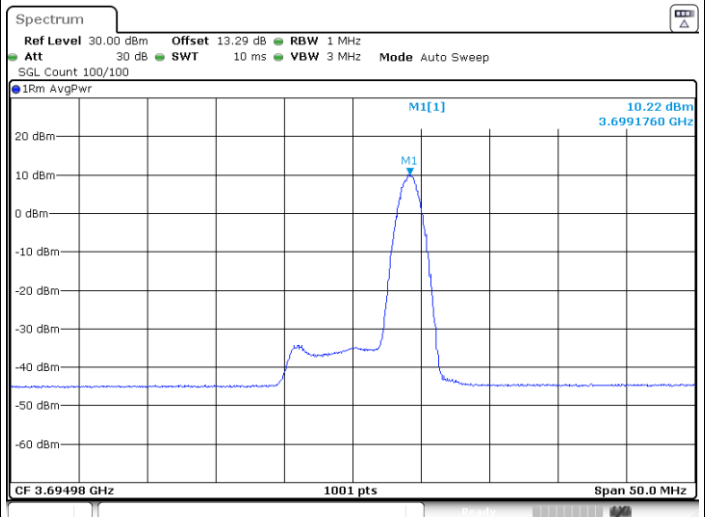
FR1 Part 96 n48 / 10MHz

256QAM

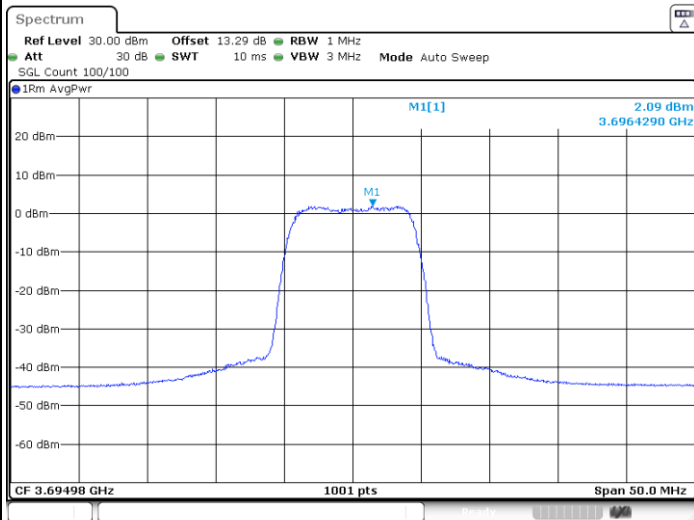
Highest Channel / 1RB0



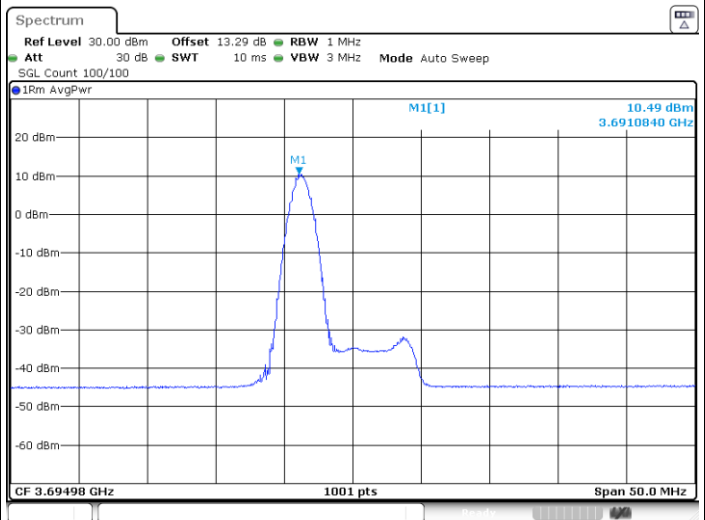
Highest Channel / 1RBmax



Highest Channel / FullRB



Highest Channel / 1RB1

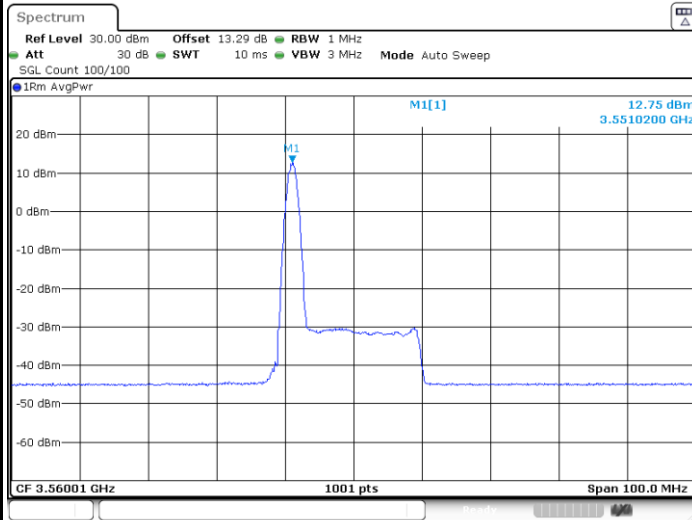




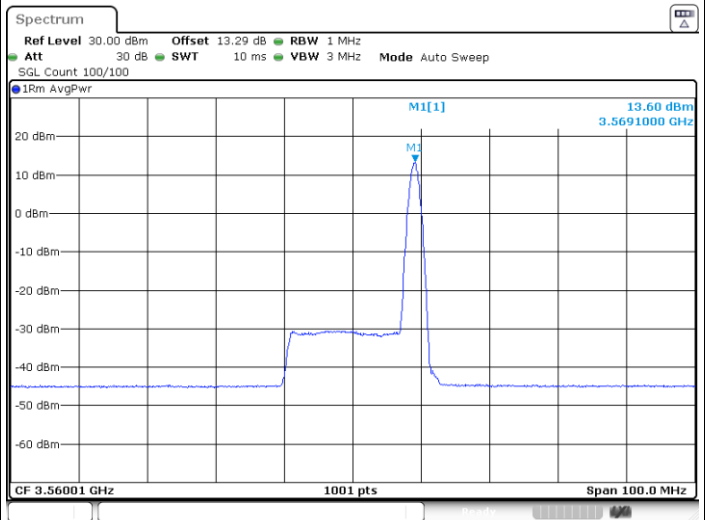
FR1 Part 96 n48 / 20MHz

QPSK

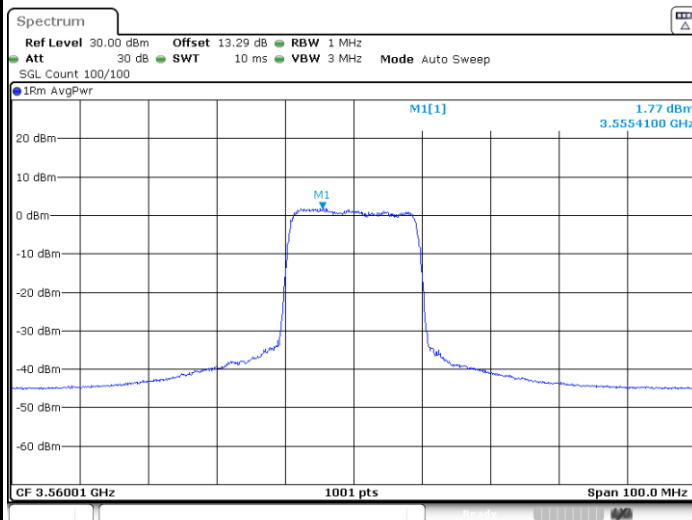
Lowest Channel / 1RB0



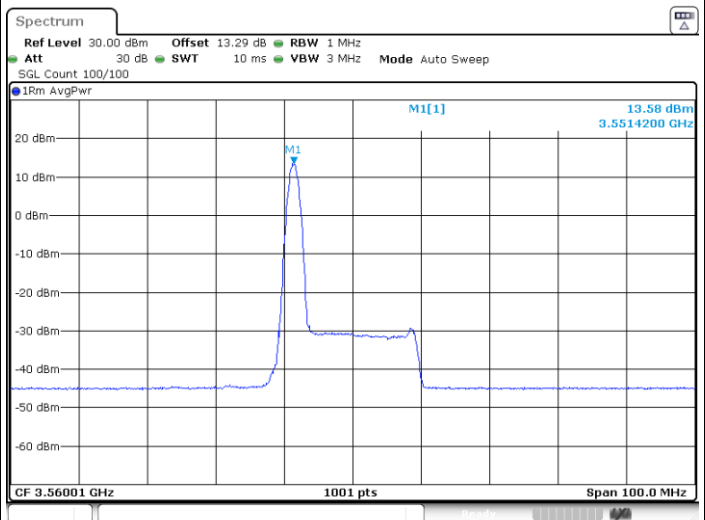
Lowest Channel / 1RBmax



Lowest Channel / FullIRB



Lowest Channel / 1RB1

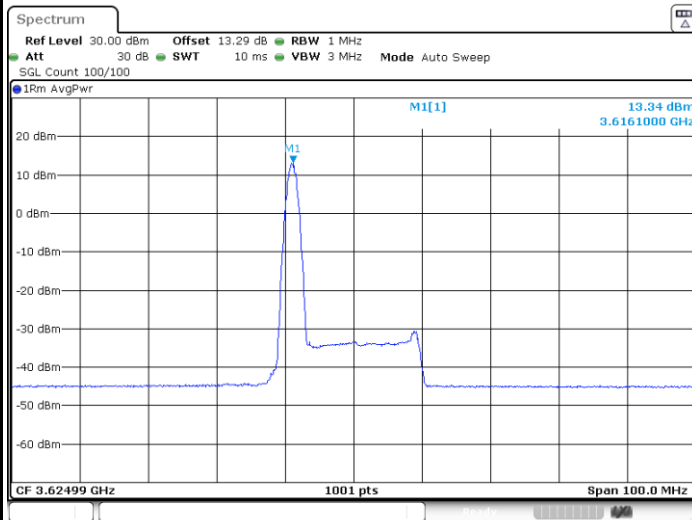




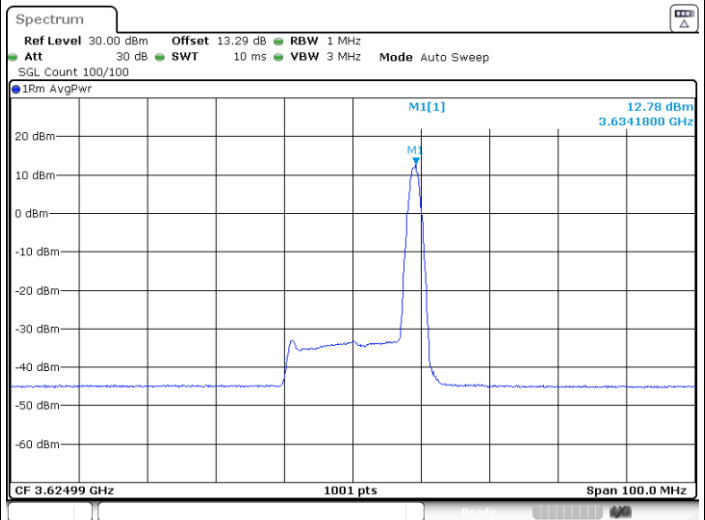
FR1 Part 96 n48 / 20MHz

QPSK

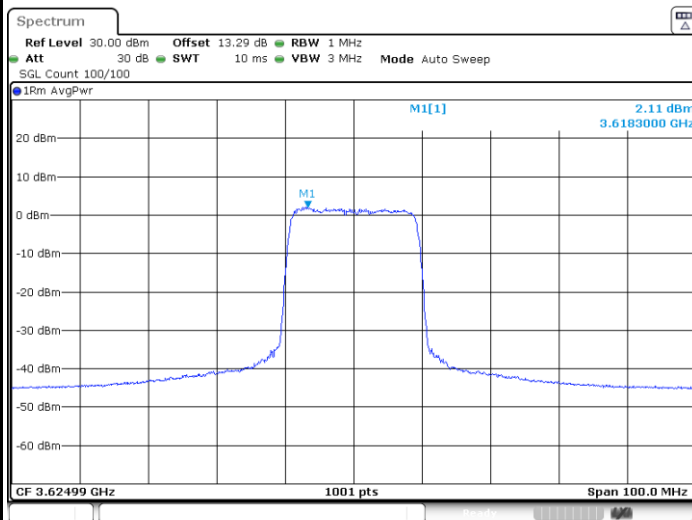
Middle Channel / 1RB0



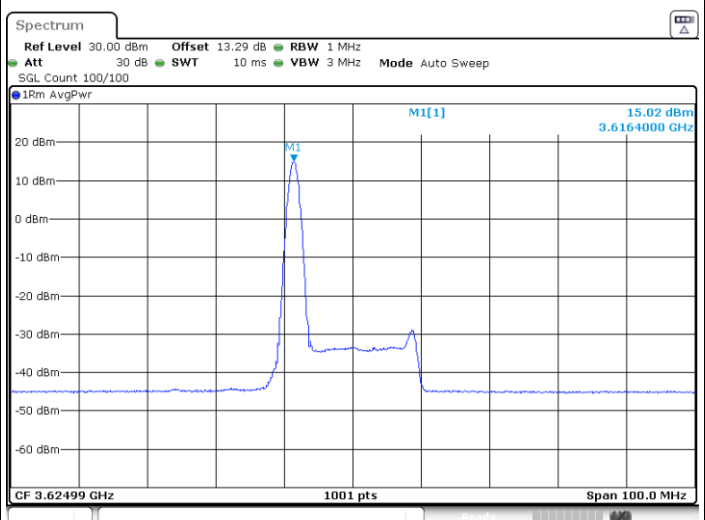
Middle Channel / 1RBmax



Middle Channel / FullRB



Middle Channel / 1RB1

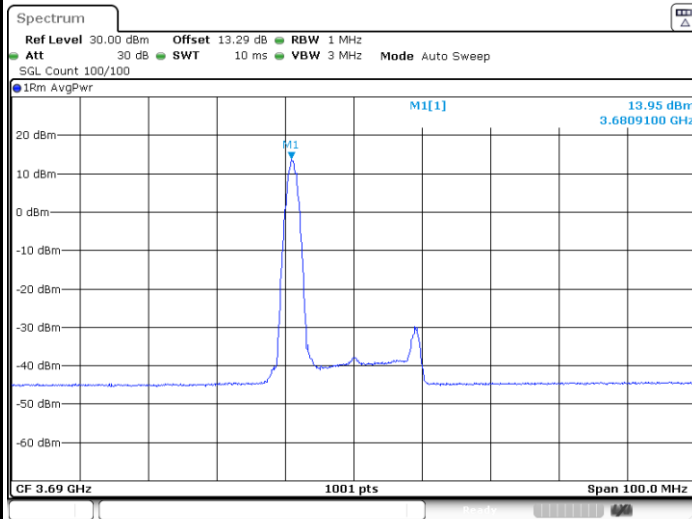




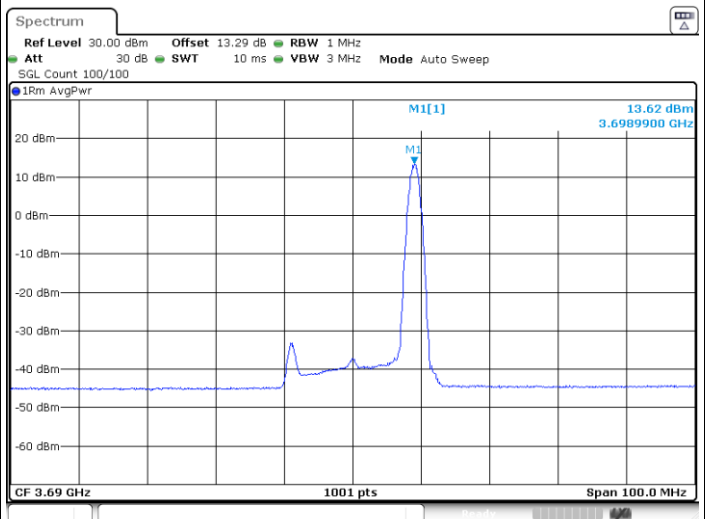
FR1 Part 96 n48 / 20MHz

QPSK

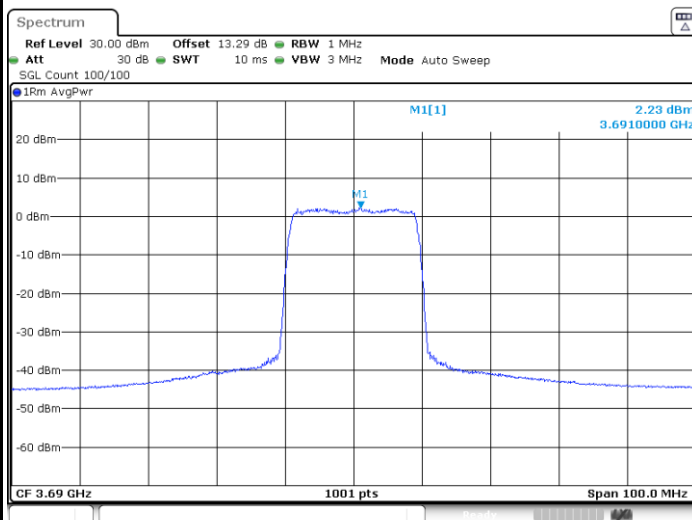
Highest Channel / 1RB0



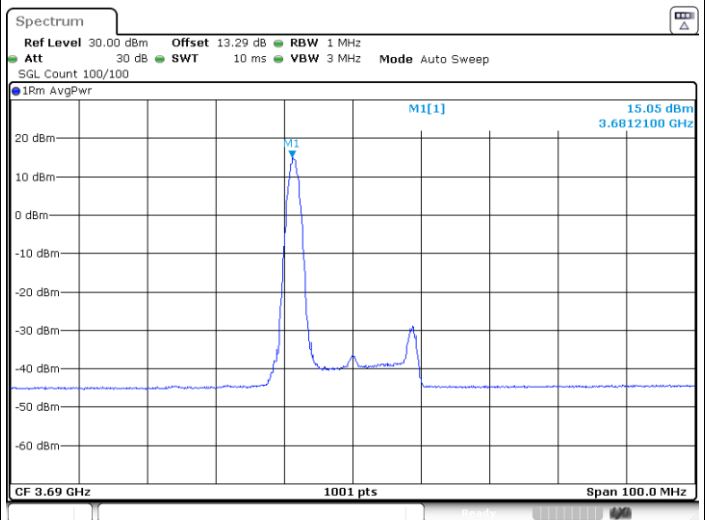
Highest Channel / 1RBmax



Highest Channel / FullRB



Highest Channel / 1RB1

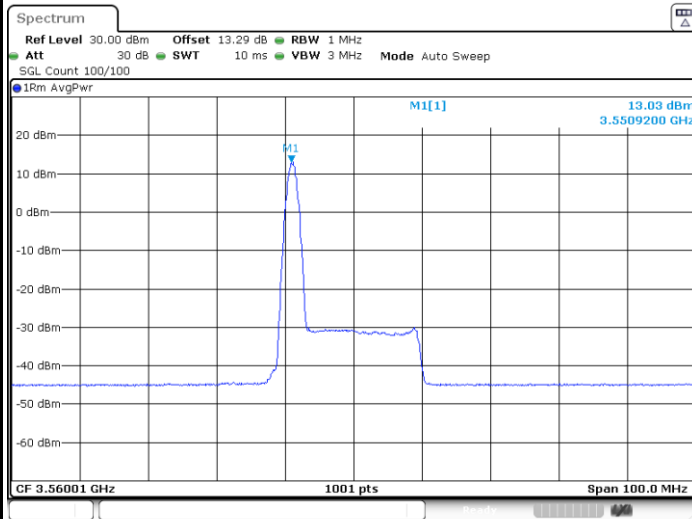




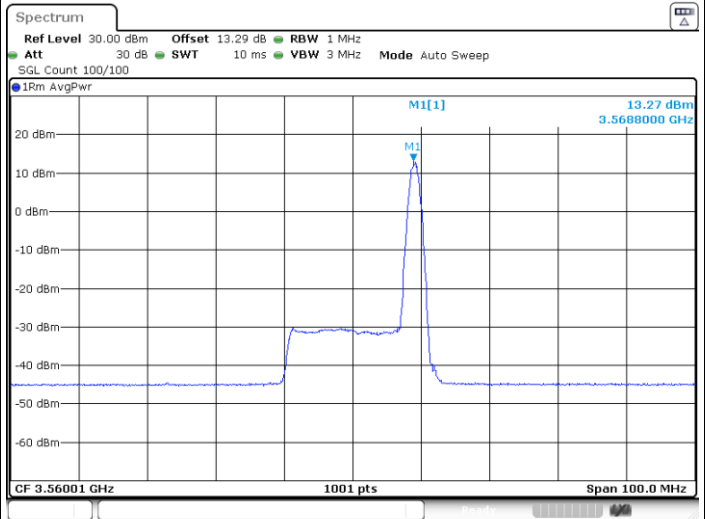
FR1 Part 96 n48 / 20MHz

16QAM

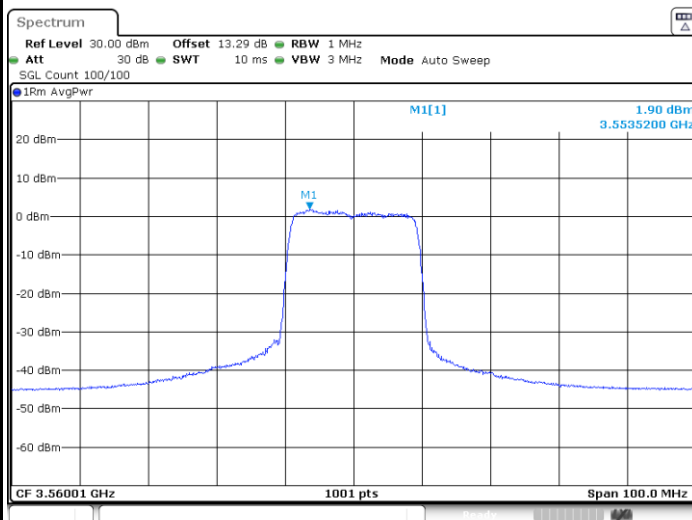
Lowest Channel / 1RB0



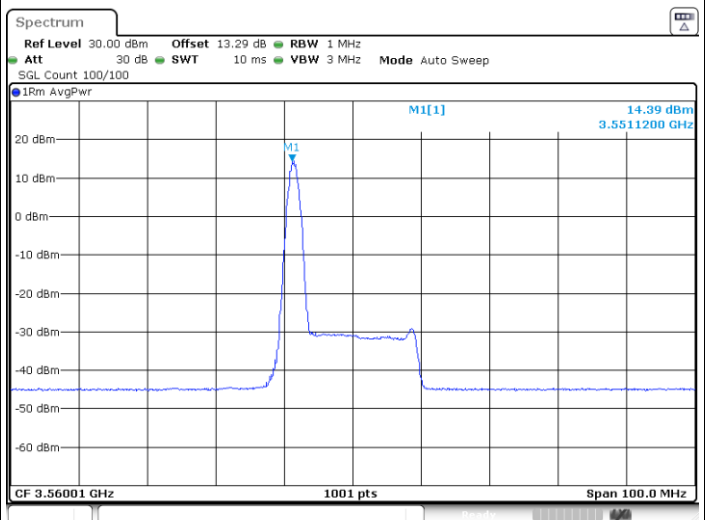
Lowest Channel / 1RBmax



Lowest Channel / FullRB



Lowest Channel / 1RB1

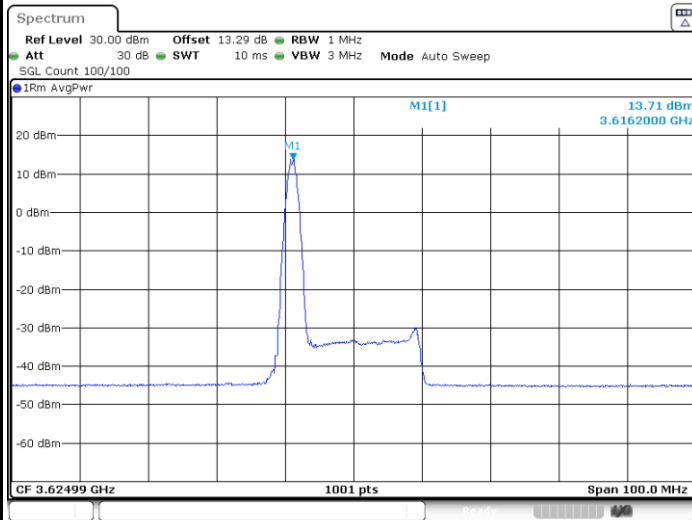




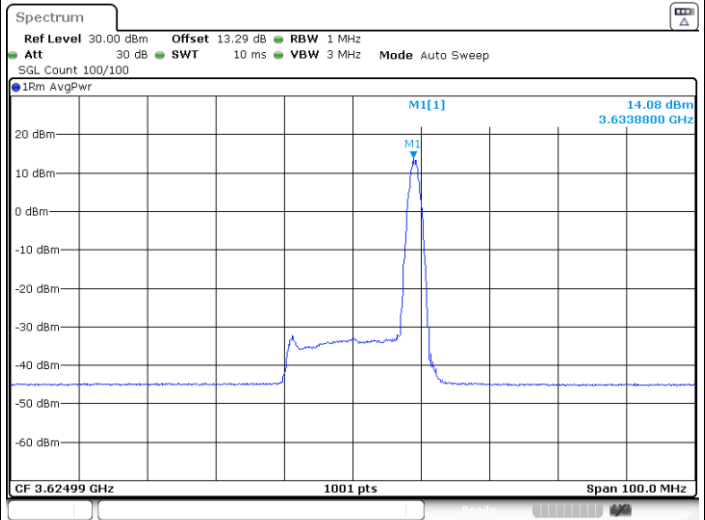
FR1 Part 96 n48 / 20MHz

16QAM

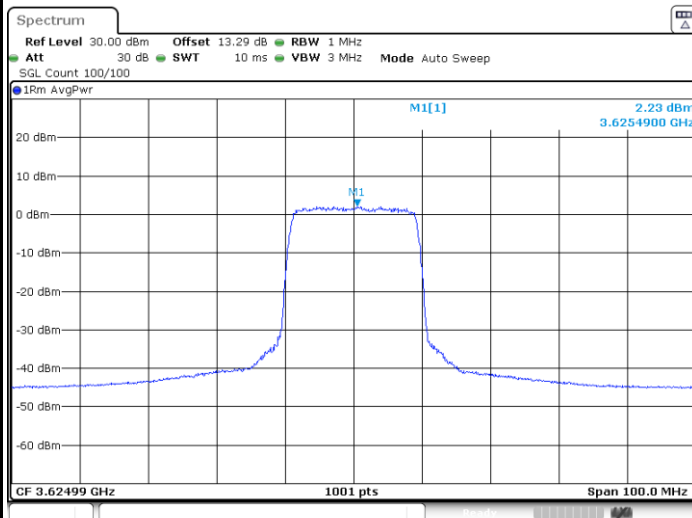
Middle Channel / 1RB0



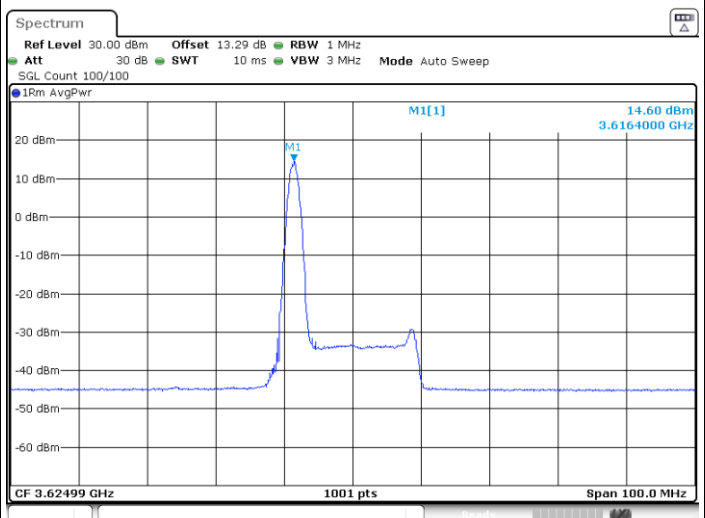
Middle Channel / 1RBmax



Middle Channel / FullRB



Middle Channel / 1RB1

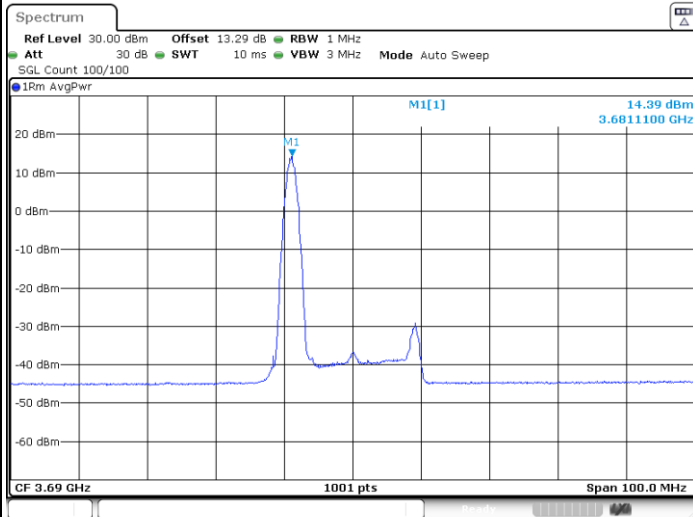




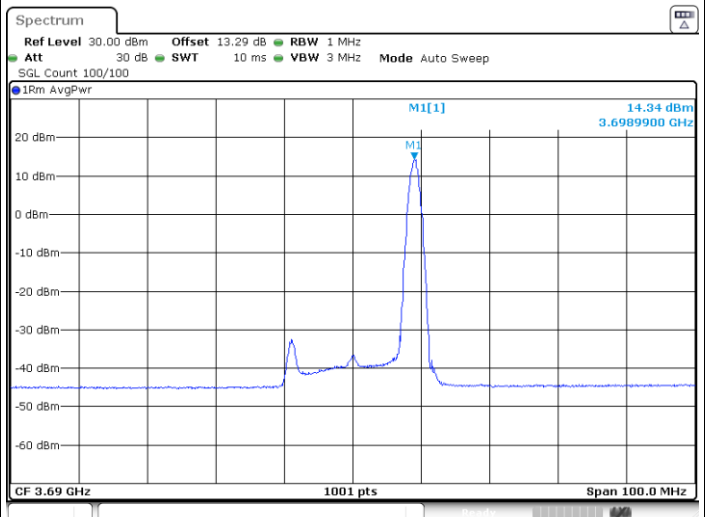
FR1 Part 96 n48 / 20MHz

16QAM

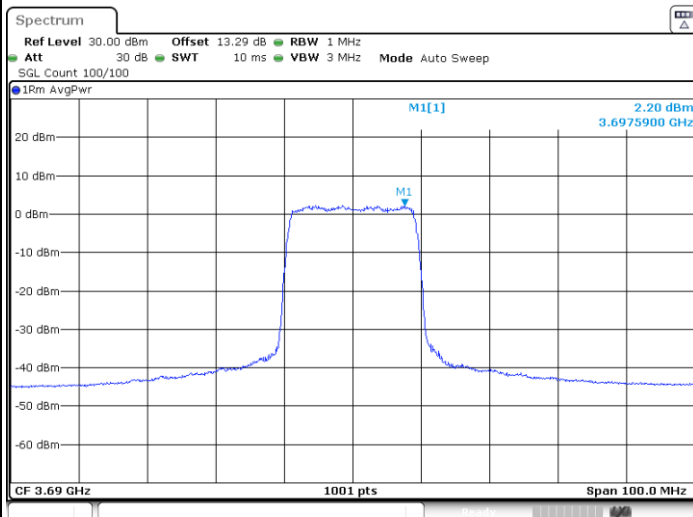
Highest Channel / 1RB0



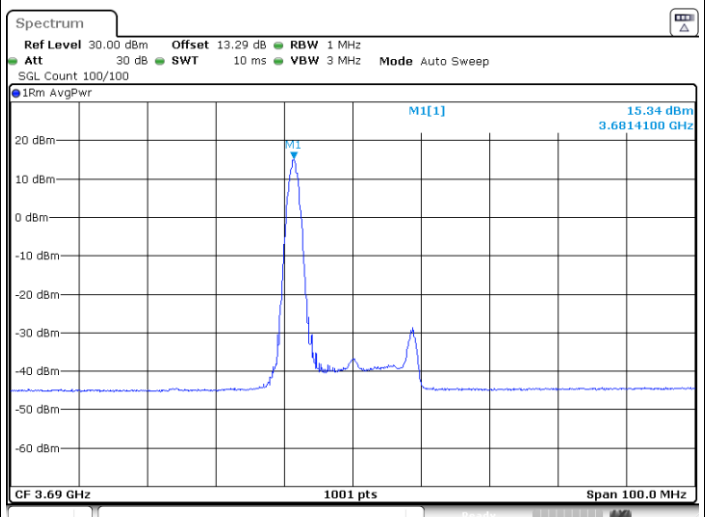
Highest Channel / 1RBmax



Highest Channel / FullRB



Highest Channel / 1RB1



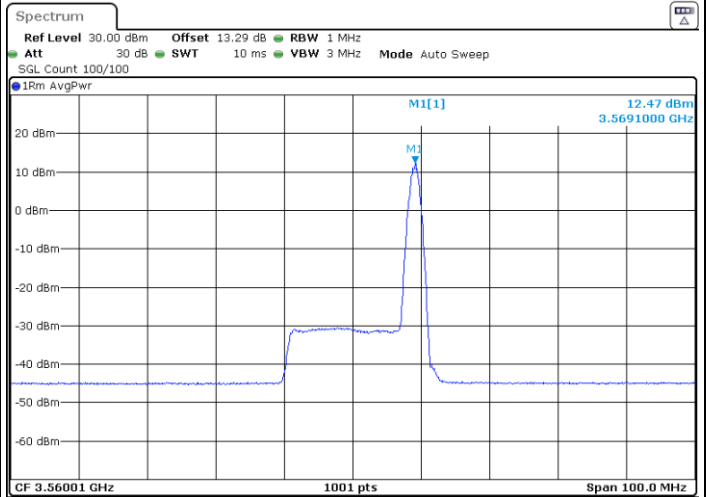
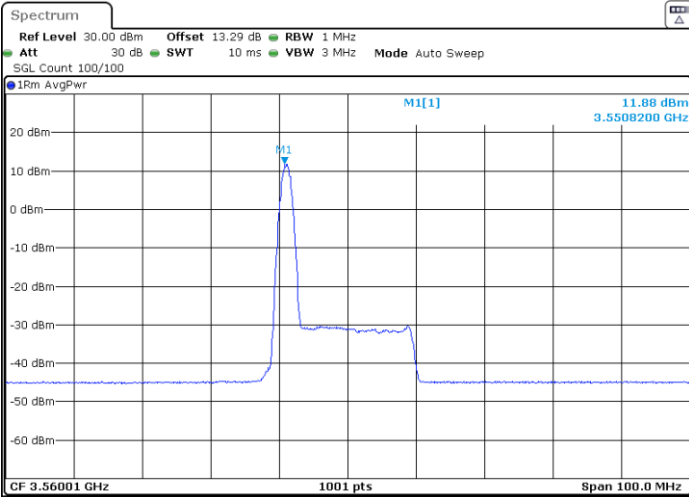


FR1 Part 96 n48 / 20MHz

64QAM

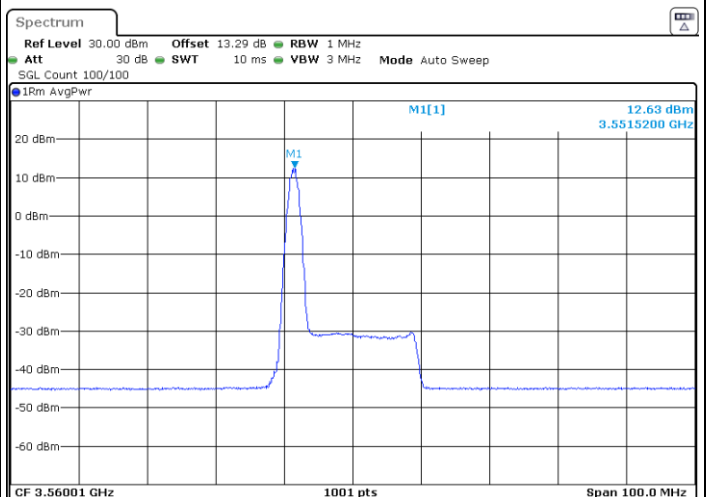
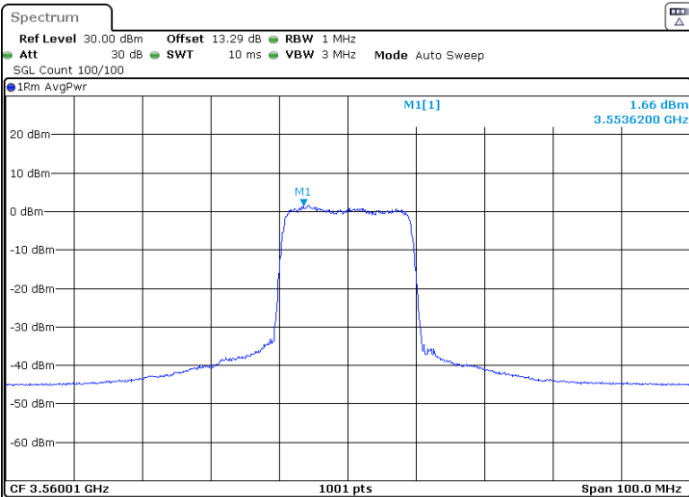
Lowest Channel / 1RB0

Lowest Channel / 1RBmax



Lowest Channel / FullRB

Lowest Channel / 1RB1

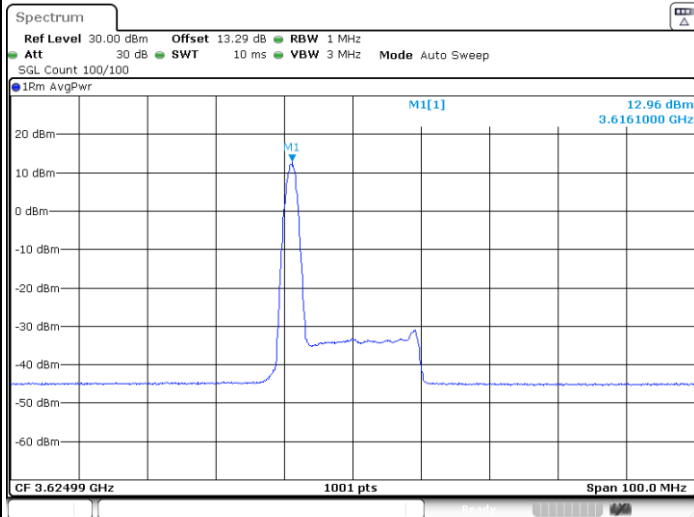




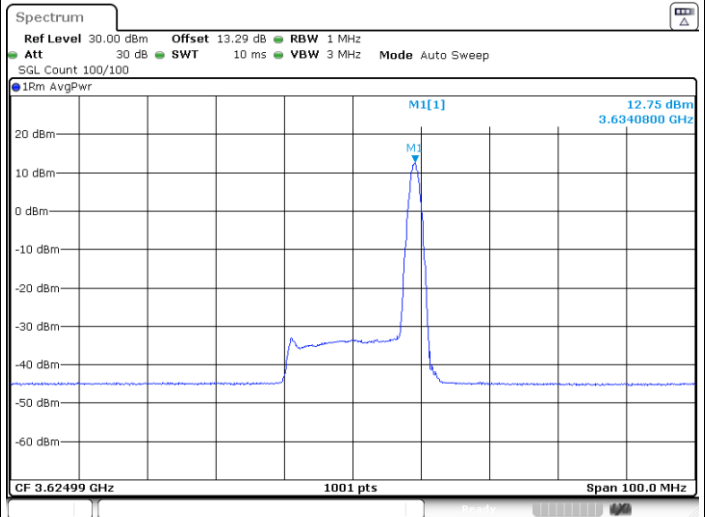
FR1 Part 96 n48 / 20MHz

64QAM

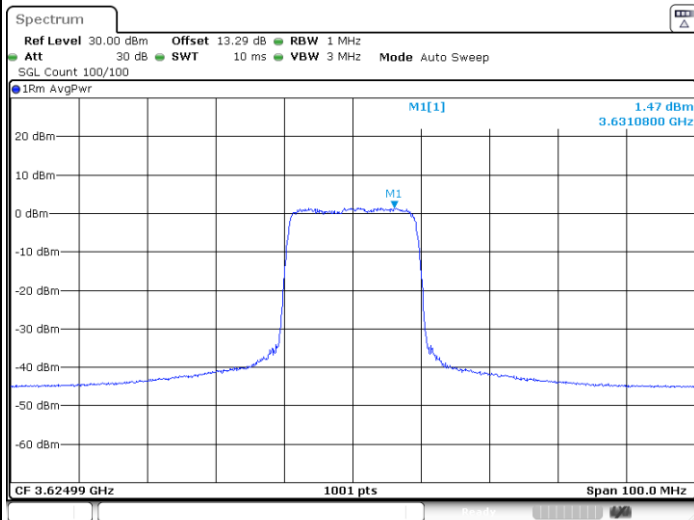
Middle Channel / 1RB0



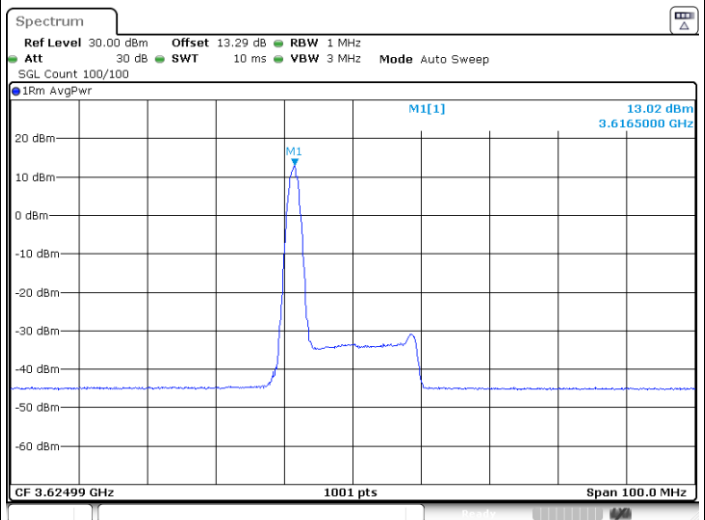
Middle Channel / 1RBmax



Middle Channel / FullRB



Middle Channel / 1RB1

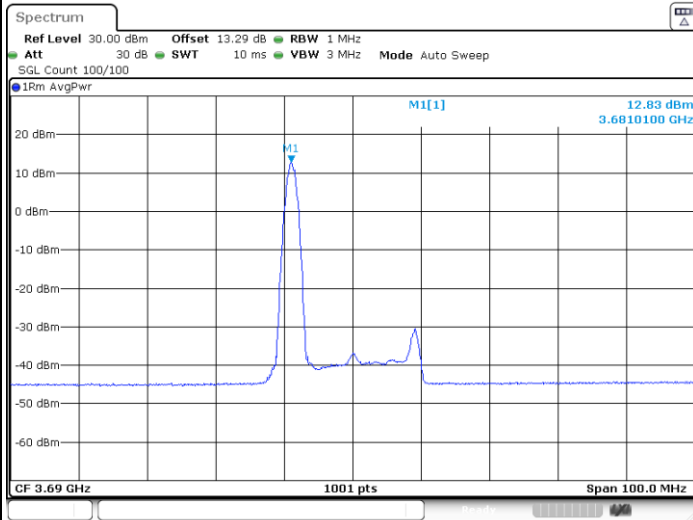




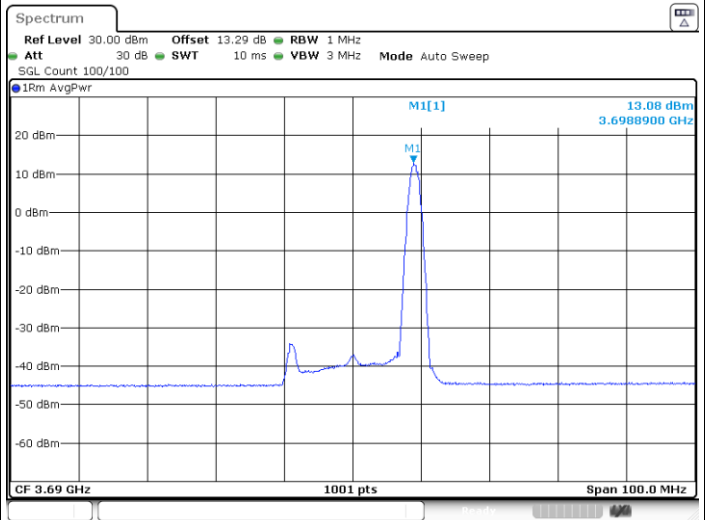
FR1 Part 96 n48 / 20MHz

64QAM

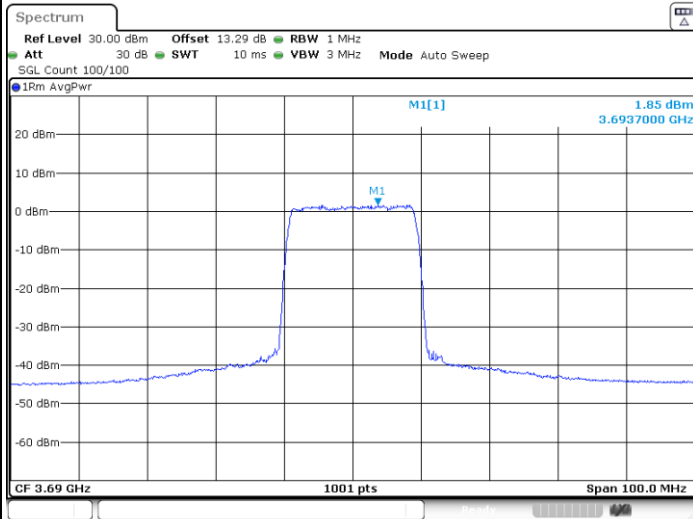
Highest Channel / 1RB0



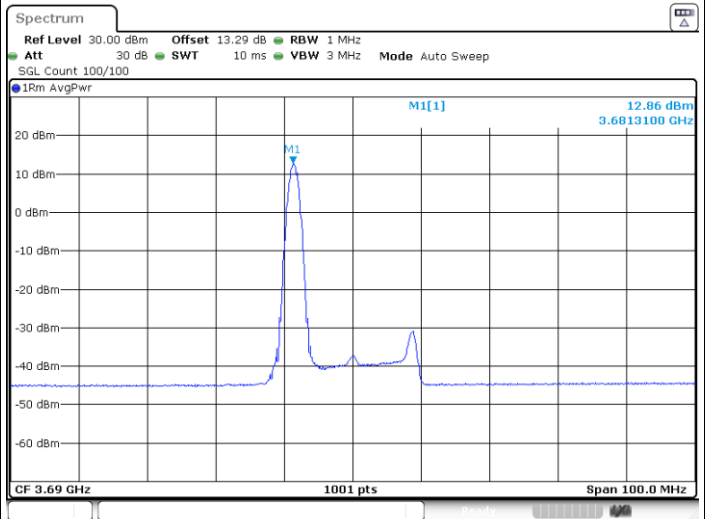
Highest Channel / 1RBmax



Highest Channel / FullRB



Highest Channel / 1RB1

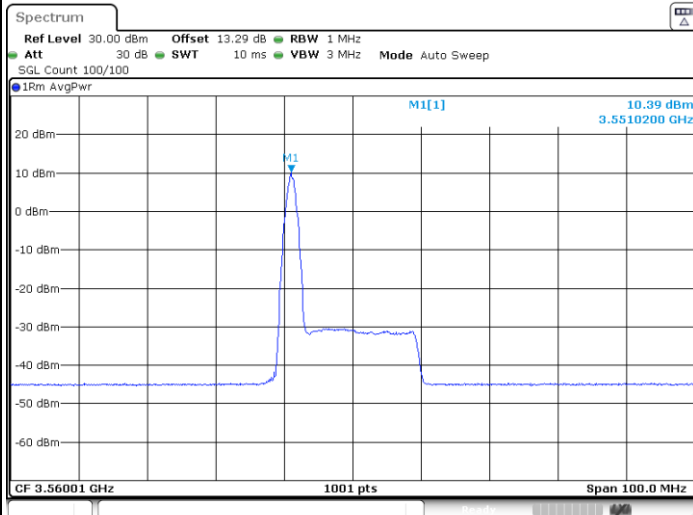




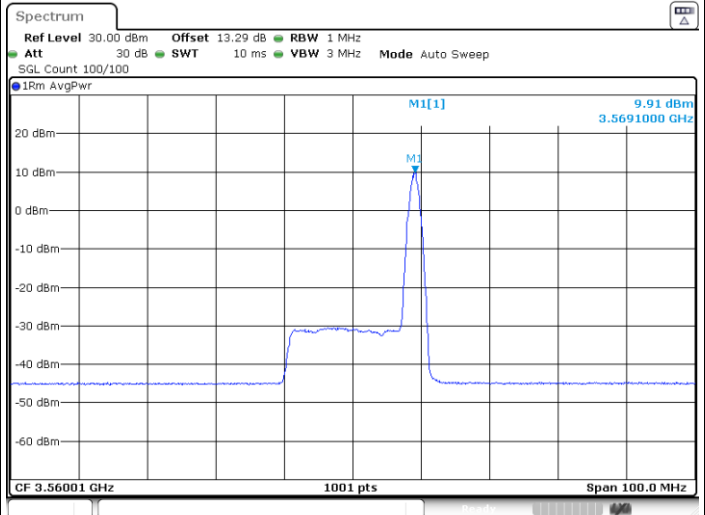
FR1 Part 96 n48 / 20MHz

256QAM

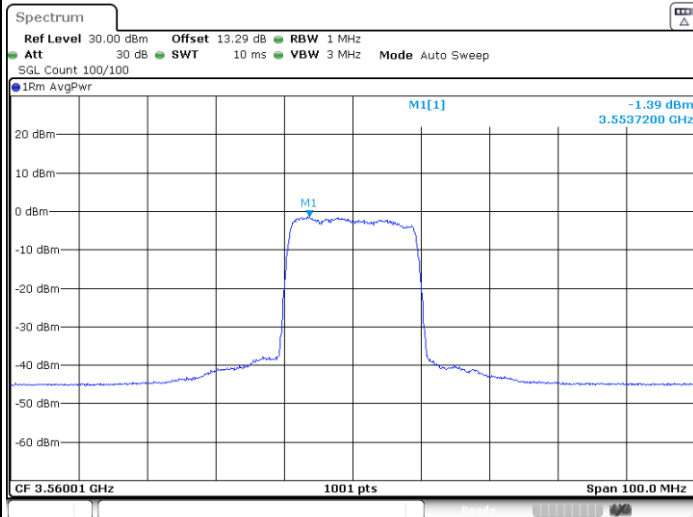
Lowest Channel / 1RB0



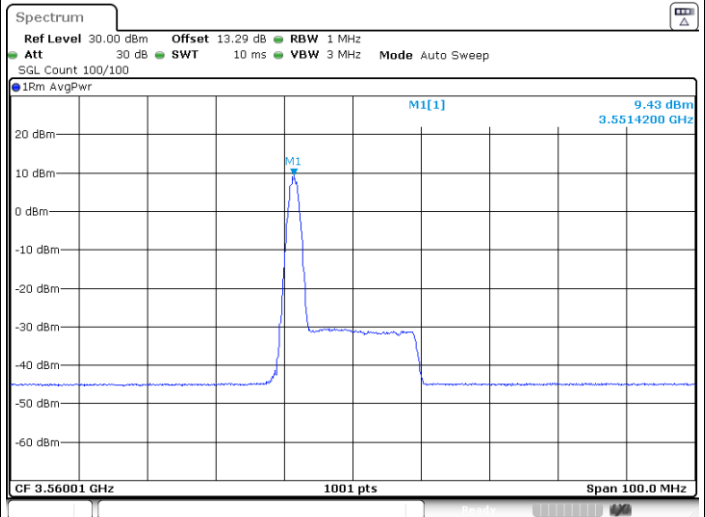
Lowest Channel / 1RBmax



Lowest Channel / FullRB



Lowest Channel / 1RB1

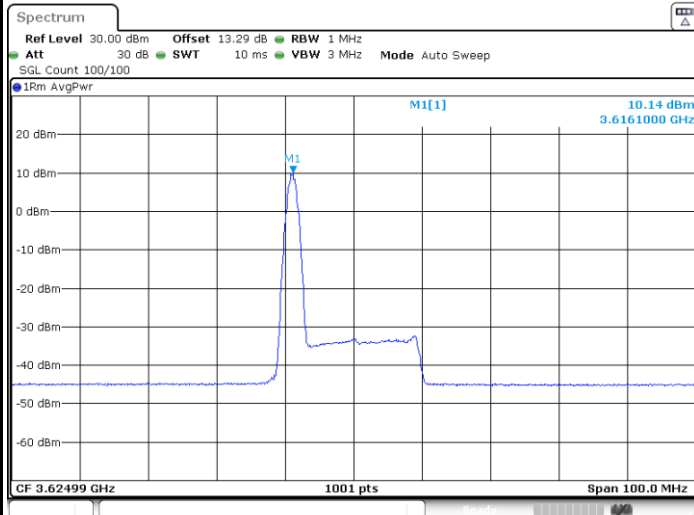




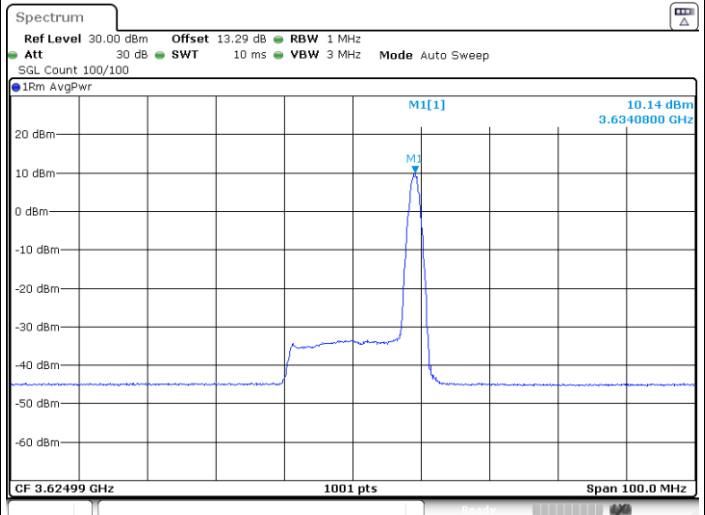
FR1 Part 96 n48 / 20MHz

256QAM

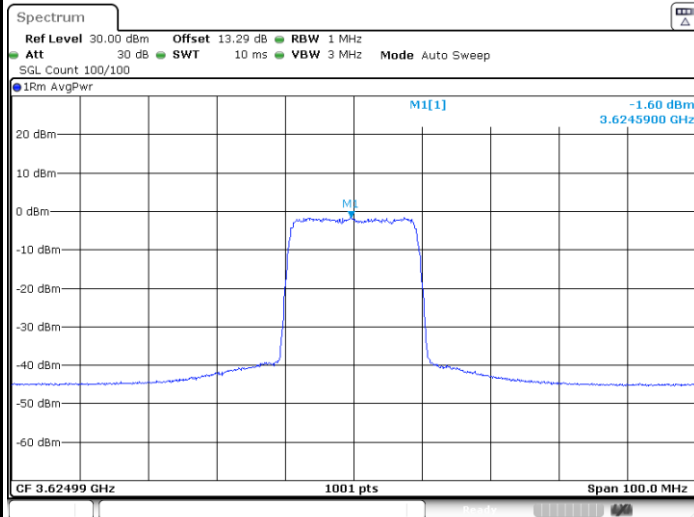
Middle Channel / 1RB0



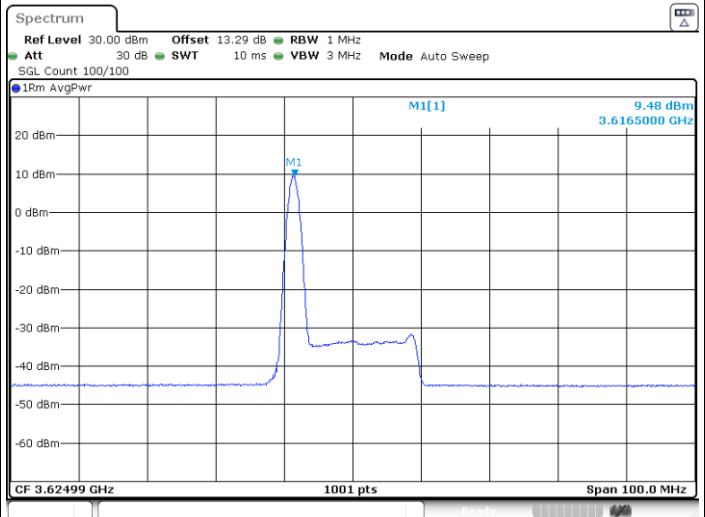
Middle Channel / 1RBmax



Middle Channel / FullRB



Middle Channel / 1RB1

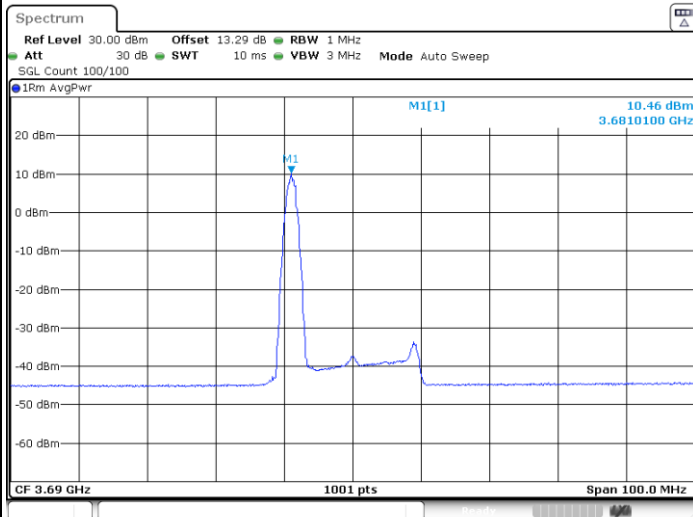




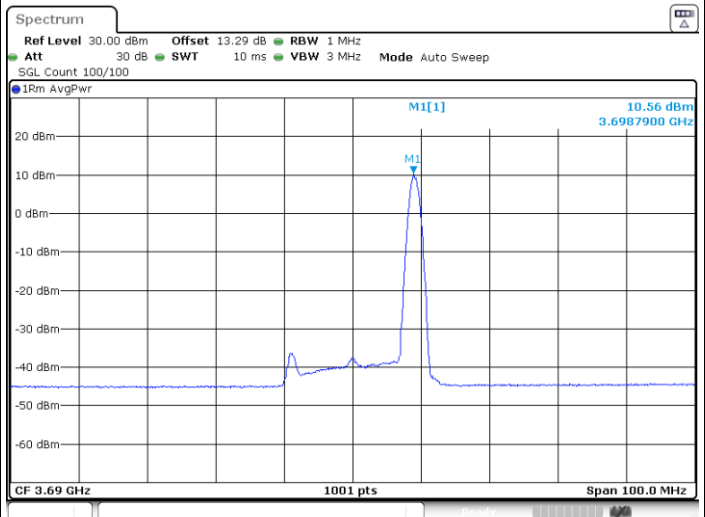
FR1 Part 96 n48 / 20MHz

256QAM

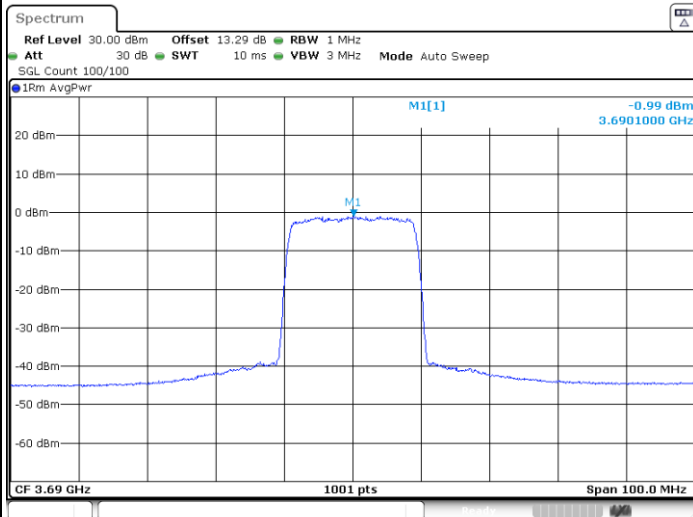
Highest Channel / 1RB0



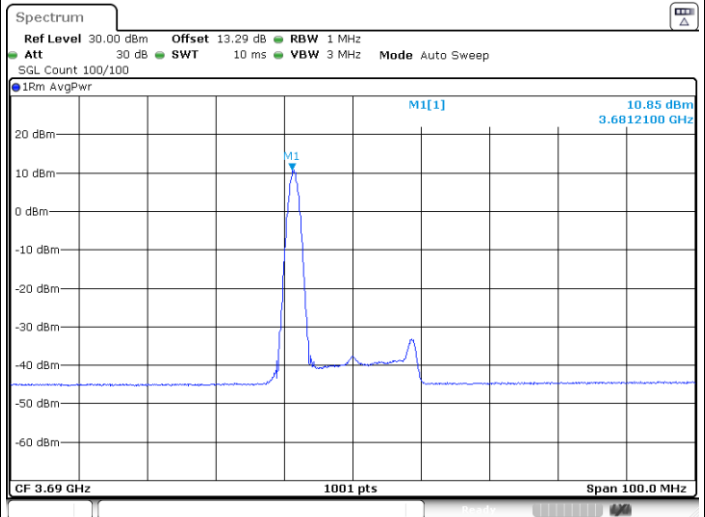
Highest Channel / 1RBmax



Highest Channel / FullRB



Highest Channel / 1RB1

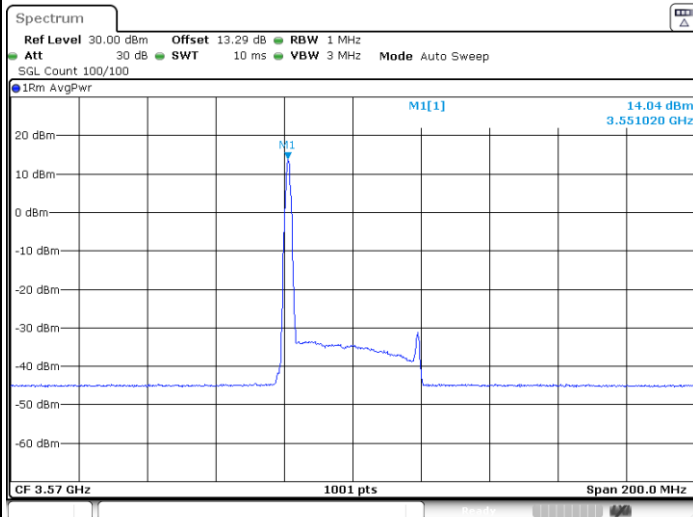




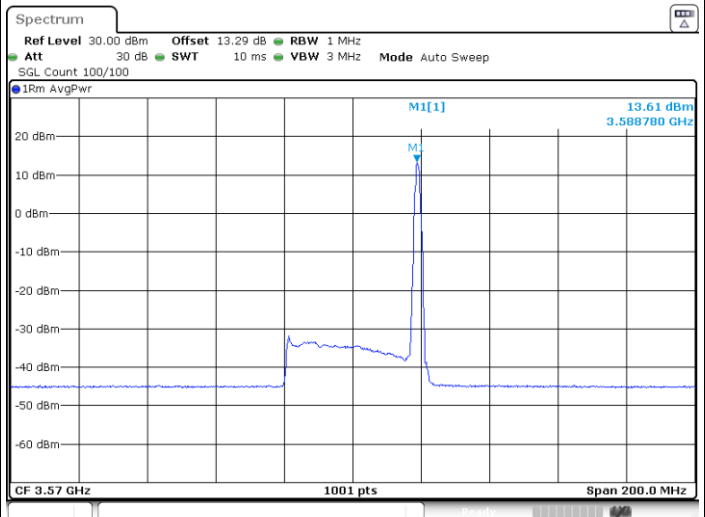
FR1 Part 96 n48 / 40MHz

QPSK

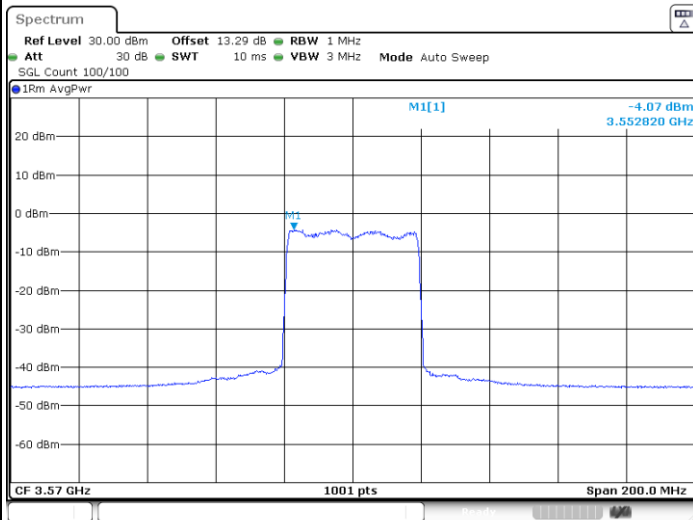
Lowest Channel / 1RB0



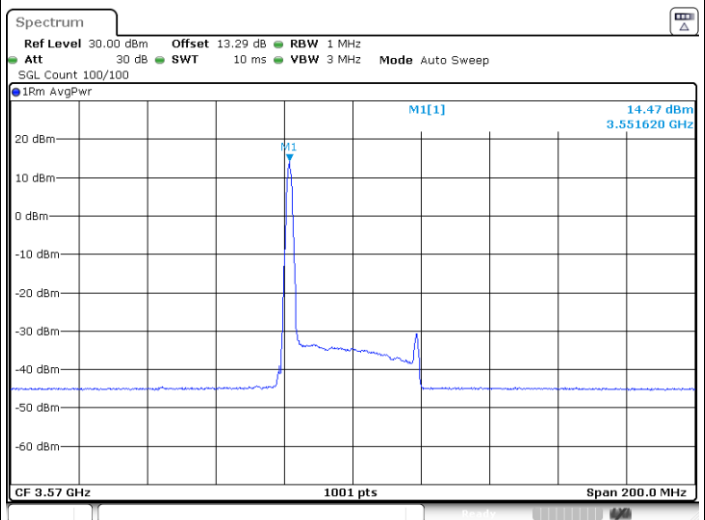
Lowest Channel / 1RBmax



Lowest Channel / FullRB



Lowest Channel / 1RB1

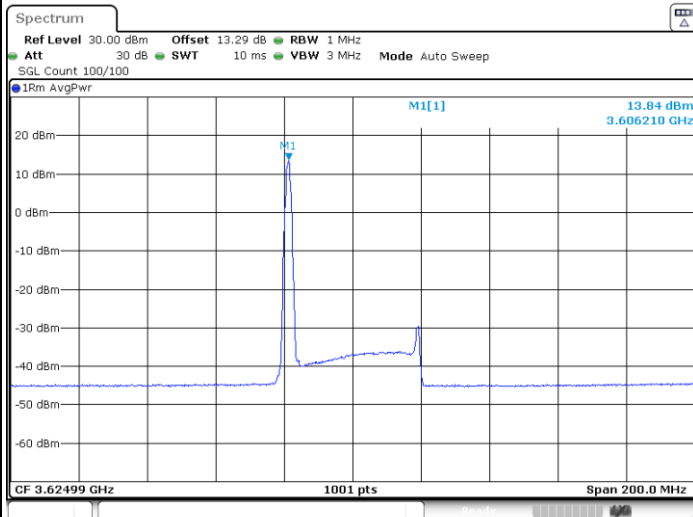




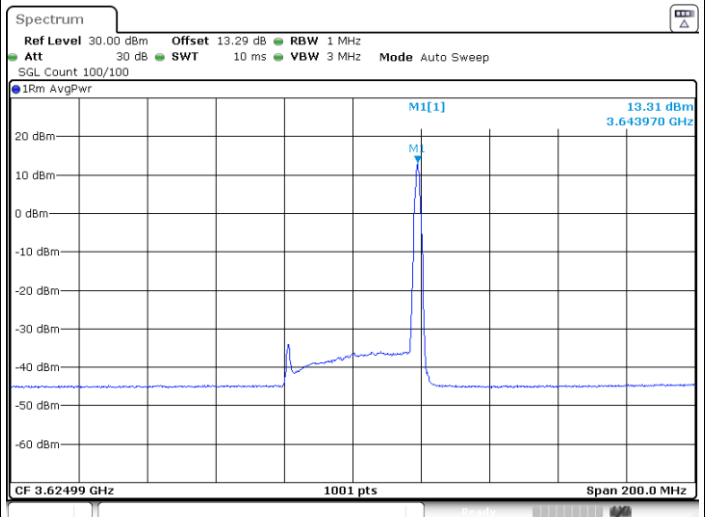
FR1 Part 96 n48 / 40MHz

QPSK

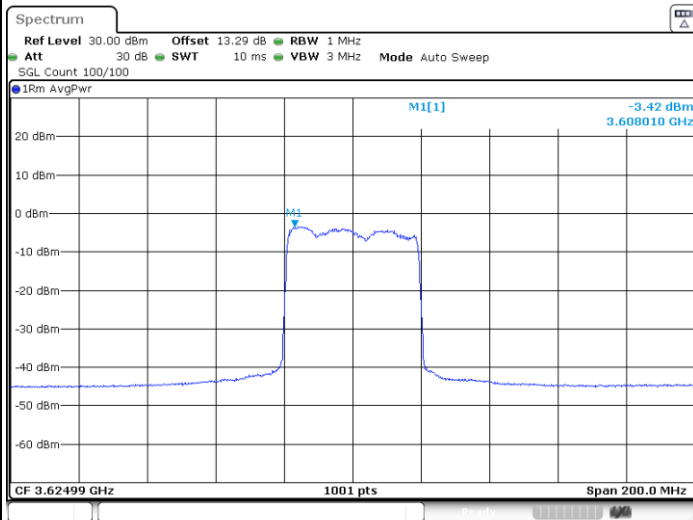
Middle Channel / 1RB0



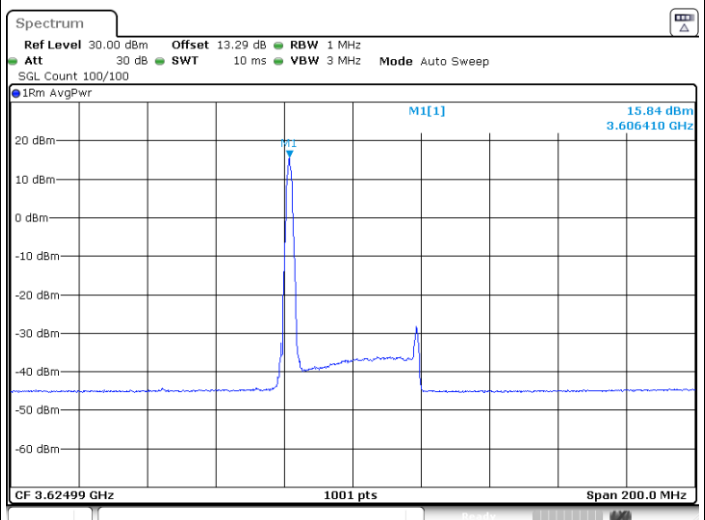
Middle Channel / 1RBmax



Middle Channel / FullRB



Middle Channel / 1RB1

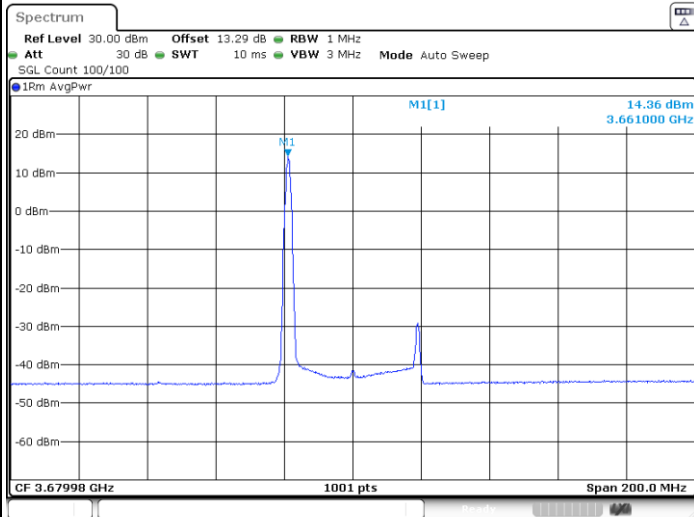




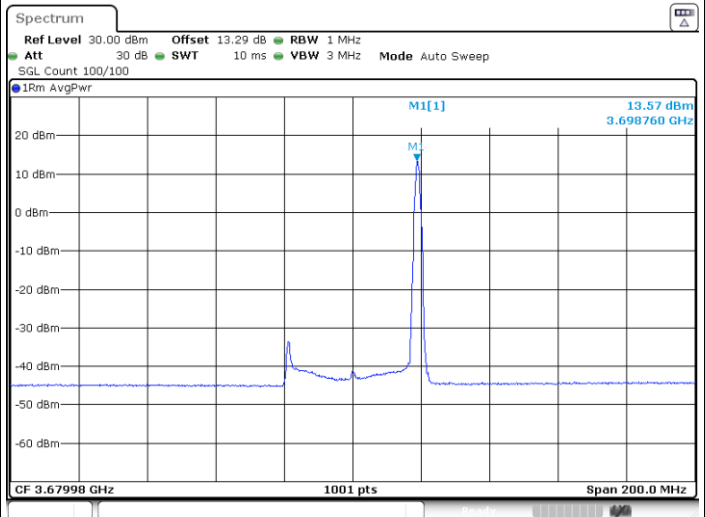
FR1 Part 96 n48 / 40MHz

QPSK

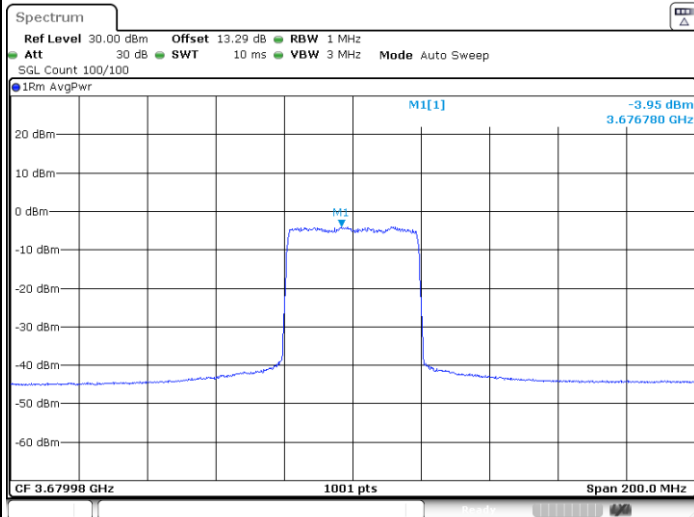
Highest Channel / 1RB0



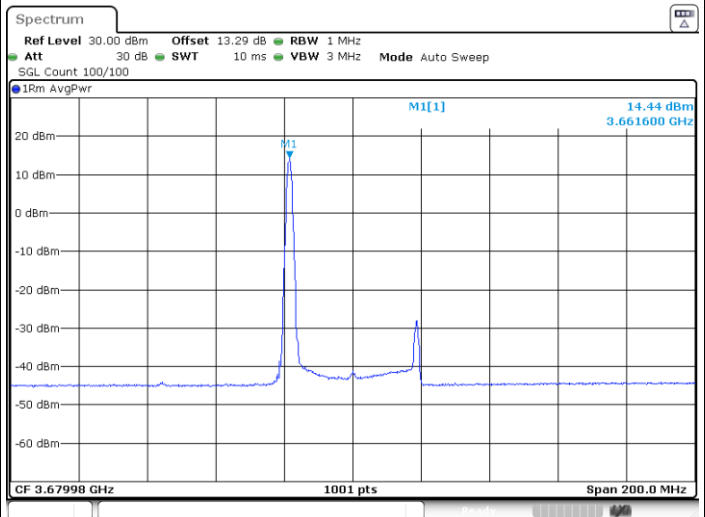
Highest Channel / 1RBmax



Highest Channel / FullRB



Highest Channel / 1RB1

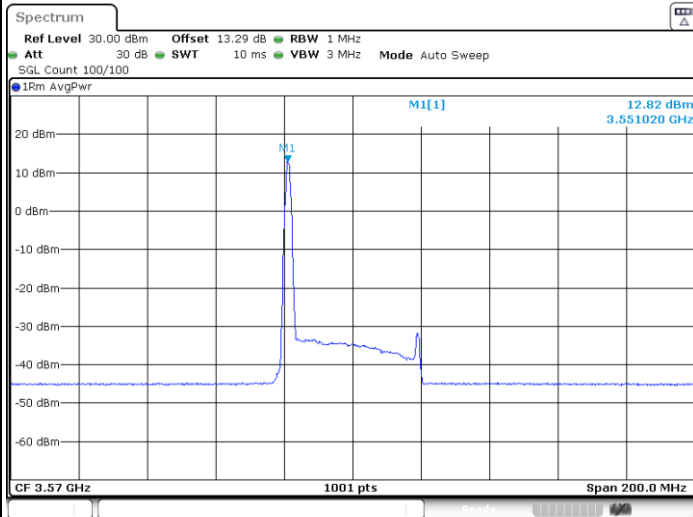




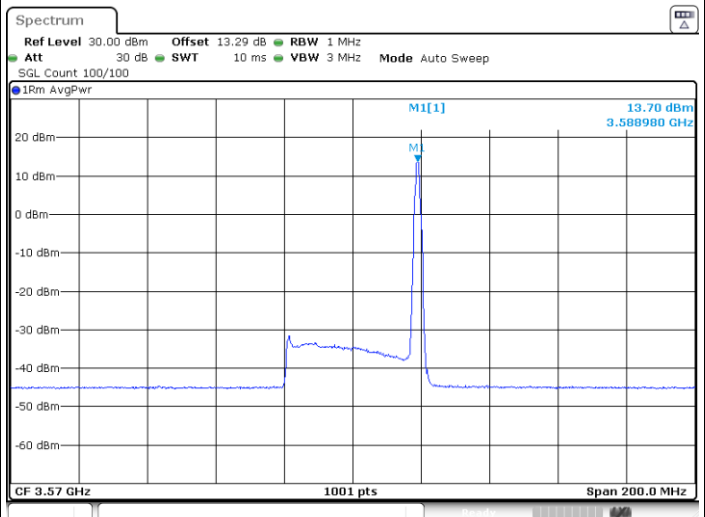
FR1 Part 96 n48 / 40MHz

16QAM

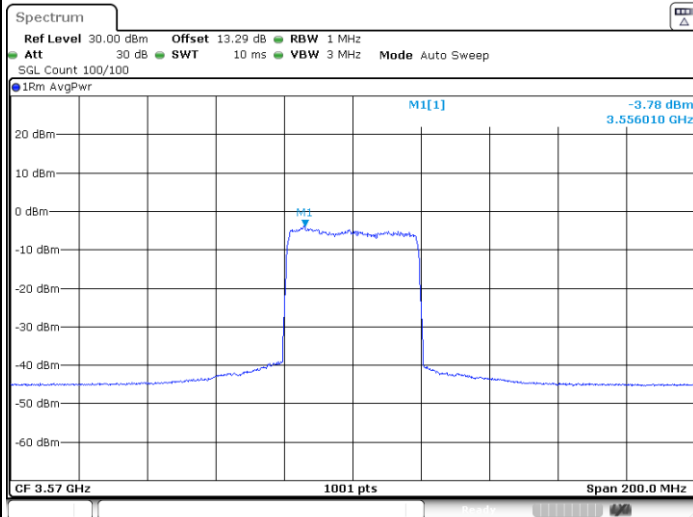
Lowest Channel / 1RB0



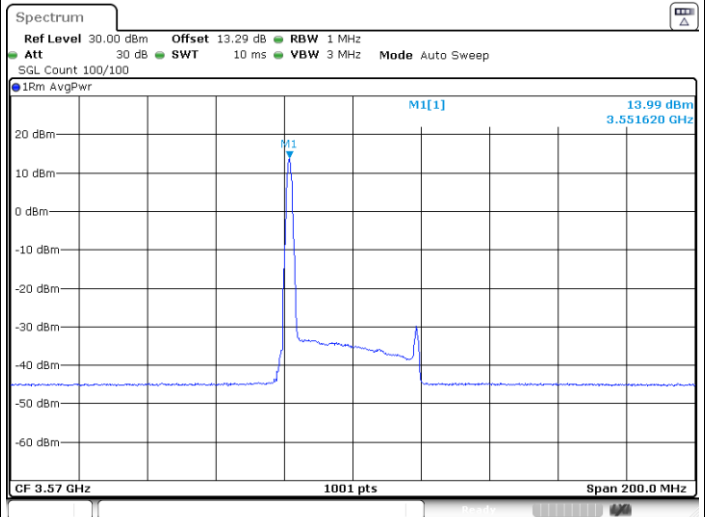
Lowest Channel / 1RBmax



Lowest Channel / FullRB



Lowest Channel / 1RB1

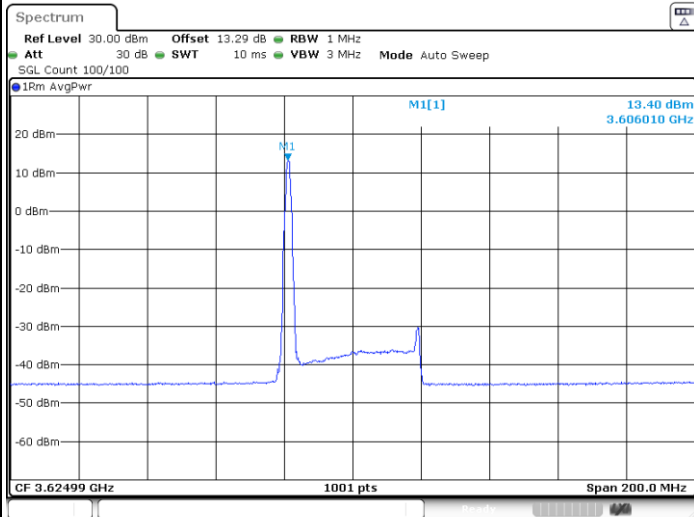




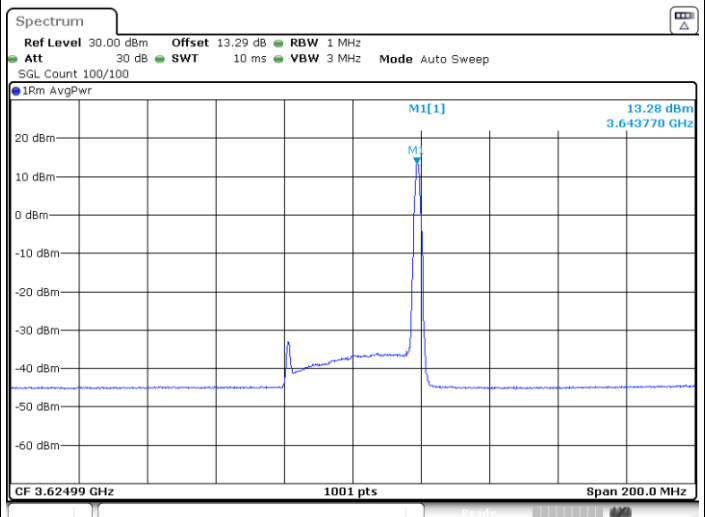
FR1 Part 96 n48 / 40MHz

16QAM

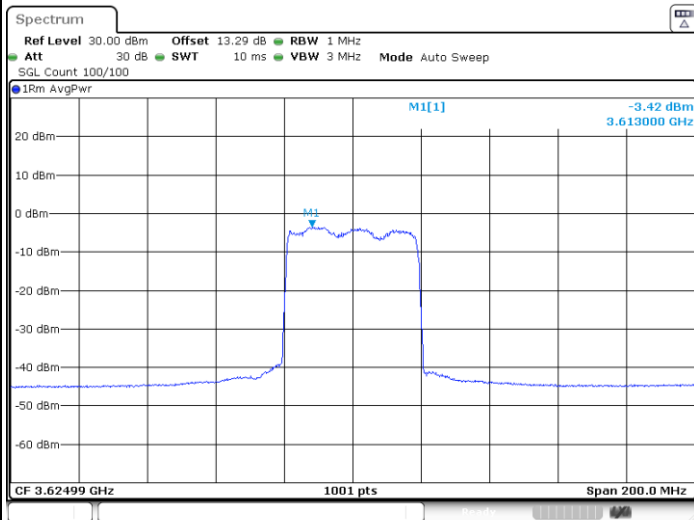
Middle Channel / 1RB0



Middle Channel / 1RBmax



Middle Channel / FullRB



Middle Channel / 1RB1

