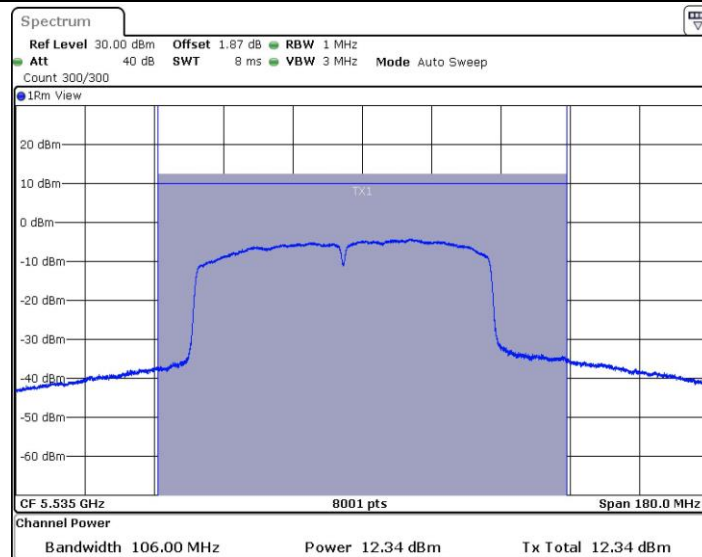
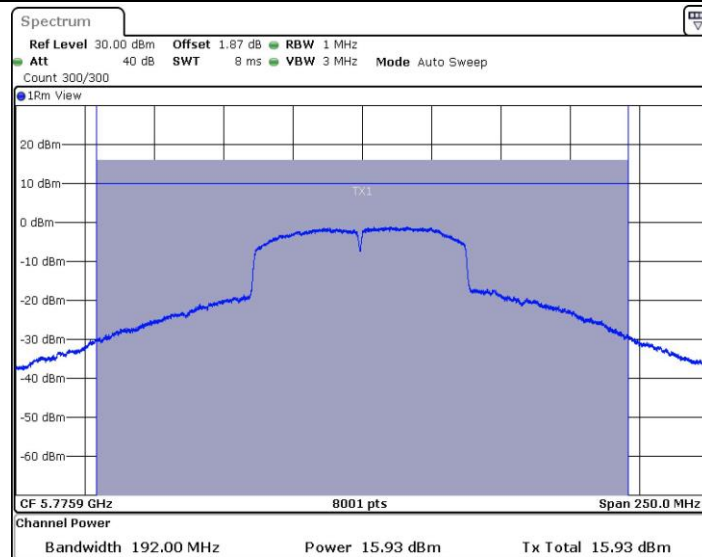
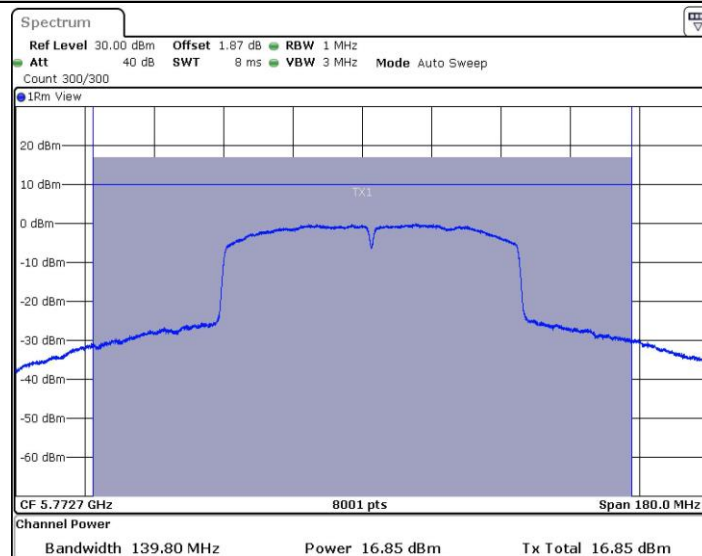




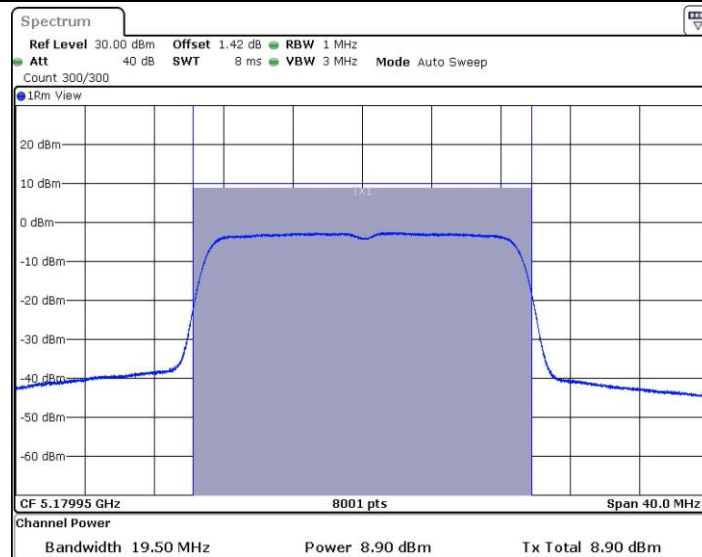
11AC80SISO_Ant1_5530



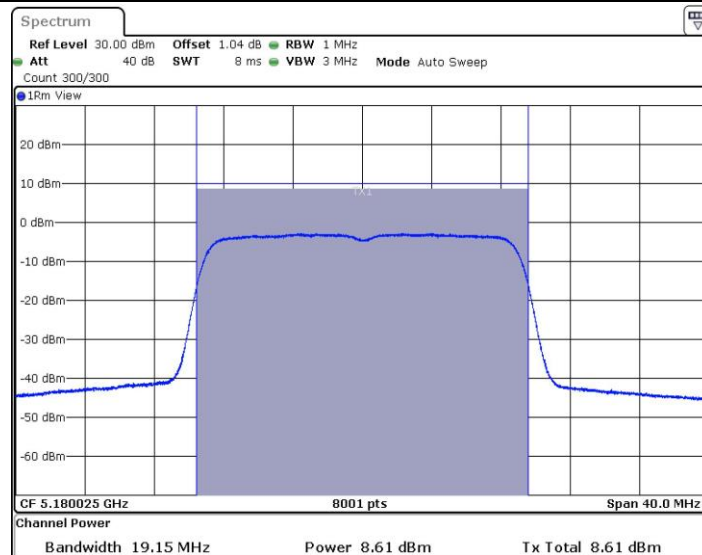
11AC80SISO_Ant2_5775



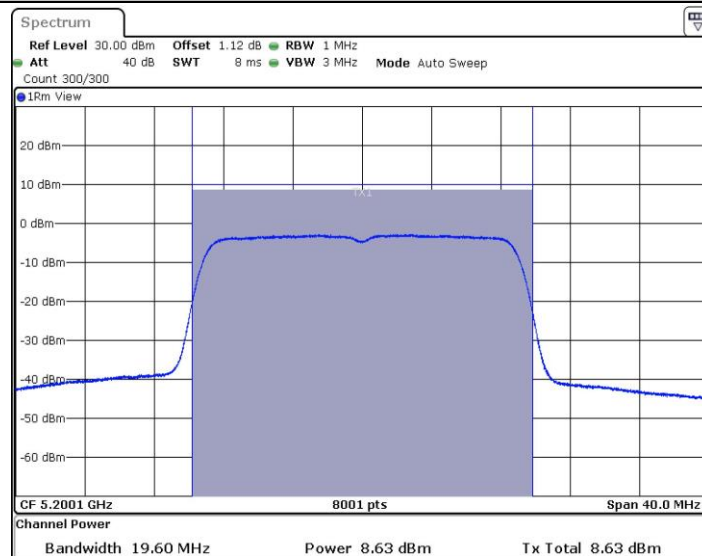
11AC80SISO_Ant1_5775



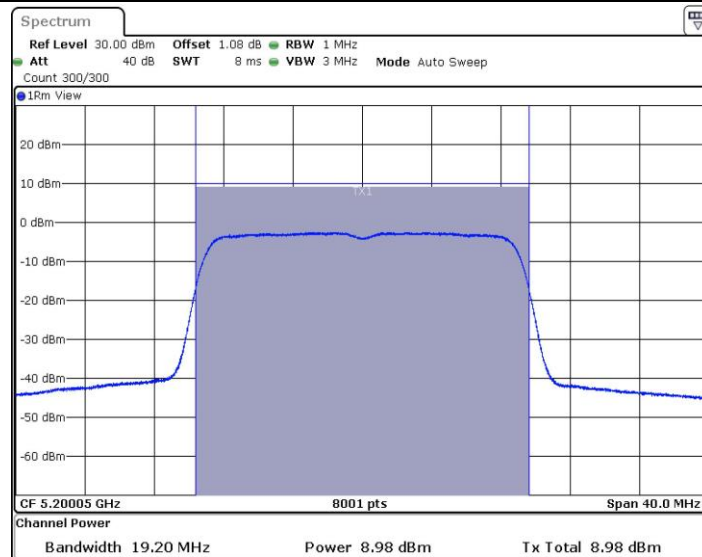
11N20MIMO_Ant2_5180



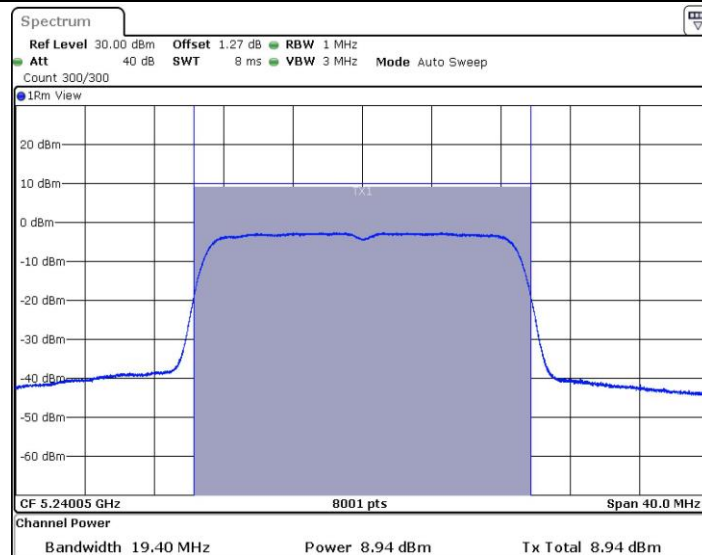
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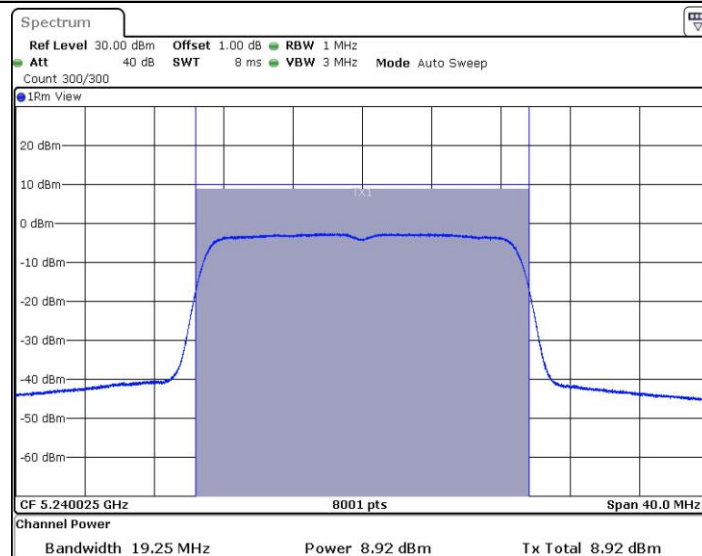
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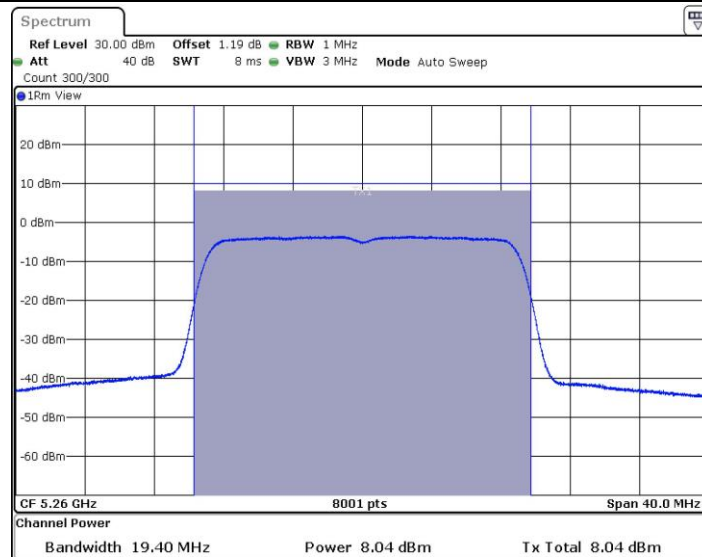
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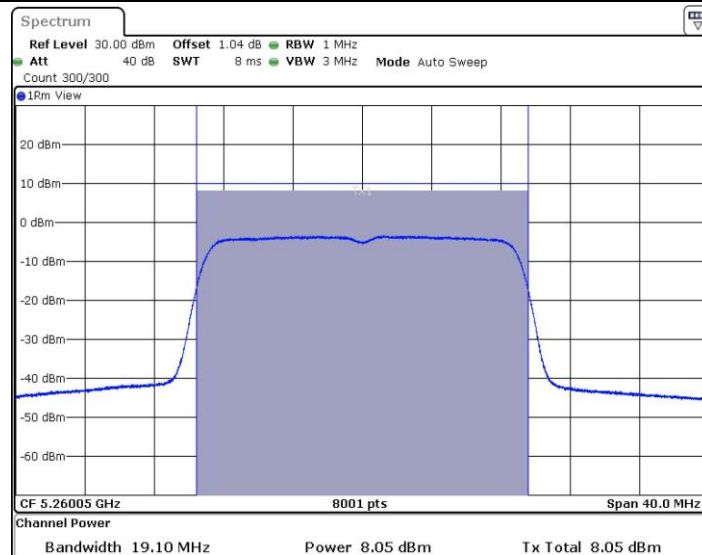
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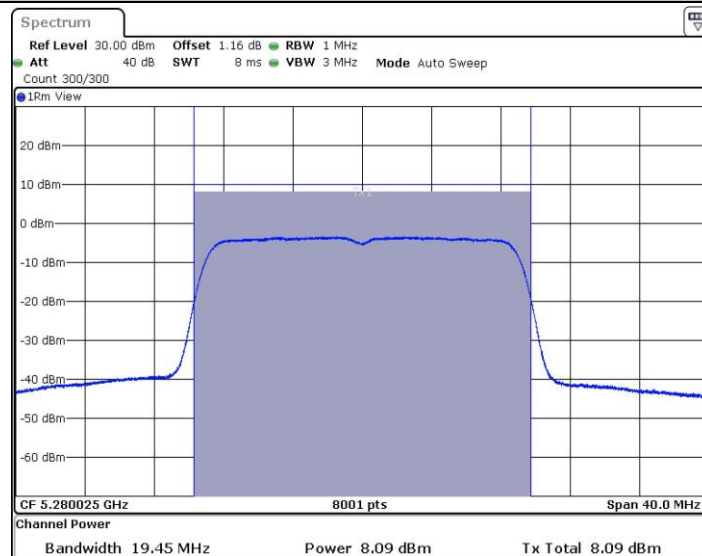
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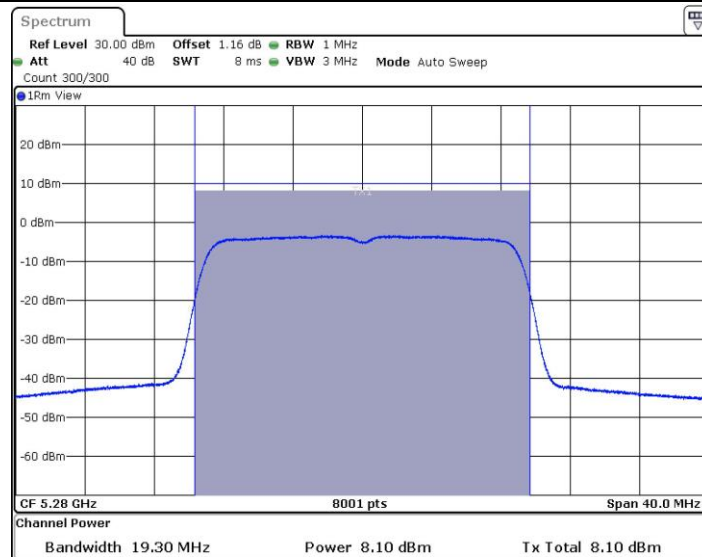
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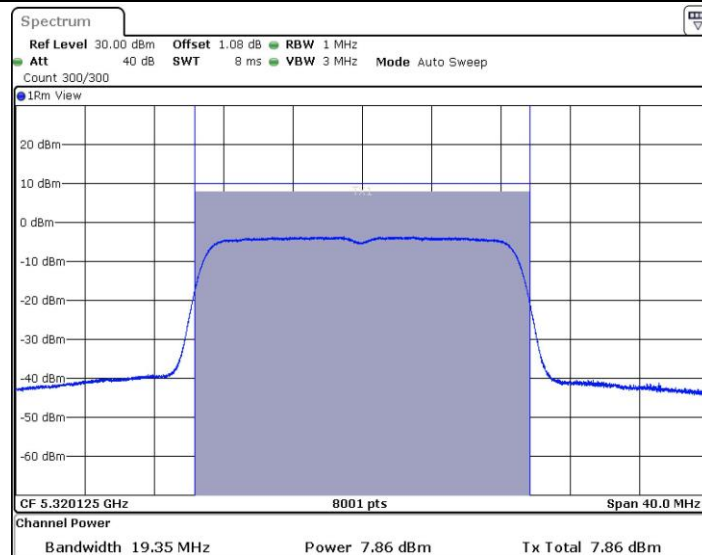
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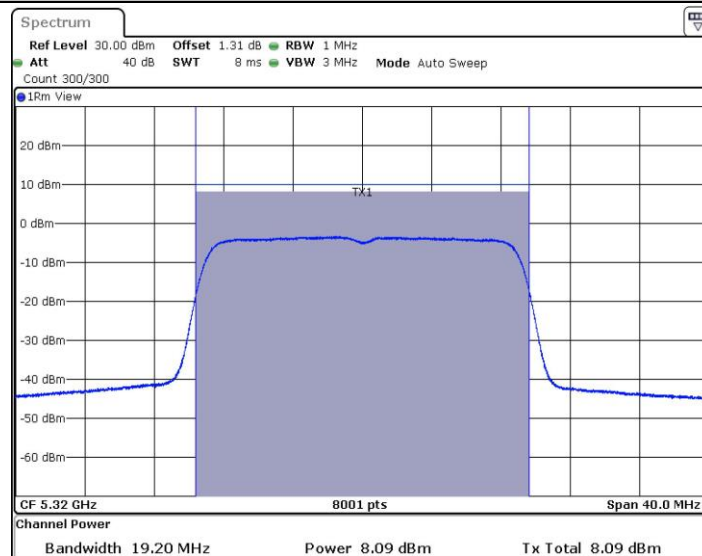
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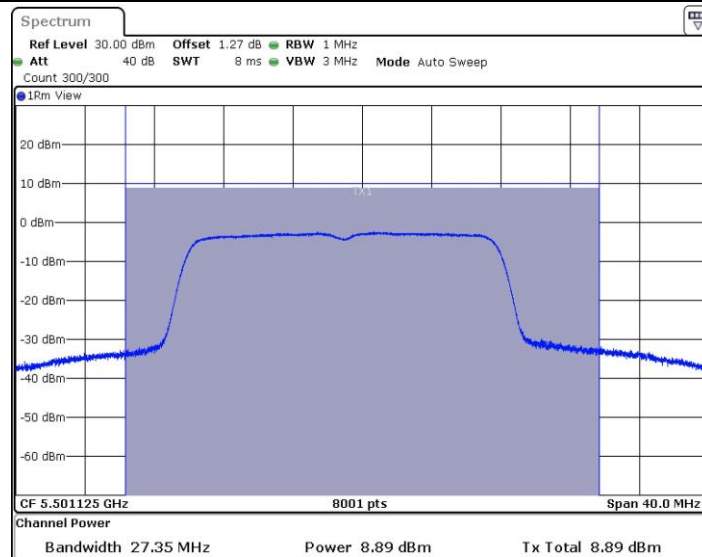
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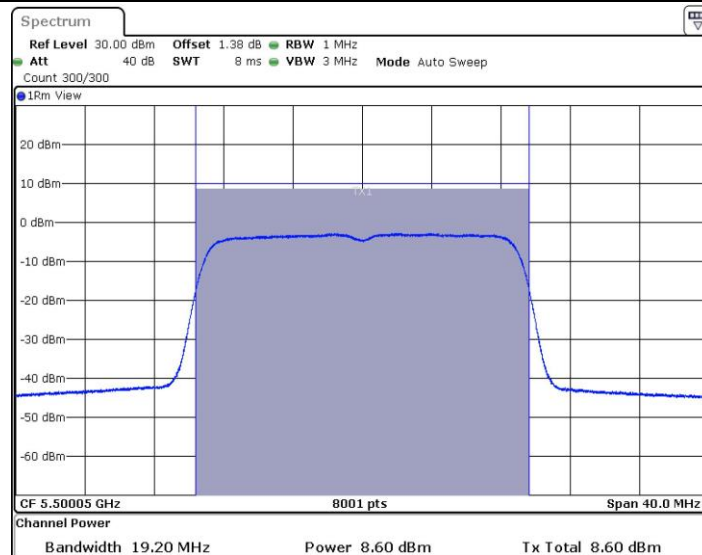
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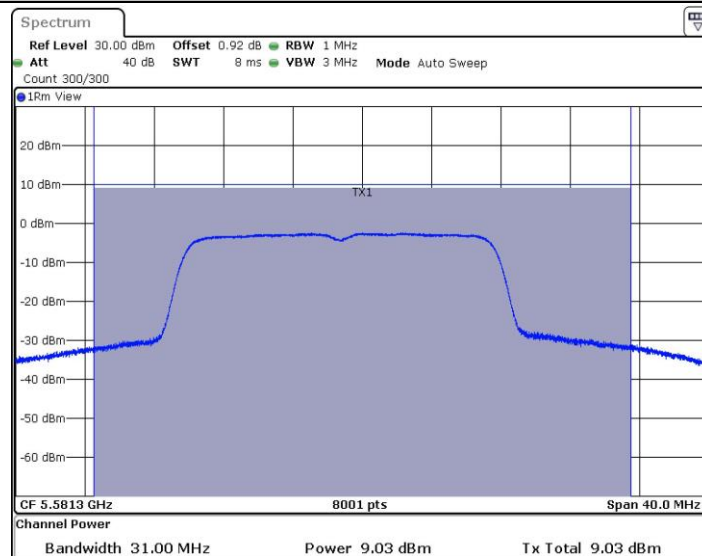
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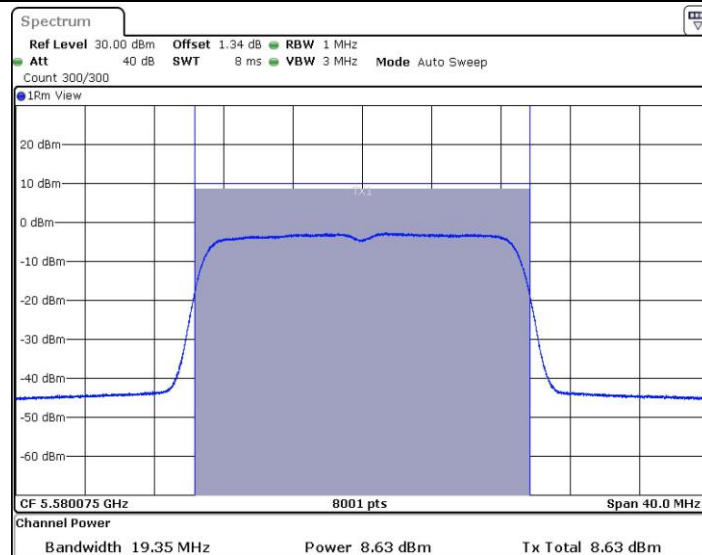
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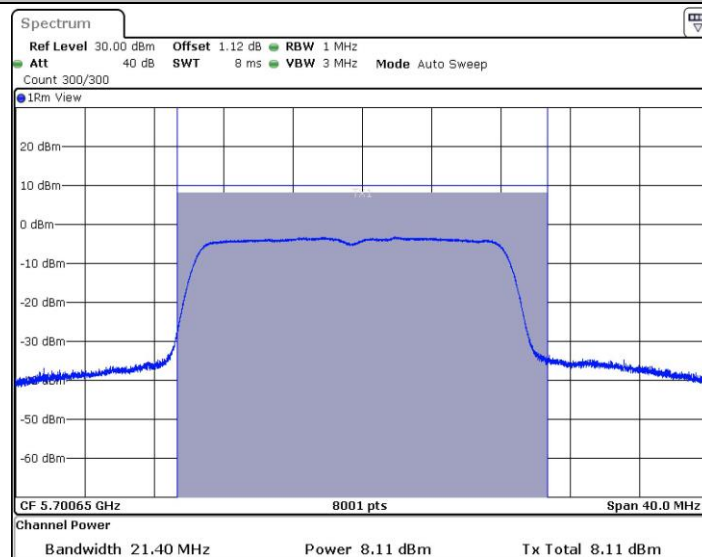
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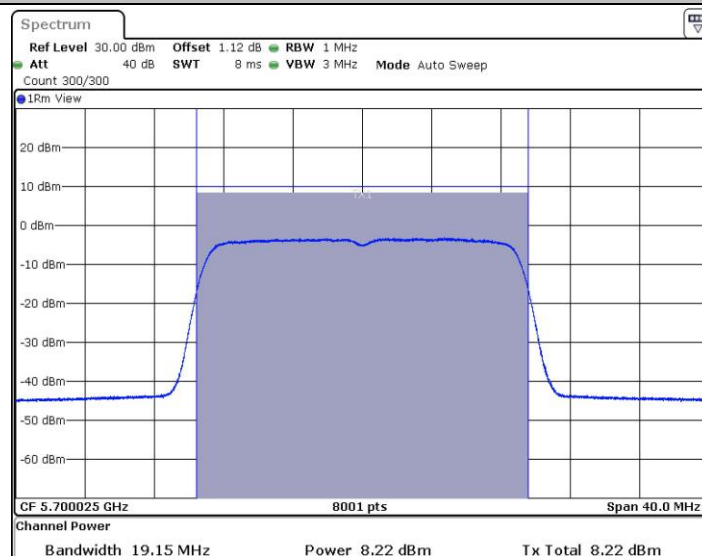
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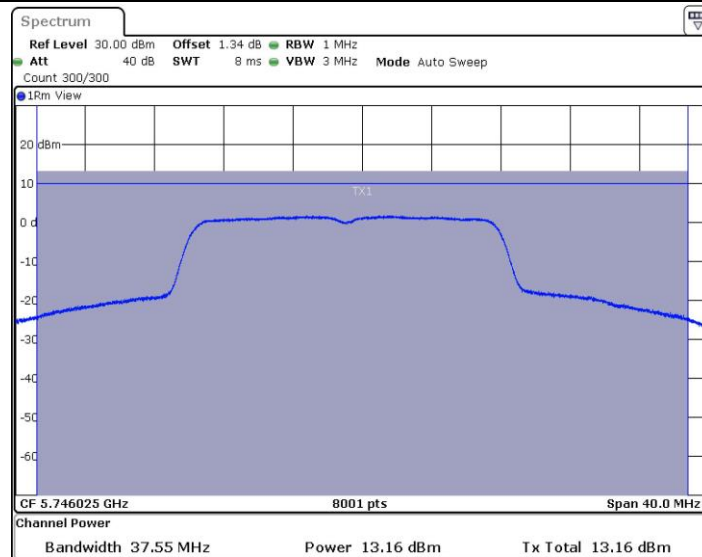
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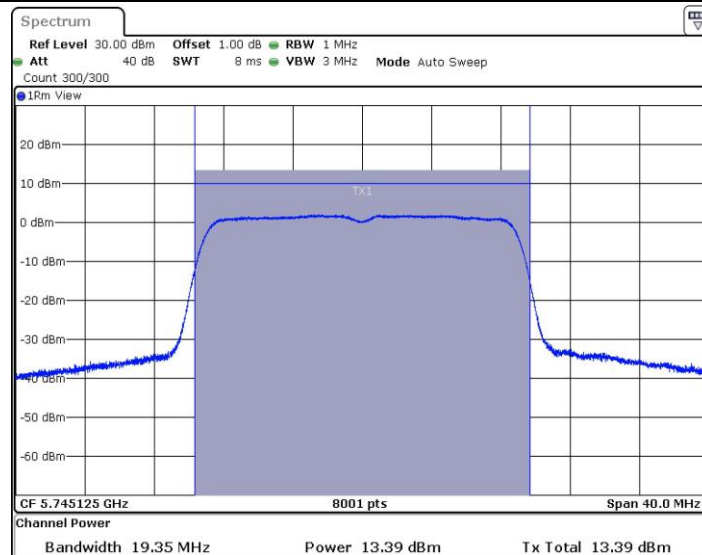
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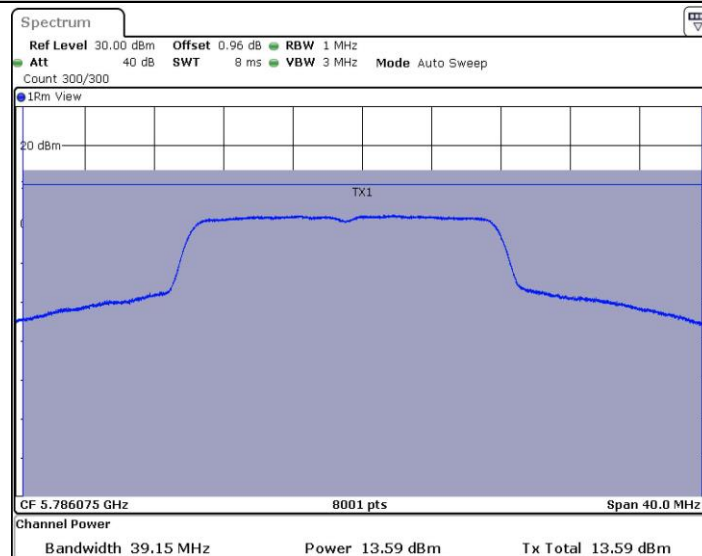
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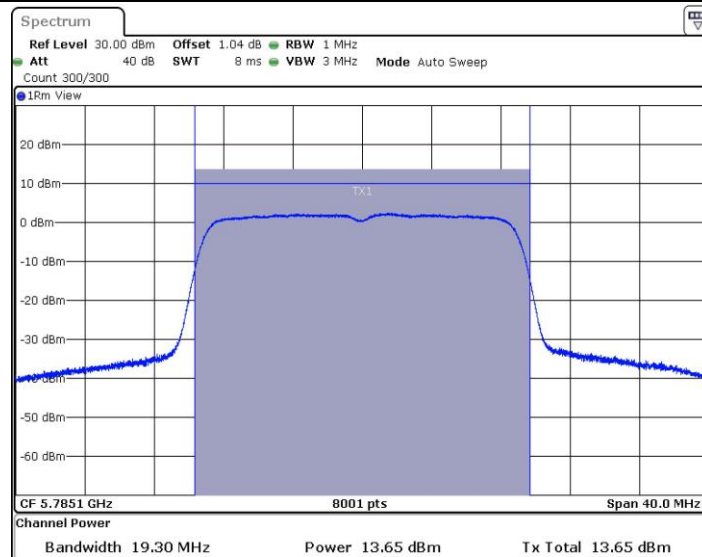
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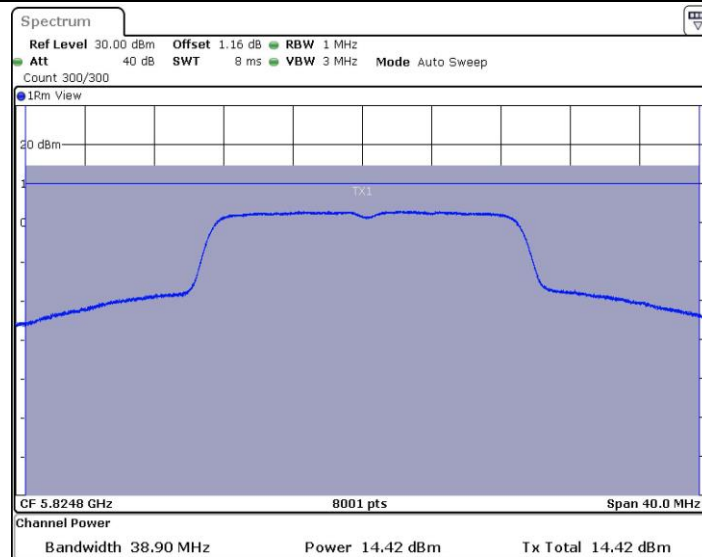
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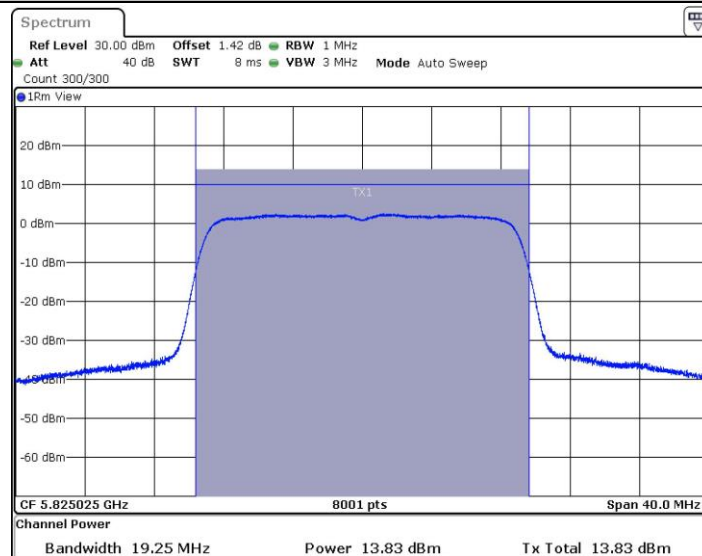
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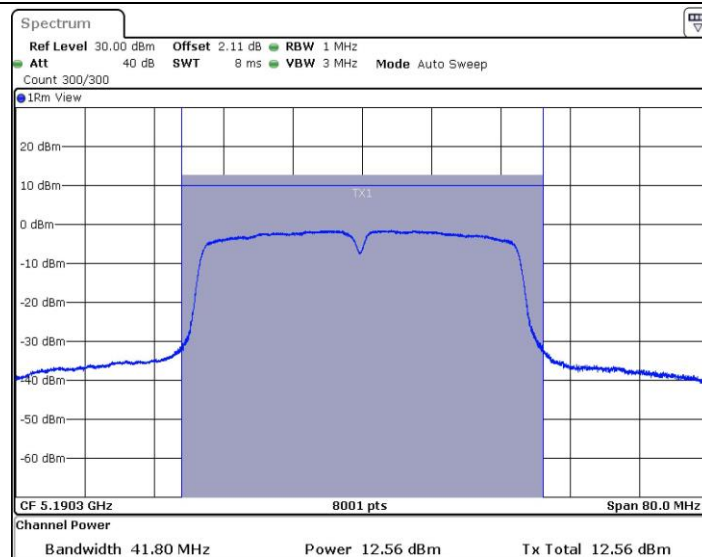
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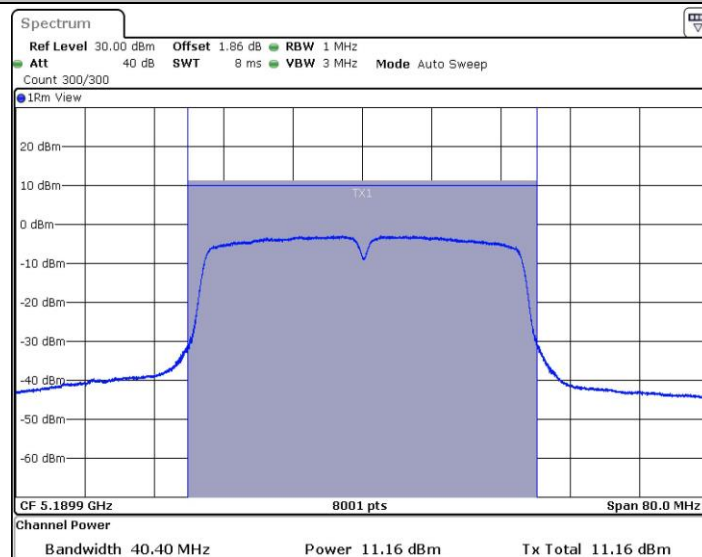
11N20MIMO_Ant2_5825



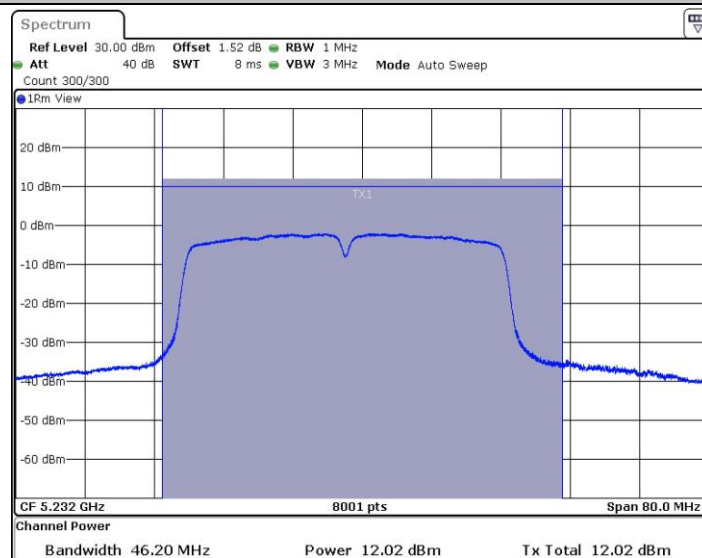
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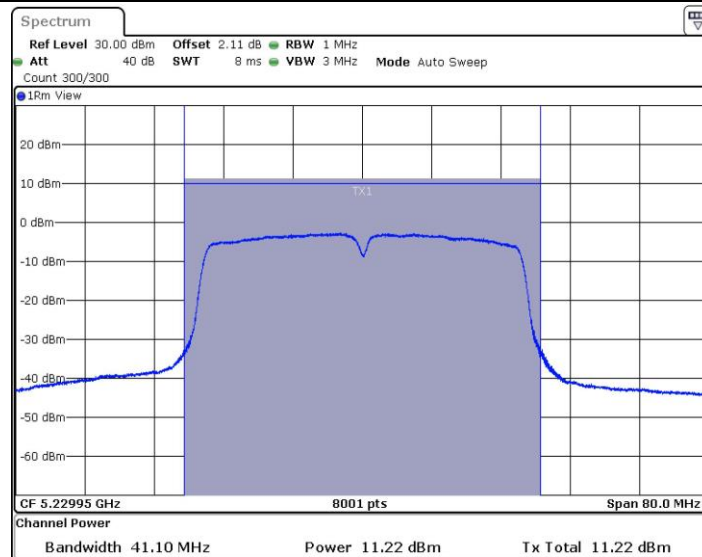
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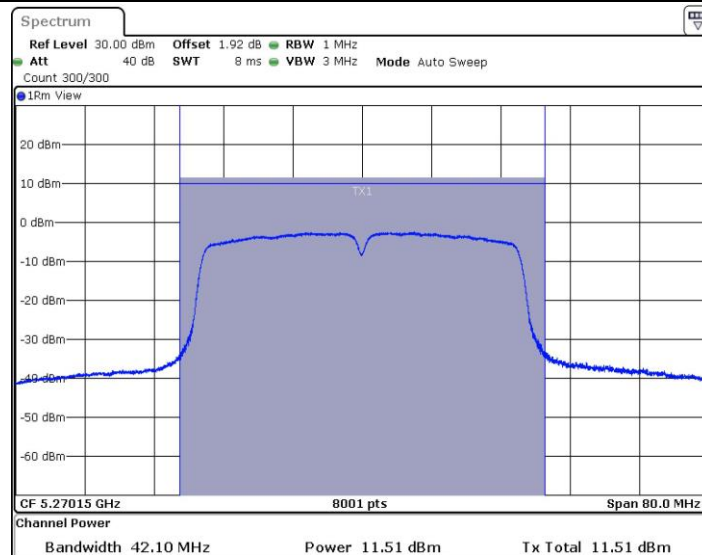
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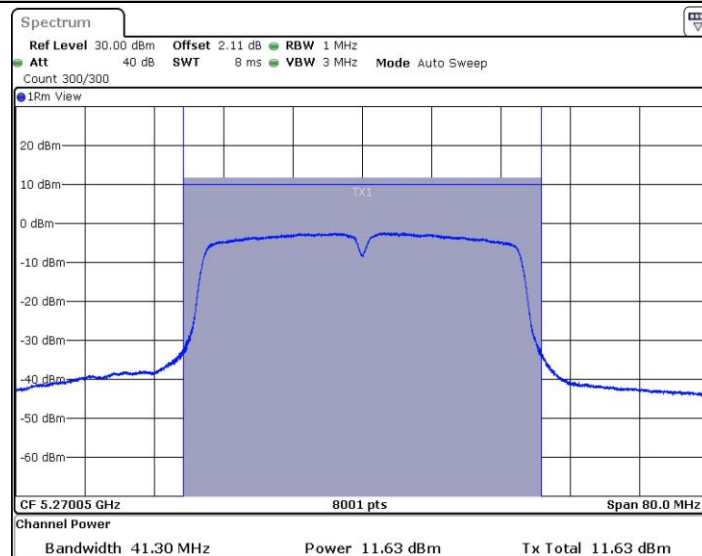
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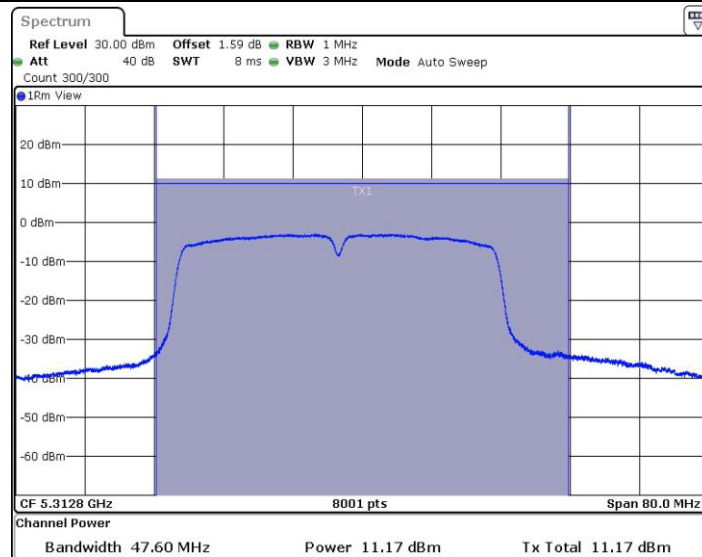
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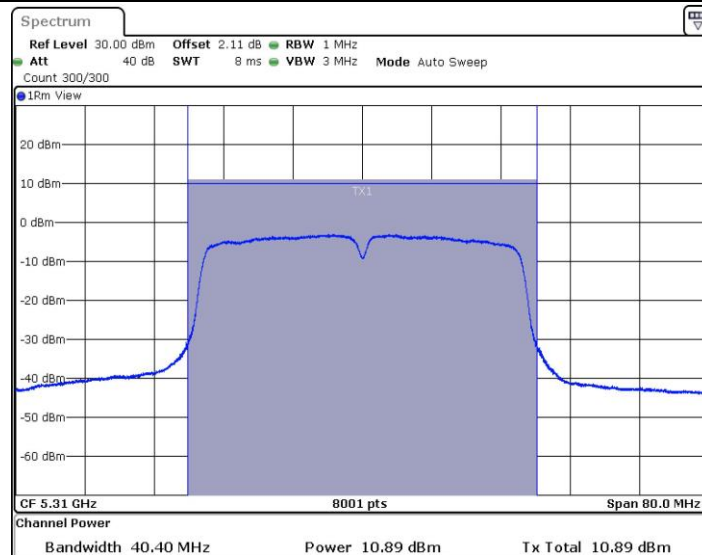
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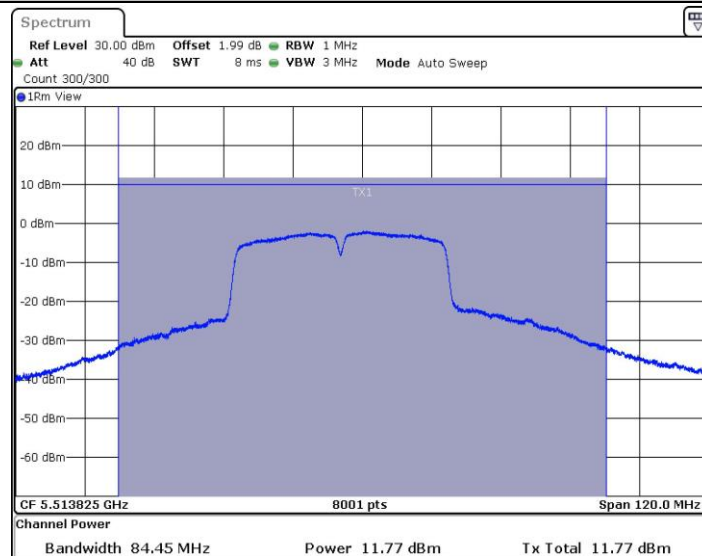
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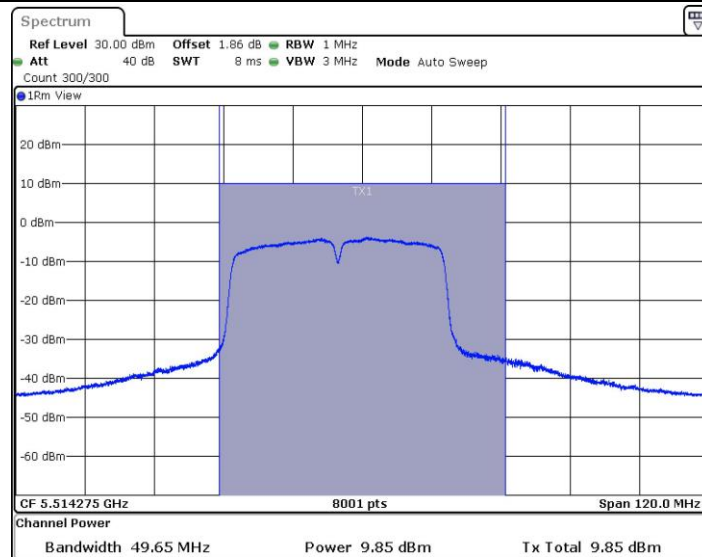
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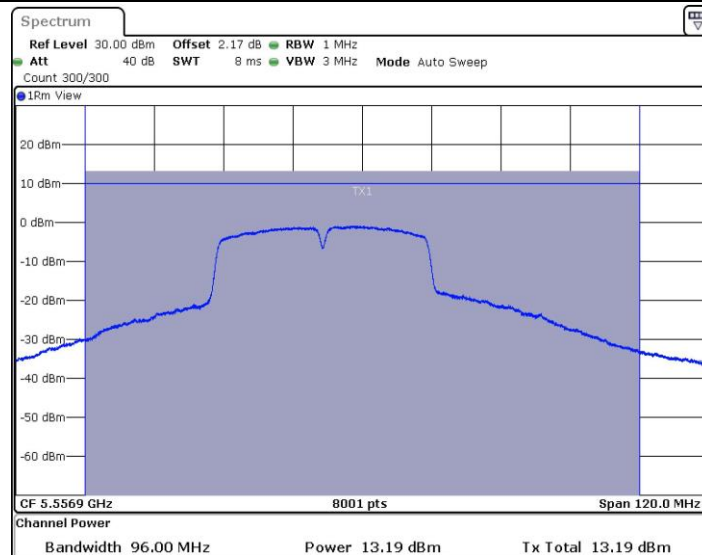
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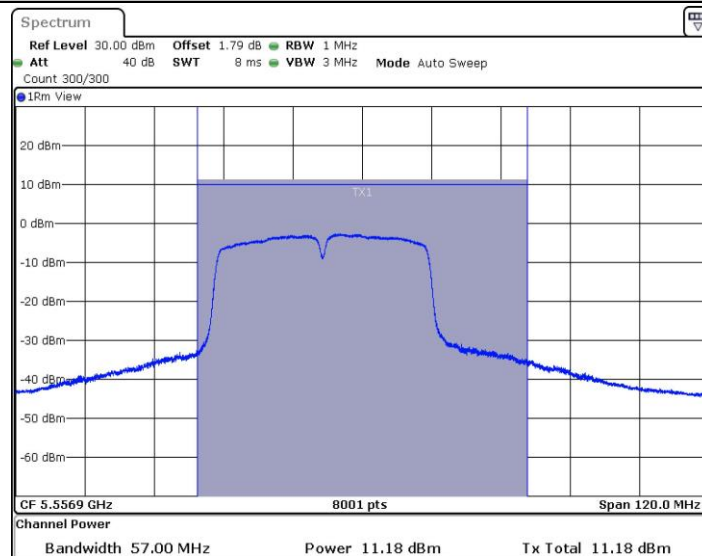
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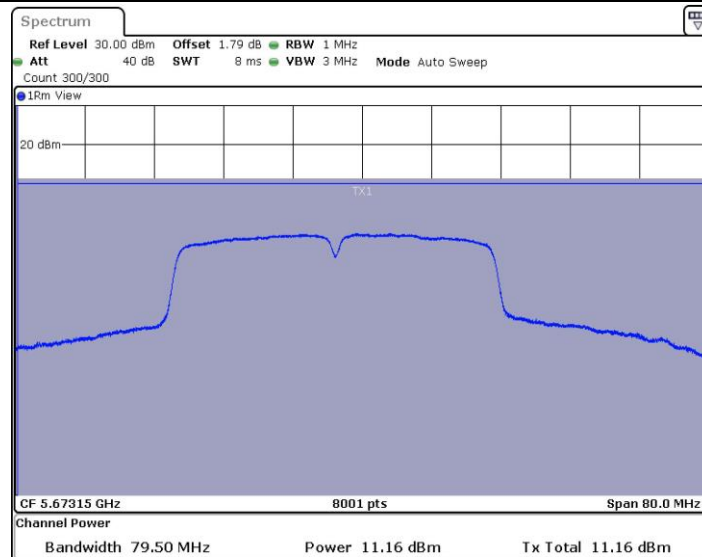
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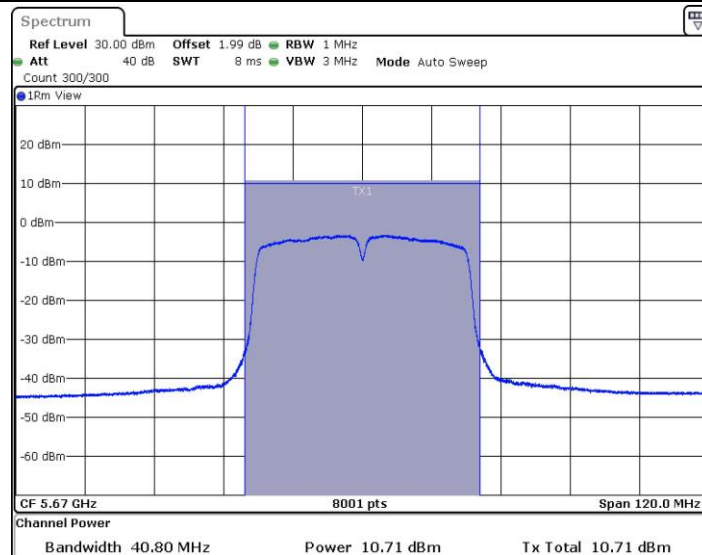
11N40MIMO_Ant2_5550



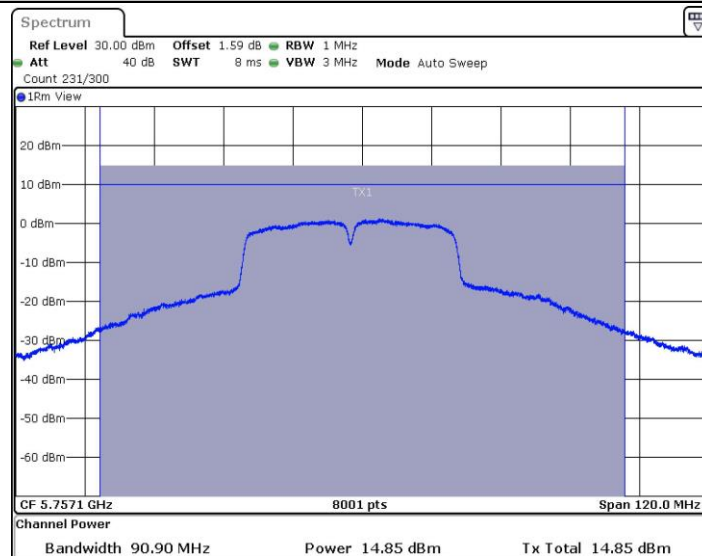
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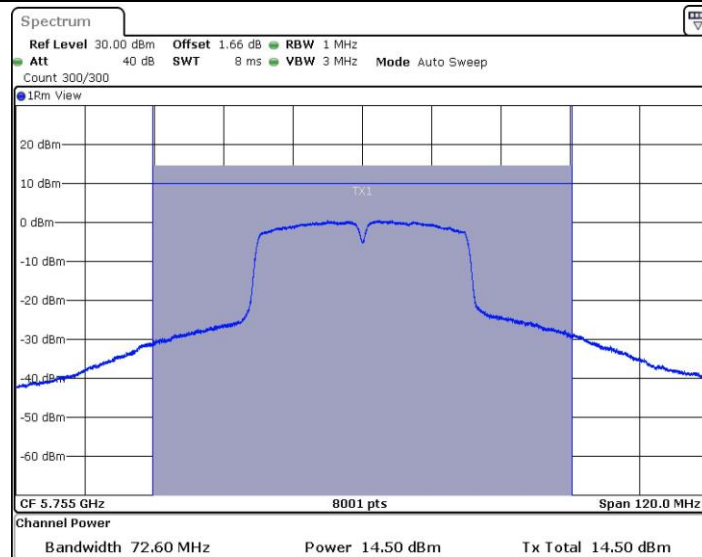
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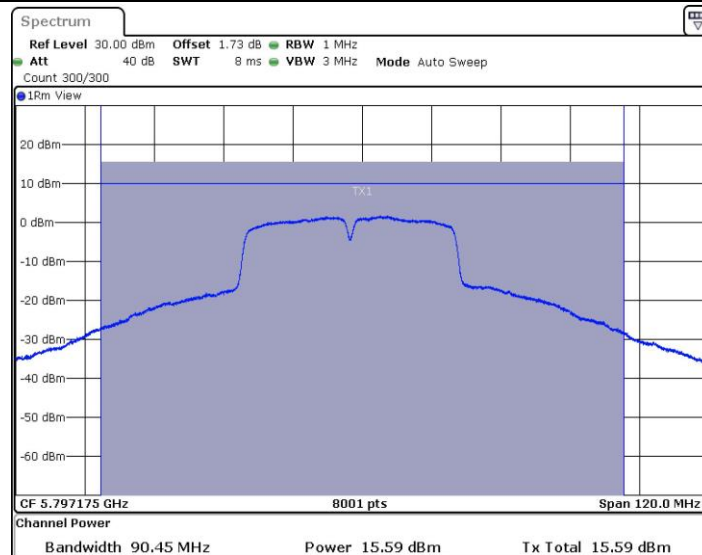
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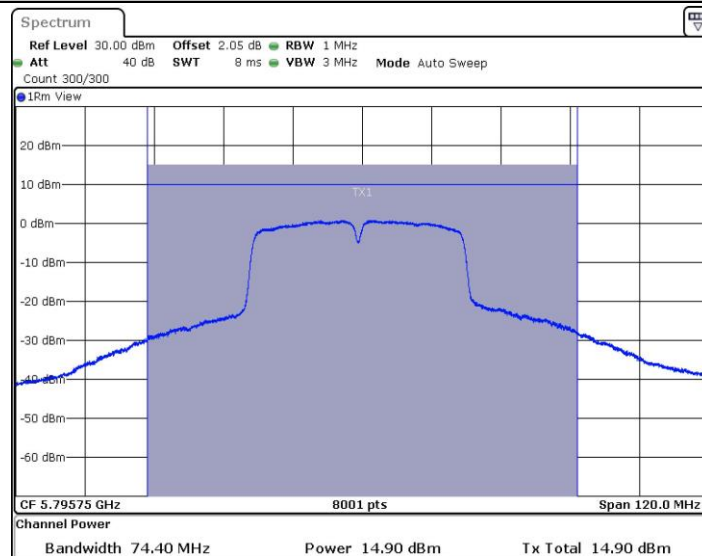
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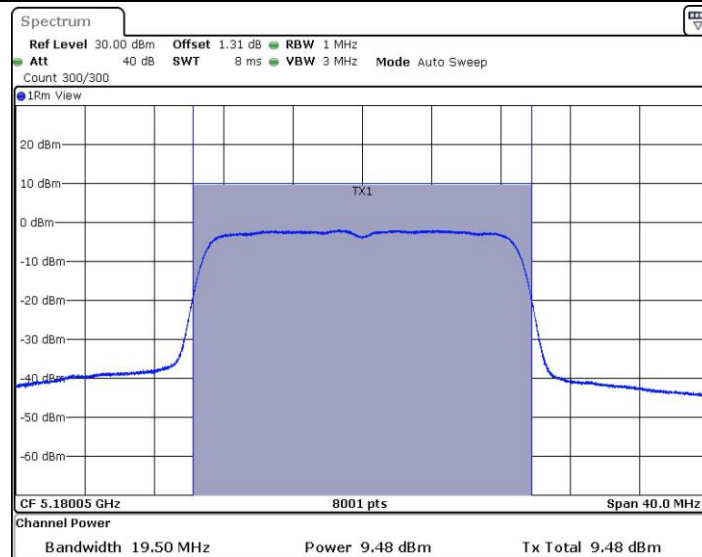
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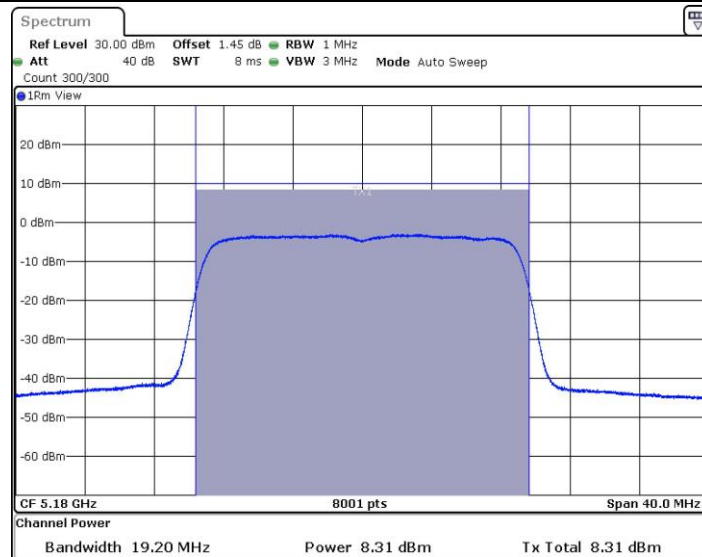
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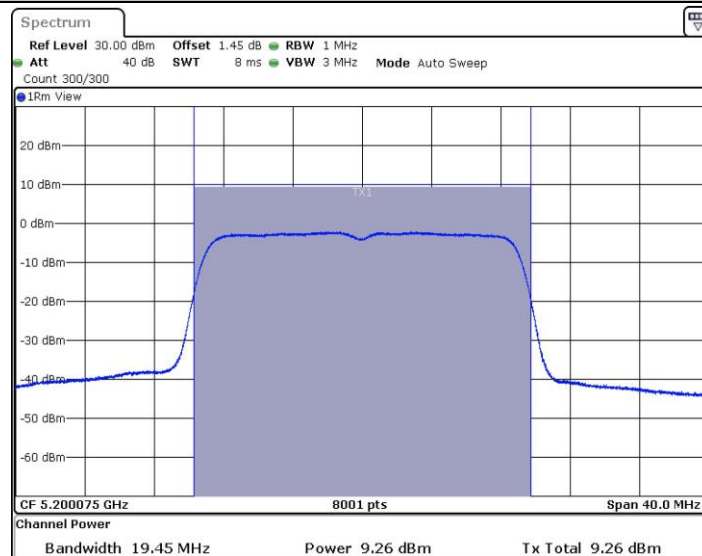
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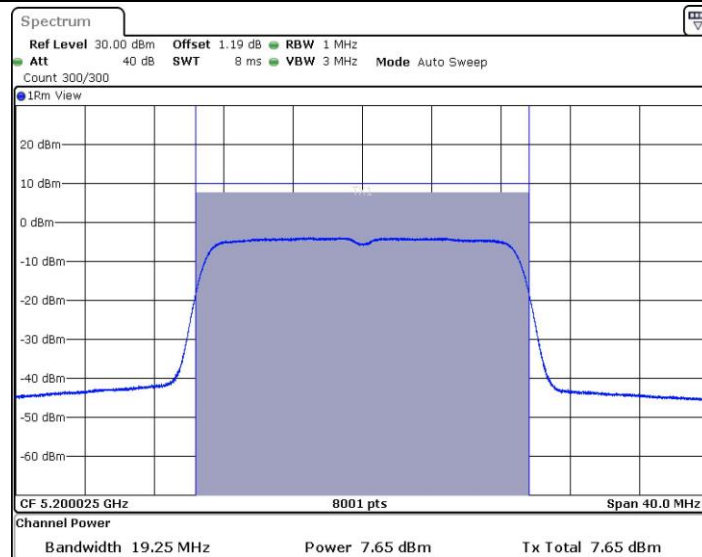
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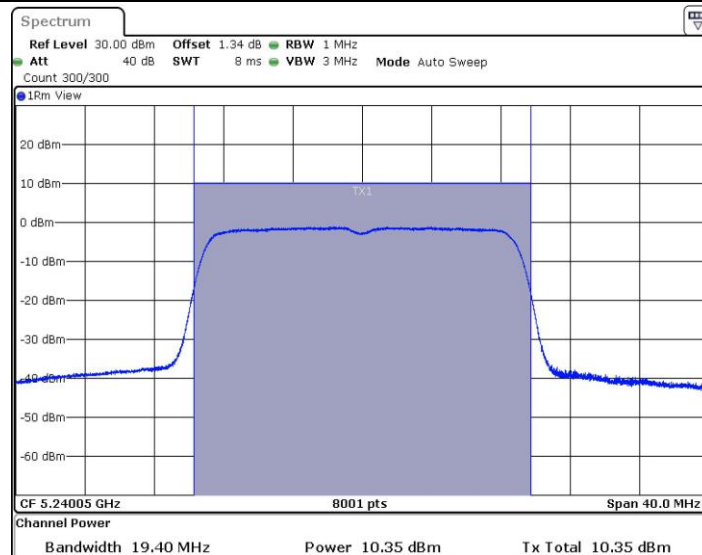
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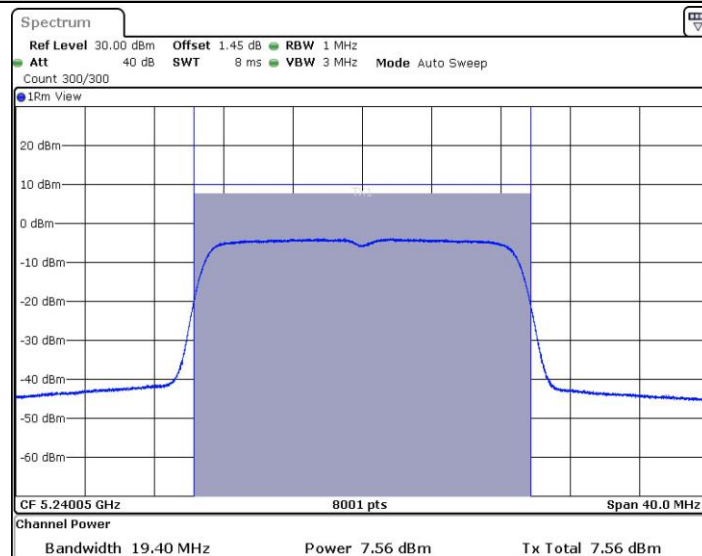
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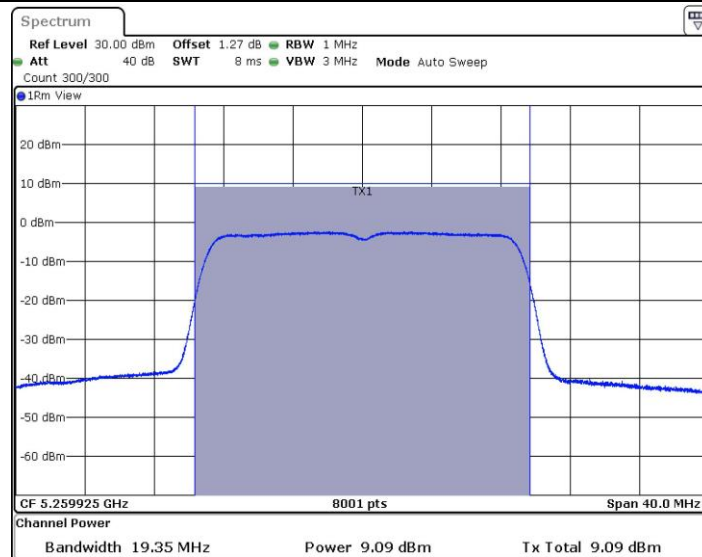
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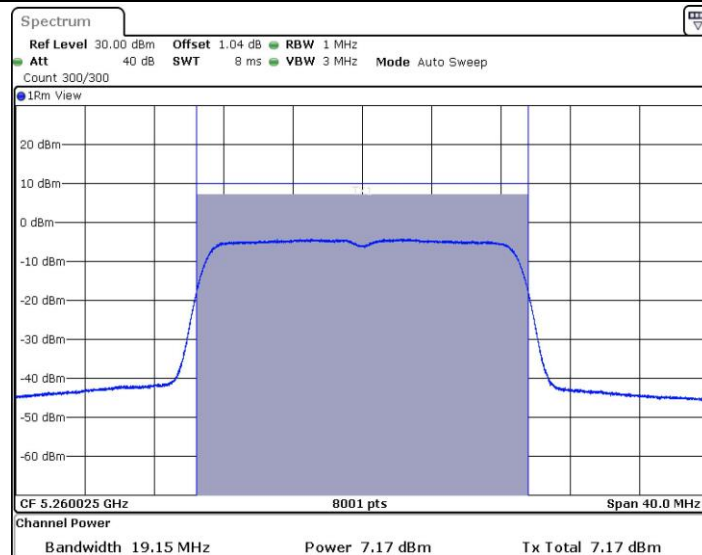
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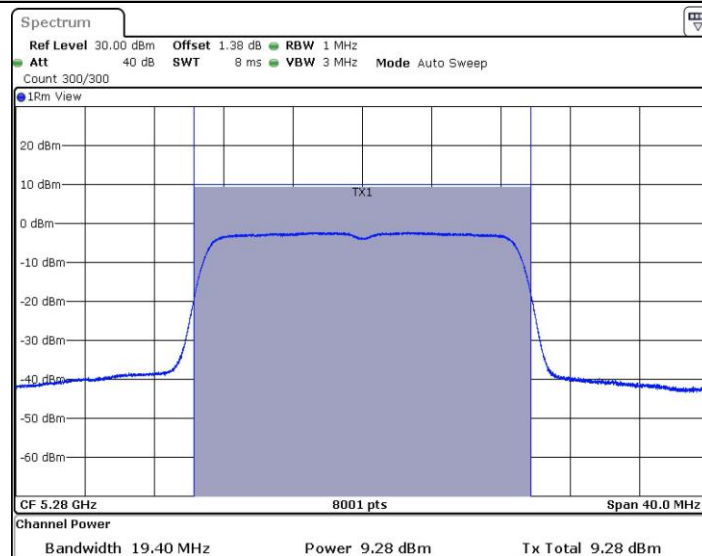
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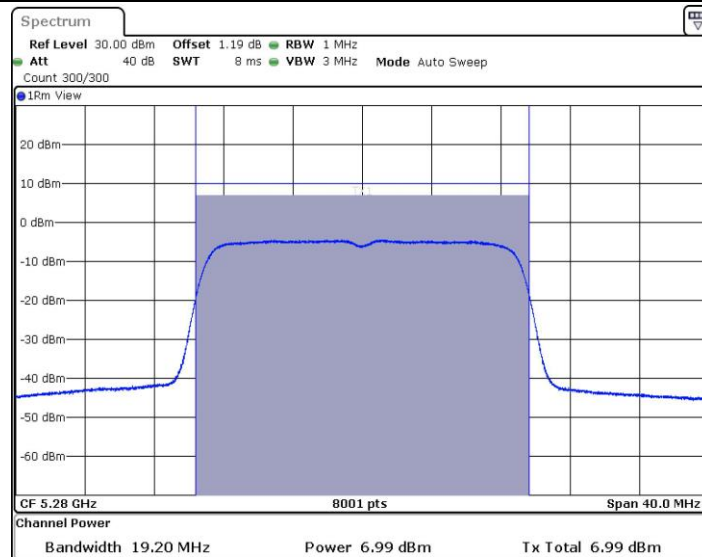
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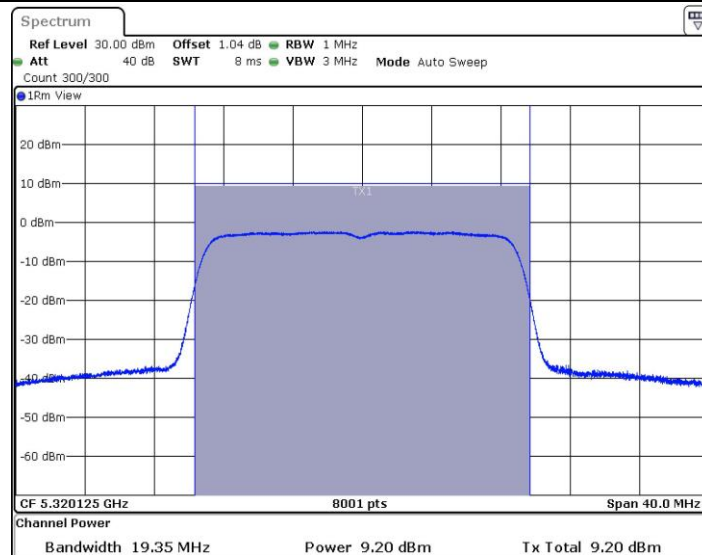
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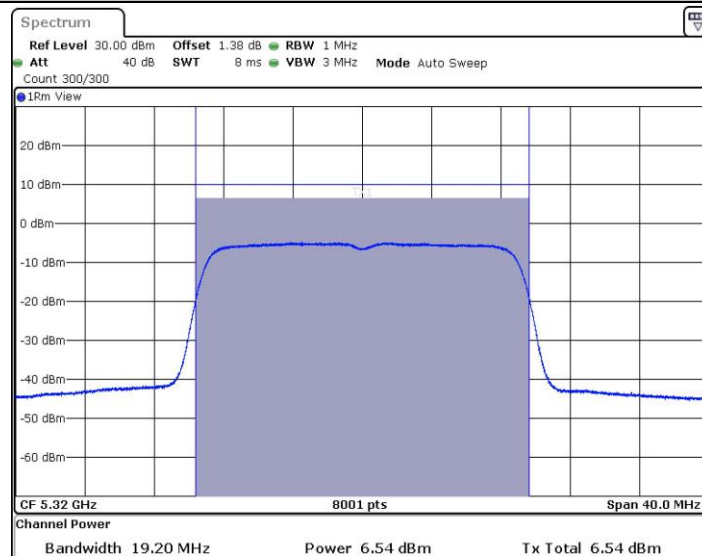
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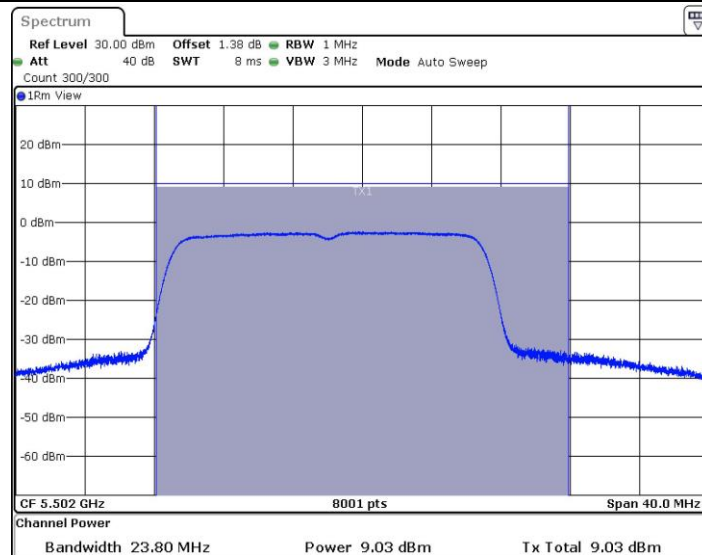
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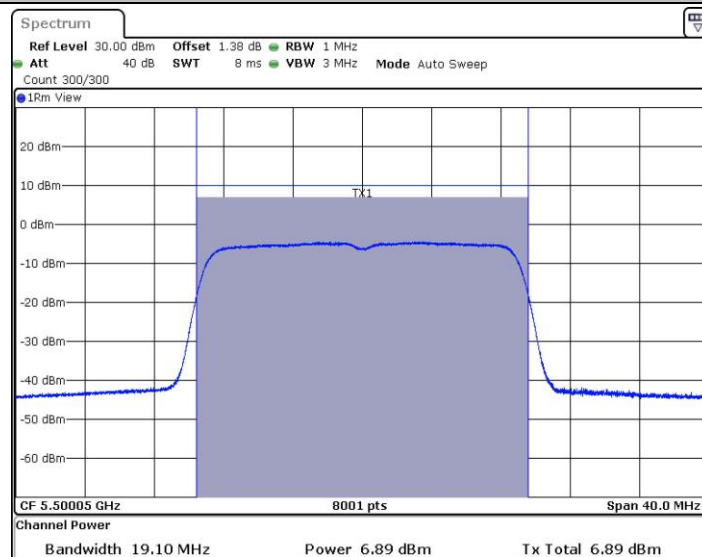
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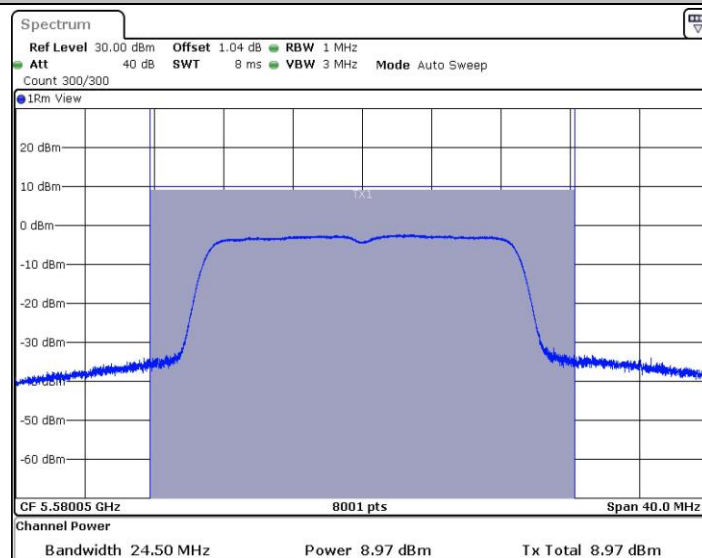
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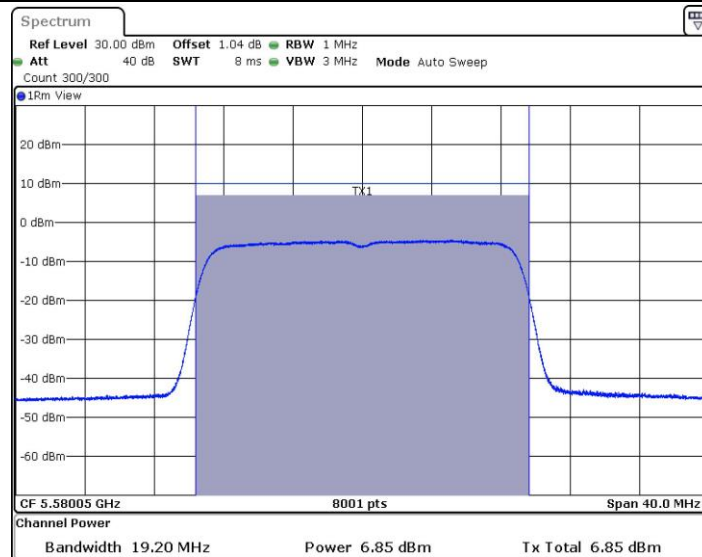
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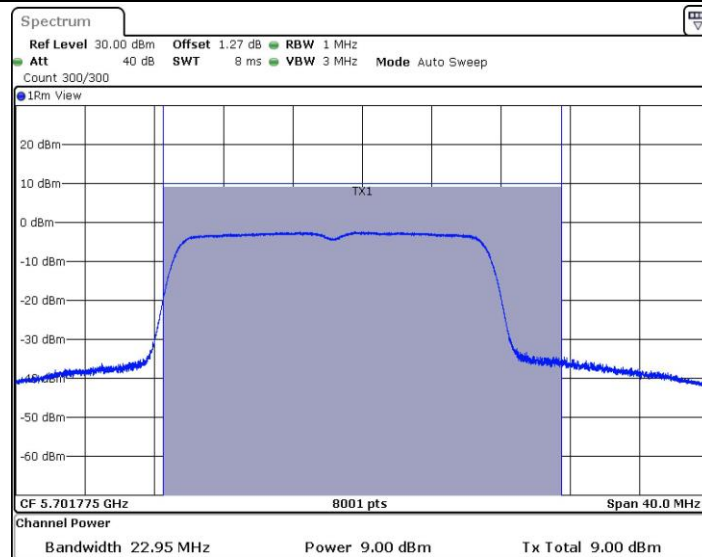
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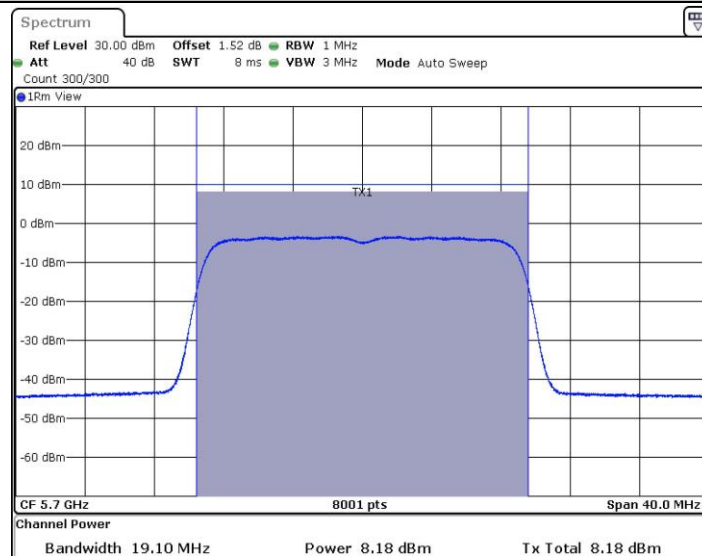
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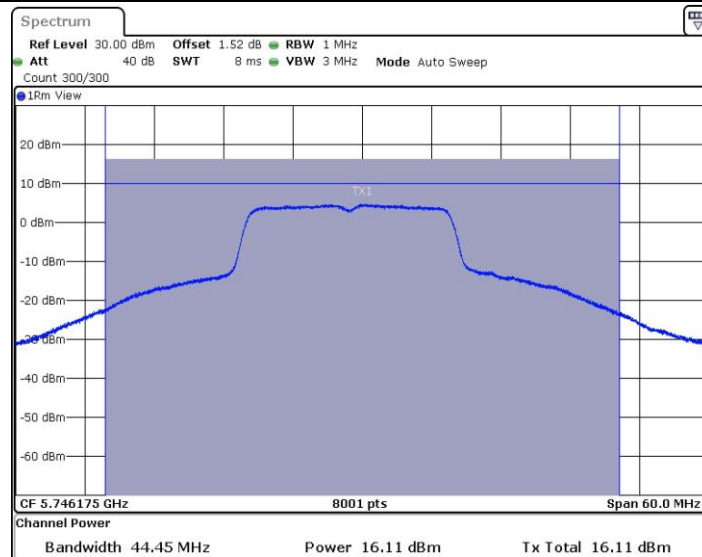
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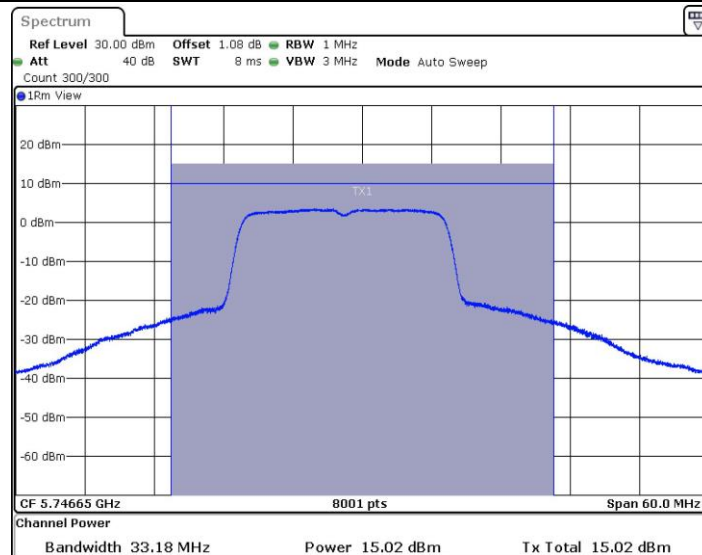
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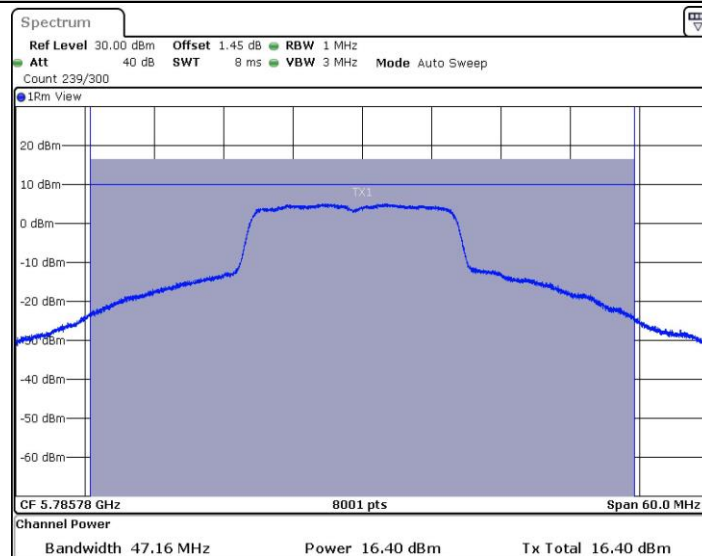
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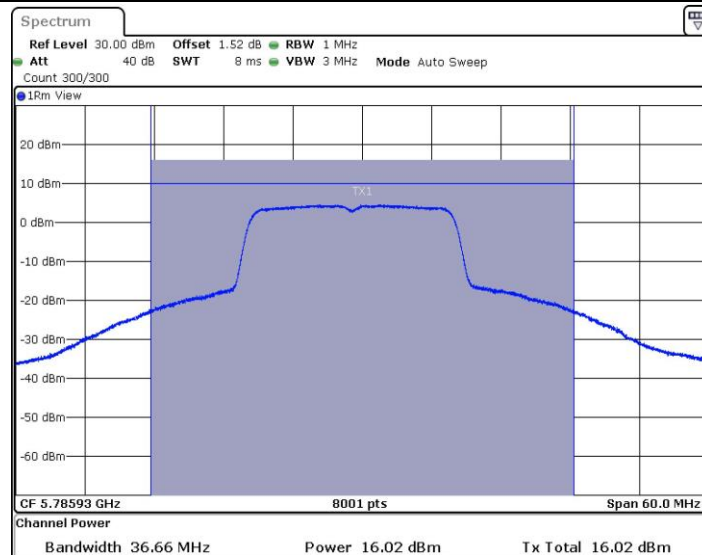
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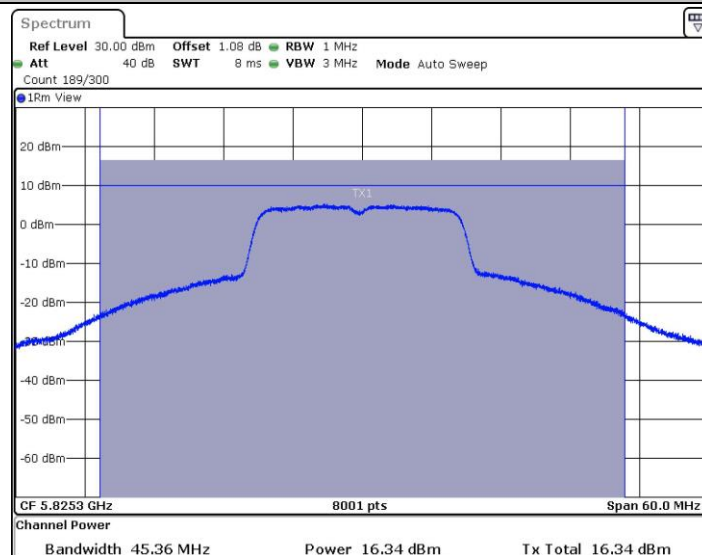
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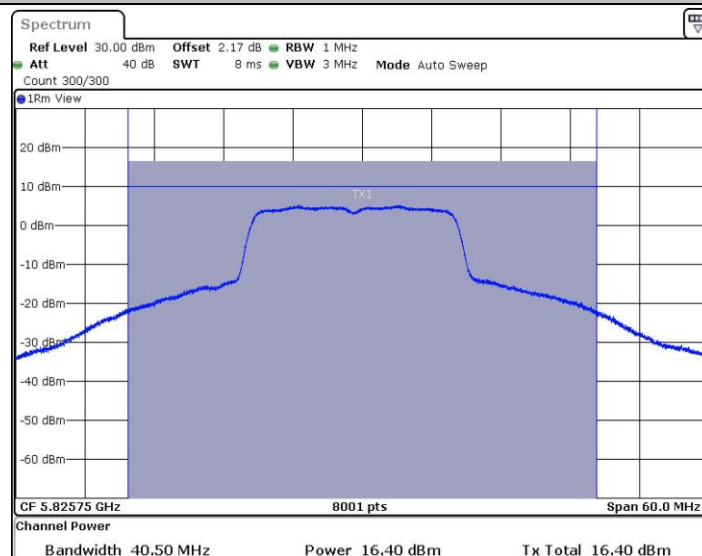
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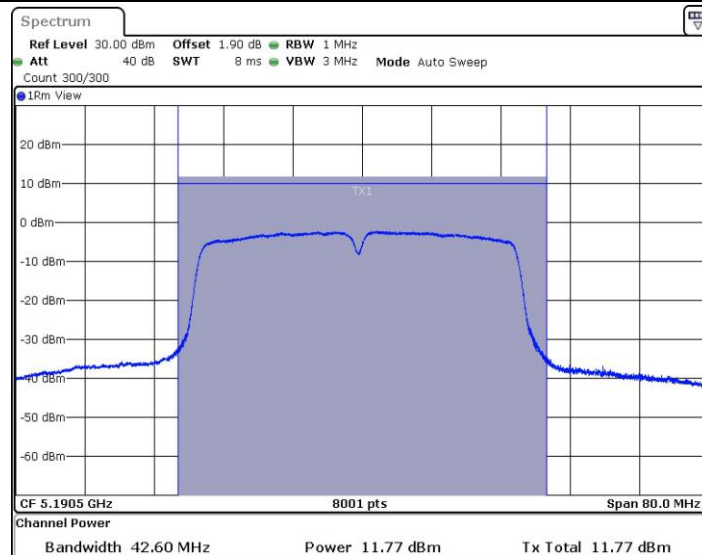
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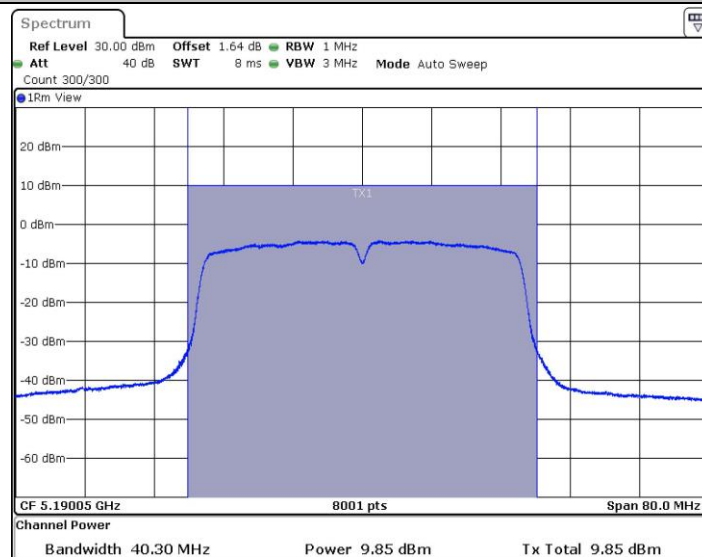
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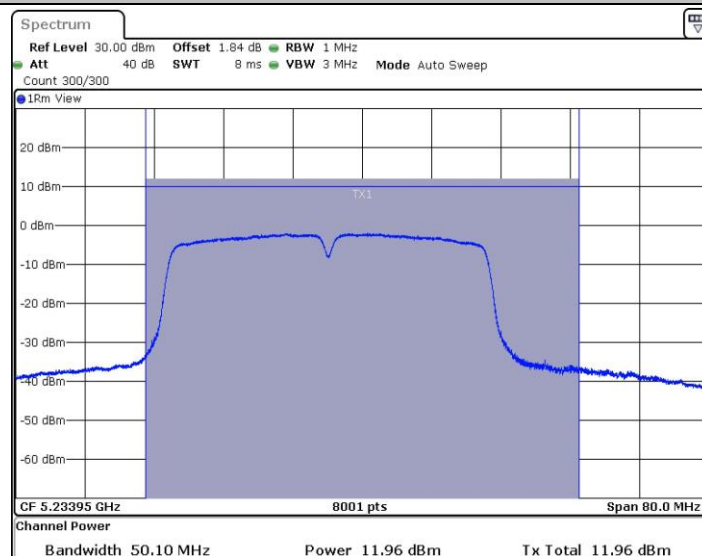
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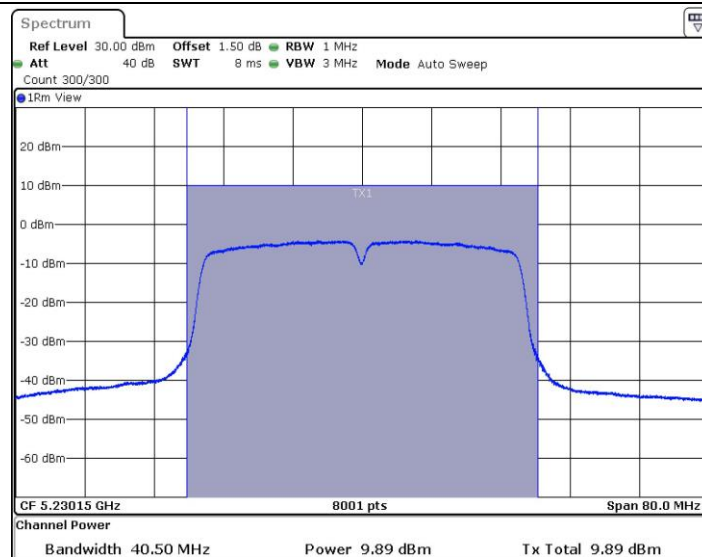
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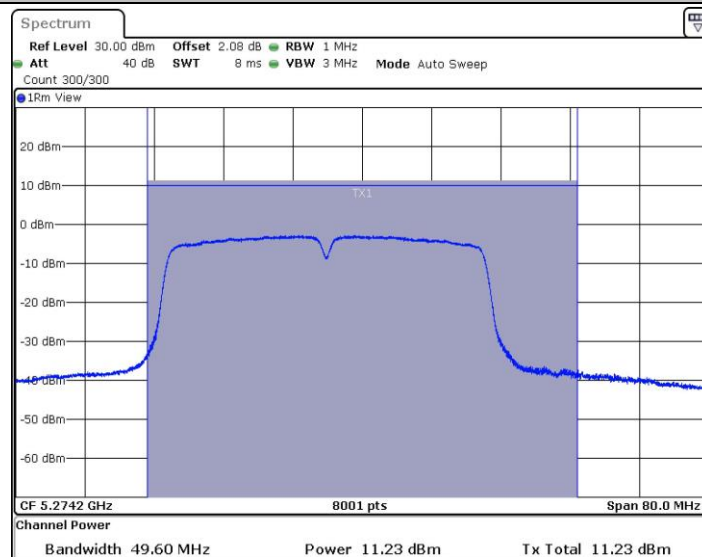
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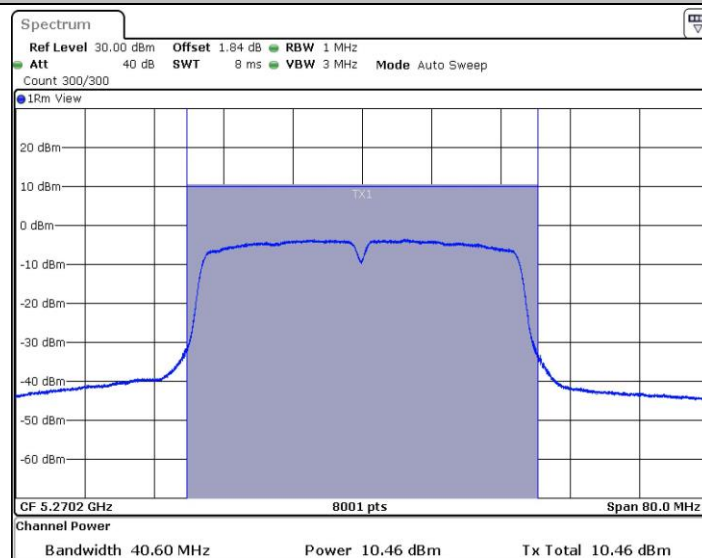
11AC40MIMO_Ant2_5230



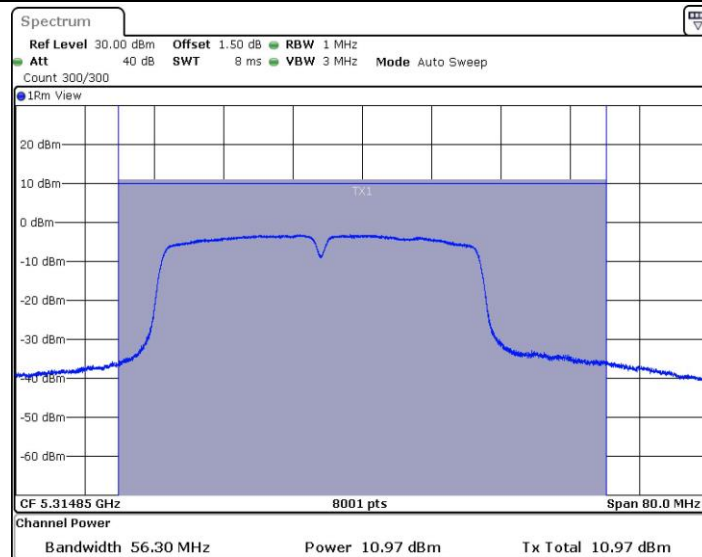
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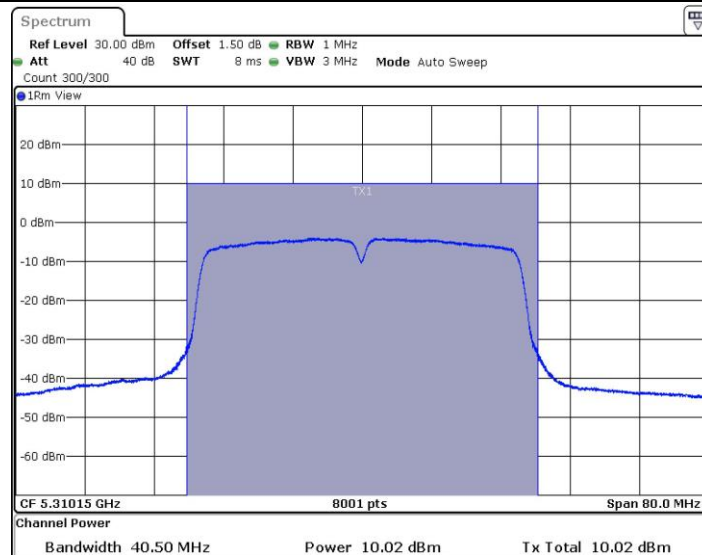
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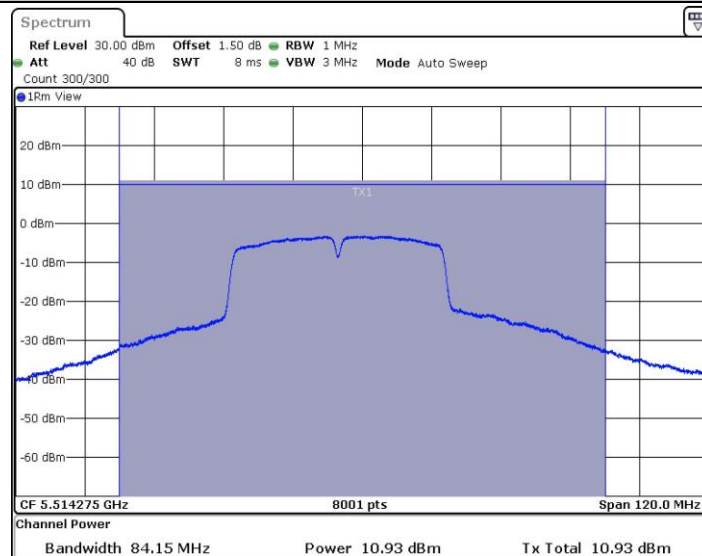
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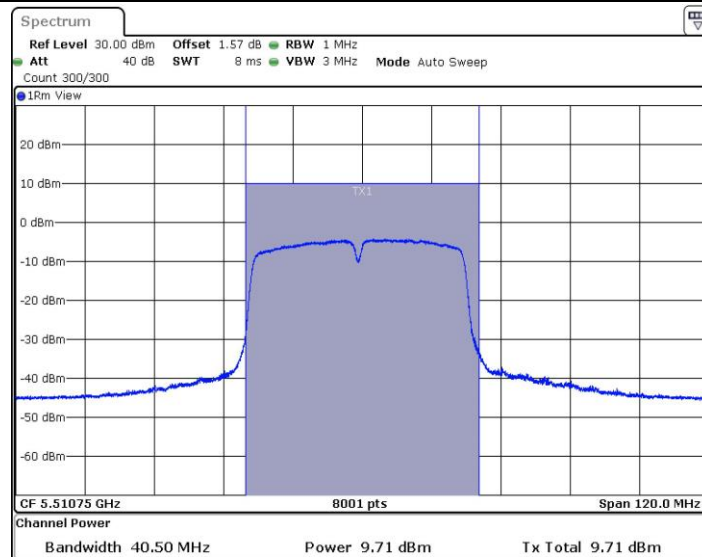
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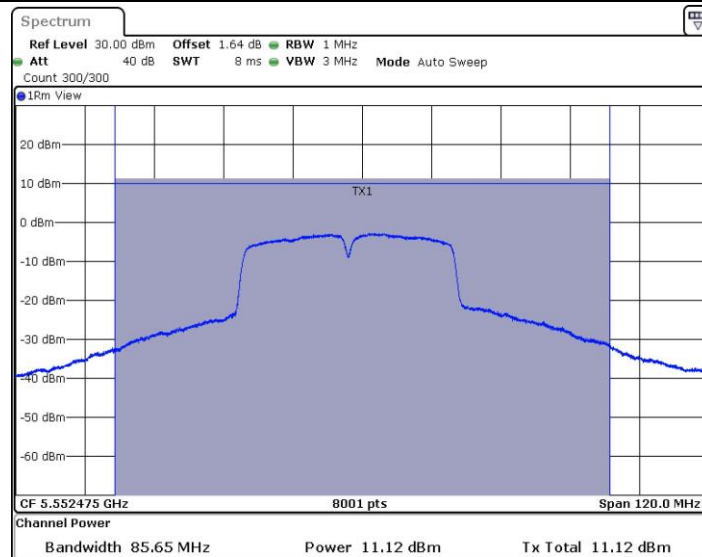
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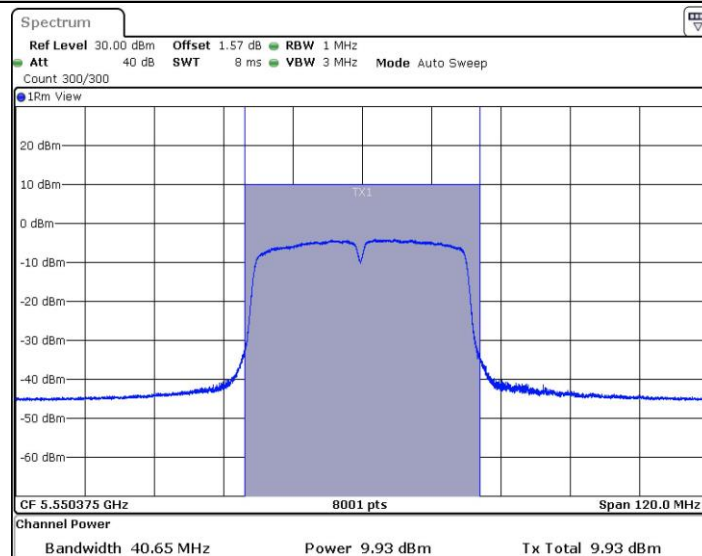
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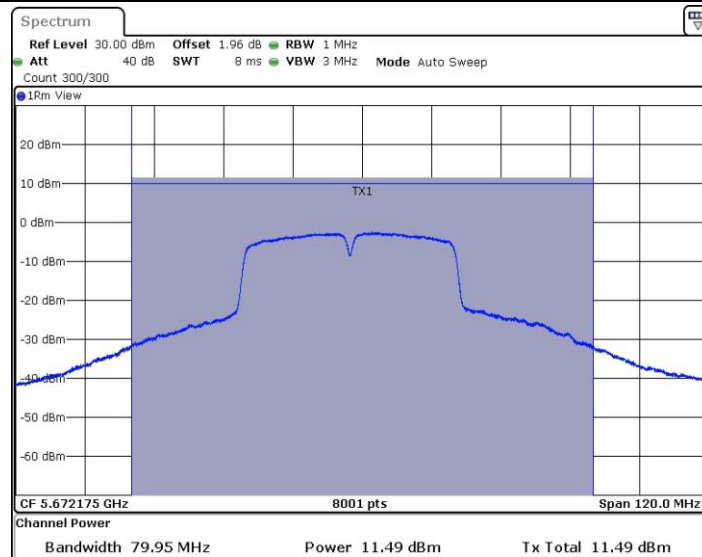
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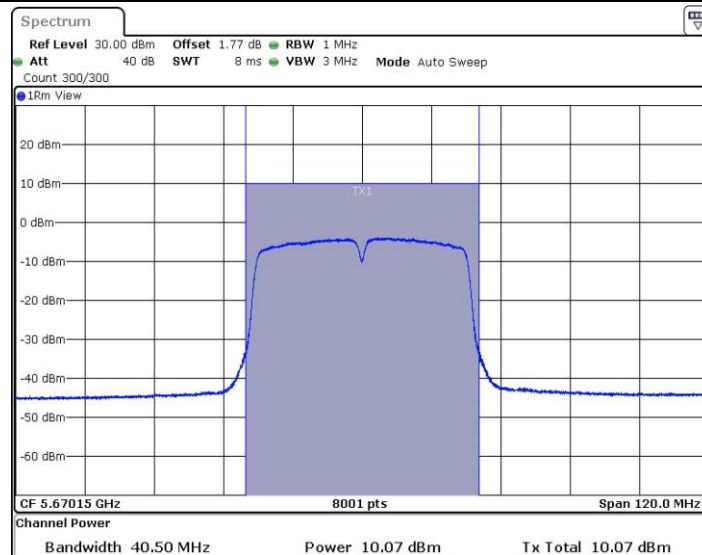
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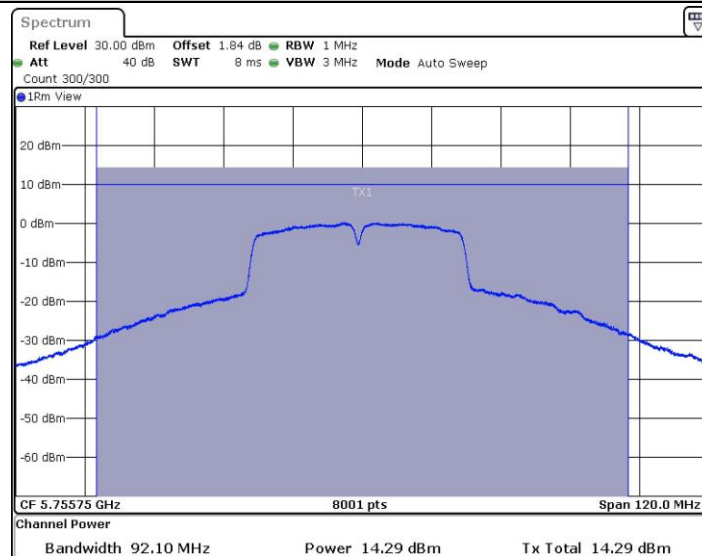
11AC40MIMO_Ant1_5550



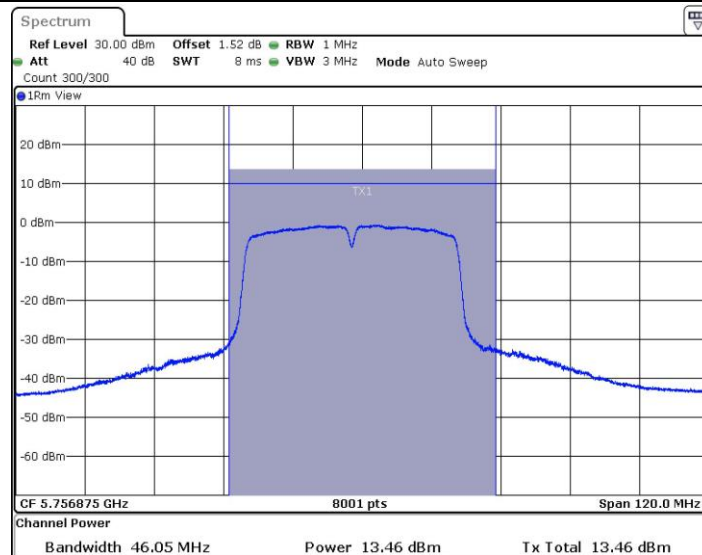
11AC40MIMO_Ant2_5670



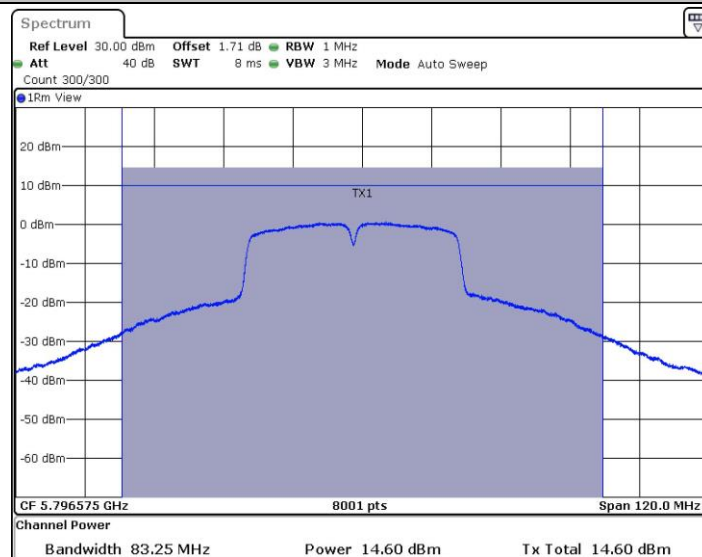
11AC40MIMO_Ant1_5670



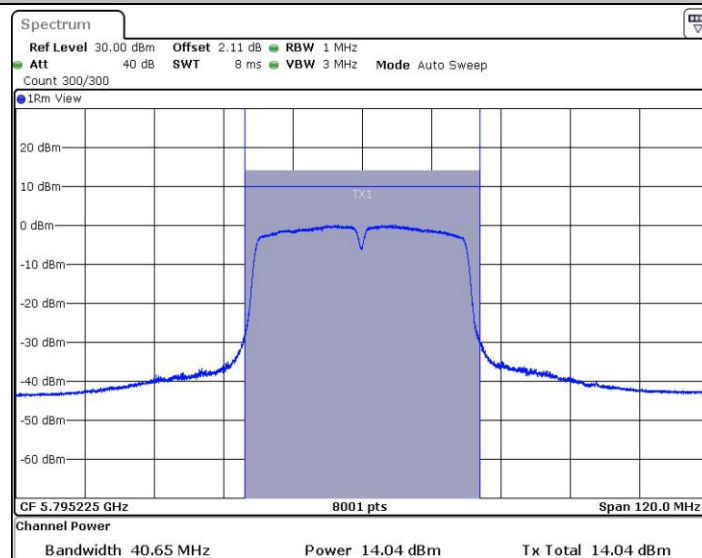
11AC40MIMO_Ant2_5755



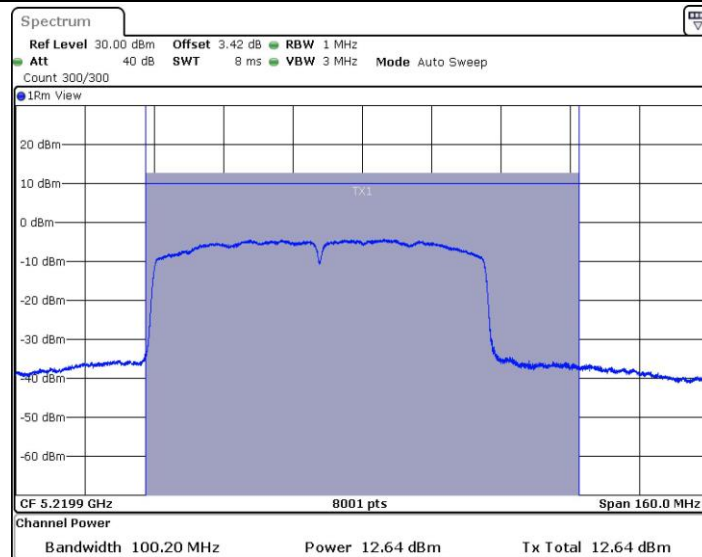
11AC40MIMO_Ant1_5755



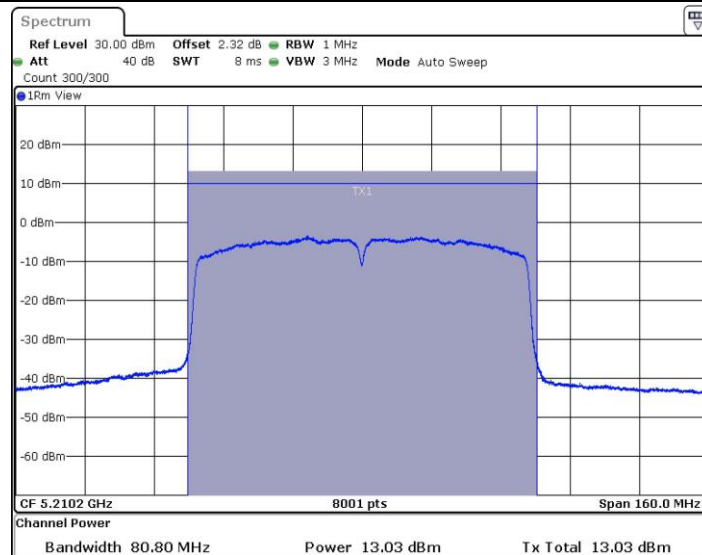
11AC40MIMO_Ant2_5795



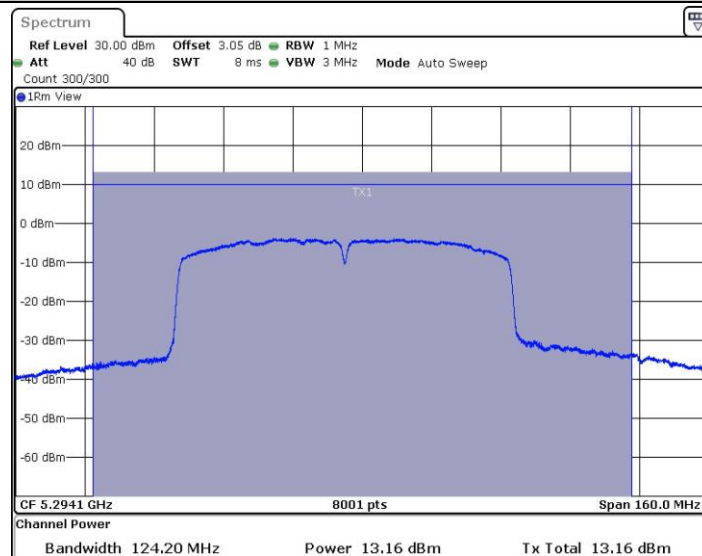
11AC40MIMO_Ant1_5795



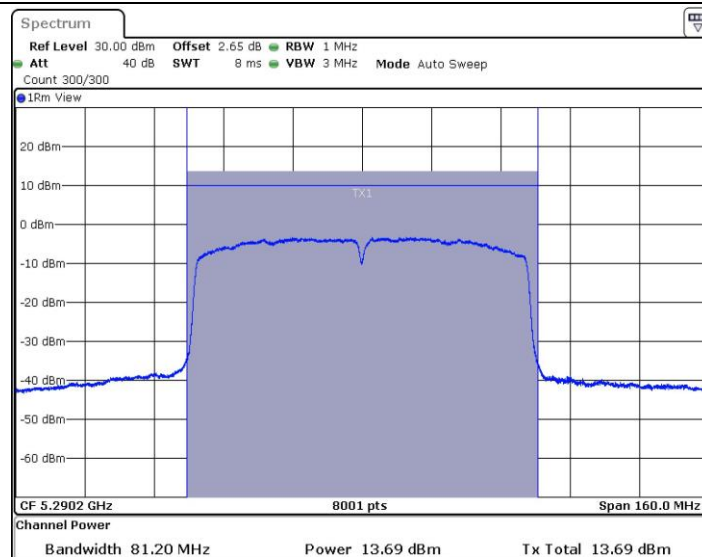
11AC80MIMO_Ant2_5210



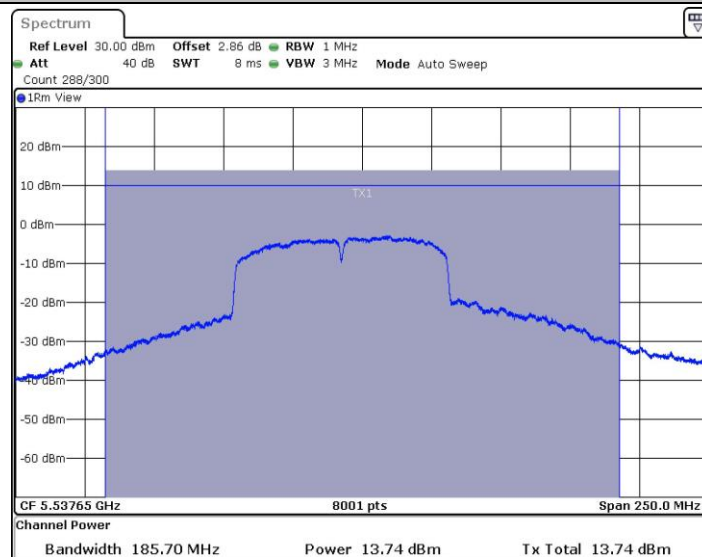
11AC80MIMO_Ant1_5210



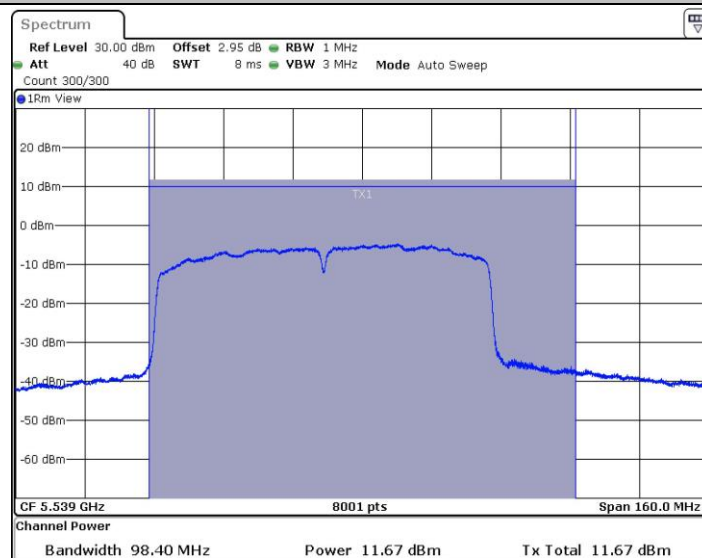
11AC80MIMO_Ant2_5290



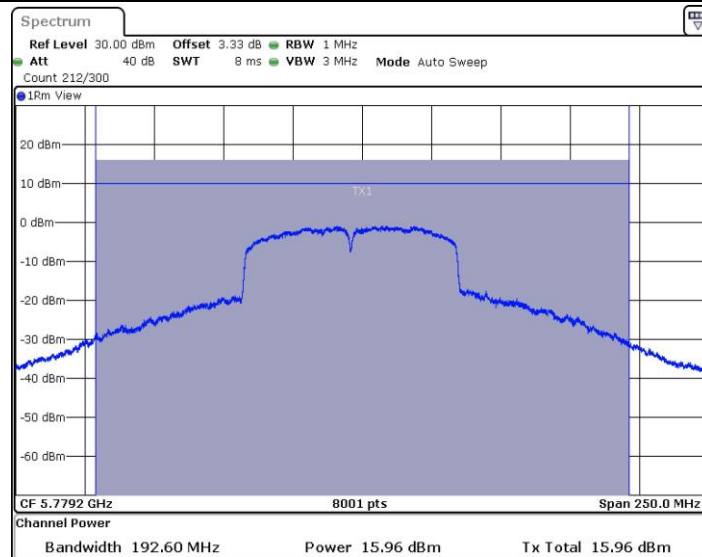
11AC80MIMO_Ant1_5290



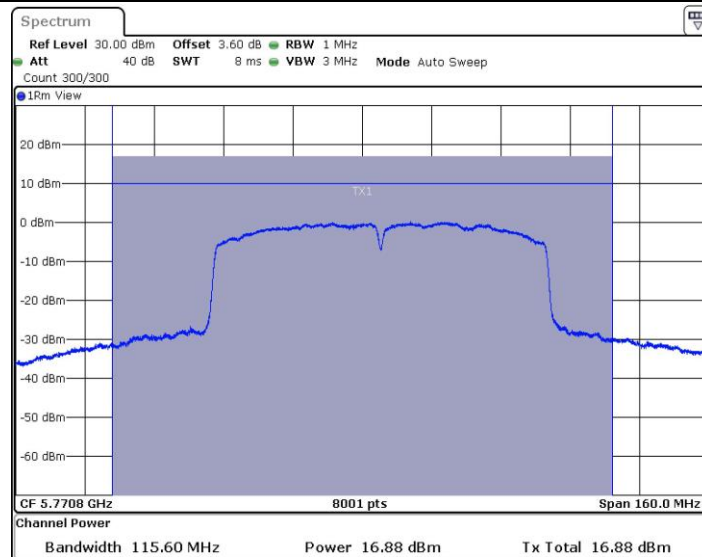
11AC80MIMO_Ant2_5530



11AC80MIMO_Ant1_5530



11AC80MIMO_Ant2_5775



11AC80MIMO_Ant1_5775

Appendix B2: Maximum EIRP

Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant2	5180	17.95	<=36	PASS
	Ant1	5180	17.76	<=36	PASS
	Ant2	5200	17.96	<=36	PASS
	Ant1	5200	17.86	<=36	PASS
	Ant2	5240	18.19	<=36	PASS
	Ant1	5240	17.90	<=36	PASS
	Ant2	5260	17.55	<=30	PASS
	Ant1	5260	17.08	<=30	PASS
	Ant2	5280	17.55	<=30	PASS
	Ant1	5280	17.14	<=30	PASS
	Ant2	5320	17.46	<=30	PASS
	Ant1	5320	17.04	<=30	PASS
	Ant2	5500	15.86	<=30	PASS
	Ant1	5500	16.43	<=30	PASS
	Ant2	5580	16.63	<=30	PASS
	Ant1	5580	17.89	<=30	PASS
	Ant2	5700	17.83	<=30	PASS
	Ant1	5700	18.71	<=30	PASS
	Ant2	5745	19.66	<=36	PASS
	Ant1	5745	21.26	<=36	PASS
	Ant2	5785	20.36	<=36	PASS
	Ant1	5785	21.45	<=36	PASS
	Ant2	5825	21.51	<=36	PASS
	Ant1	5825	21.30	<=36	PASS
11N20SISO	Ant2	5180	13.13	<=36	PASS
	Ant1	5180	13.86	<=36	PASS
	Ant2	5200	13.49	<=36	PASS
	Ant1	5200	14.31	<=36	PASS
	Ant2	5240	13.80	<=36	PASS
	Ant1	5240	14.55	<=36	PASS
	Ant2	5260	12.90	<=30	PASS
	Ant1	5260	13.87	<=30	PASS
	Ant2	5280	13.19	<=30	PASS
	Ant1	5280	13.72	<=30	PASS
	Ant2	5320	12.91	<=30	PASS
	Ant1	5320	13.55	<=30	PASS
	Ant2	5500	13.86	<=30	PASS
	Ant1	5500	14.03	<=30	PASS

	Ant2	5580	14.49	<=30	PASS
	Ant1	5580	14.24	<=30	PASS
	Ant2	5700	13.32	<=30	PASS
	Ant1	5700	13.93	<=30	PASS
	Ant2	5745	18.01	<=36	PASS
	Ant1	5745	19.15	<=36	PASS
	Ant2	5785	18.79	<=36	PASS
	Ant1	5785	19.53	<=36	PASS
	Ant2	5825	19.70	<=36	PASS
	Ant1	5825	19.28	<=36	PASS
11N40SISO	Ant2	5190	16.67	<=36	PASS
	Ant1	5190	16.02	<=36	PASS
	Ant2	5230	17.08	<=36	PASS
	Ant1	5230	16.35	<=36	PASS
	Ant2	5270	16.21	<=30	PASS
	Ant1	5270	16.83	<=30	PASS
	Ant2	5310	15.84	<=30	PASS
	Ant1	5310	16.35	<=30	PASS
	Ant2	5510	15.59	<=30	PASS
	Ant1	5510	15.15	<=30	PASS
	Ant2	5550	16.53	<=30	PASS
	Ant1	5550	16.44	<=30	PASS
	Ant2	5670	16.71	<=30	PASS
	Ant1	5670	16.06	<=30	PASS
	Ant2	5755	20.36	<=36	PASS
	Ant1	5755	19.86	<=36	PASS
	Ant2	5795	20.82	<=36	PASS
	Ant1	5795	20.23	<=36	PASS
11AC20SISO	Ant2	5180	14.24	<=36	PASS
	Ant1	5180	12.63	<=36	PASS
	Ant2	5200	13.69	<=36	PASS
	Ant1	5200	12.80	<=36	PASS
	Ant2	5240	14.26	<=36	PASS
	Ant1	5240	12.54	<=36	PASS
	Ant2	5260	14.16	<=30	PASS
	Ant1	5260	12.66	<=30	PASS
	Ant2	5280	14.29	<=30	PASS
	Ant1	5280	13.39	<=30	PASS
	Ant2	5320	14.47	<=30	PASS
	Ant1	5320	14.10	<=30	PASS
	Ant2	5500	13.75	<=30	PASS
	Ant1	5500	12.05	<=30	PASS
	Ant2	5580	14.20	<=30	PASS

	Ant1	5580	12.51	<=30	PASS
	Ant2	5700	14.13	<=30	PASS
	Ant1	5700	13.45	<=30	PASS
	Ant2	5745	20.80	<=36	PASS
	Ant1	5745	20.48	<=36	PASS
	Ant2	5785	21.09	<=36	PASS
	Ant1	5785	21.12	<=36	PASS
	Ant2	5825	21.33	<=36	PASS
	Ant1	5825	21.53	<=36	PASS
11AC40SISO	Ant2	5190	15.06	<=36	PASS
	Ant1	5190	15.50	<=36	PASS
	Ant2	5230	15.07	<=36	PASS
	Ant1	5230	15.59	<=36	PASS
	Ant2	5270	15.57	<=30	PASS
	Ant1	5270	15.77	<=30	PASS
	Ant2	5310	16.35	<=30	PASS
	Ant1	5310	16.08	<=30	PASS
	Ant2	5510	16.43	<=30	PASS
	Ant1	5510	15.47	<=30	PASS
	Ant2	5550	16.16	<=30	PASS
	Ant1	5550	15.64	<=30	PASS
	Ant2	5670	15.86	<=30	PASS
	Ant1	5670	15.73	<=30	PASS
	Ant2	5755	18.99	<=36	PASS
	Ant1	5755	19.04	<=36	PASS
	Ant2	5795	19.45	<=36	PASS
	Ant1	5795	19.26	<=36	PASS
11AC80SISO	Ant2	5210	15.88	<=36	PASS
	Ant1	5210	18.25	<=36	PASS
	Ant2	5290	17.50	<=30	PASS
	Ant1	5290	18.63	<=30	PASS
	Ant2	5530	17.76	<=30	PASS
	Ant1	5530	16.68	<=30	PASS
	Ant2	5775	19.95	<=36	PASS
	Ant1	5775	21.19	<=36	PASS
11N20MIMO	Ant2	5180	12.92	<=36	PASS
	Ant1	5180	12.95	<=36	PASS
	total	5180	18.99	<=34.81	PASS
	Ant2	5200	12.65	<=36	PASS
	Ant1	5200	13.32	<=36	PASS
	total	5200	18.99	<=34.81	PASS
	Ant2	5240	12.96	<=36	PASS
	Ant1	5240	13.26	<=36	PASS

	total	5240	19.09	<=34.81	PASS
	Ant2	5260	12.06	<=30	PASS
	Ant1	5260	12.39	<=30	PASS
	total	5260	18.29	<=28.81	PASS
	Ant2	5280	12.11	<=30	PASS
	Ant1	5280	12.44	<=30	PASS
	total	5280	18.29	<=28.81	PASS
	Ant2	5320	11.88	<=30	PASS
	Ant1	5320	12.43	<=30	PASS
	total	5320	18.19	<=28.81	PASS
	Ant2	5500	12.91	<=30	PASS
	Ant1	5500	12.94	<=30	PASS
	total	5500	18.99	<=28.81	PASS
	Ant2	5580	13.05	<=30	PASS
	Ant1	5580	12.97	<=30	PASS
	total	5580	18.99	<=28.81	PASS
	Ant2	5700	12.13	<=30	PASS
	Ant1	5700	12.56	<=30	PASS
	total	5700	18.39	<=28.81	PASS
	Ant2	5745	17.18	<=36	PASS
	Ant1	5745	17.73	<=36	PASS
	total	5745	23.49	<=34.81	PASS
	Ant2	5785	17.61	<=36	PASS
	Ant1	5785	17.99	<=36	PASS
	total	5785	23.79	<=34.81	PASS
	Ant2	5825	18.44	<=36	PASS
	Ant1	5825	18.17	<=36	PASS
	total	5825	24.29	<=34.81	PASS
11N40MIMO	Ant2	5190	16.58	<=36	PASS
	Ant1	5190	15.50	<=36	PASS
	total	5190	22.09	<=34.81	PASS
	Ant2	5230	16.04	<=36	PASS
	Ant1	5230	15.56	<=36	PASS
	total	5230	21.79	<=34.81	PASS
	Ant2	5270	15.53	<=30	PASS
	Ant1	5270	15.97	<=30	PASS
	total	5270	21.79	<=28.81	PASS
	Ant2	5310	15.19	<=30	PASS
	Ant1	5310	15.23	<=30	PASS
	total	5310	21.19	<=28.81	PASS
	Ant2	5510	15.79	<=30	PASS
	Ant1	5510	14.19	<=30	PASS
	total	5510	21.09	<=28.81	PASS

	Ant2	5550	17.21	<=30	PASS
	Ant1	5550	15.52	<=30	PASS
	total	5550	22.49	<=28.81	PASS
	Ant2	5670	15.18	<=30	PASS
	Ant1	5670	15.05	<=30	PASS
	total	5670	21.19	<=28.81	PASS
	Ant2	5755	18.87	<=36	PASS
	Ant1	5755	18.84	<=36	PASS
	total	5755	24.89	<=34.81	PASS
	Ant2	5795	19.61	<=36	PASS
	Ant1	5795	19.24	<=36	PASS
	total	5795	25.49	<=34.81	PASS
11AC20MIMO	Ant2	5180	13.50	<=36	PASS
	Ant1	5180	12.65	<=36	PASS
	total	5180	19.09	<=34.81	PASS
	Ant2	5200	13.28	<=36	PASS
	Ant1	5200	11.99	<=36	PASS
	total	5200	18.69	<=34.81	PASS
	Ant2	5240	14.36	<=36	PASS
	Ant1	5240	11.90	<=36	PASS
	total	5240	19.39	<=34.81	PASS
	Ant2	5260	13.11	<=30	PASS
	Ant1	5260	11.51	<=30	PASS
	total	5260	18.39	<=28.81	PASS
	Ant2	5280	13.30	<=30	PASS
	Ant1	5280	11.33	<=30	PASS
	total	5280	18.49	<=28.81	PASS
	Ant2	5320	13.22	<=30	PASS
	Ant1	5320	10.88	<=30	PASS
	total	5320	18.29	<=28.81	PASS
	Ant2	5500	13.05	<=30	PASS
	Ant1	5500	11.23	<=30	PASS
	total	5500	18.29	<=28.81	PASS
	Ant2	5580	12.99	<=30	PASS
	Ant1	5580	11.19	<=30	PASS
	total	5580	18.19	<=28.81	PASS
	Ant2	5700	13.02	<=30	PASS
	Ant1	5700	12.52	<=30	PASS
	total	5700	18.79	<=28.81	PASS
	Ant2	5745	20.13	<=36	PASS
	Ant1	5745	19.36	<=36	PASS
	total	5745	25.79	<=34.81	PASS
	Ant2	5785	20.42	<=36	PASS

	Ant1	5785	20.36	<=36	PASS
	total	5785	26.39	<=34.81	PASS
	Ant2	5825	20.36	<=36	PASS
	Ant1	5825	20.74	<=36	PASS
	total	5825	26.59	<=34.81	PASS
11AC40MIMO	Ant2	5190	15.79	<=36	PASS
	Ant1	5190	14.19	<=36	PASS
	total	5190	21.09	<=34.81	PASS
	Ant2	5230	15.98	<=36	PASS
	Ant1	5230	14.23	<=36	PASS
	total	5230	21.29	<=34.81	PASS
	Ant2	5270	15.25	<=30	PASS
	Ant1	5270	14.80	<=30	PASS
	total	5270	21.09	<=28.81	PASS
	Ant2	5310	14.99	<=30	PASS
	Ant1	5310	14.36	<=30	PASS
	total	5310	20.69	<=28.81	PASS
	Ant2	5510	14.95	<=30	PASS
	Ant1	5510	14.05	<=30	PASS
	total	5510	20.59	<=28.81	PASS
	Ant2	5550	15.14	<=30	PASS
	Ant1	5550	14.27	<=30	PASS
	total	5550	20.79	<=28.81	PASS
	Ant2	5670	15.51	<=30	PASS
	Ant1	5670	14.41	<=30	PASS
	total	5670	20.99	<=28.81	PASS
	Ant2	5755	18.31	<=36	PASS
	Ant1	5755	17.80	<=36	PASS
	total	5755	24.09	<=34.81	PASS
	Ant2	5795	18.62	<=36	PASS
	Ant1	5795	18.38	<=36	PASS
	total	5795	24.49	<=34.81	PASS
11AC80MIMO	Ant2	5210	16.66	<=36	PASS
	Ant1	5210	17.37	<=36	PASS
	total	5210	22.99	<=34.81	PASS
	Ant2	5290	17.18	<=24	PASS
	Ant1	5290	18.03	<=24	PASS
	total	5290	23.59	<=22.81	PASS
	Ant2	5530	17.72	<=30	PASS
	Ant1	5530	16.01	<=30	PASS
	total	5530	22.99	<=28.81	PASS
	Ant2	5775	19.98	<=36	PASS
	Ant1	5775	21.22	<=36	PASS

Total Quality. Assured.

	total	5775	26.9	≤ 34.81	PASS
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Note: The Duty Cycle Factor is compensated in the graph.

Appendix C: Maximum power spectral density

Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant2	5180	8.06	<=11	PASS
	Ant1	5180	7.43	<=11	PASS
	Ant2	5200	8.55	<=11	PASS
	Ant1	5200	7.31	<=11	PASS
	Ant2	5240	8.4	<=11	PASS
	Ant1	5240	7.76	<=11	PASS
	Ant2	5260	7.64	<=11	PASS
	Ant1	5260	6.81	<=11	PASS
	Ant2	5280	7.43	<=11	PASS
	Ant1	5280	6.82	<=11	PASS
	Ant2	5320	7.41	<=11	PASS
	Ant1	5320	6.92	<=11	PASS
	Ant2	5500	6.1	<=11	PASS
	Ant1	5500	6.13	<=11	PASS
	Ant2	5580	6.87	<=11	PASS
	Ant1	5580	8.24	<=11	PASS
	Ant2	5700	8.01	<=11	PASS
	Ant1	5700	8.67	<=11	PASS
	Ant2	5745	8.89	<=30	PASS
	Ant1	5745	9.45	<=30	PASS
	Ant2	5785	9.26	<=30	PASS
	Ant1	5785	9.86	<=30	PASS
	Ant2	5825	10.45	<=30	PASS
	Ant1	5825	9.62	<=30	PASS
11N20SISO	Ant2	5180	3.41	<=11	PASS
	Ant1	5180	3.67	<=11	PASS
	Ant2	5200	3.31	<=11	PASS
	Ant1	5200	3.52	<=11	PASS
	Ant2	5240	3.76	<=11	PASS
	Ant1	5240	3.92	<=11	PASS
	Ant2	5260	2.69	<=11	PASS
	Ant1	5260	3.2	<=11	PASS
	Ant2	5280	3.2	<=11	PASS
	Ant1	5280	3	<=11	PASS
	Ant2	5320	3.09	<=11	PASS
	Ant1	5320	2.86	<=11	PASS
	Ant2	5500	3.98	<=11	PASS

	Ant1	5500	3.77	<=11	PASS
	Ant2	5580	4.75	<=11	PASS
	Ant1	5580	4.02	<=11	PASS
	Ant2	5700	3.53	<=11	PASS
	Ant1	5700	3.96	<=11	PASS
	Ant2	5745	6.54	<=30	PASS
	Ant1	5745	7.53	<=30	PASS
	Ant2	5785	7.08	<=30	PASS
	Ant1	5785	8.02	<=30	PASS
	Ant2	5825	8.8	<=30	PASS
	Ant1	5825	7.86	<=30	PASS
11N40SISO	Ant2	5190	3.81	<=11	PASS
	Ant1	5190	3.26	<=11	PASS
	Ant2	5230	4.14	<=11	PASS
	Ant1	5230	3.4	<=11	PASS
	Ant2	5270	3.77	<=11	PASS
	Ant1	5270	3.61	<=11	PASS
	Ant2	5310	3.79	<=11	PASS
	Ant1	5310	3.36	<=11	PASS
	Ant2	5510	3.11	<=11	PASS
	Ant1	5510	2.66	<=11	PASS
	Ant2	5550	4.23	<=11	PASS
	Ant1	5550	3.83	<=11	PASS
	Ant2	5670	4.15	<=11	PASS
	Ant1	5670	3.08	<=11	PASS
	Ant2	5755	6.37	<=30	PASS
	Ant1	5755	5.87	<=30	PASS
	Ant2	5795	7.63	<=30	PASS
	Ant1	5795	5.69	<=30	PASS
11AC20SISO	Ant2	5180	3.62	<=11	PASS
	Ant1	5180	2.61	<=11	PASS
	Ant2	5200	3.14	<=11	PASS
	Ant1	5200	2.23	<=11	PASS
	Ant2	5240	4.73	<=11	PASS
	Ant1	5240	2.36	<=11	PASS
	Ant2	5260	4.01	<=11	PASS
	Ant1	5260	2.12	<=11	PASS
	Ant2	5280	4.89	<=11	PASS
	Ant1	5280	2.98	<=11	PASS
	Ant2	5320	4.14	<=11	PASS
	Ant1	5320	3.89	<=11	PASS
	Ant2	5500	4.15	<=11	PASS
	Ant1	5500	2.49	<=11	PASS

	Ant2	5580	4.51	<=11	PASS
	Ant1	5580	2.02	<=11	PASS
	Ant2	5700	3.94	<=11	PASS
	Ant1	5700	3.32	<=11	PASS
	Ant2	5745	9.34	<=30	PASS
	Ant1	5745	8.92	<=30	PASS
	Ant2	5785	9.76	<=30	PASS
	Ant1	5785	9.9	<=30	PASS
	Ant2	5825	9.95	<=30	PASS
	Ant1	5825	9.6	<=30	PASS
11AC40SISO	Ant2	5190	4.57	<=11	PASS
	Ant1	5190	2.95	<=11	PASS
	Ant2	5230	4.69	<=11	PASS
	Ant1	5230	2.58	<=11	PASS
	Ant2	5270	3.5	<=11	PASS
	Ant1	5270	2.55	<=11	PASS
	Ant2	5310	3.71	<=11	PASS
	Ant1	5310	3.04	<=11	PASS
	Ant2	5510	4.14	<=11	PASS
	Ant1	5510	2.88	<=11	PASS
	Ant2	5550	3.4	<=11	PASS
	Ant1	5550	2.66	<=11	PASS
	Ant2	5670	4.13	<=11	PASS
	Ant1	5670	3.03	<=11	PASS
	Ant2	5755	5.7	<=30	PASS
	Ant1	5755	4.73	<=30	PASS
	Ant2	5795	5.39	<=30	PASS
	Ant1	5795	4.91	<=30	PASS
11AC80SISO	Ant2	5210	0.69	<=11	PASS
	Ant1	5210	2.55	<=11	PASS
	Ant2	5290	2.3	<=11	PASS
	Ant1	5290	3.34	<=11	PASS
	Ant2	5530	2.63	<=11	PASS
	Ant1	5530	1.47	<=11	PASS
	Ant2	5775	3.79	<=30	PASS
	Ant1	5775	4.23	<=30	PASS
11N20MIMO	Ant2	5180	4.05	<=11	PASS
	Ant1	5180	4.63	<=11	PASS
	total	5180	7.36	<=9.81	PASS
	Ant2	5200	3.72	<=11	PASS
	Ant1	5200	4.62	<=11	PASS
	total	5200	7.20	<=9.81	PASS
	Ant2	5240	3.97	<=11	PASS

	Ant1	5240	4.75	<=11	PASS
	total	5240	7.39	<=9.81	PASS
	Ant2	5260	3.34	<=11	PASS
	Ant1	5260	3.81	<=11	PASS
	total	5260	6.59	<=9.81	PASS
	Ant2	5280	3.4	<=11	PASS
	Ant1	5280	3.72	<=11	PASS
	total	5280	6.57	<=9.81	PASS
	Ant2	5320	3.29	<=11	PASS
	Ant1	5320	3.66	<=11	PASS
	total	5320	6.49	<=9.81	PASS
	Ant2	5500	4.12	<=11	PASS
	Ant1	5500	4.09	<=11	PASS
	total	5500	7.12	<=9.81	PASS
	Ant2	5580	4.33	<=11	PASS
	Ant1	5580	4.85	<=11	PASS
	total	5580	7.61	<=9.81	PASS
	Ant2	5700	3.79	<=11	PASS
	Ant1	5700	4.08	<=11	PASS
	total	5700	6.95	<=9.81	PASS
	Ant2	5745	6.78	<=30	PASS
	Ant1	5745	7.45	<=30	PASS
	total	5745	10.14	<=28.81	PASS
	Ant2	5785	6.92	<=30	PASS
	Ant1	5785	7.94	<=30	PASS
	total	5785	10.47	<=28.81	PASS
	Ant2	5825	8	<=30	PASS
	Ant1	5825	8.37	<=30	PASS
	total	5825	11.20	<=28.81	PASS
11N40MIMO	Ant2	5190	3.82	<=11	PASS
	Ant1	5190	5.01	<=11	PASS
	total	5190	7.47	<=9.81	PASS
	Ant2	5230	3.36	<=11	PASS
	Ant1	5230	4.49	<=11	PASS
	total	5230	6.97	<=9.81	PASS
	Ant2	5270	2.91	<=11	PASS
	Ant1	5270	4.89	<=11	PASS
	total	5270	7.02	<=9.81	PASS
	Ant2	5310	3.06	<=11	PASS
	Ant1	5310	4.68	<=11	PASS
	total	5310	6.96	<=9.81	PASS
	Ant2	5510	3.91	<=11	PASS
	Ant1	5510	3.1	<=11	PASS

	total	5510	6.53	<=9.81	PASS
	Ant2	5550	4.83	<=11	PASS
	Ant1	5550	4.49	<=11	PASS
	total	5550	7.67	<=9.81	PASS
	Ant2	5670	3.63	<=11	PASS
	Ant1	5670	3.94	<=11	PASS
	total	5670	6.80	<=9.81	PASS
	Ant2	5755	5.47	<=30	PASS
	Ant1	5755	6.17	<=30	PASS
	total	5755	8.84	<=28.81	PASS
	Ant2	5795	6.28	<=30	PASS
	Ant1	5795	6.46	<=30	PASS
11AC20MIMO	total	5795	9.38	<=28.81	PASS
	Ant2	5180	3.76	<=11	PASS
	Ant1	5180	4.57	<=11	PASS
	total	5180	7.19	<=9.81	PASS
	Ant2	5200	3.65	<=11	PASS
	Ant1	5200	3.05	<=11	PASS
	total	5200	6.37	<=9.81	PASS
	Ant2	5240	4.14	<=11	PASS
	Ant1	5240	3.46	<=11	PASS
	total	5240	6.82	<=9.81	PASS
	Ant2	5260	3.57	<=11	PASS
	Ant1	5260	3.03	<=11	PASS
	total	5260	6.32	<=9.81	PASS
	Ant2	5280	3.95	<=11	PASS
	Ant1	5280	2.64	<=11	PASS
	total	5280	6.35	<=9.81	PASS
	Ant2	5320	3.62	<=11	PASS
	Ant1	5320	2.7	<=11	PASS
	total	5320	6.19	<=9.81	PASS
	Ant2	5500	3.56	<=11	PASS
	Ant1	5500	2.73	<=11	PASS
	total	5500	6.18	<=9.81	PASS
	Ant2	5580	3.81	<=11	PASS
	Ant1	5580	3.56	<=11	PASS
	total	5580	6.70	<=9.81	PASS
	Ant2	5700	3.34	<=11	PASS
	Ant1	5700	4.38	<=11	PASS
	total	5700	6.90	<=9.81	PASS
	Ant2	5745	9.13	<=30	PASS
	Ant1	5745	9.74	<=30	PASS
	total	5745	12.46	<=28.81	PASS

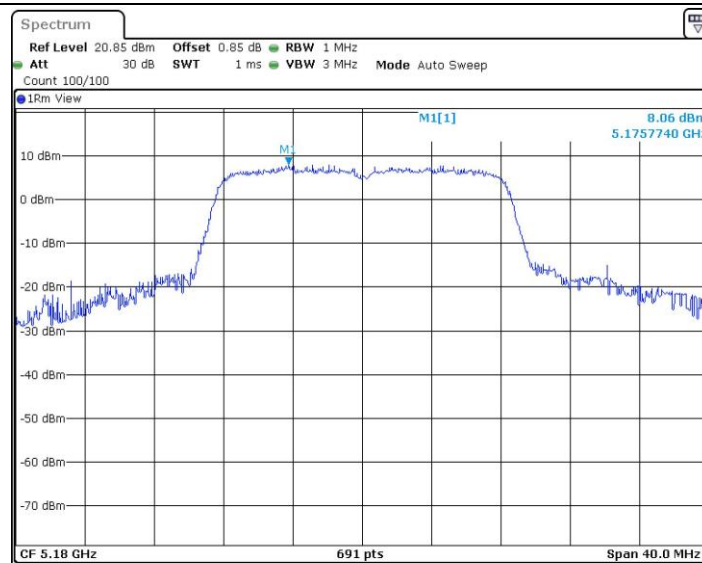
	Ant2	5785	9.22	<=30	PASS
	Ant1	5785	10.13	<=30	PASS
	total	5785	12.71	<=28.81	PASS
	Ant2	5825	9.07	<=30	PASS
	Ant1	5825	11.73	<=30	PASS
	total	5825	13.61	<=28.81	PASS
11AC40MIMO	Ant2	5190	3.72	<=11	PASS
	Ant1	5190	3.22	<=11	PASS
	total	5190	6.49	<=9.81	PASS
	Ant2	5230	3.88	<=11	PASS
	Ant1	5230	3.91	<=11	PASS
	total	5230	6.91	<=9.81	PASS
	Ant2	5270	3.11	<=11	PASS
	Ant1	5270	3.89	<=11	PASS
	total	5270	6.53	<=9.81	PASS
	Ant2	5310	2.89	<=11	PASS
	Ant1	5310	3.37	<=11	PASS
	total	5310	6.15	<=9.81	PASS
	Ant2	5510	3.16	<=11	PASS
	Ant1	5510	3.77	<=11	PASS
	total	5510	6.49	<=9.81	PASS
	Ant2	5550	3.36	<=11	PASS
	Ant1	5550	3.91	<=11	PASS
	total	5550	6.65	<=9.81	PASS
	Ant2	5670	3.93	<=11	PASS
	Ant1	5670	4.19	<=11	PASS
	total	5670	7.07	<=9.81	PASS
	Ant2	5755	4.67	<=30	PASS
	Ant1	5755	5.44	<=30	PASS
	total	5755	8.08	<=28.81	PASS
	Ant2	5795	5.77	<=30	PASS
	Ant1	5795	6.65	<=30	PASS
	total	5795	9.24	<=28.81	PASS
11AC80MIMO	Ant2	5210	2.51	<=11	PASS
	Ant1	5210	3.97	<=11	PASS
	total	5210	6.31	<=9.81	PASS
	Ant2	5290	2.72	<=11	PASS
	Ant1	5290	4.57	<=11	PASS
	total	5290	6.75	<=9.81	PASS
	Ant2	5530	4.19	<=11	PASS
	Ant1	5530	3.61	<=11	PASS
	total	5530	6.92	<=9.81	PASS
	Ant2	5775	4.08	<=30	PASS

Total Quality. Assured.

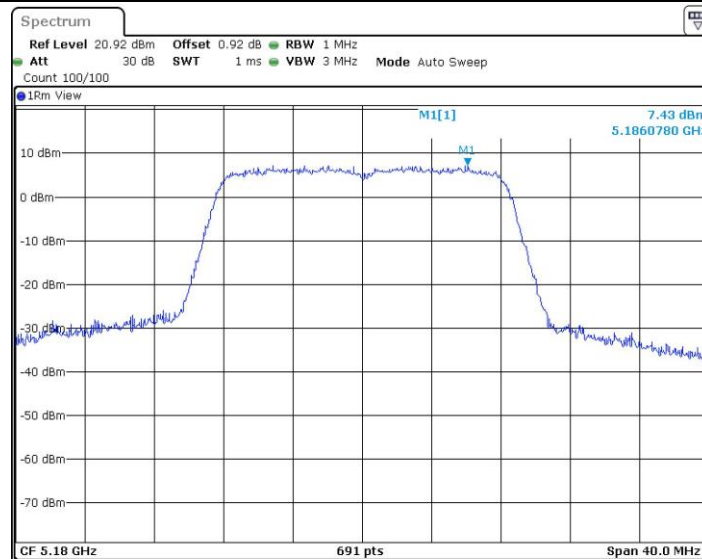
	Ant1	5775	6.97	<=30	PASS
	total	5775	8.77	<=28.81	PASS

Note: The Duty Cycle Factor is compensated in the graph.

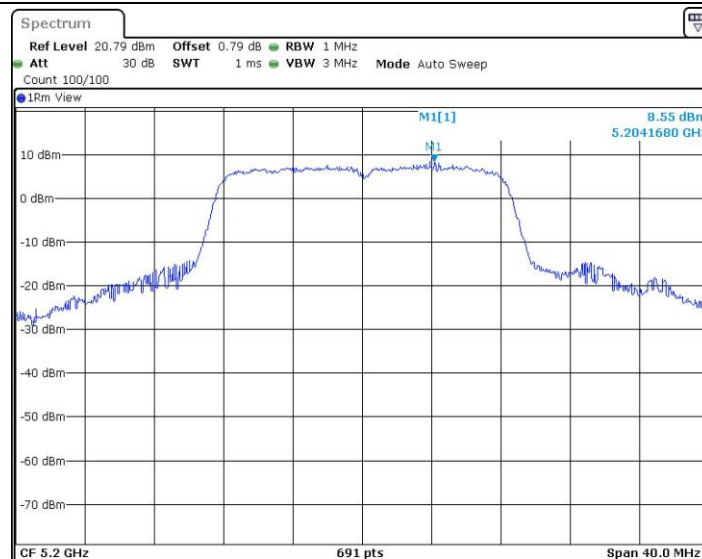
Test Graphs



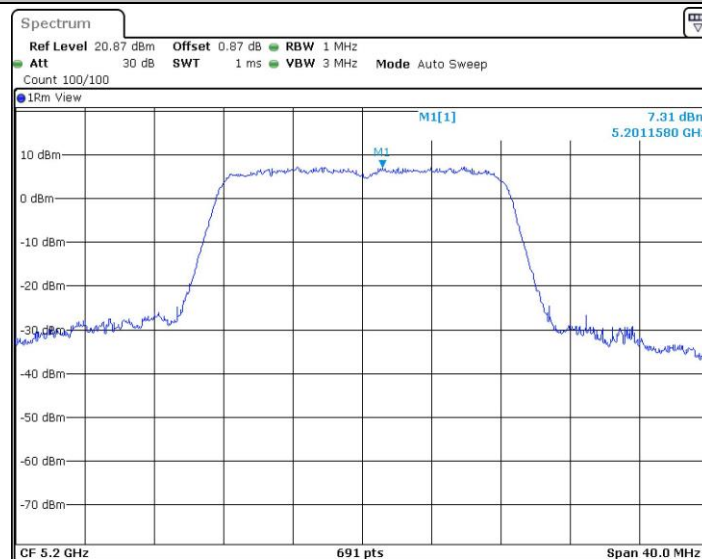
11A_Ant2_5180



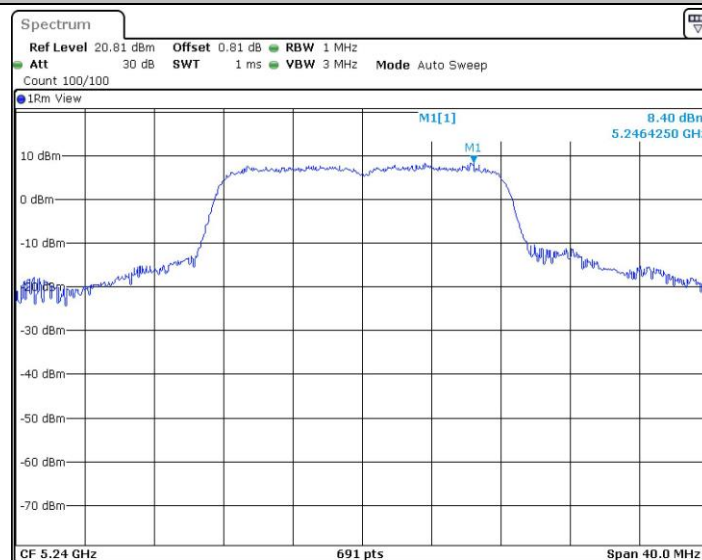
11A_Ant1_5180



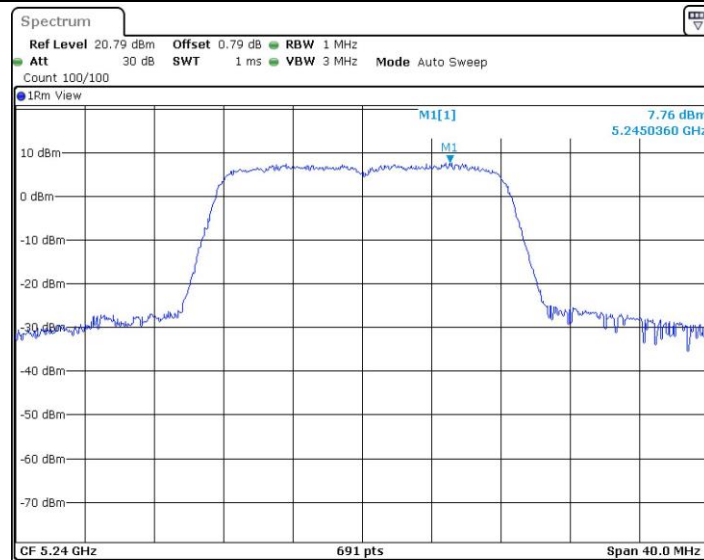
11A_Ant2_5200



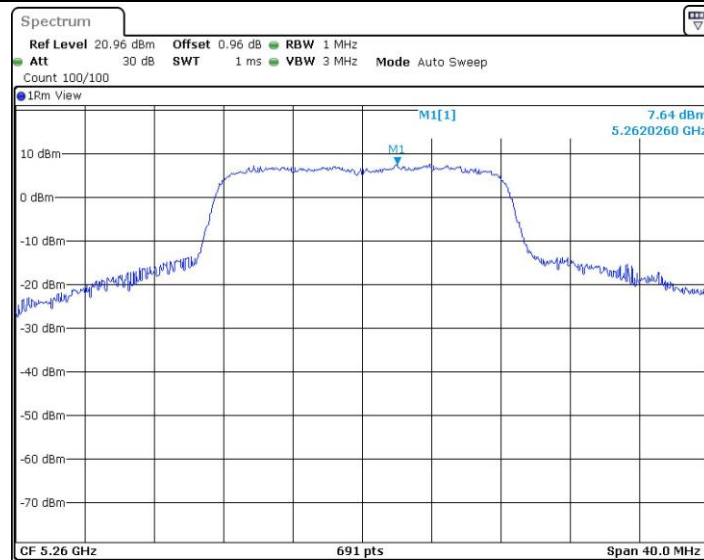
11A_Ant1_5200



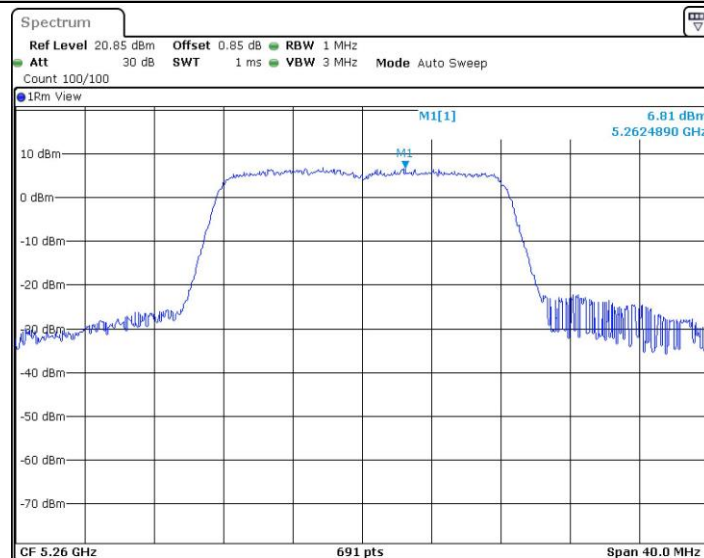
11A_Ant2_5240



11A_Ant1_5240



11A_Ant2_5260



11A_Ant1_5260