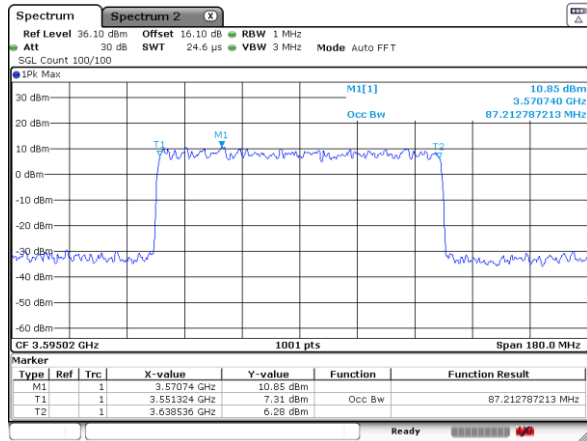




FR1 n48 / 90MHz / CP-OFDM

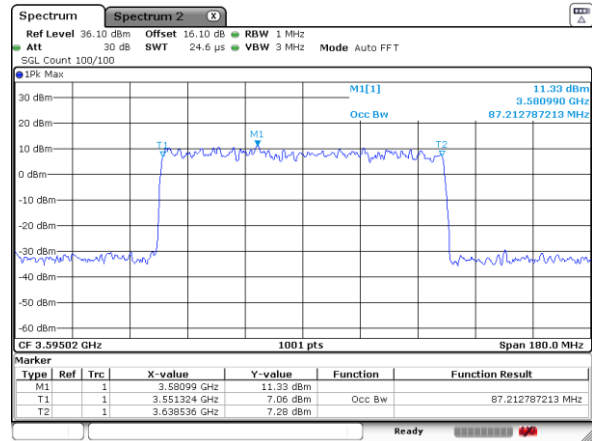
ANT0-64QAM

Lowest Channel



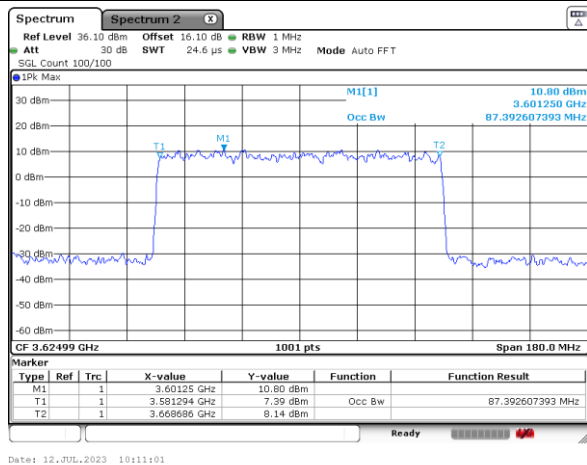
ANT1-64QAM

Lowest Channel



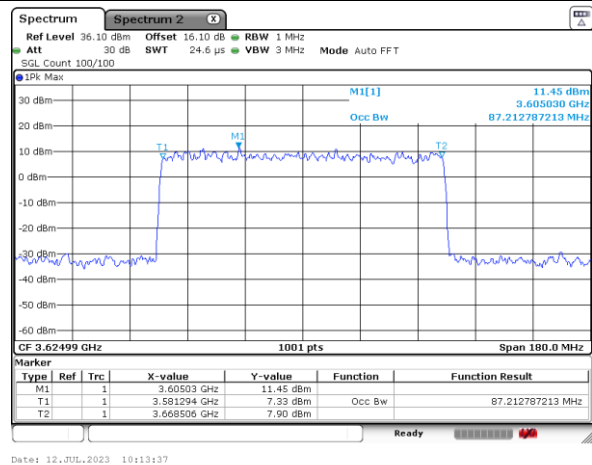
ANT0-64QAM

Middle Channel



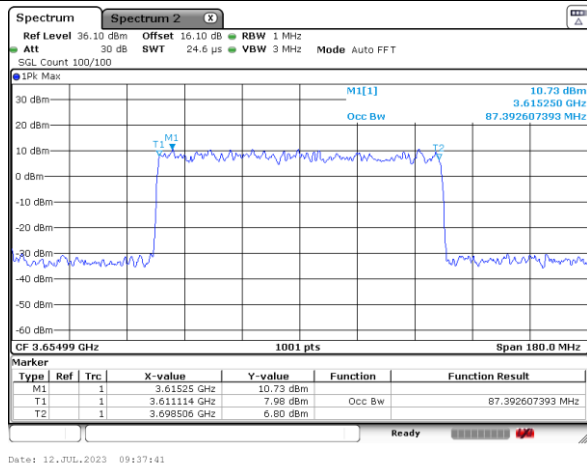
ANT1-64QAM

Middle Channel



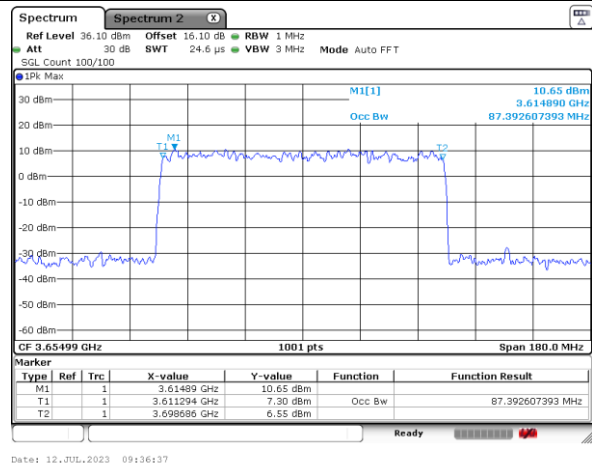
ANT0-64QAM

Highest Channel



ANT1-64QAM

Highest Channel

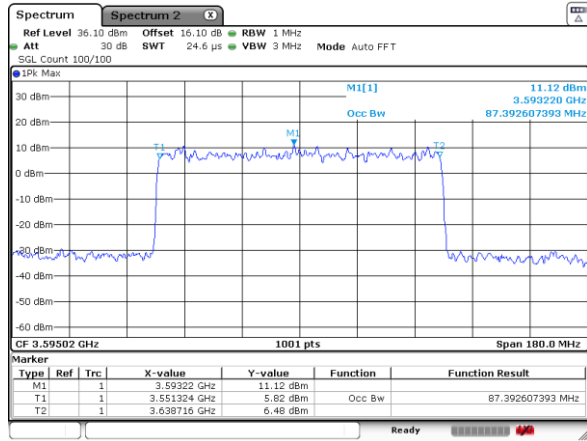




FR1 n48 / 90MHz / CP-OFDM

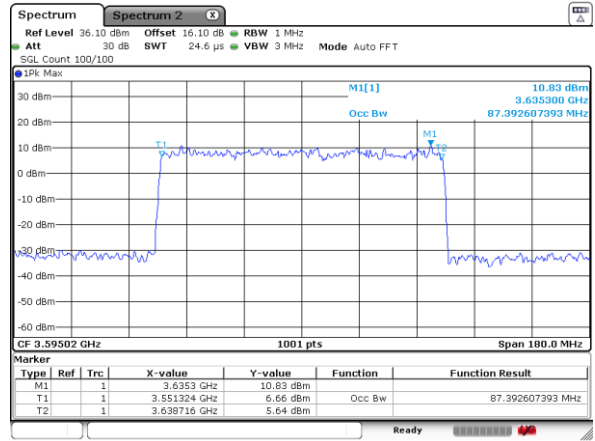
ANT0-256QAM

Lowest Channel



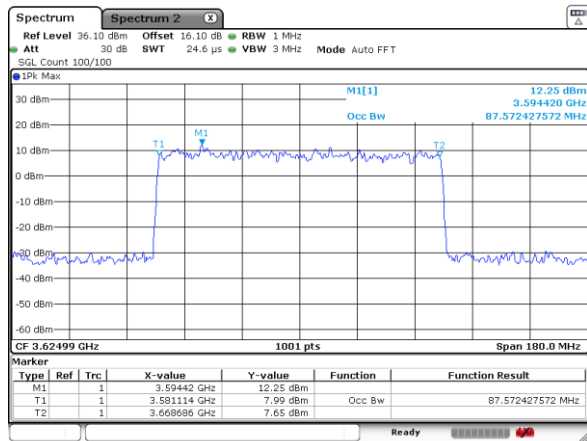
ANT1-256QAM

Lowest Channel



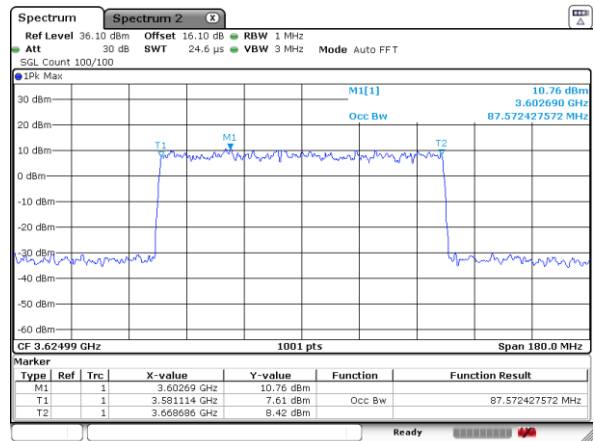
ANT0-256QAM

Middle Channel

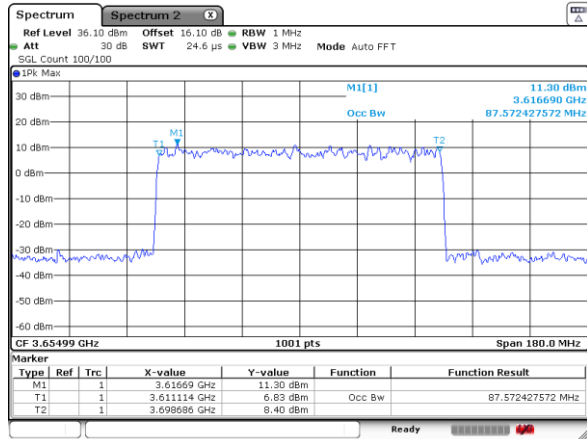


ANT1-256QAM

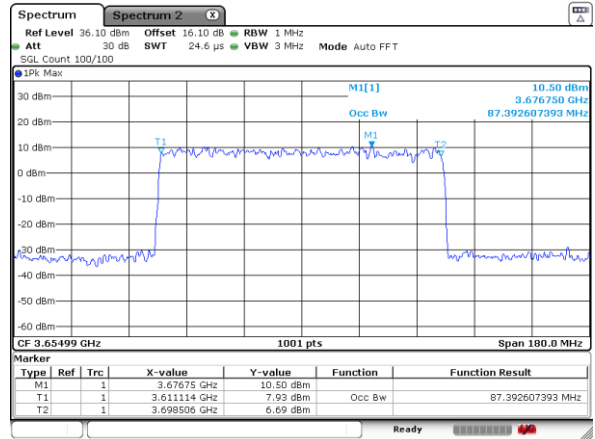
Middle Channel



Highest Channel



Highest Channel

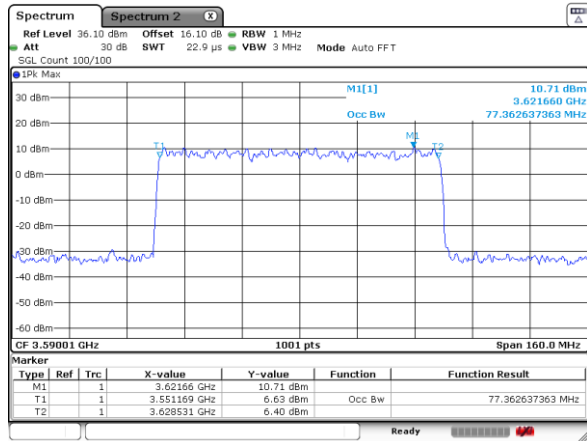




FR1 n48 / 80MHz / CP-OFDM

ANT0-64QAM

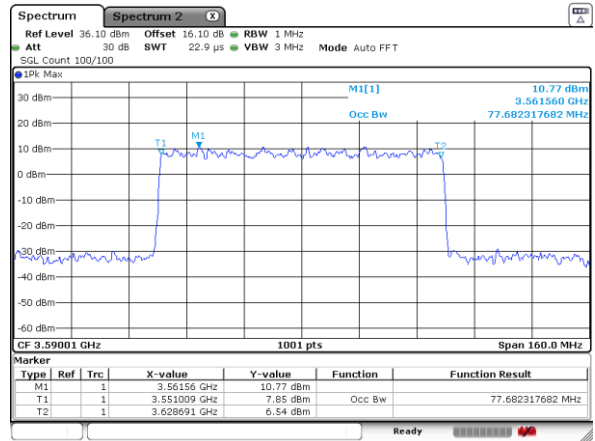
Lowest Channel



Date: 12.JUL.2023 10:43:19

ANT1-64QAM

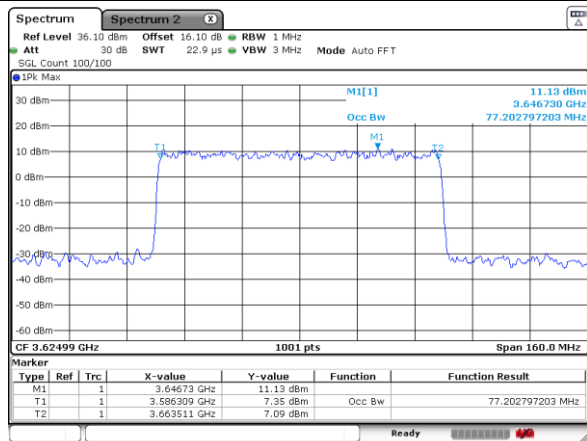
Lowest Channel



Date: 12.JUL.2023 10:44:36

ANT0-64QAM

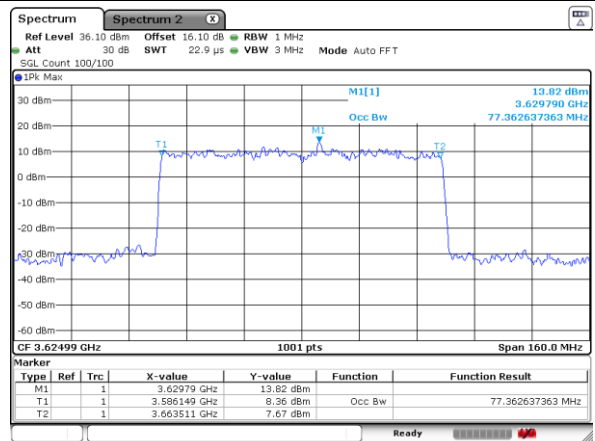
Middle Channel



Date: 12.JUL.2023 11:02:37

ANT1-64QAM

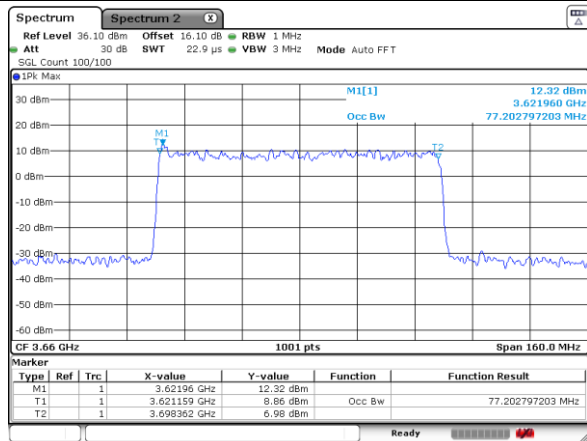
Middle Channel



Date: 12.JUL.2023 11:00:46

ANT0-64QAM

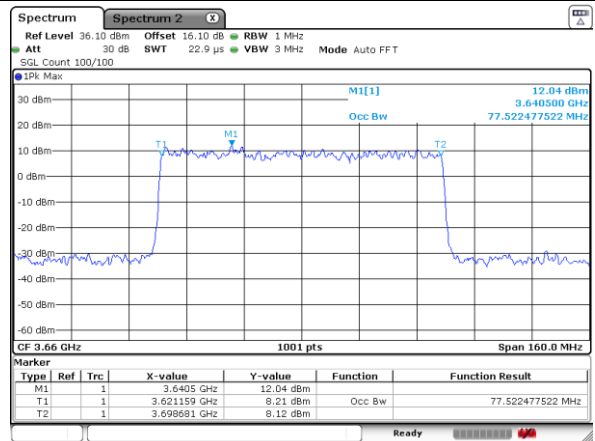
Highest Channel



Date: 12.JUL.2023 11:11:07

ANT1-64QAM

Highest Channel



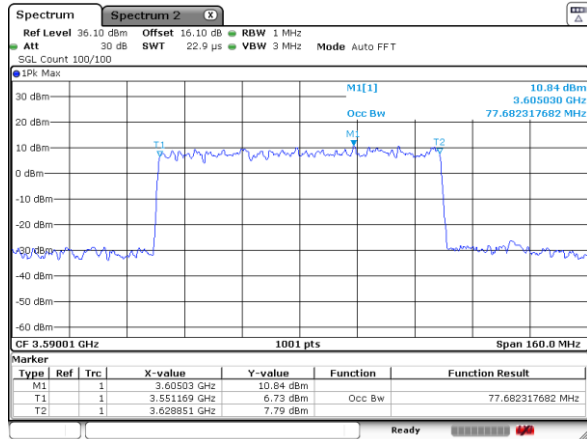
Date: 12.JUL.2023 11:10:05



FR1 n48 / 80MHz / CP-OFDM

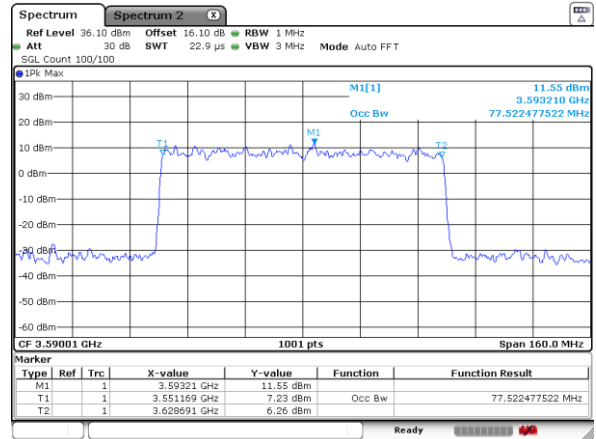
ANT0-256QAM

Lowest Channel



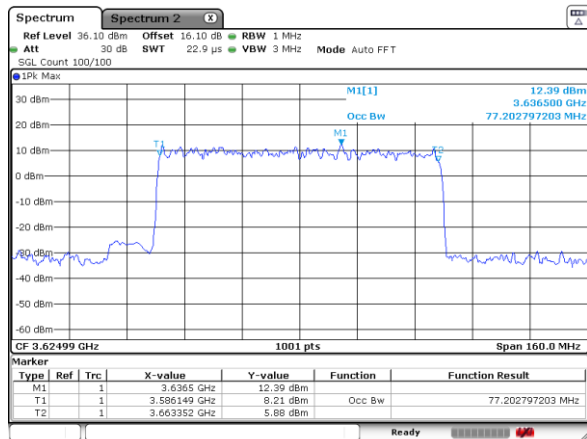
ANT1-256QAM

Lowest Channel



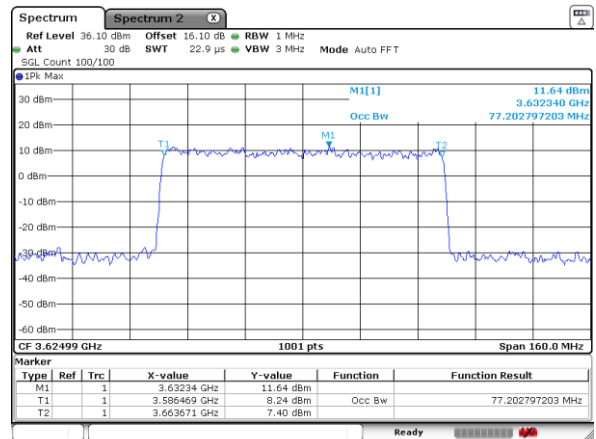
ANT0-256QAM

Middle Channel

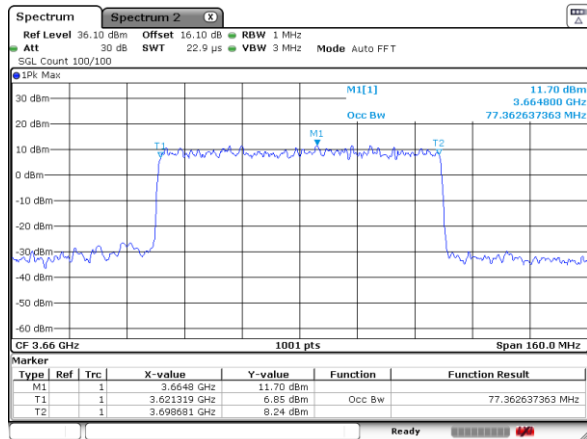


ANT1-256QAM

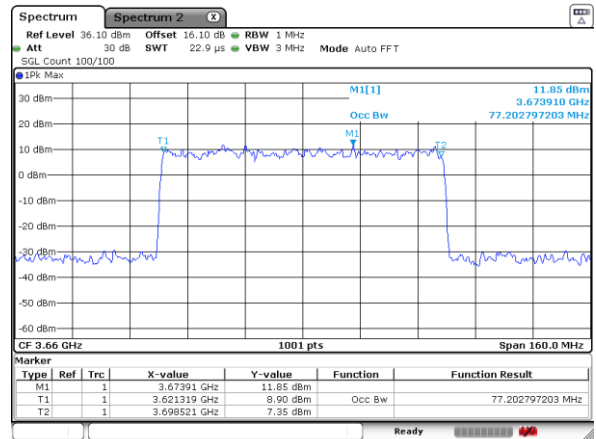
Middle Channel



Highest Channel



Highest Channel

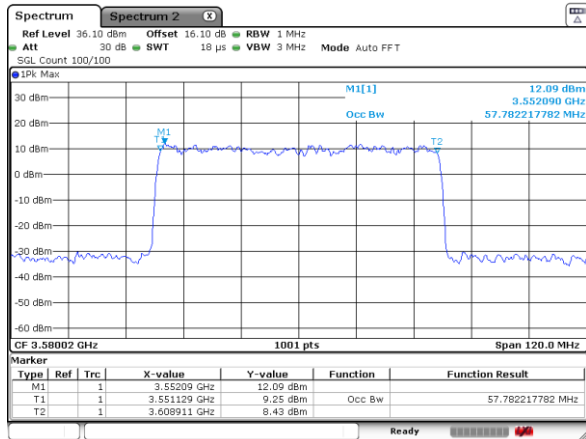




FR1 n48 / 60MHz / CP-OFDM

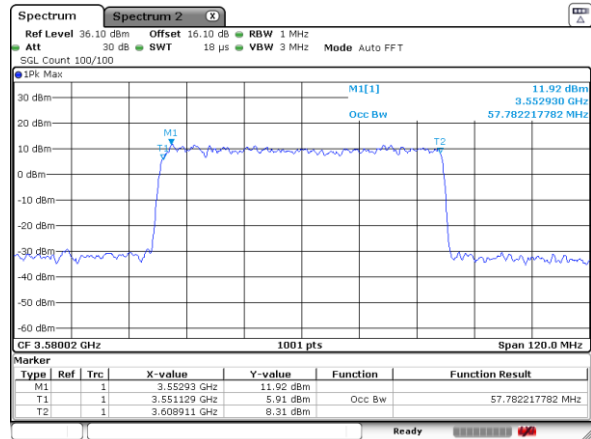
ANT0-64QAM

Lowest Channel



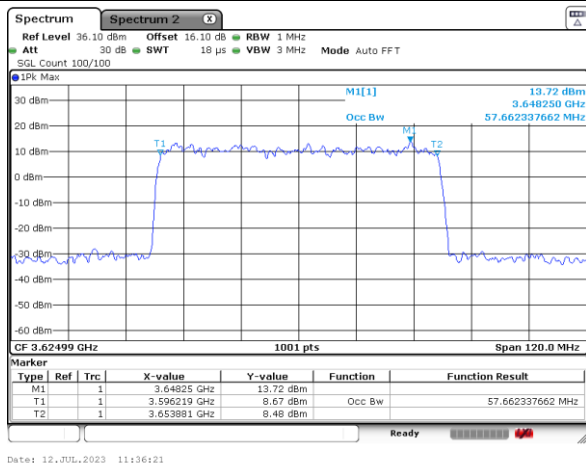
ANT1-64QAM

Lowest Channel



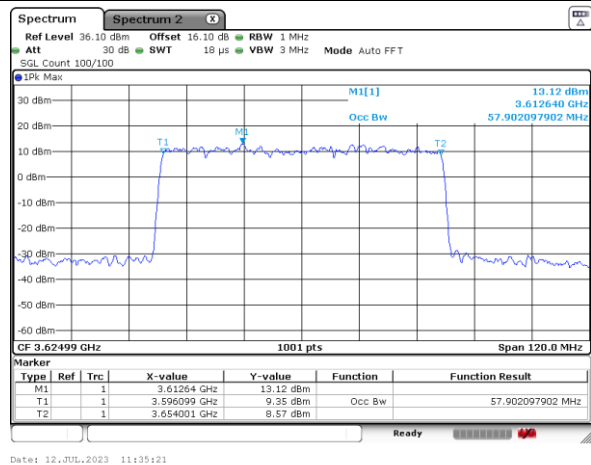
ANT0-64QAM

Middle Channel



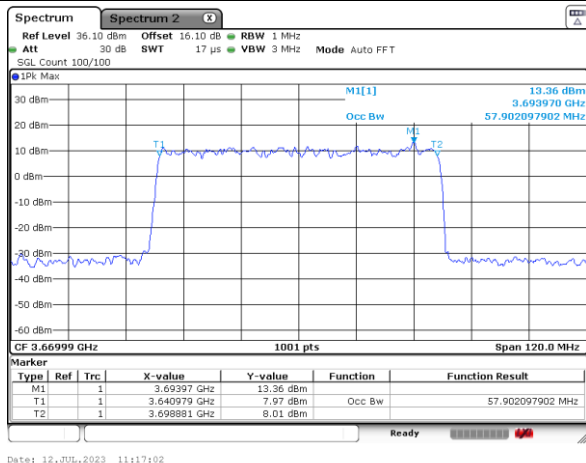
ANT1-64QAM

Middle Channel



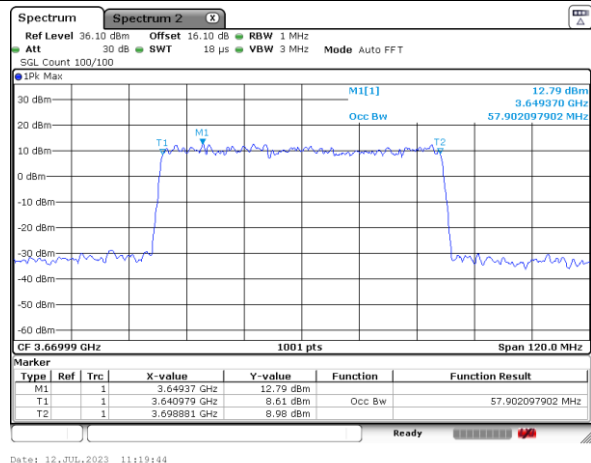
ANT0-64QAM

Highest Channel



ANT1-64QAM

Highest Channel

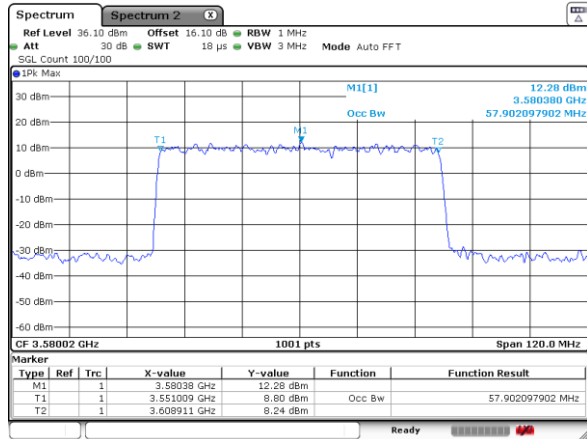




FR1 n48 / 60MHz / CP-OFDM

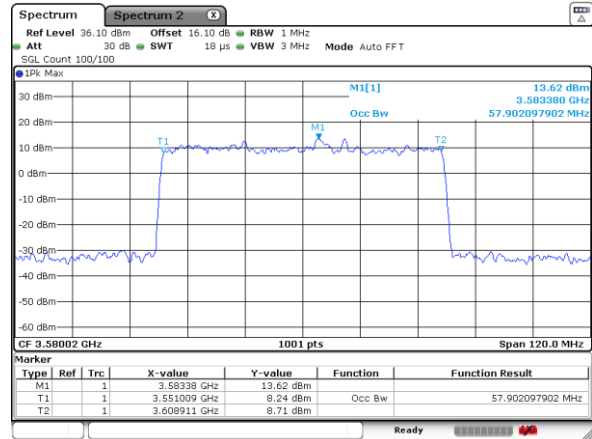
ANT0-256QAM

Lowest Channel



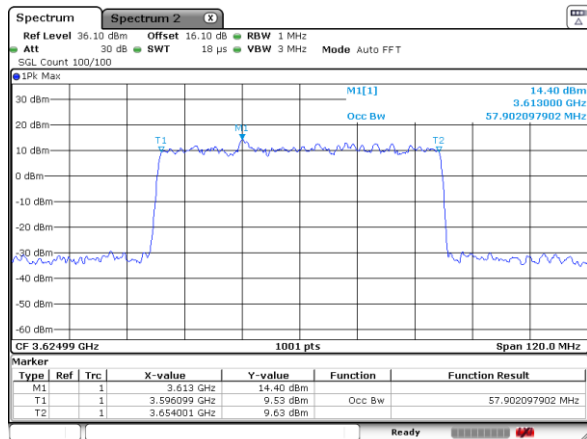
ANT1-256QAM

Lowest Channel



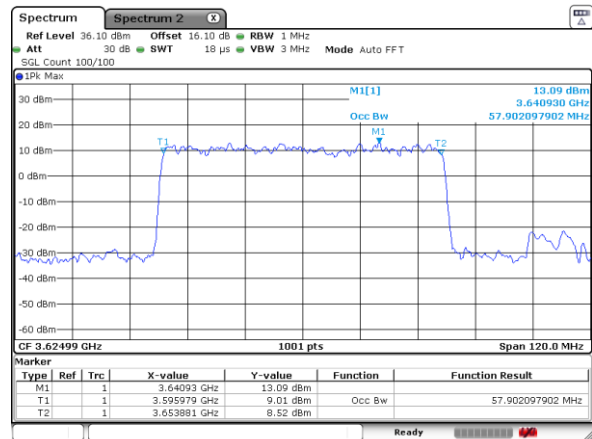
ANT0-256QAM

Middle Channel

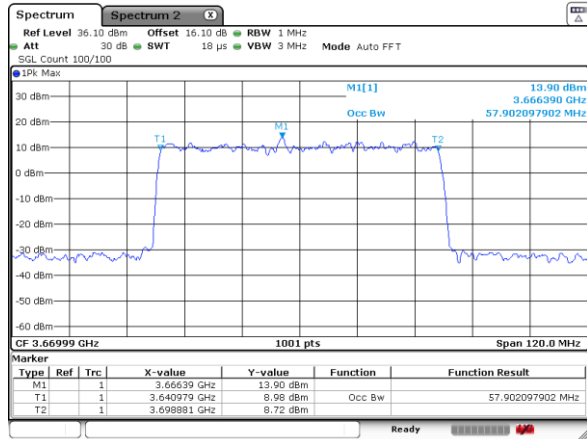


ANT1-256QAM

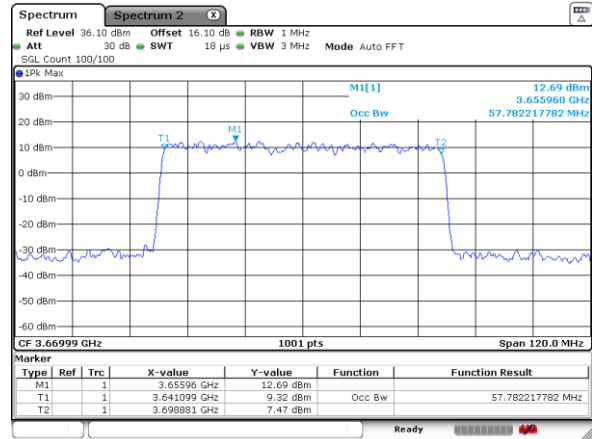
Middle Channel



Highest Channel



Highest Channel

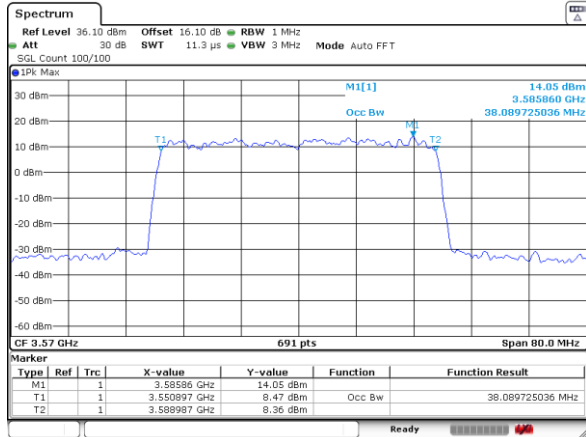




FR1 n48 / 40MHz / CP-OFDM

ANT0-64QAM

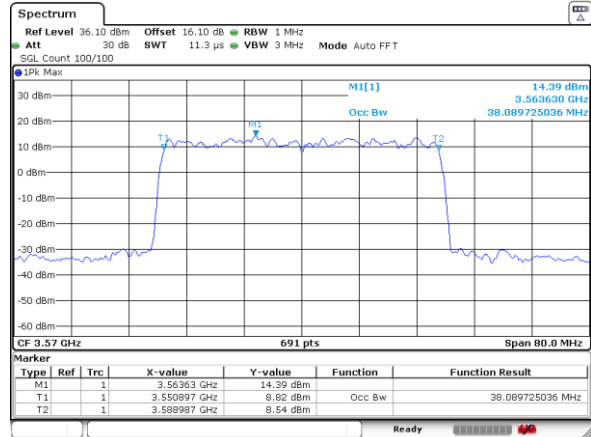
Lowest Channel



Date: 19_JUL_2023 20:51:36

ANT1-64QAM

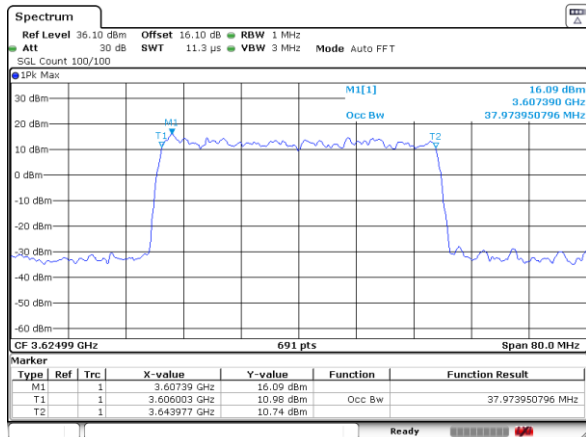
Lowest Channel



Date: 19_JUL_2023 21:25:27

ANT0-64QAM

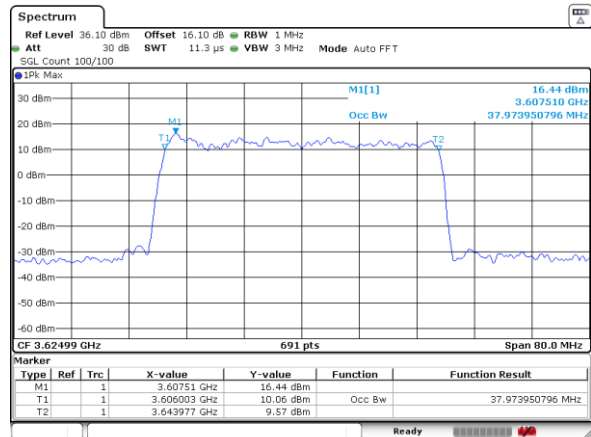
Middle Channel



Date: 19_JUL_2023 21:04:21

ANT1-64QAM

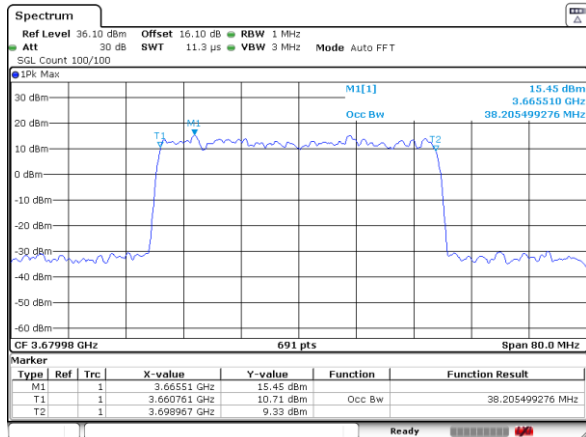
Middle Channel



Date: 19_JUL_2023 21:07:54

ANT0-64QAM

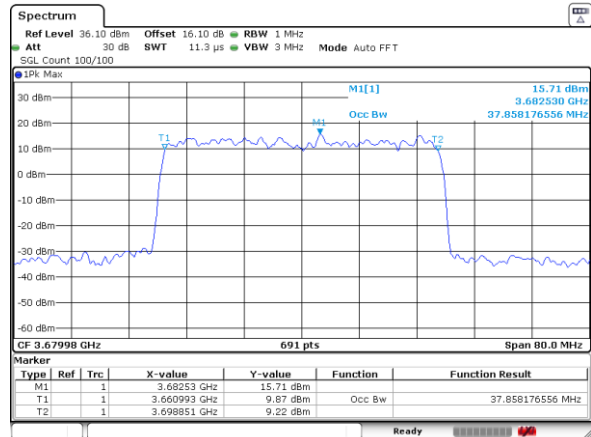
Highest Channel



Date: 19_JUL_2023 21:59:42

ANT1-64QAM

Highest Channel



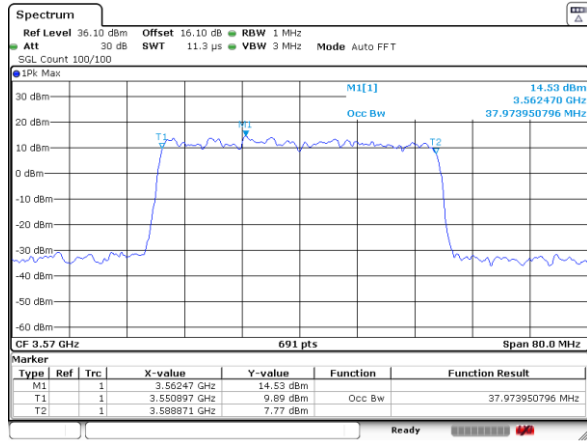
Date: 19_JUL_2023 22:09:03



FR1 n48 / 40MHz / CP-OFDM

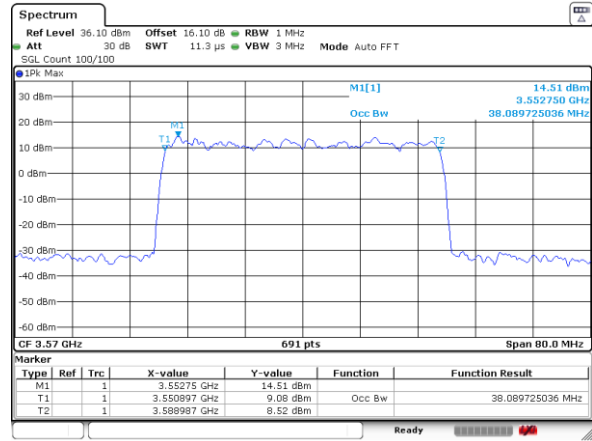
ANT0-256QAM

Lowest Channel



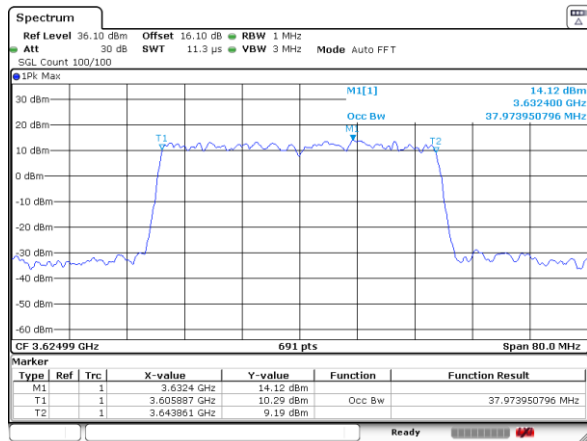
ANT1-256QAM

Lowest Channel



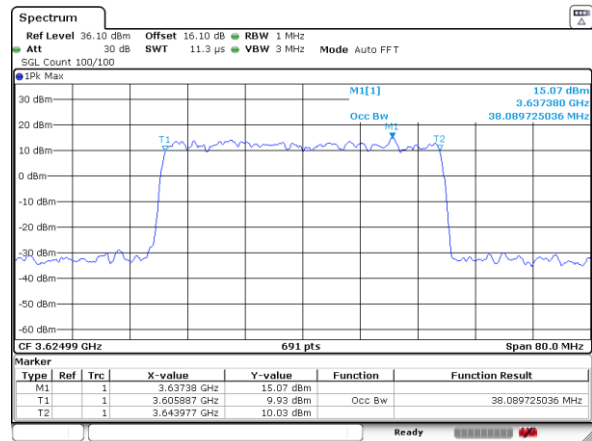
ANT0-256QAM

Middle Channel

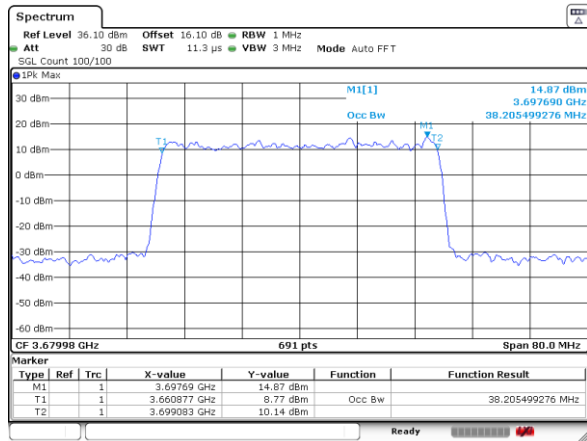


ANT1-256QAM

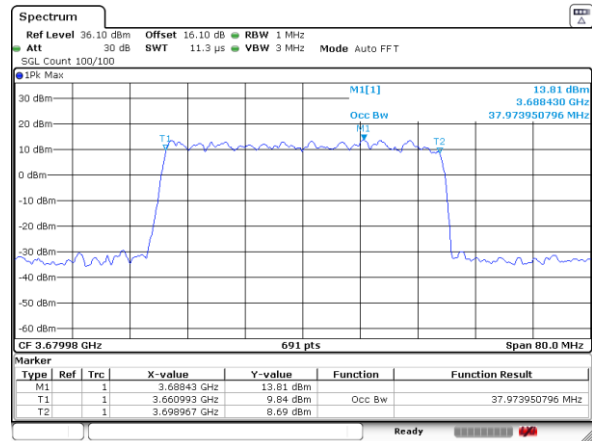
Middle Channel



Highest Channel



Highest Channel





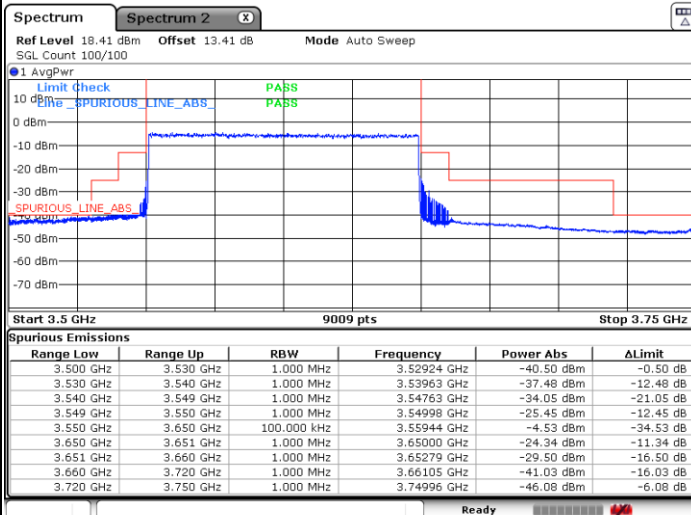
Conducted Band Edge

FR1 Part 96 n48 / 100MHz

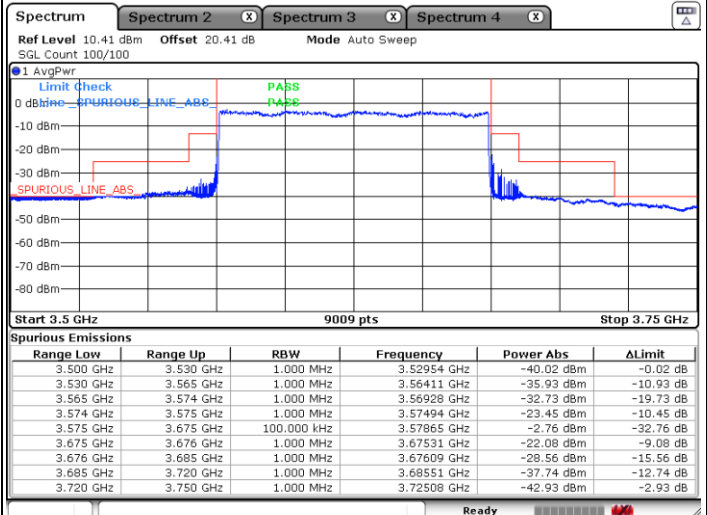
ANT0 64QAM

Lowest Channel / FullRB

Middle Channel / FullRB

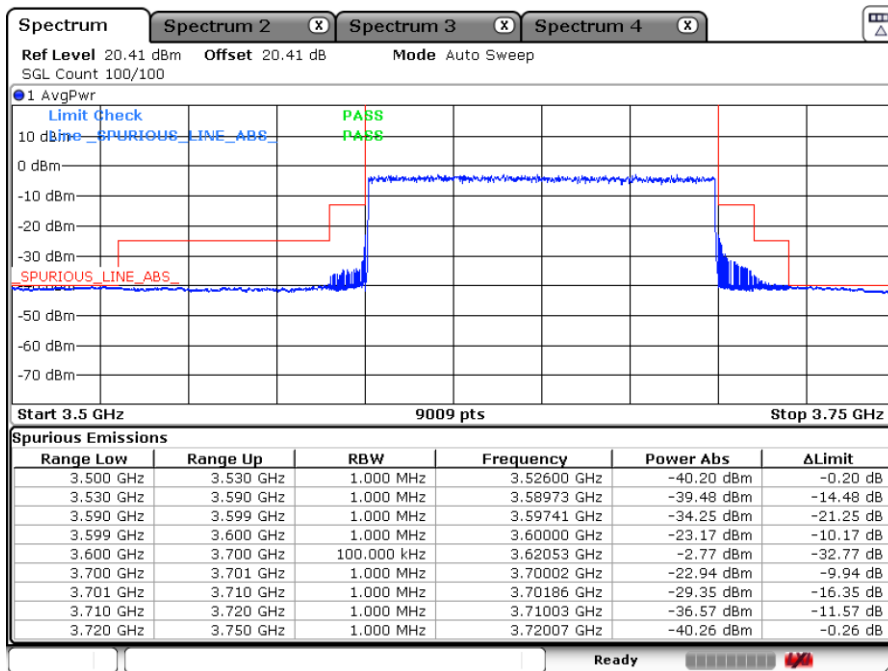


Date: 28.JUN.2023 07:48:31



Date: 28.JUN.2023 00:54:30

Highest Channel / FullRB



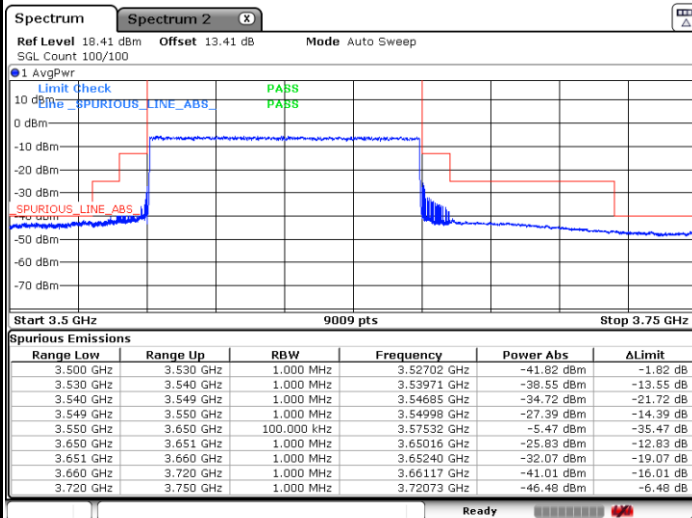
Date: 28.JUN.2023 02:14:08



FR1 Part 96 n48 / 100MHz

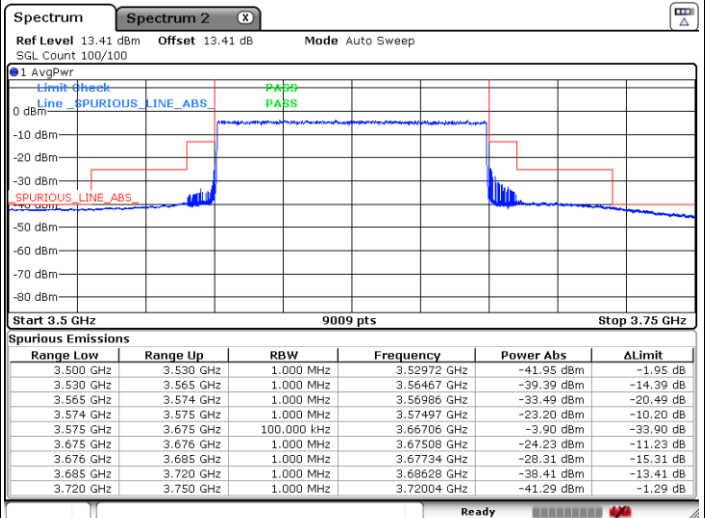
ANT1 64QAM

Lowest Channel / FullRB



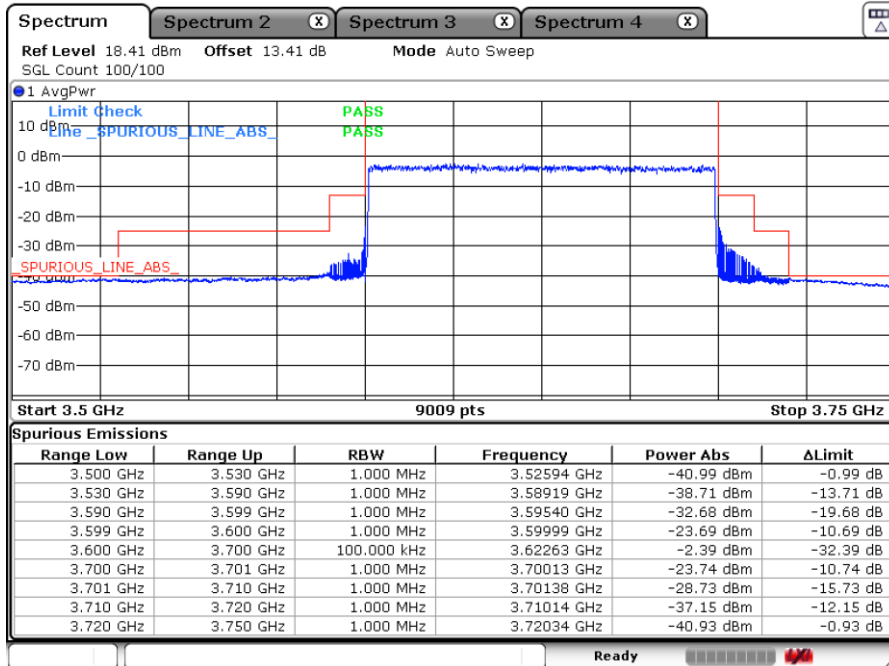
Date: 28.JUN.2023 07:45:18

Middle Channel / FullRB



Date: 27.JUN.2023 23:06:16

Highest Channel / FullRB



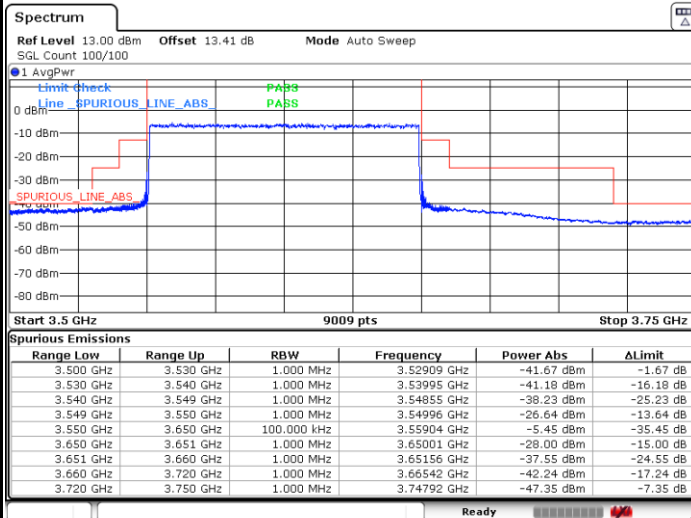
Date: 28.JUN.2023 02:07:03



FR1 Part 96 n48 / 100MHz

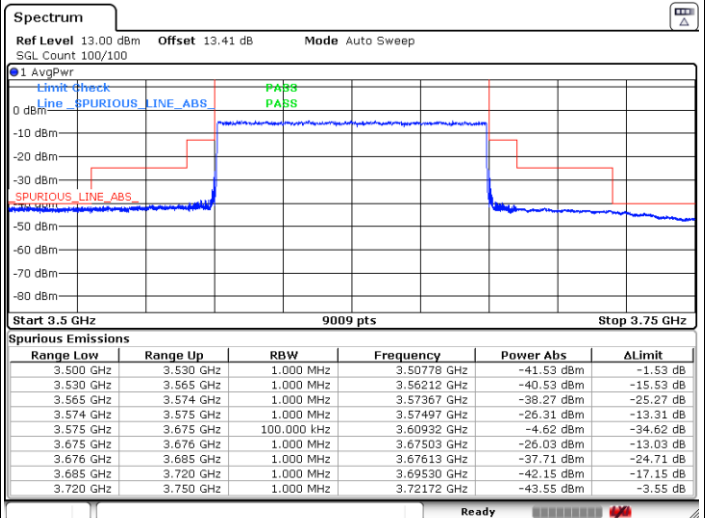
ANT0 256QAM

Lowest Channel / FullRB



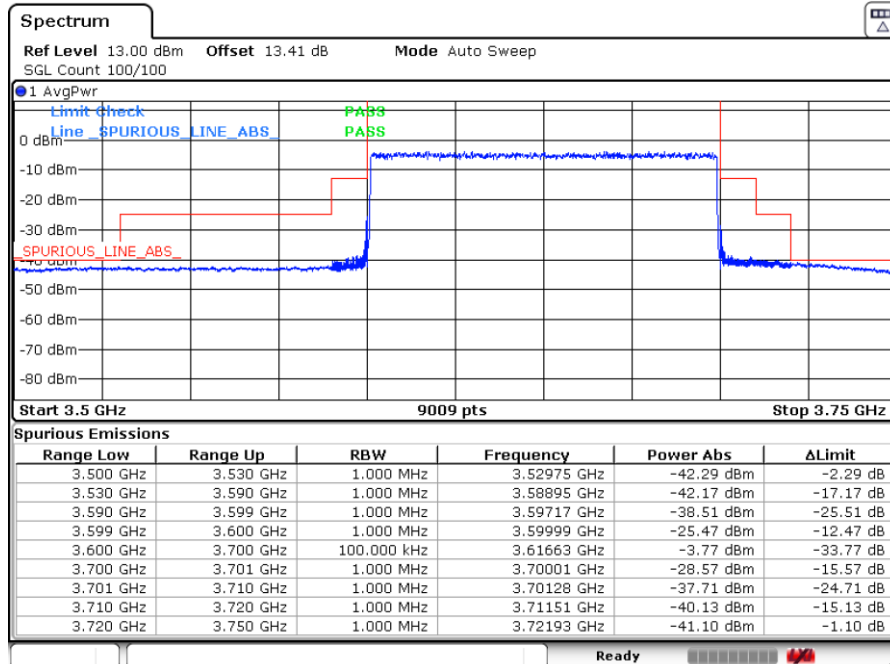
Date: 28 JUN 2023 18:27:09

Middle Channel / FullRB



Date: 28 JUN 2023 17:26:41

Highest Channel / FullRB



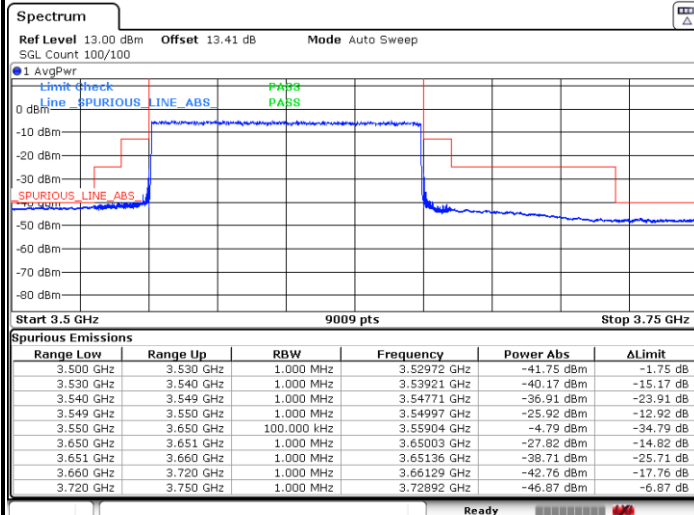
Date: 28 JUN 2023 18:11:04



FR1 Part 96 n48 / 100MHz

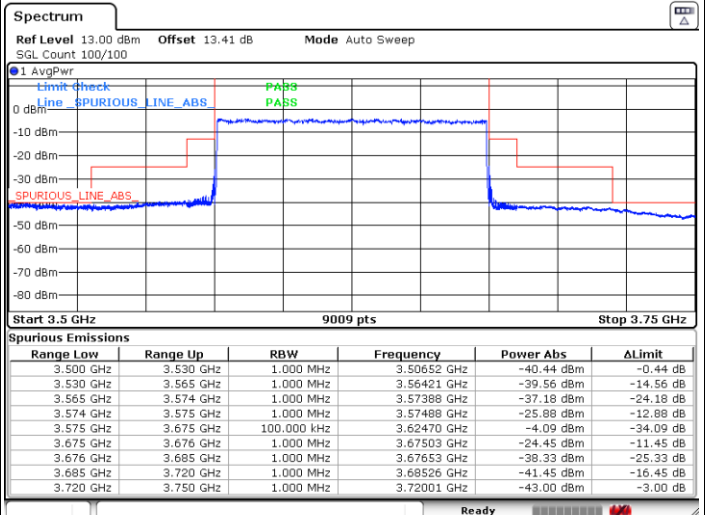
ANT1 256QAM

Lowest Channel / FullRB



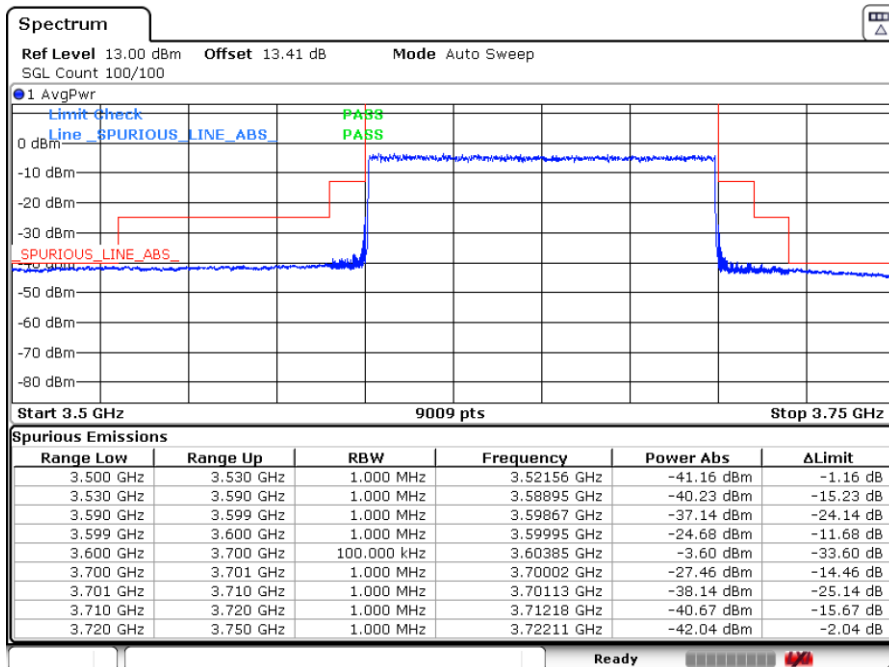
Date: 28.JUN.2023 18:33:47

Middle Channel / FullRB



Date: 28.JUN.2023 17:08:32

Highest Channel / FullRB



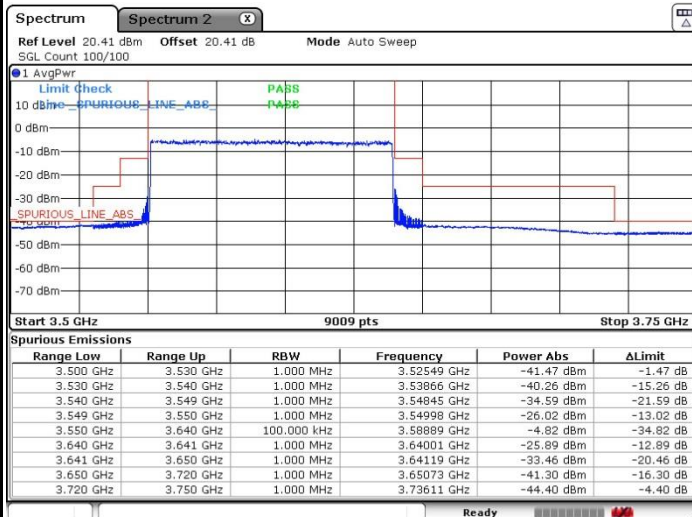
Date: 28.JUN.2023 18:09:19



FR1 Part 96 n48 / 90MHz

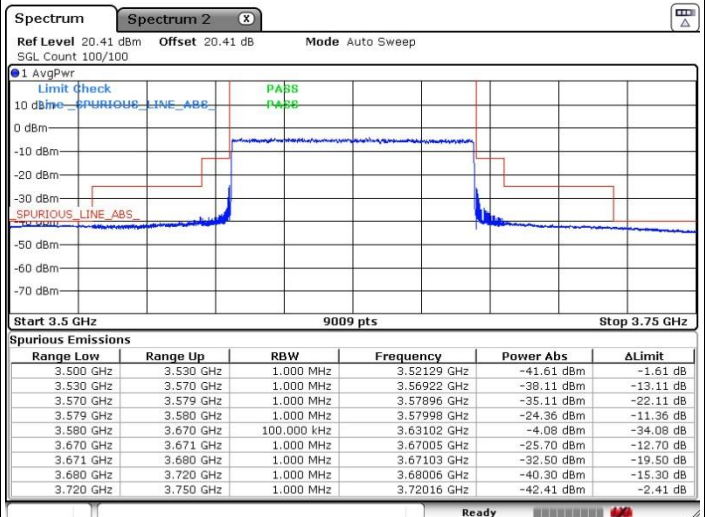
ANT0 64QAM

Lowest Channel / FullRB



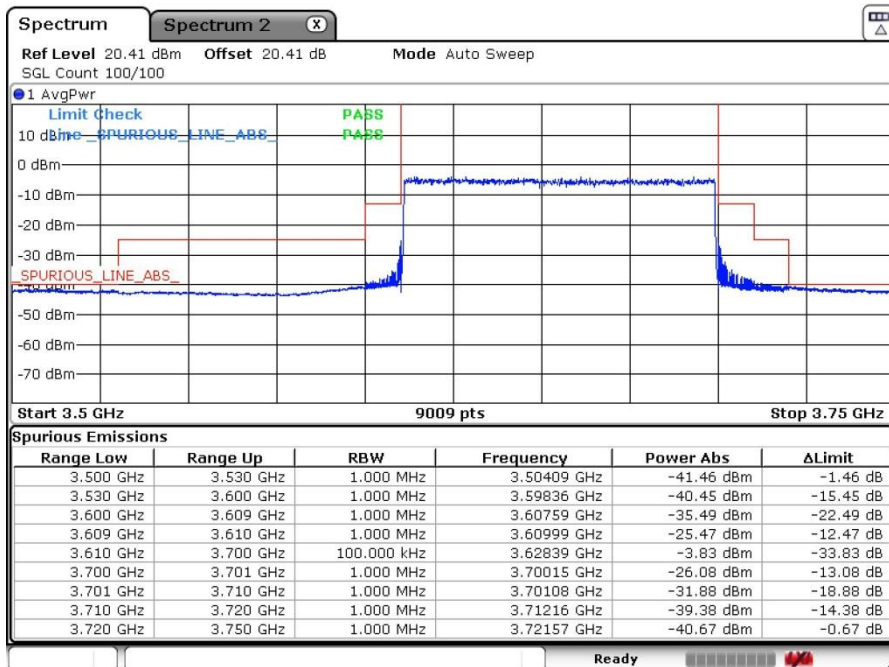
Date: 11.JUL.2023 23:12:23

Middle Channel / FullRB



Date: 11.JUL.2023 23:30:52

Highest Channel / FullRB



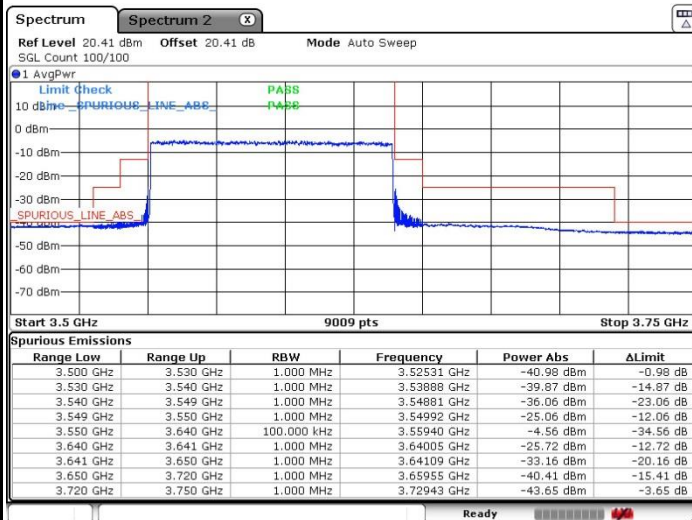
Date: 11.JUL.2023 23:57:17



FR1 Part 96 n48 / 90MHz

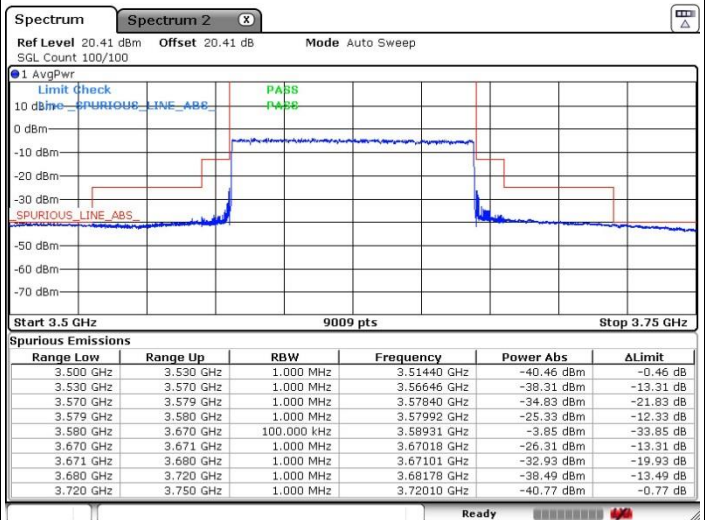
ANT1 64QAM

Lowest Channel / FullRB



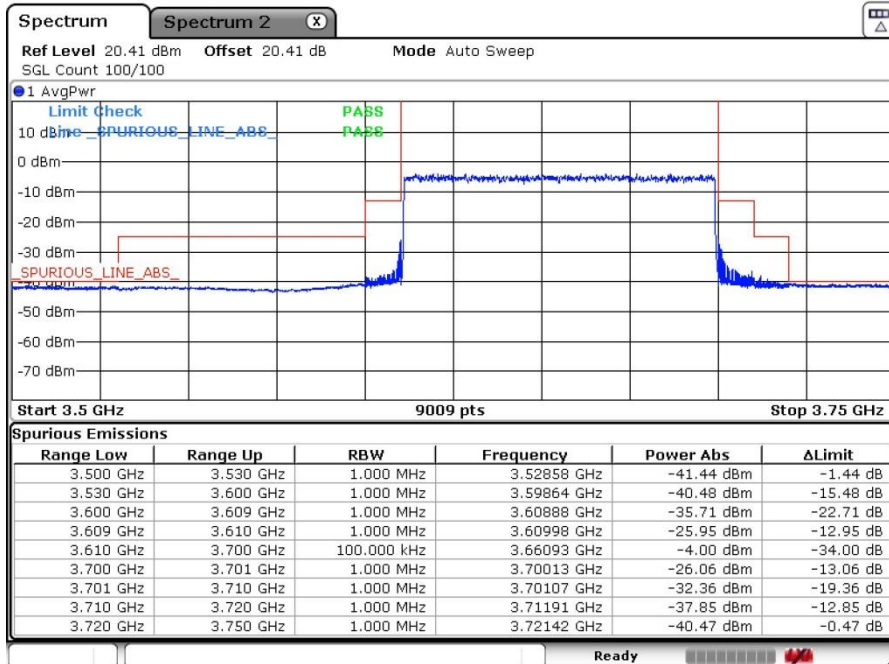
Date: 11.JUL.2023 23:11:21

Middle Channel / FullRB



Date: 11.JUL.2023 23:29:58

Highest Channel / FullRB



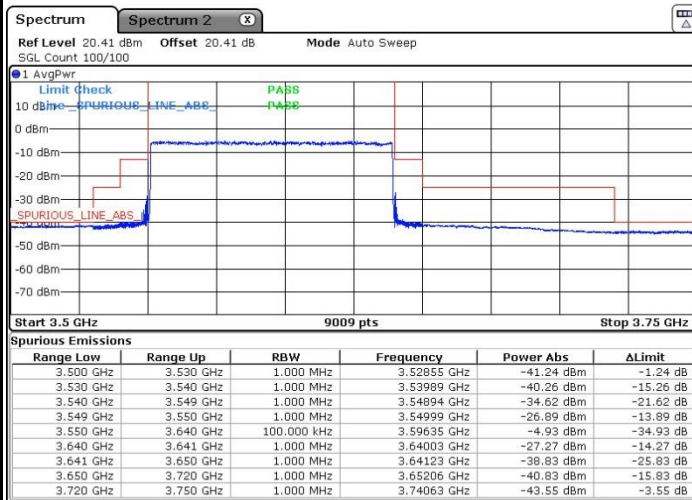
Date: 11.JUL.2023 23:56:40



FR1 Part 96 n48 / 90MHz

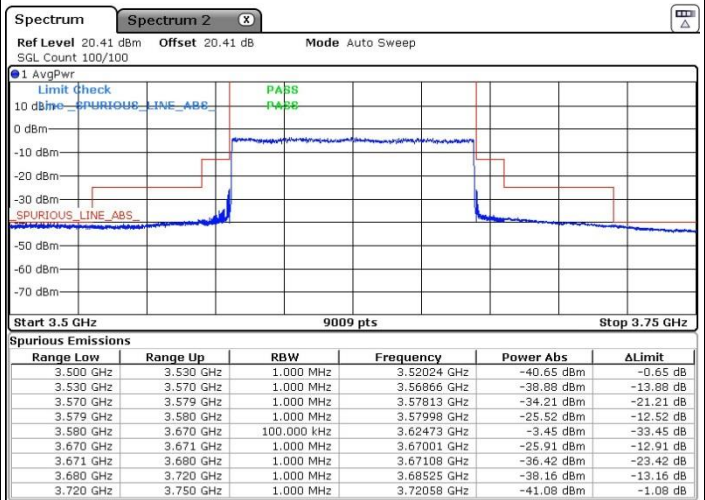
ANT0 256QAM

Lowest Channel / FullRB



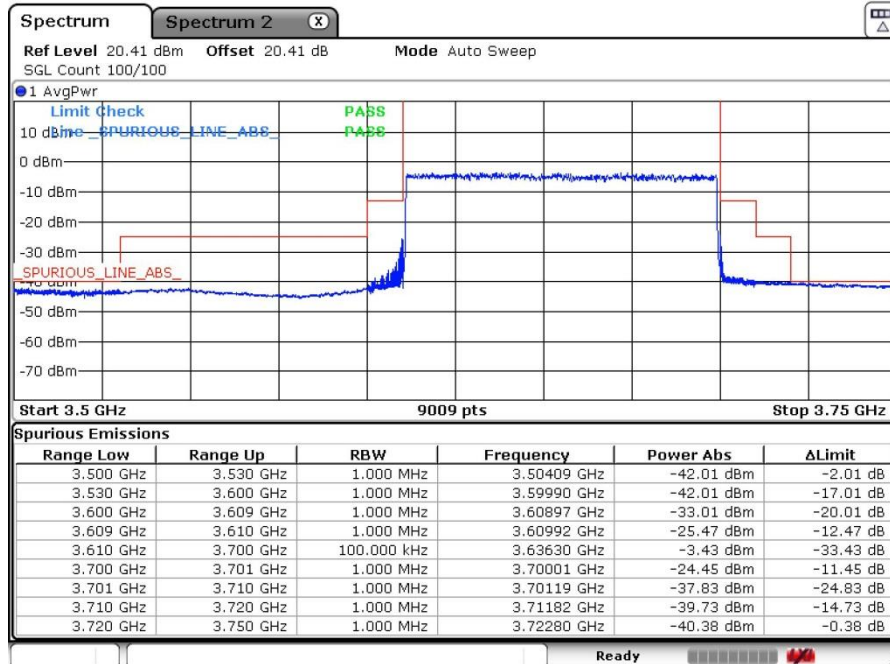
Date: 11.JUL.2023 20:57:03

Middle Channel / FullRB



Date: 11.JUL.2023 21:39:18

Highest Channel / FullRB



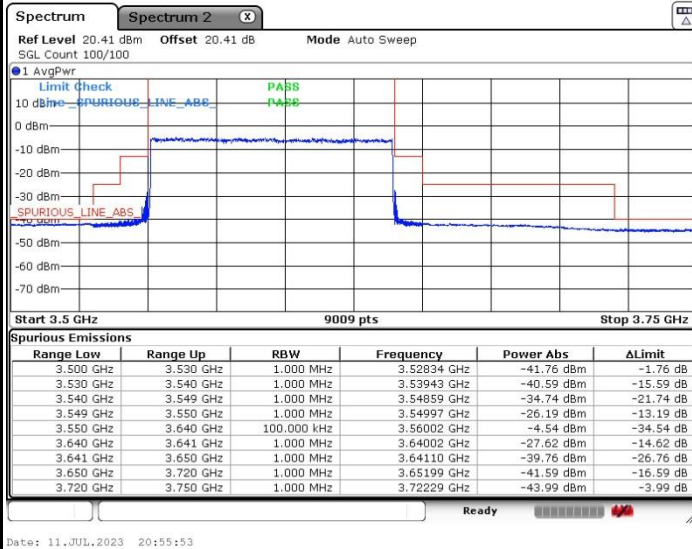
Date: 11.JUL.2023 22:00:30



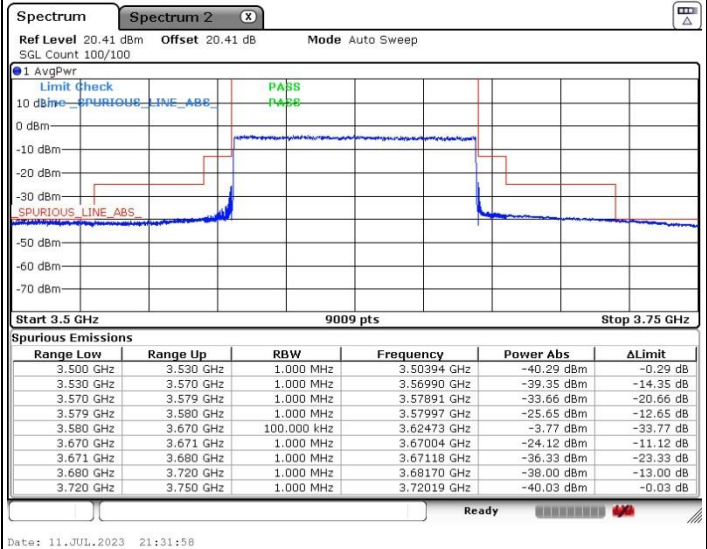
FR1 Part 96 n48 / 90MHz

ANT1 256QAM

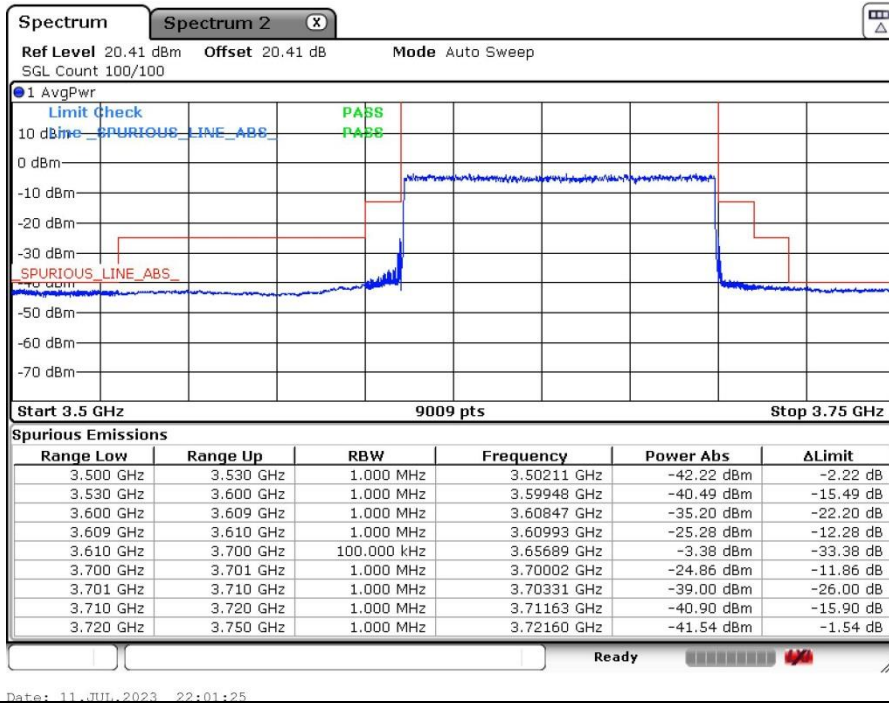
Lowest Channel / FullRB



Middle Channel / FullRB



Highest Channel / FullRB

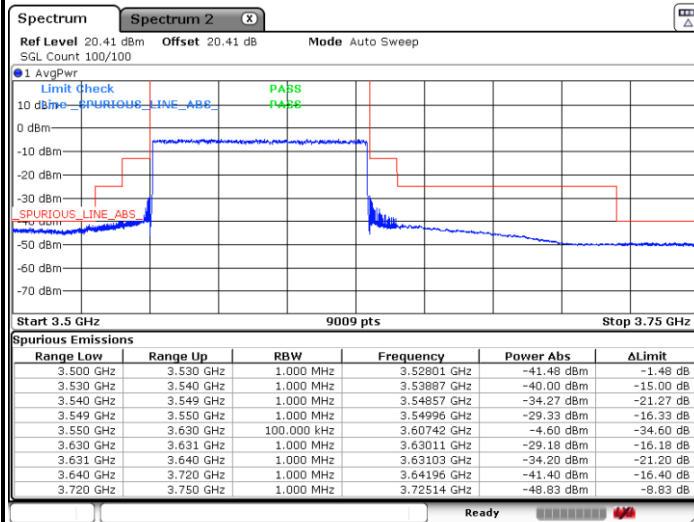




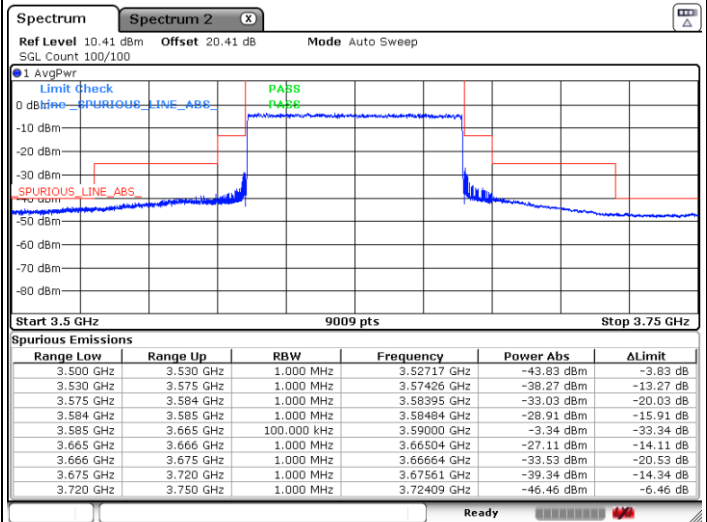
FR1 Part 96 n48 / 80MHz

ANT0 64QAM

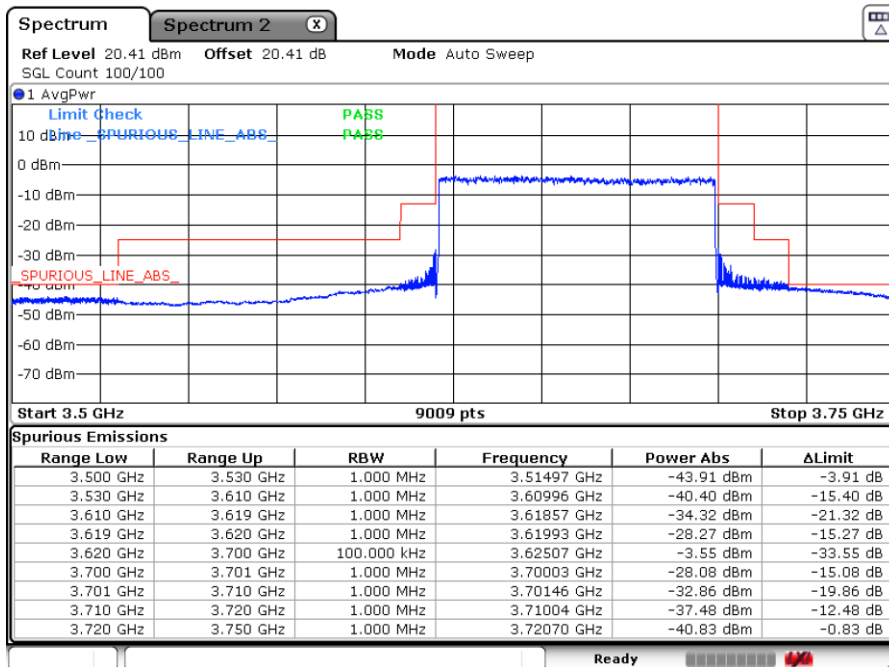
Lowest Channel / FullRB



Middle Channel / FullRB



Highest Channel / FullRB

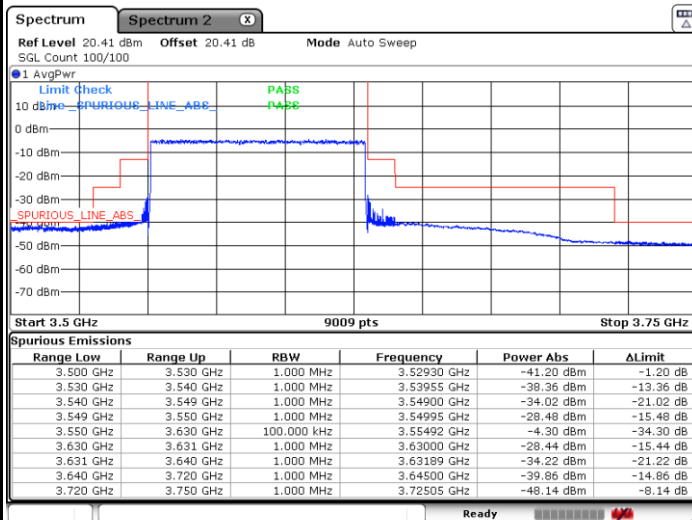




FR1 Part 96 n48 / 80MHz

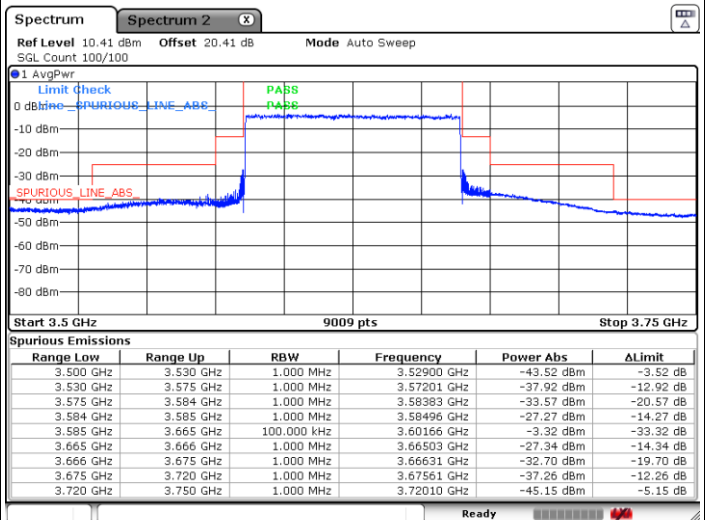
ANT1 64QAM

Lowest Channel / FullRB



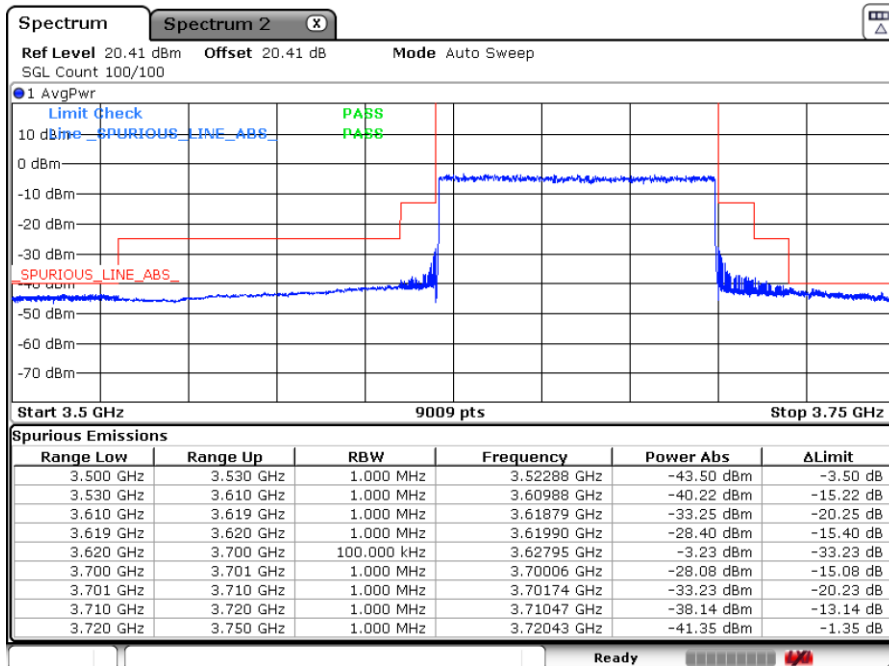
Date: 11.JUL.2023 17:18:49

Middle Channel / FullRB



Date: 11.JUL.2023 16:07:34

Highest Channel / FullRB



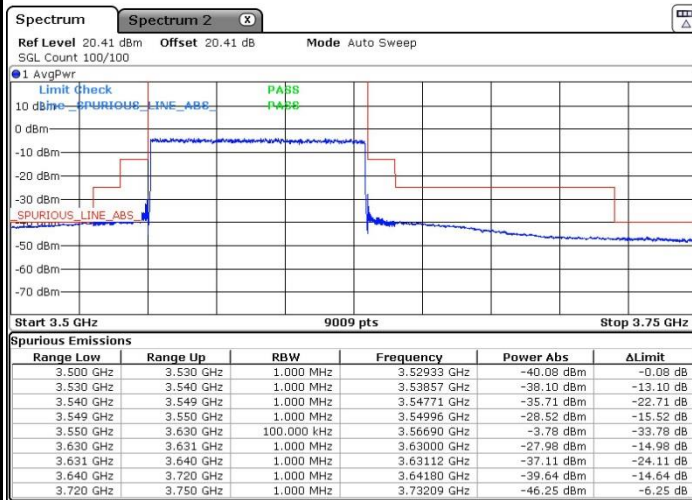
Date: 11.JUL.2023 17:02:01



FR1 Part 96 n48 / 80MHz

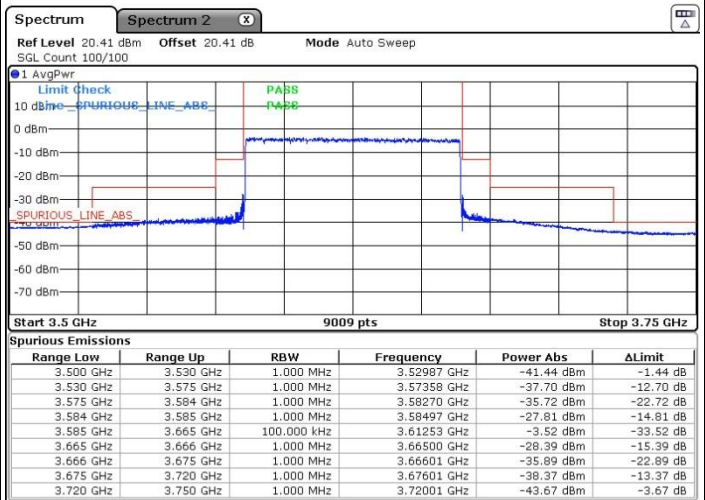
ANT0 256QAM

Lowest Channel / FullRB



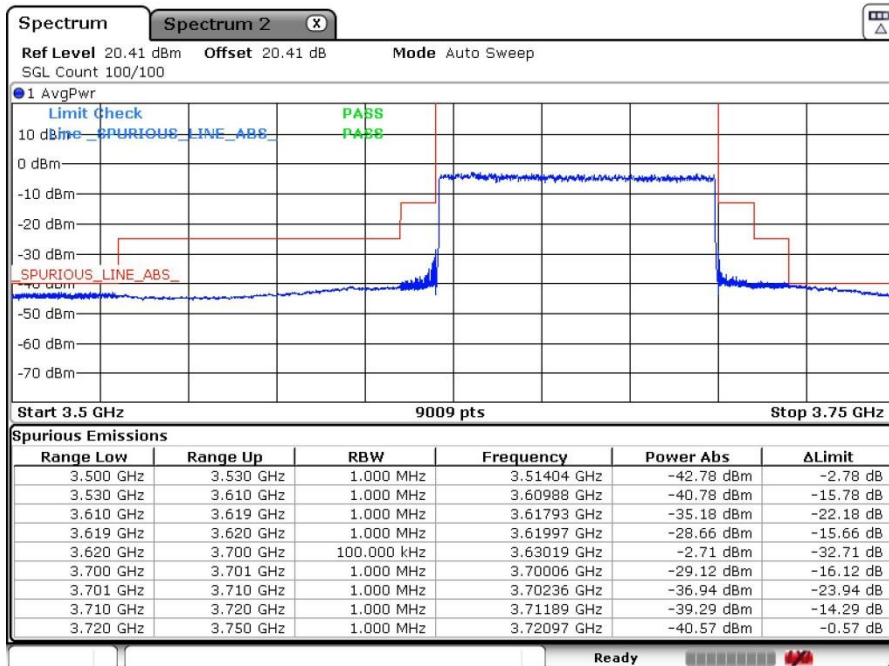
Date: 11.JUL.2023 18:18:58

Middle Channel / FullRB



Date: 11.JUL.2023 19:58:48

Highest Channel / FullRB



Date: 11.JUL.2023 20:25:13