

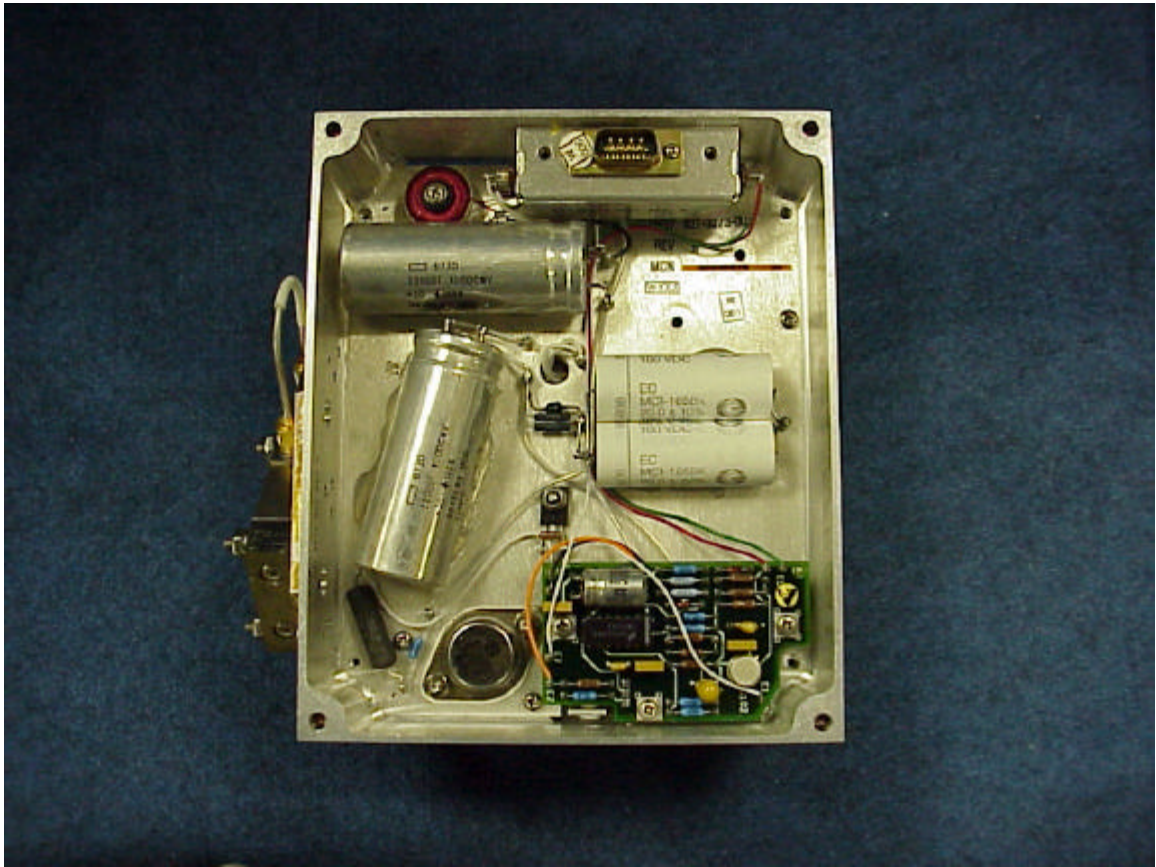


Figure 8-11. PA Rear Open



Figure 8-12. PA Front End

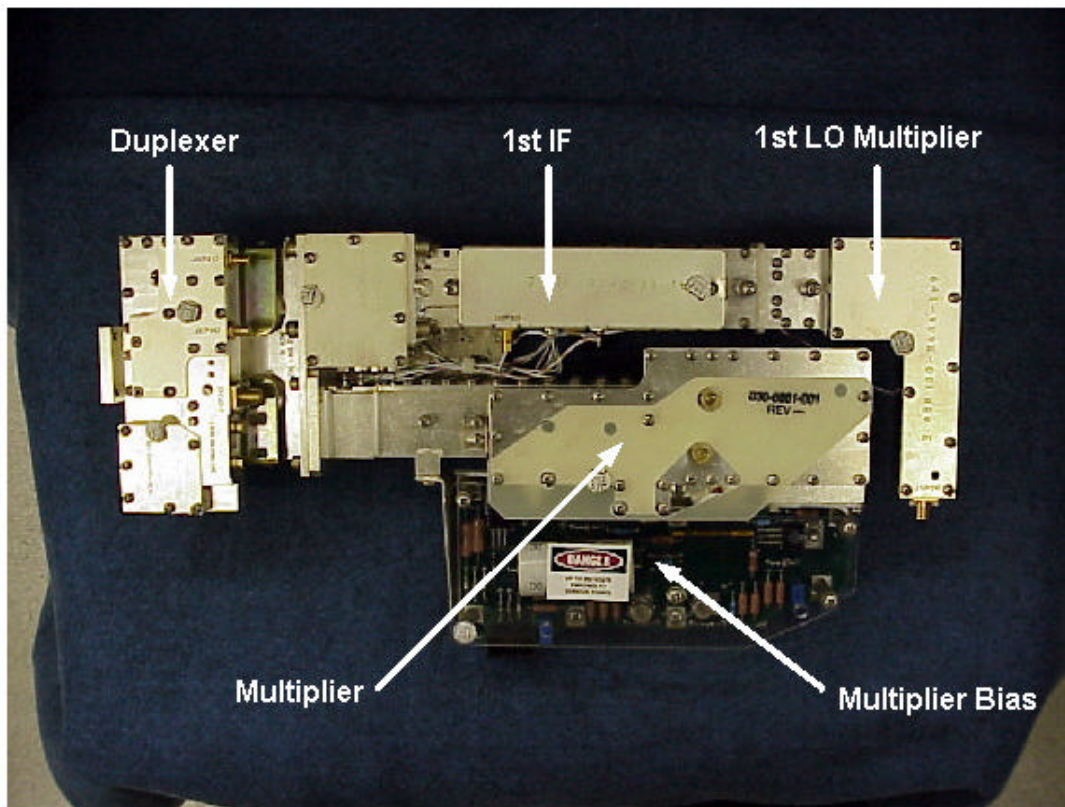


Figure 8-13. RF Center Section Left

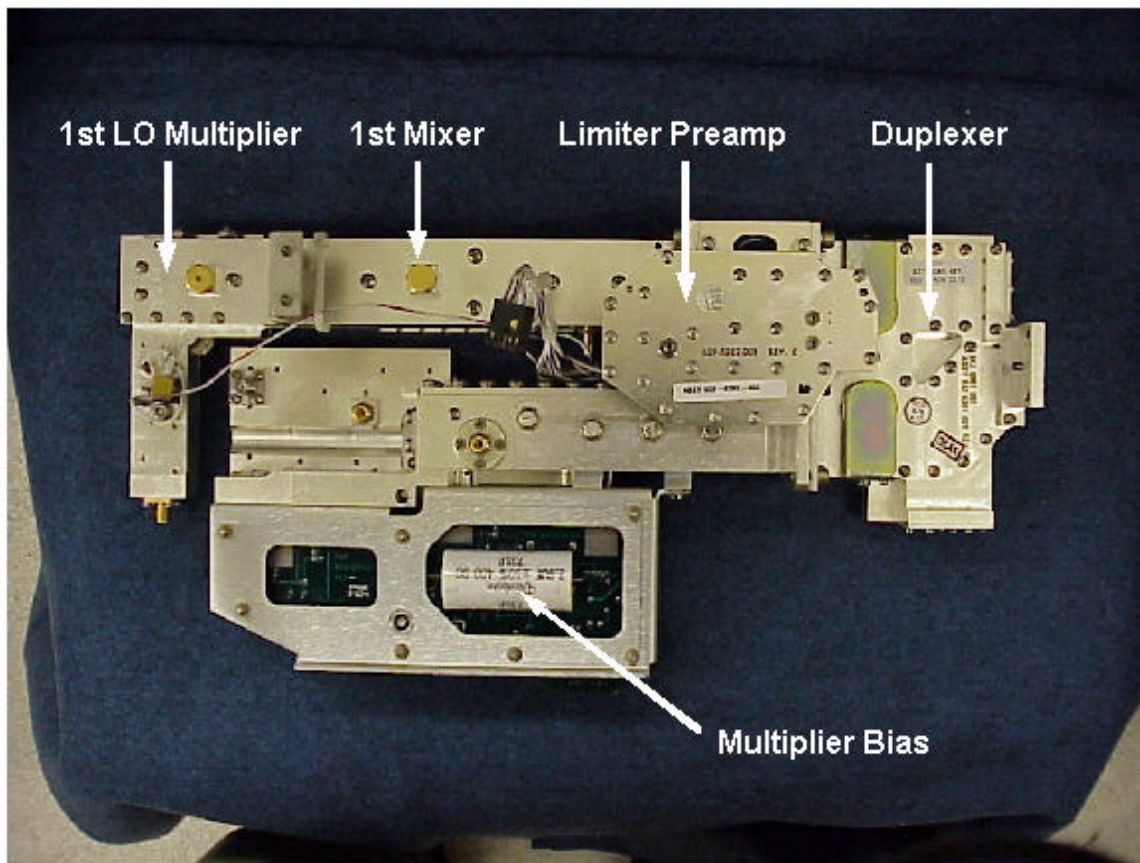


Figure 8-14. RF Center Section Right

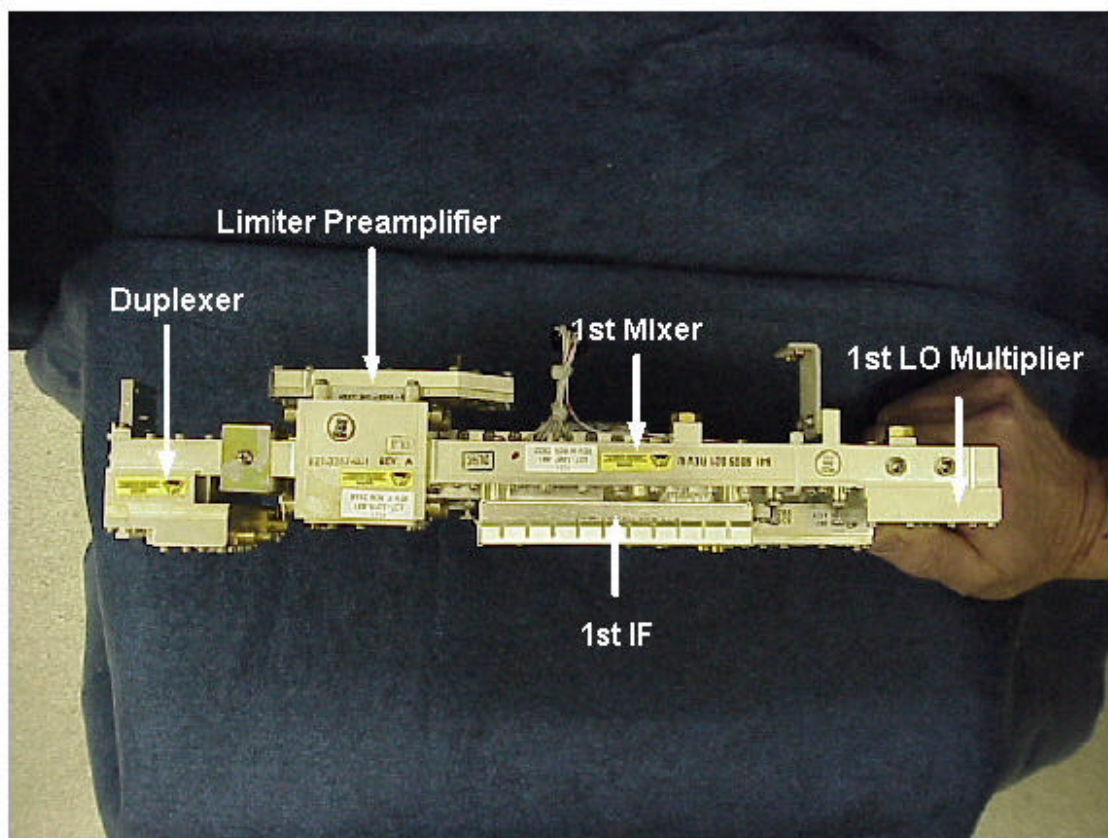


Figure 8-15. RF Center Section Top



Figure 8-16. Duplexer

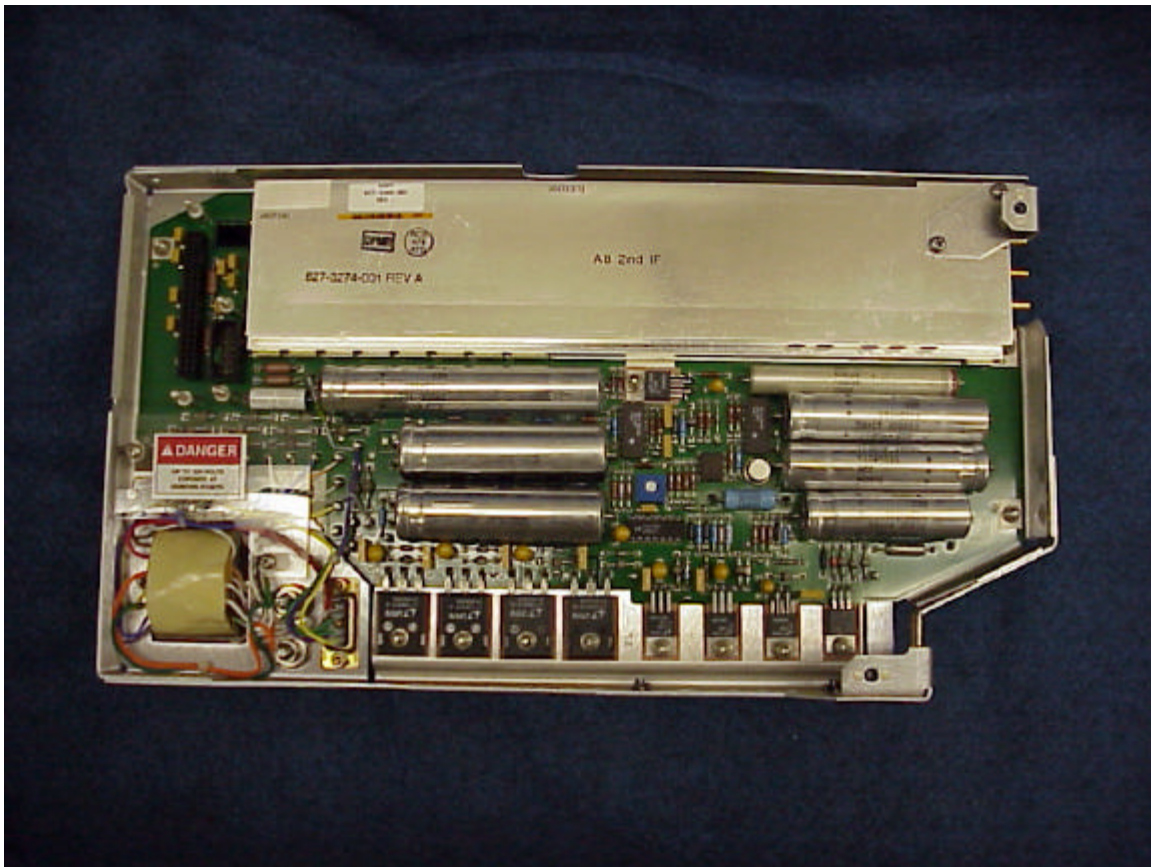


Figure 8-17. LVPS Second IF Top

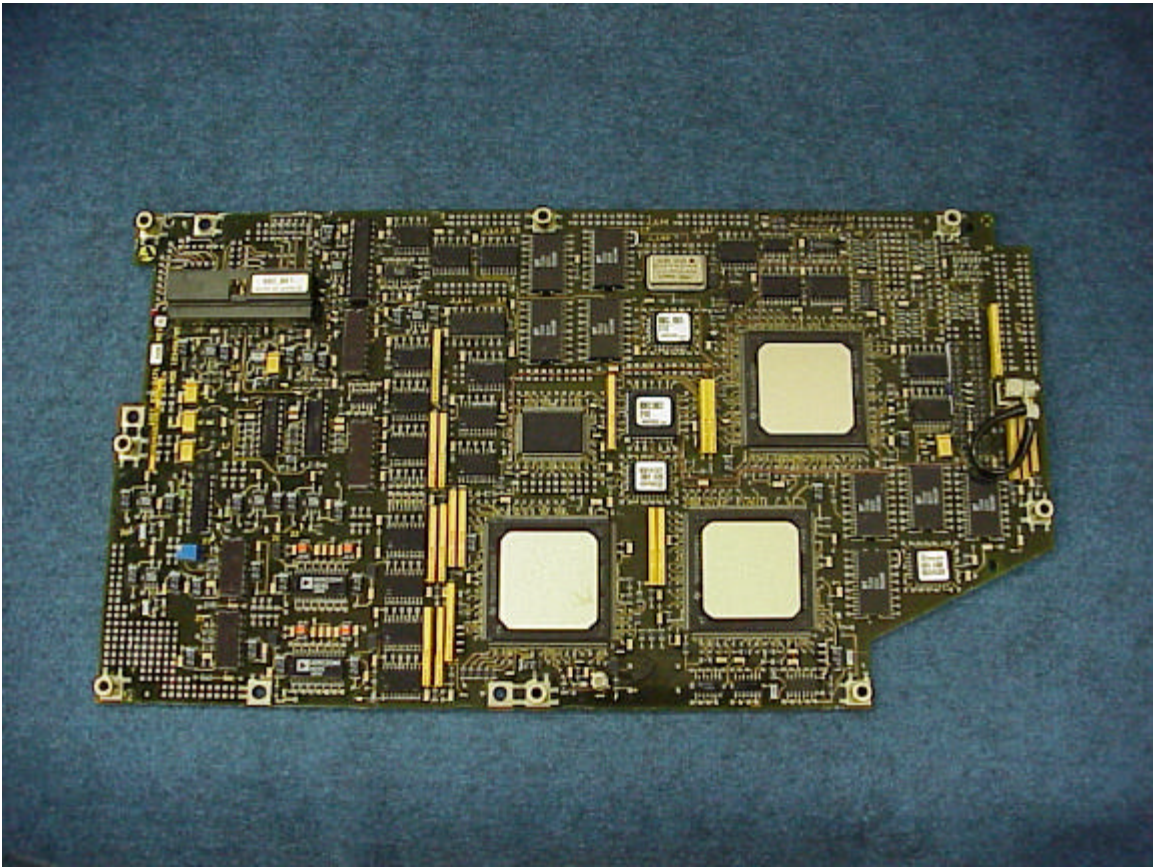


Figure 8-18. Sampler Front

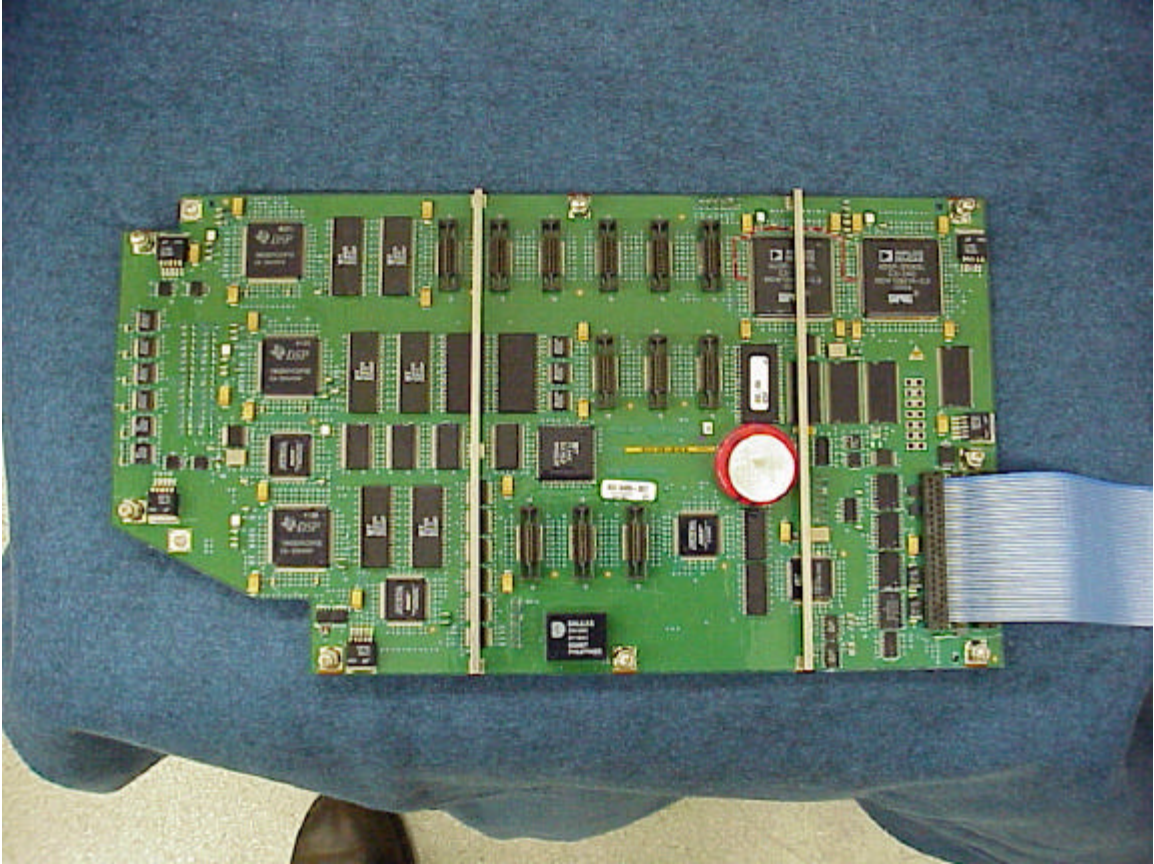


Figure 8-19. DSP Front

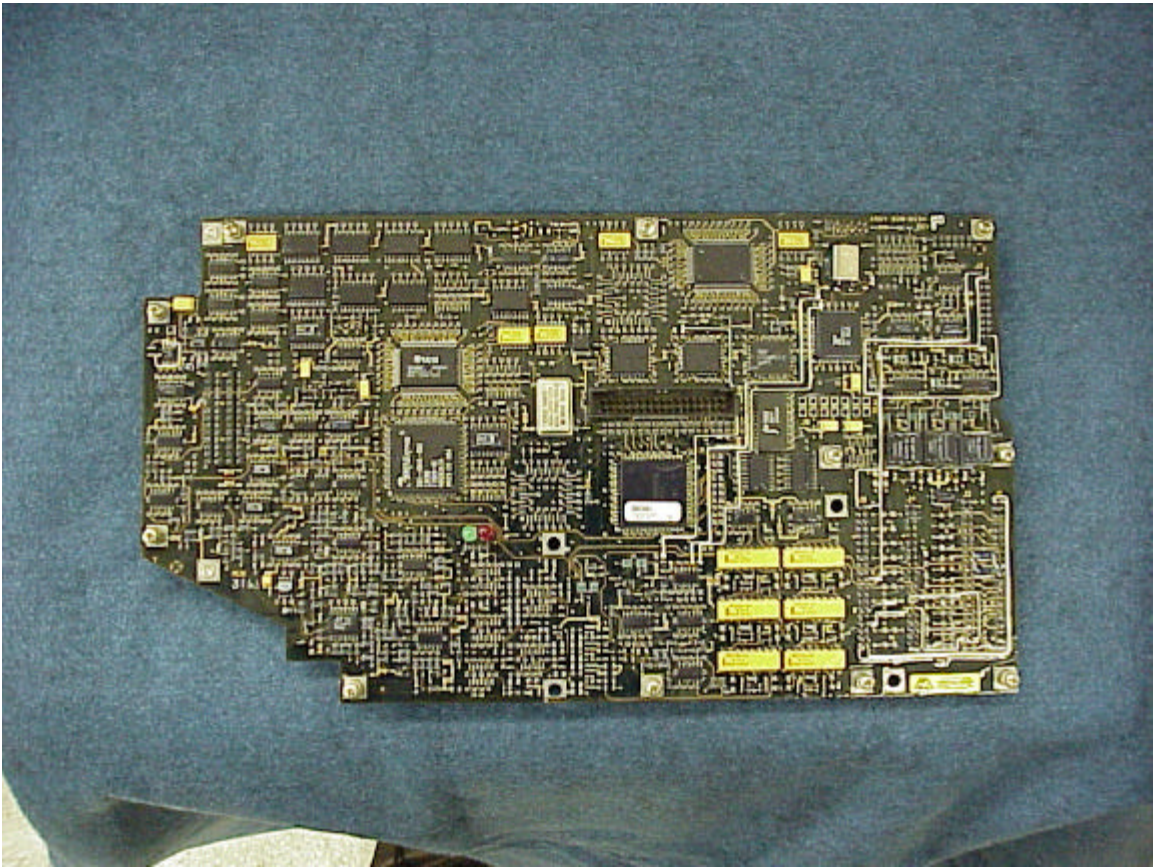


Figure 8-20. CPU Front

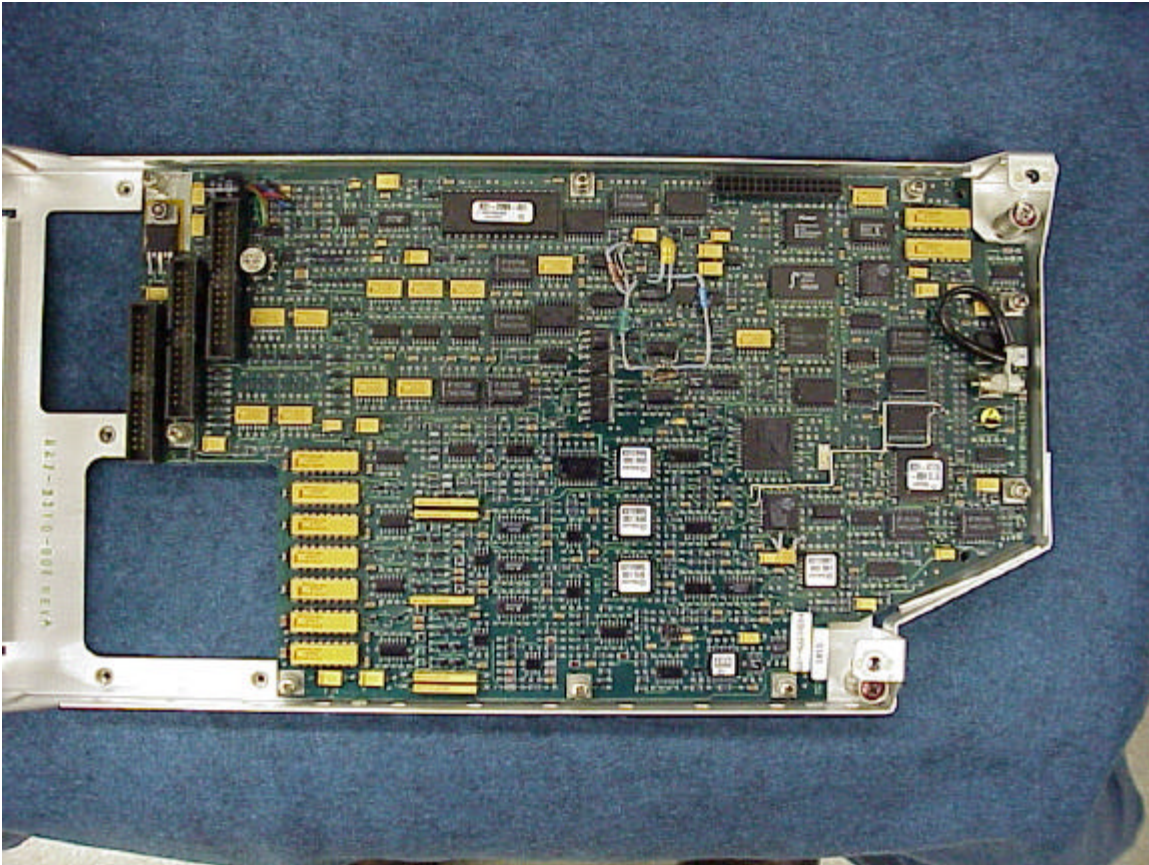


Figure 8-21. IO Card Front

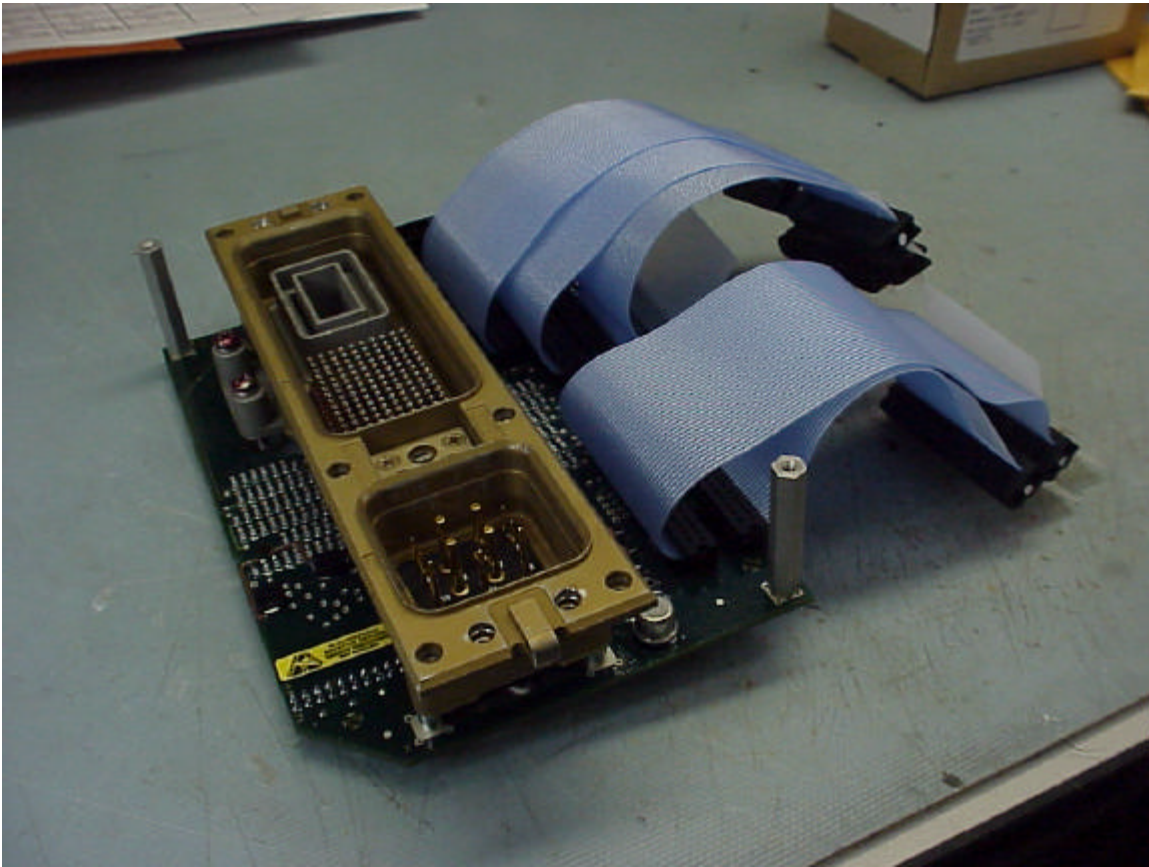


Figure 8-22. Rear Interconnect Rear