

Appendix II - Photograph of EUT 2

< I/O Port of EUT >



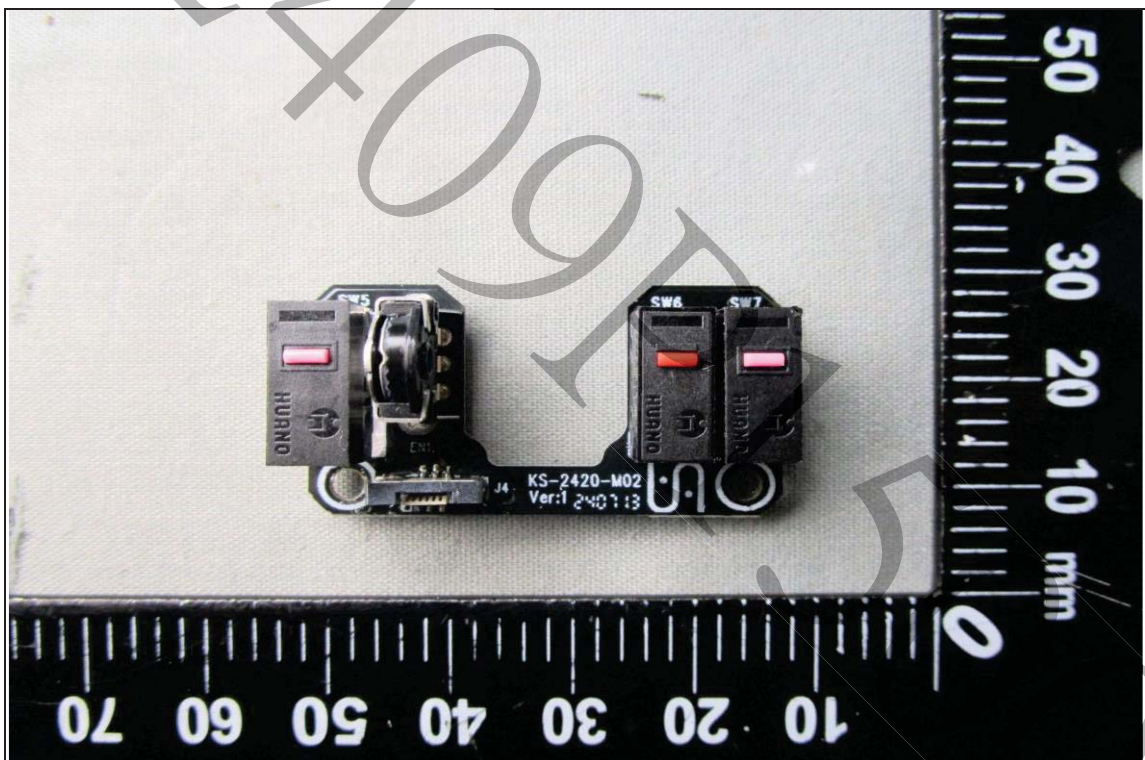
< Inside View of EUT >



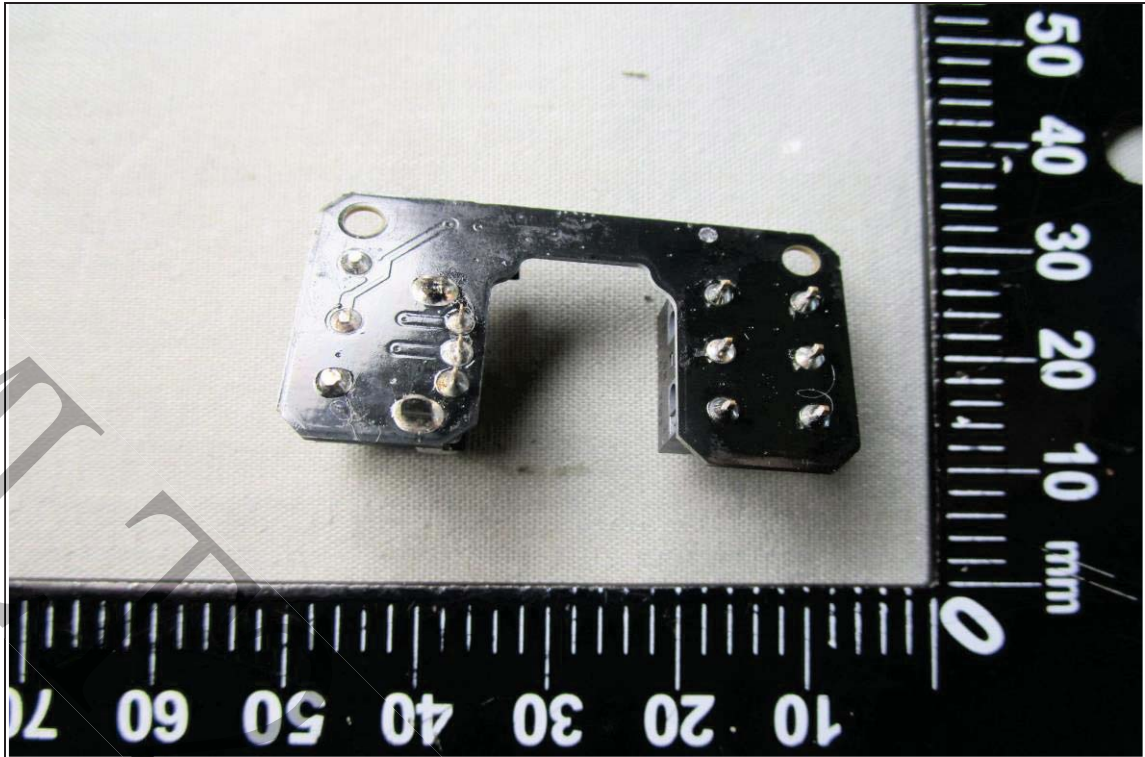
< Inside View of EUT >



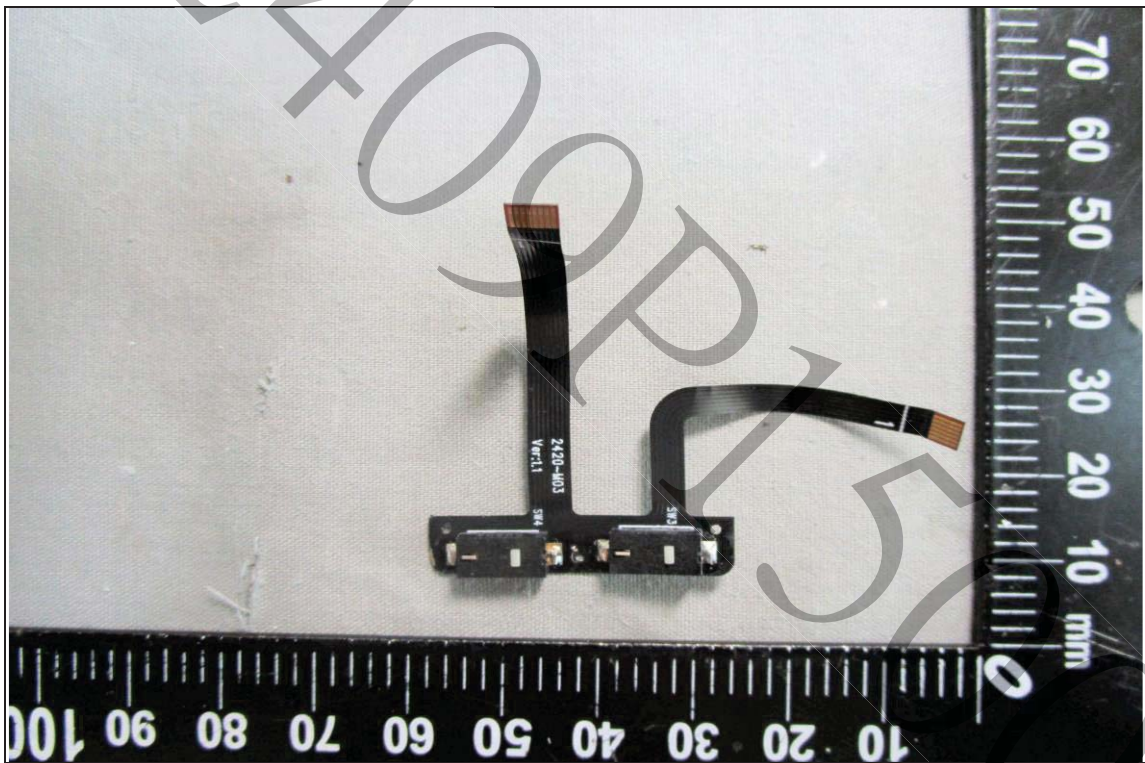
< PCB1-F >



< PCB1-R >



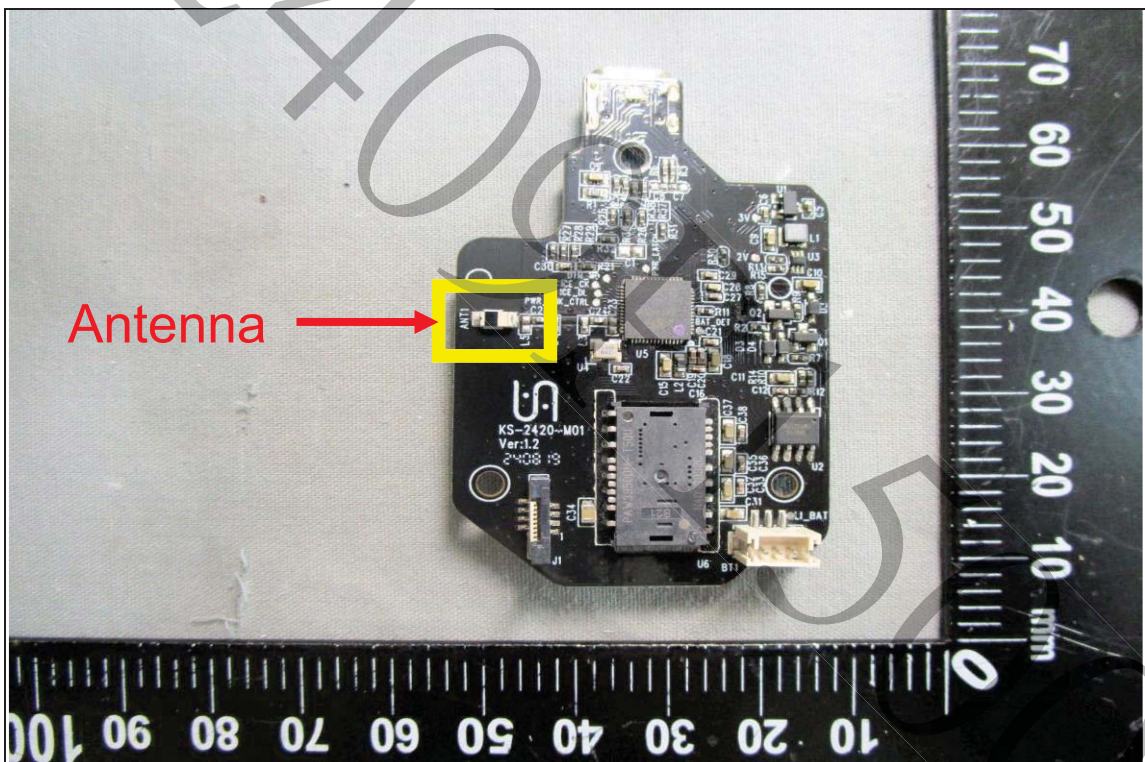
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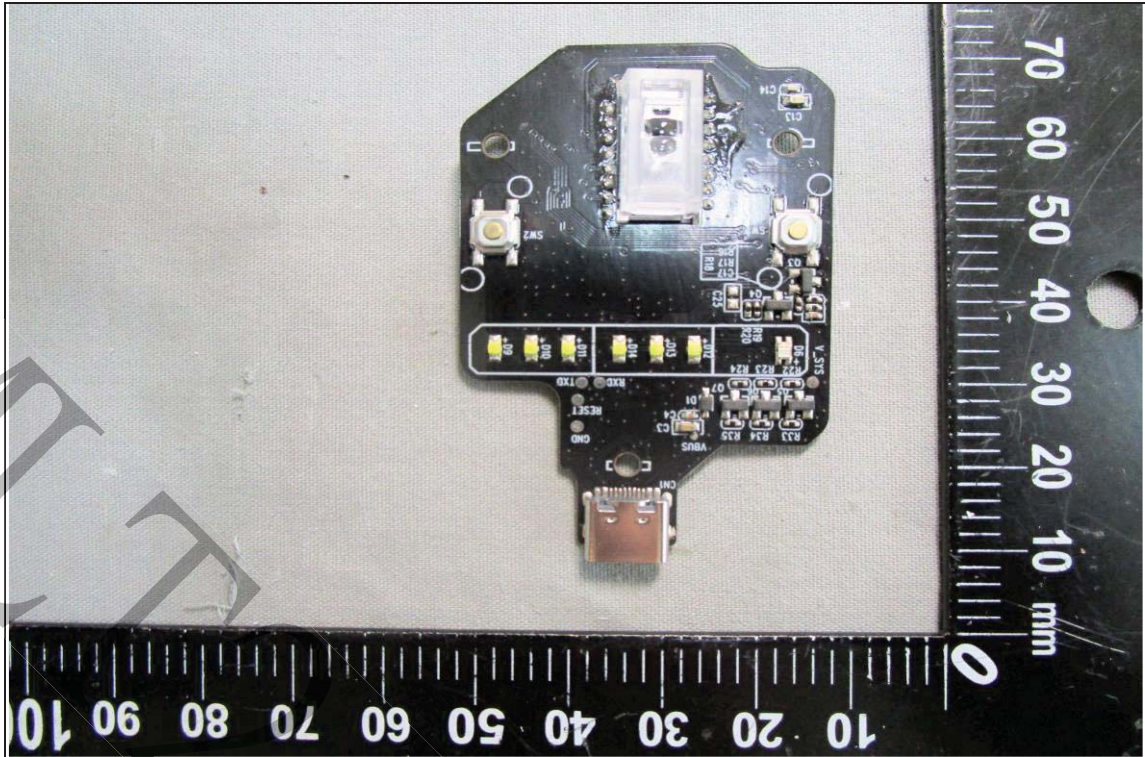
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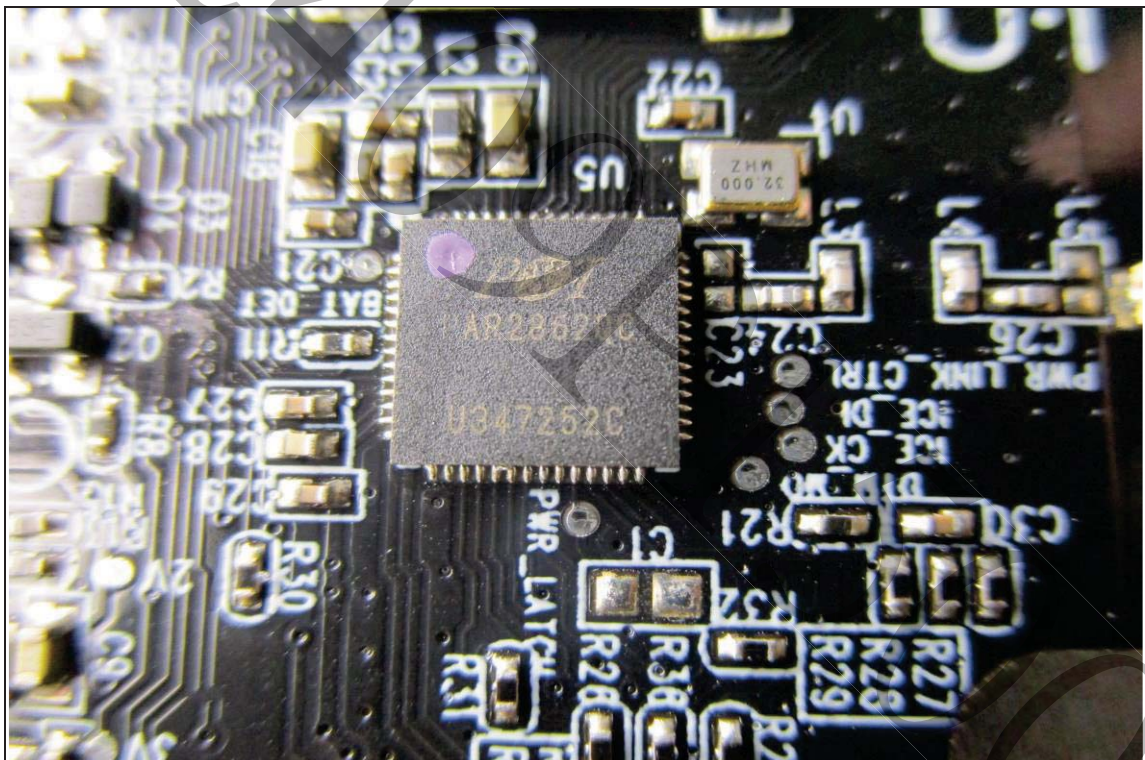
< PCB3-F >



< PCB3-R >



< Module Close in >



< Battery-F >



< Battery-R >

