

APPENDIX REPORT

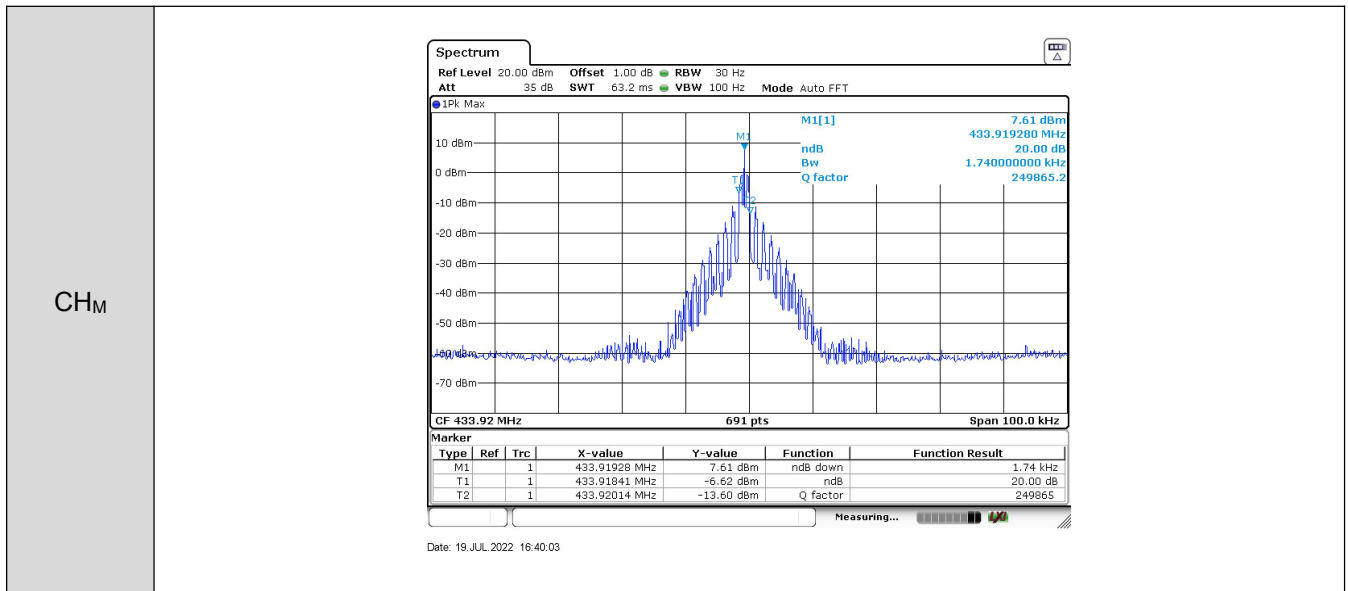
Project No.	SHT2112073506EW		
Test sample No.	YPHT21120735009	Model No.	Mate102
Start test date	2022-07-19	Finish date	2022-07-20
Temperature	23.6℃	Humidity	43%
Test Engineer	Xiaoqin Li	Auditor	Xiaodong Zhao

Appendix clause	Test item	Result
A	20dB Bandwidth	PASS
B	99% Occupied Bandwidth	PASS
C	Deactivation Time	PASS
D	Duty Cycle Corrected Factor	PASS

Appendix A: 20dB bandwidth

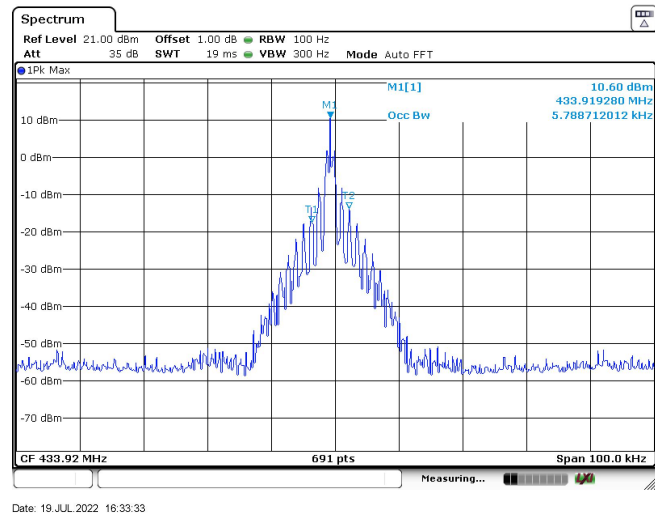
Test Channel	20dB Bandwidth (kHz)	Limit (kHz)	Result
CH _M	1.74	1084.80	Pass

NOTE:Limit=0.25%*Center Frequency=1084.80 kHz



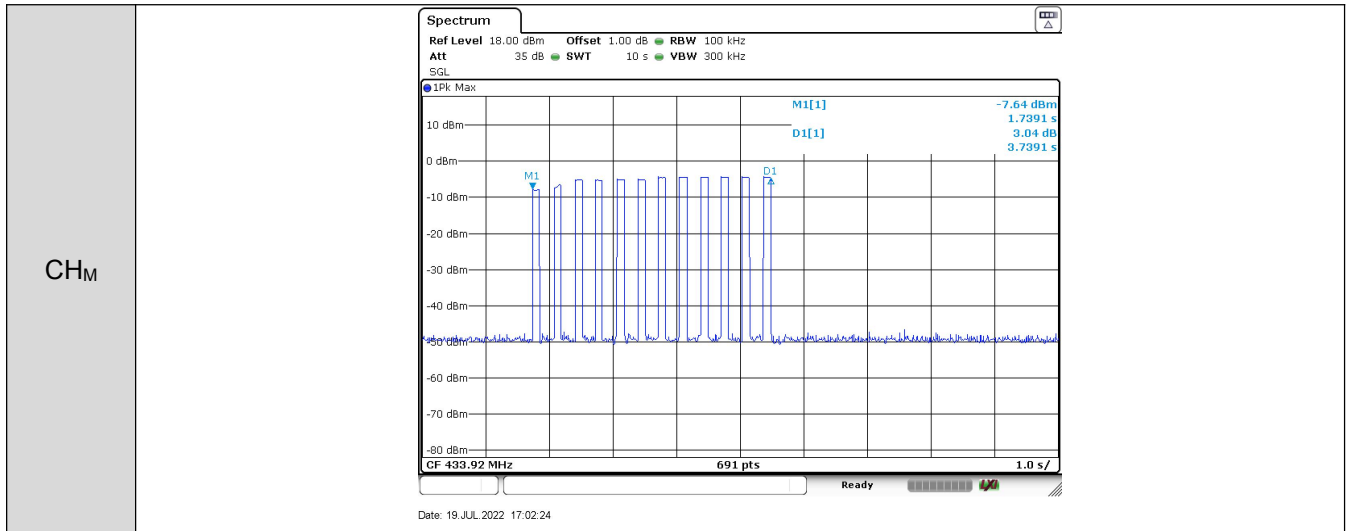
Appendix B: 99% Occupied Bandwidth

Test Channel	99% Occupied Bandwidth (kHz)	Limit (kHz)	Result
CH _M	5.79	-	Pass

CH_M

Appendix C: Deactivation Time

Transmission time (second)	Limit (second)	Result
3.74	5	Pass

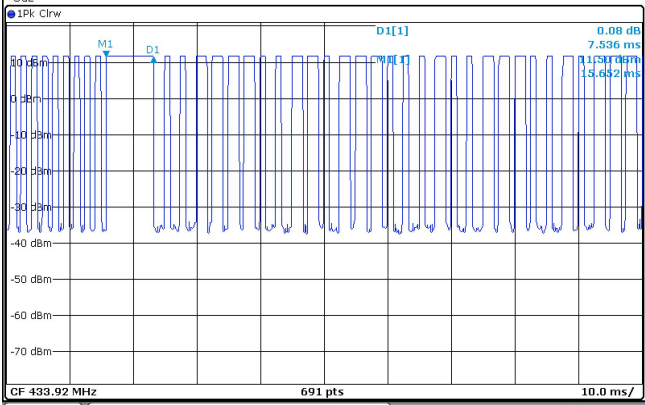
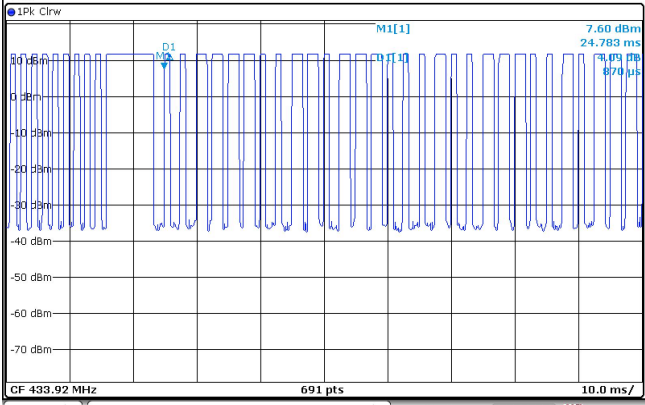
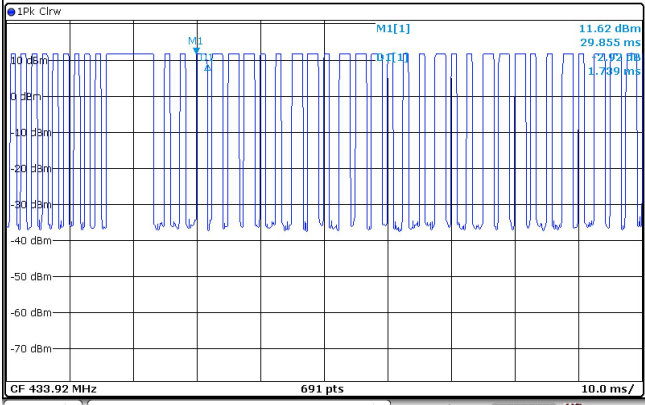


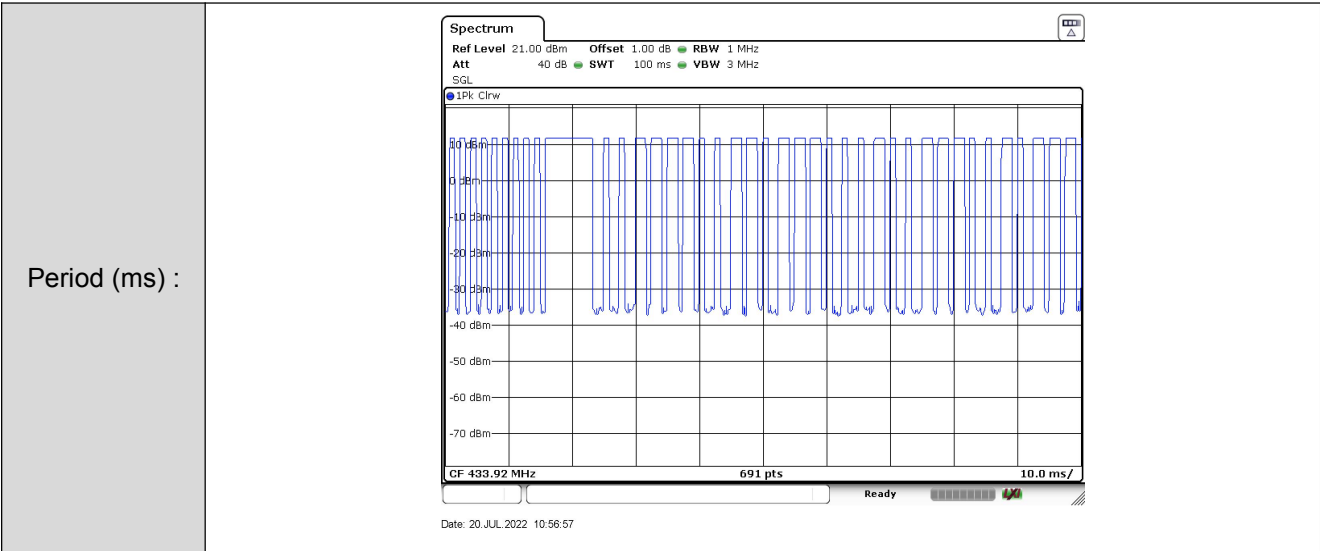
Appendix D: Duty Cycle Corrected Factor

T _{ON1} (ms) :	7.54
T _{ON1} number	1
T _{ON2} (ms) :	0.87
T _{ON2} number	22
T _{ON3} (ms) :	1.74
T _{ON3} number	17
Period (ms) :	100
Duty Cycle :	$= (7.54 \times 1 + 0.87 \times 22 + 1.74 \times 17) / 100 = 0.56$
Duty Cycle Corrected Factor:	$= 20 \times \log(0.56) = -5.04$

Note:

- 1) Duty cycle= $T_{on1} \times T_{ON1} \text{ number} + T_{on2} \times T_{ON2} \text{ number} + T_{on3} \times T_{ON3} \text{ number} / T_{period}$
- 2) Duty Cycle Corrected Factor/DCCF= $20 \times \log (\text{Duty Cycle})$

T_{ON1} (ms) :	Spectrum Ref Level 21.00 dBm Offset 1.00 dB RBW 1 MHz Att 40 dB SWT 100 ms VBW 3 MHz SGL 1Pk Clrw  CF 433.92 MHz 691 pts 10.0 ms/ Date: 20 JUL 2022 10:57:24
T_{ON2} (ms) :	Spectrum Ref Level 21.00 dBm Offset 1.00 dB RBW 1 MHz Att 40 dB SWT 100 ms VBW 3 MHz SGL 1Pk Clrw  CF 433.92 MHz 691 pts 10.0 ms/ Date: 20 JUL 2022 10:57:42
T_{ON3} (ms) :	Spectrum Ref Level 21.00 dBm Offset 1.00 dB RBW 1 MHz Att 40 dB SWT 100 ms VBW 3 MHz SGL 1Pk Clrw  CF 433.92 MHz 691 pts 10.0 ms/ Date: 20 JUL 2022 10:58:45



-----End of Report-----