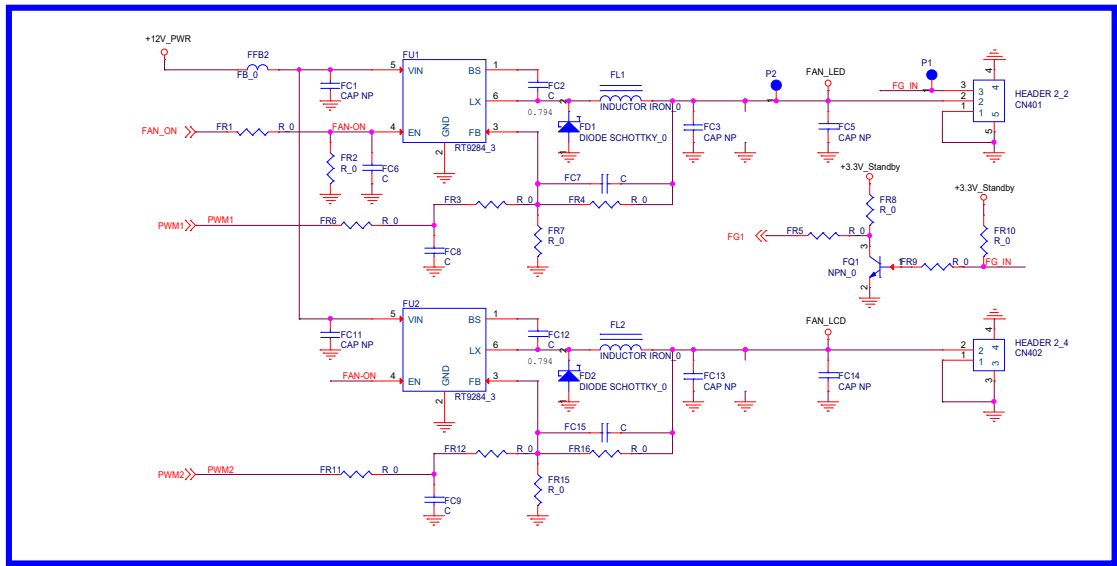




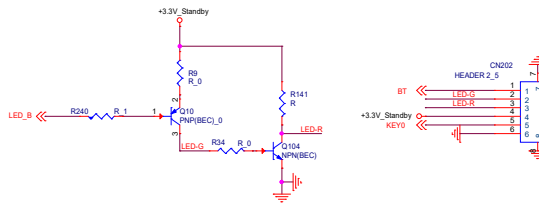
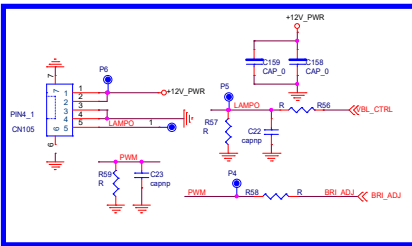
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注意：你提供大电感值。

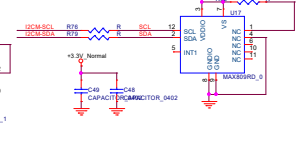
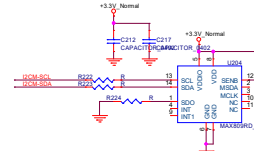
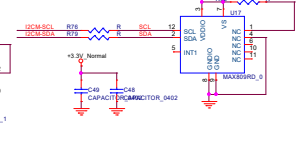
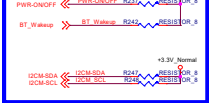
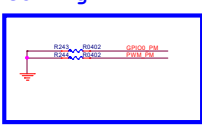
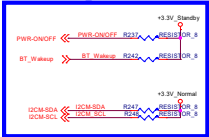
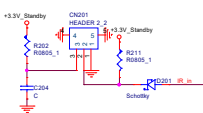
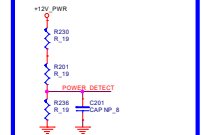
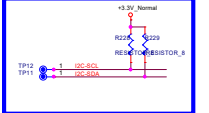
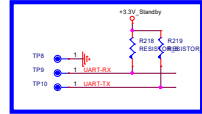
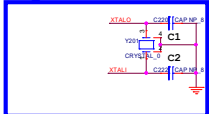
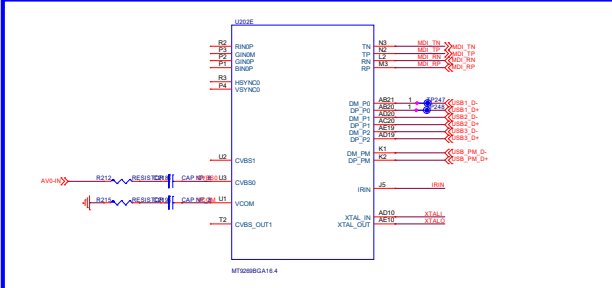
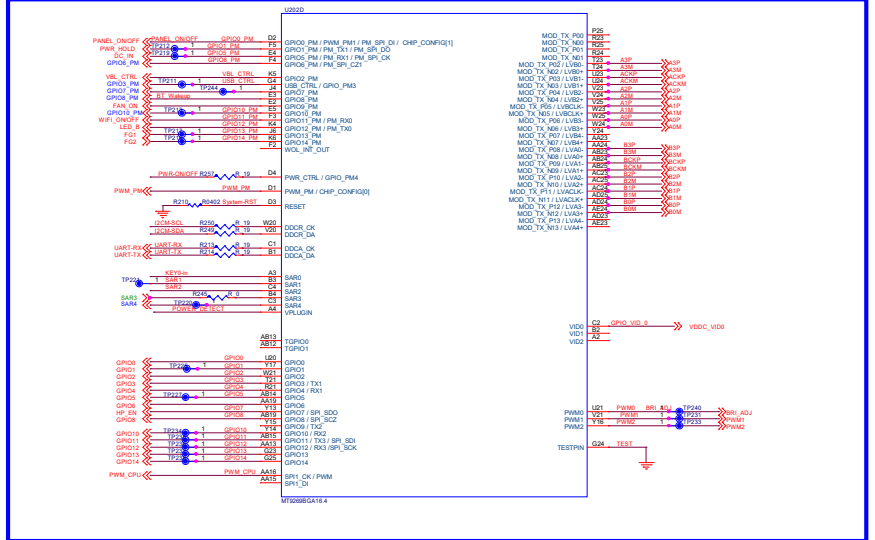
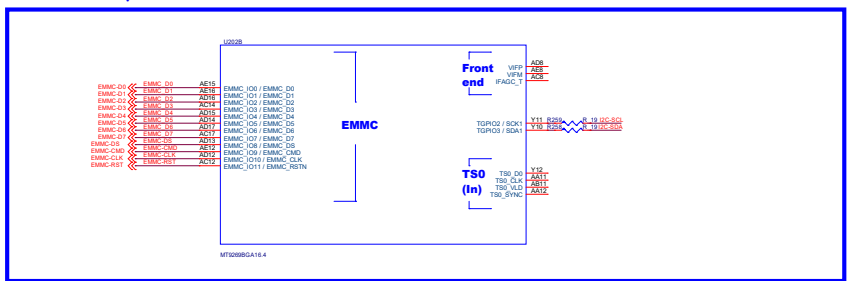
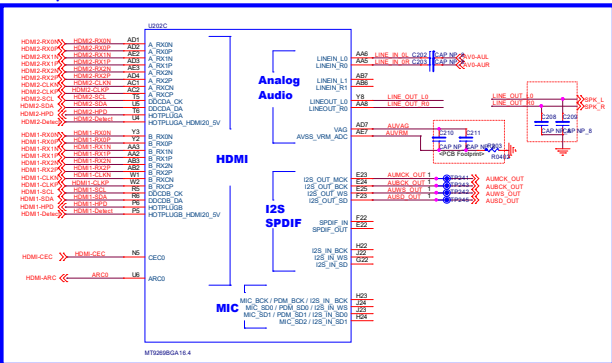
$V_{out}=0.6V \cdot (1+R1/R2)$

| VID[0] | R2     | VDDC   |
|--------|--------|--------|
| 00     | 10X    | 0.950V |
| L      | 8.008K | 1.000V |

H : Power on  
 L : Power off  
 STANDBY PWR-ON/OFF PWR-ON/OFF



|                                                                                                            |                        |
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Figure 10 shows the recommended power supply decoupling circuit for the AD9224. The circuit includes three main components connected to the power pins:

- VDDC:** A 2.8kΩ resistor is connected in series with a 22pF capacitor (CAP NP\_22) to the +0.95V\_VDDC supply. The capacitor should be placed close to the pin.
- AVDDC\_MOD:** A 10mA, 20mΩ resistor is connected to the +0.95V\_VDDC supply.
- DVDD\_DOR:** A 120mA, 25mΩ resistor is connected to the +0.95V\_VDDC supply.

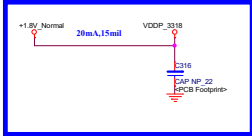
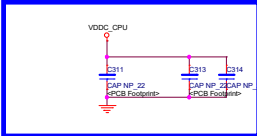


Diagram illustrating a decoupling capacitor connection:

- Capacitor value:  $0.33\mu\text{F}$
- Supply voltage:  $+3.3\text{V Standby}$
- IC pin: AVDD - M000E
- Capacitor label: CAP NP\_22
- Footprint label: PCB Footprint
- Location: Near IC

3.3V\_Netmil

10k

1k

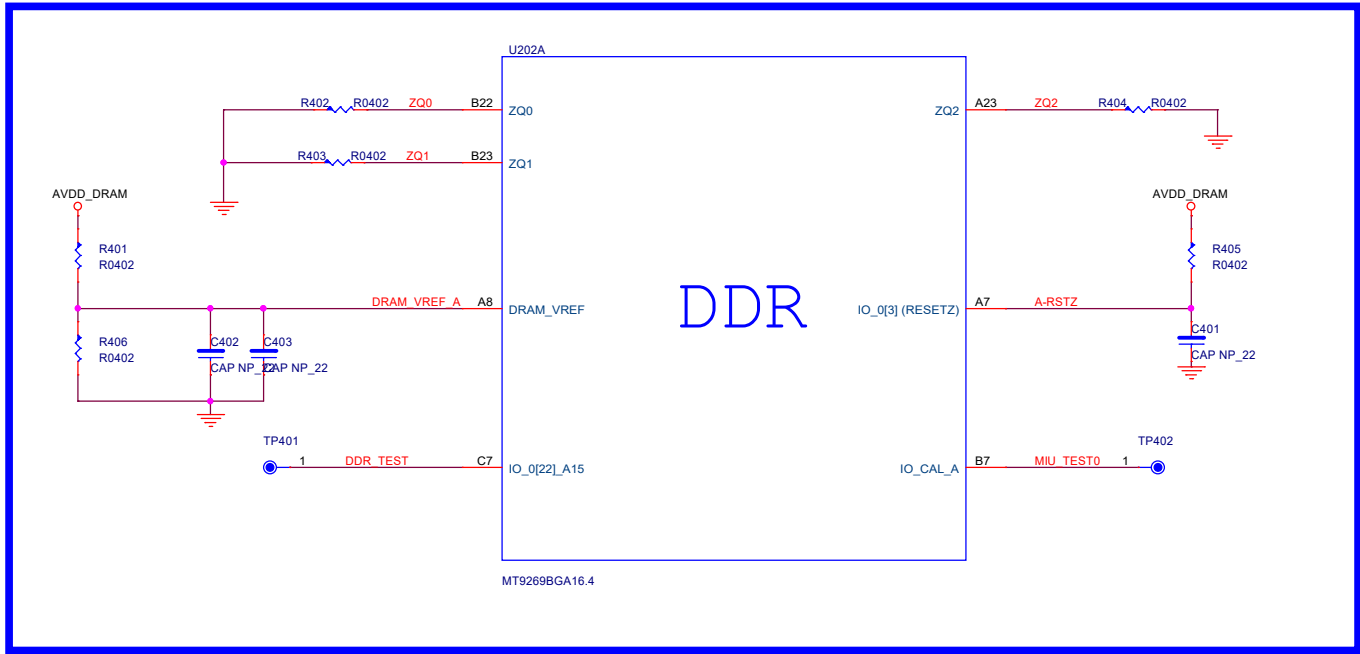
VDDP33

EXP NP\_22

PCB Footprint

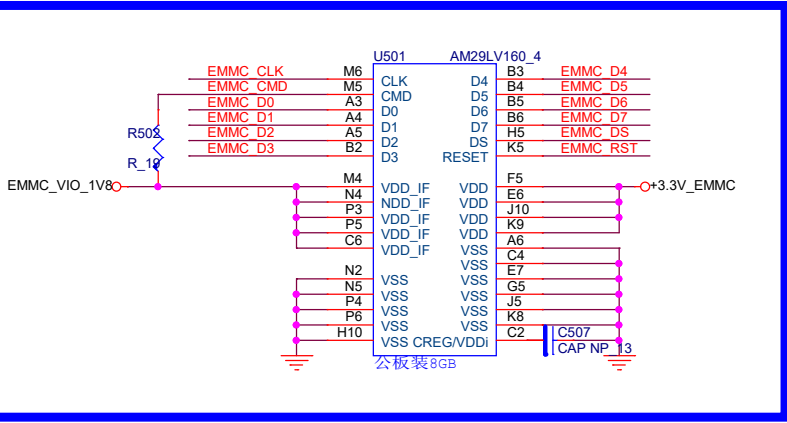
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DRAM

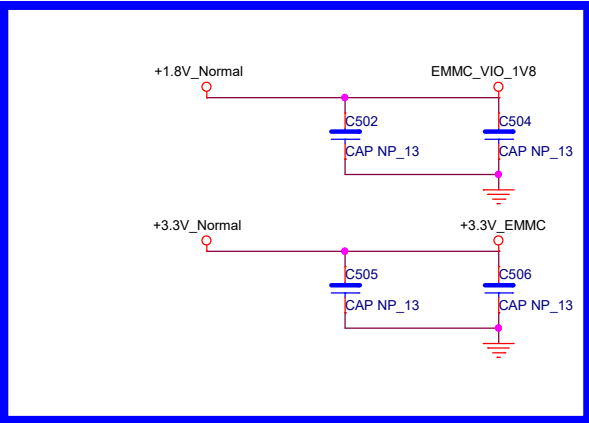


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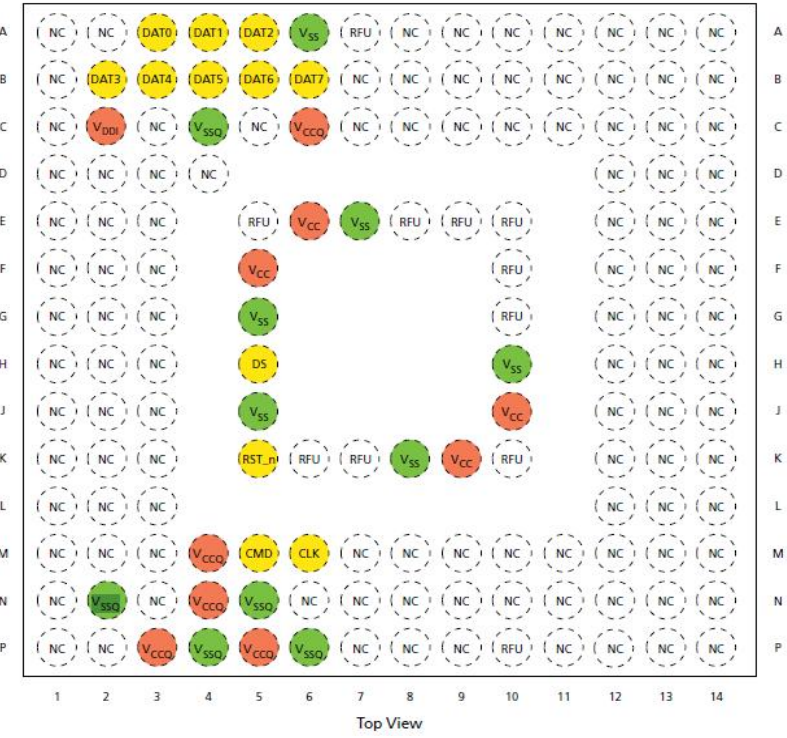
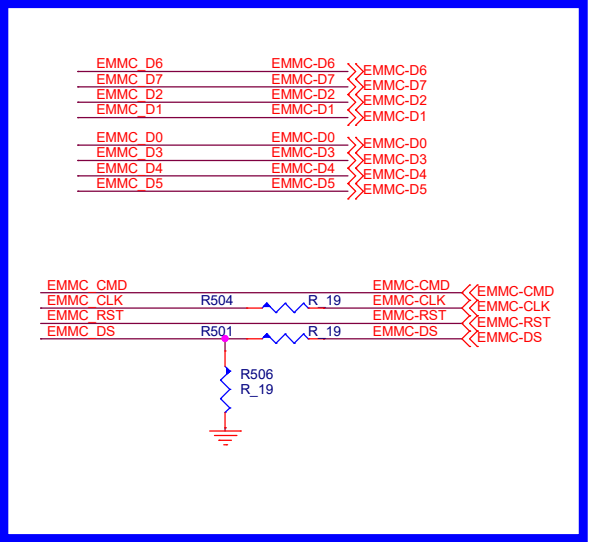
eMMC



Power



NET



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eMMC

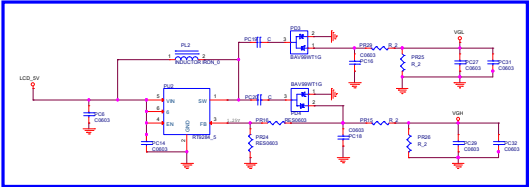
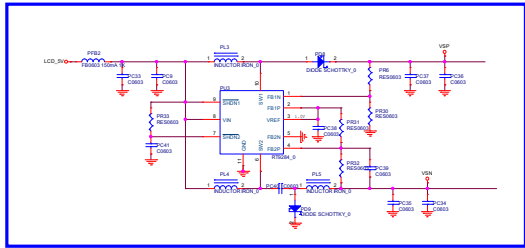
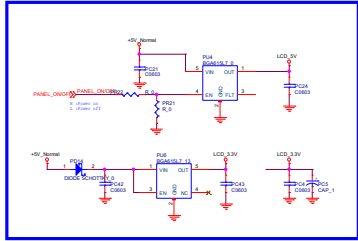
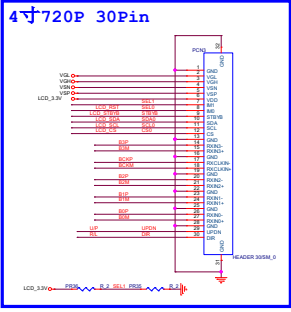
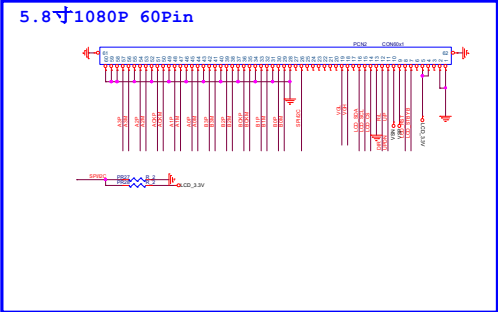
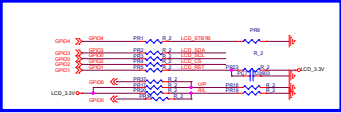
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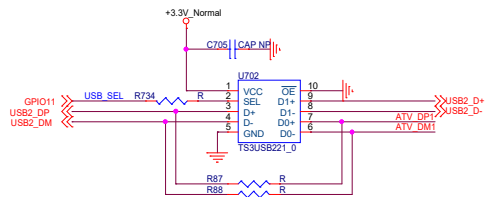
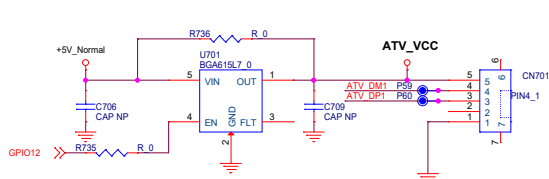
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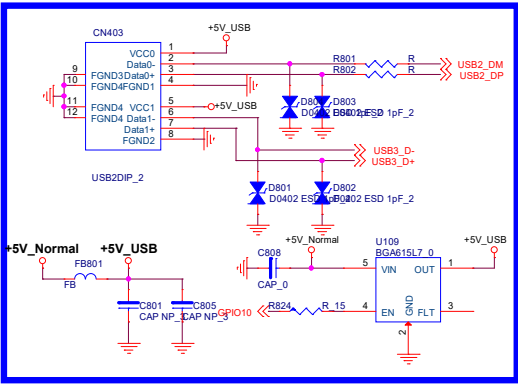


The schematic diagram illustrates the internal wiring of the HDMI2\_C2 connector. On the left, the CPT02 component is shown with its pins connected to various signal lines. These lines are then connected to the corresponding pins of the HDMI2\_C2 connector. The signal lines include TX2+, TX2-, TX1+, TX1-, TX0+, TX0-, TXC+, TXC-, TXC, CEC, SCL, SDA, RSV0, +5V, and HPD. The connector pins are labeled with their respective functions: HDMI2-RX2P, HDMI2-RX2N, HDMI2-RX1P, HDMI2-RX1N, HDMI2-RX0P, HDMI2-RX0N, HDMI2-CLKN, HDMI2-CLKP, HDMI2-SCL, HDMI2-SDA, HDMI2-CEC, HDMI2-HPD, and +5V\_HDMI2. A detailed view of the CPT02 component shows its internal structure and the connection points for the signal lines. A separate circuit diagram for the +5V\_HDMI2 signal shows a voltage divider network with resistors R722, R724, R726, and R728, and capacitors C703 and R720.

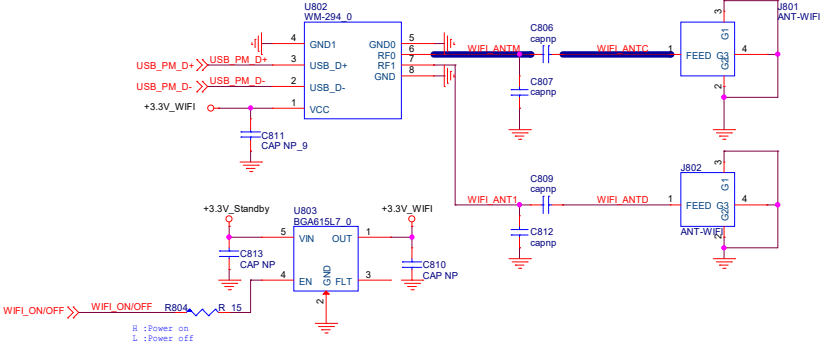


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USB Interface

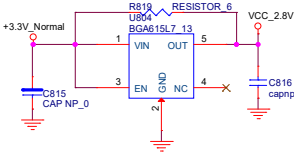
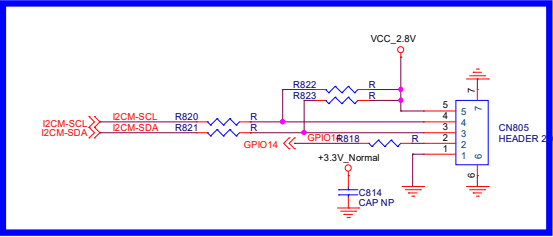
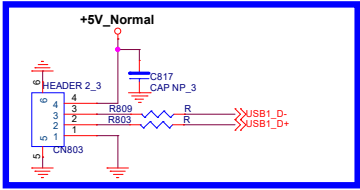
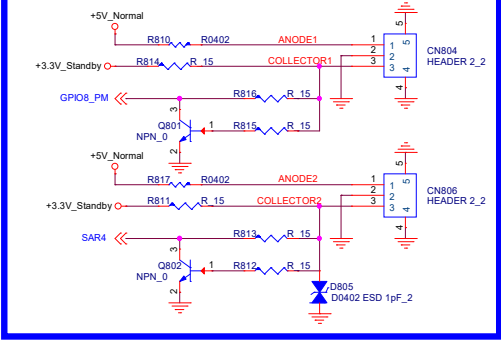


50 ohm impedance RF trace



WiFi ANT

PI TEST



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