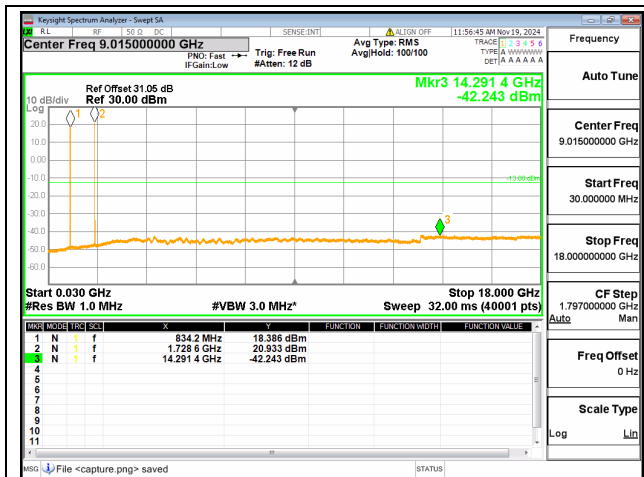
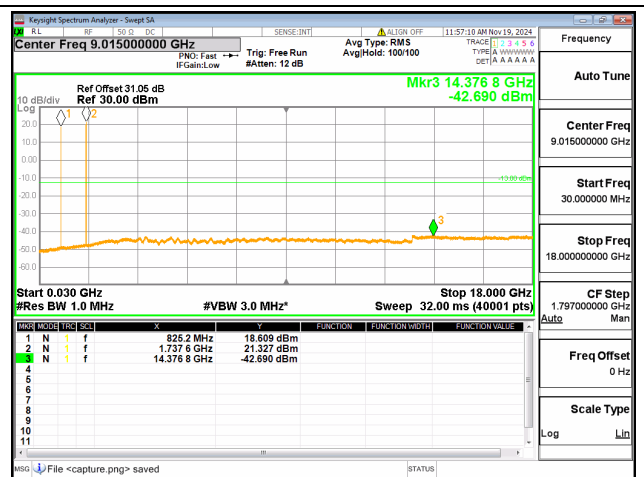
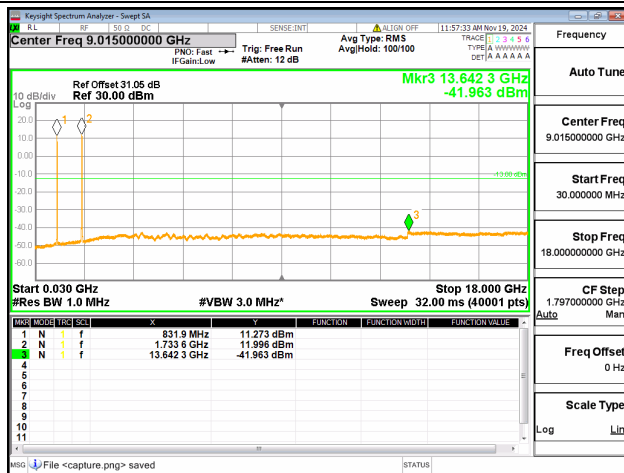




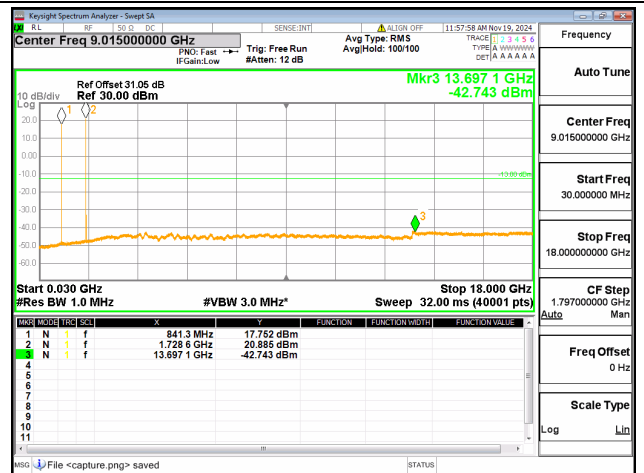
4A-5A / 10+10MHz / QPSK / Mid+Low CH /
1@0-1@49



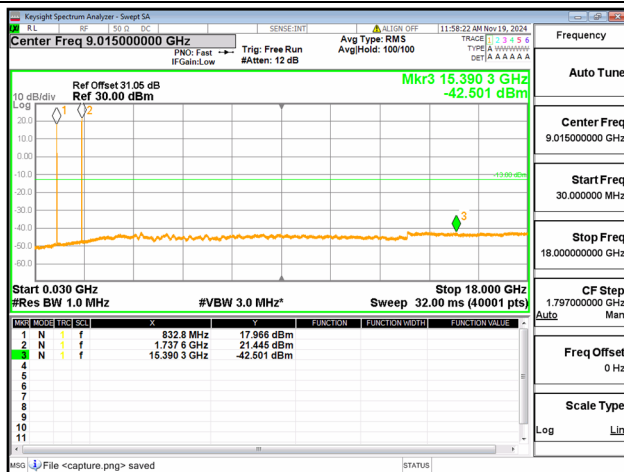
4A-5A / 10+10MHz / QPSK / Mid+Low CH /
1@49-1@0



4A-5A / 10+10MHz / QPSK / Mid+Low CH /
50@0-50@0



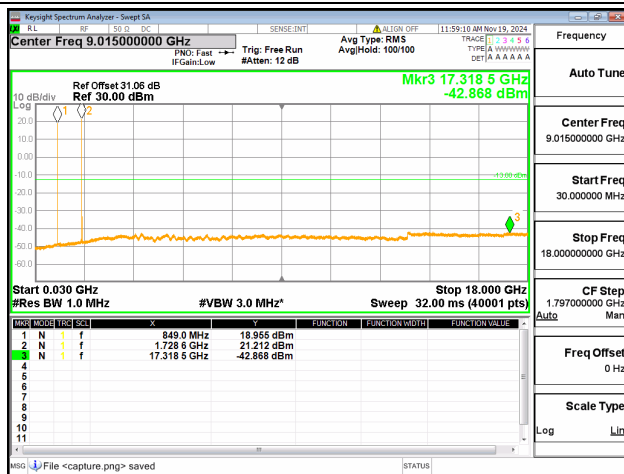
4A-5A / 10+10MHz / QPSK / Mid+Mid CH /
1@0-1@49



4A-5A / 10+10MHz / QPSK / Mid+Mid CH /
1@49-1@0



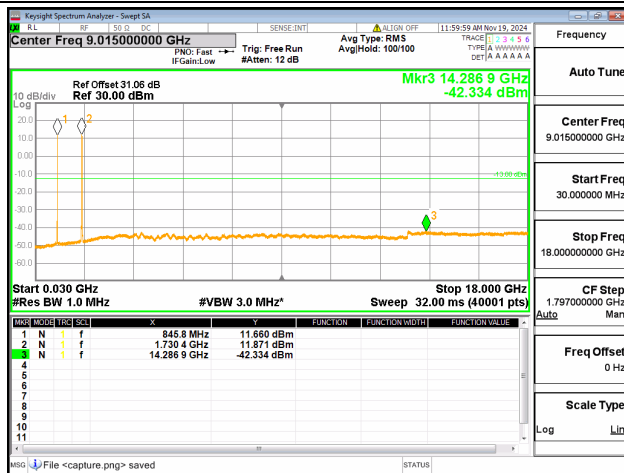
4A-5A / 10+10MHz / QPSK / Mid+Mid CH /
50@0-50@0



4A-5A / 10+10MHz / QPSK / Mid+High CH /
1@0-1@49



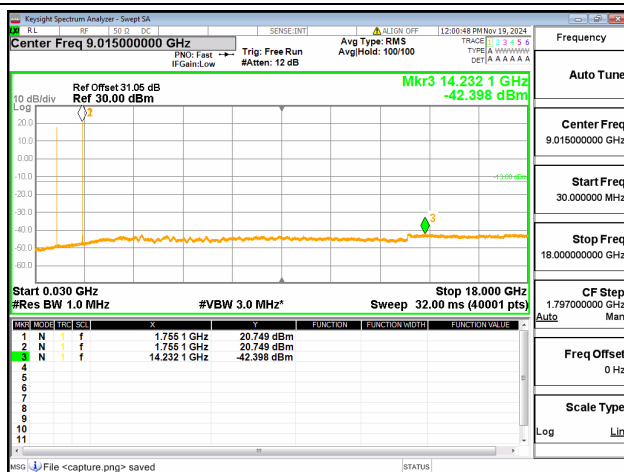
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1@49-1@0



4A-5A / 10+10MHz / QPSK / Mid+High CH /
50@0-50@0



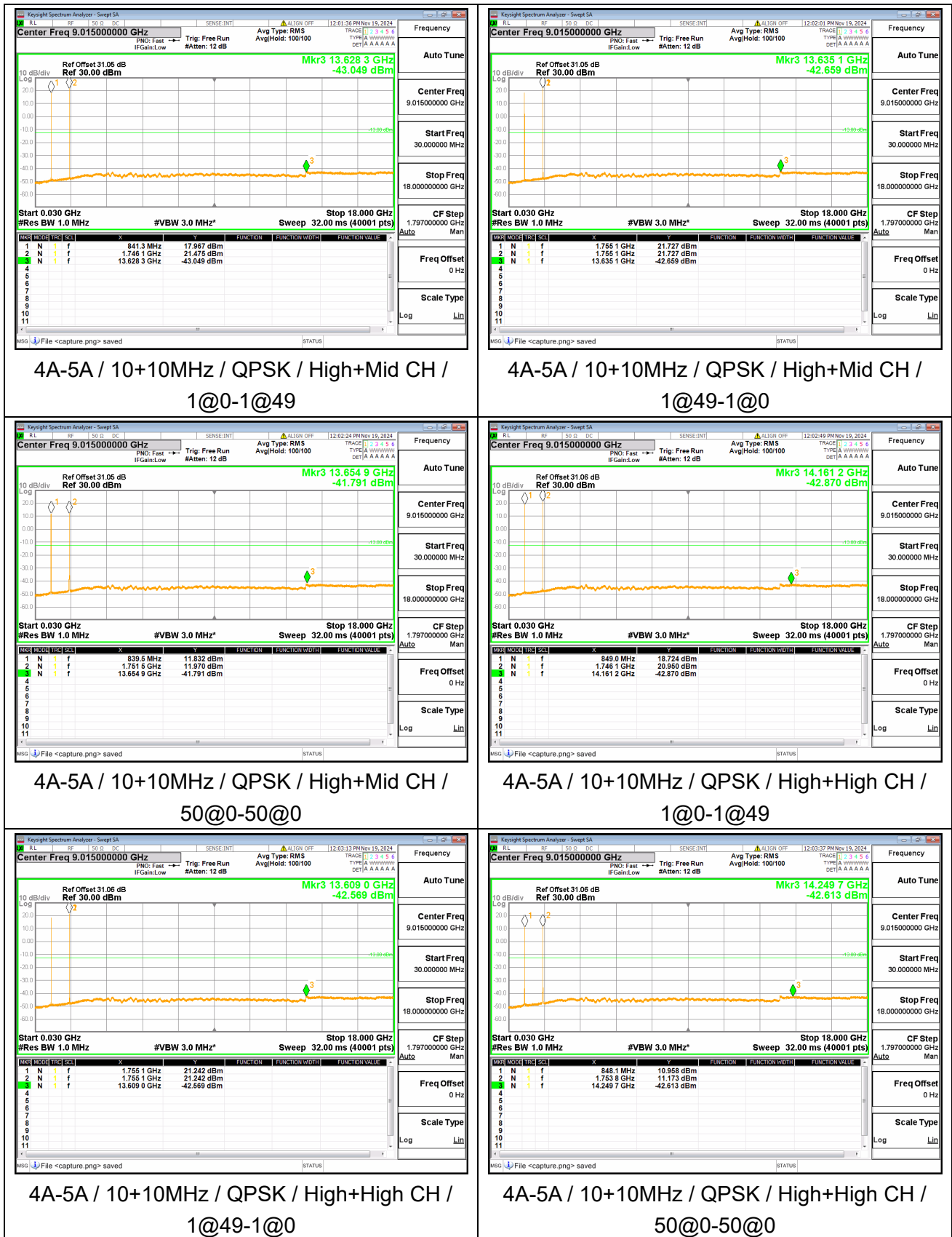
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1@0-1@49

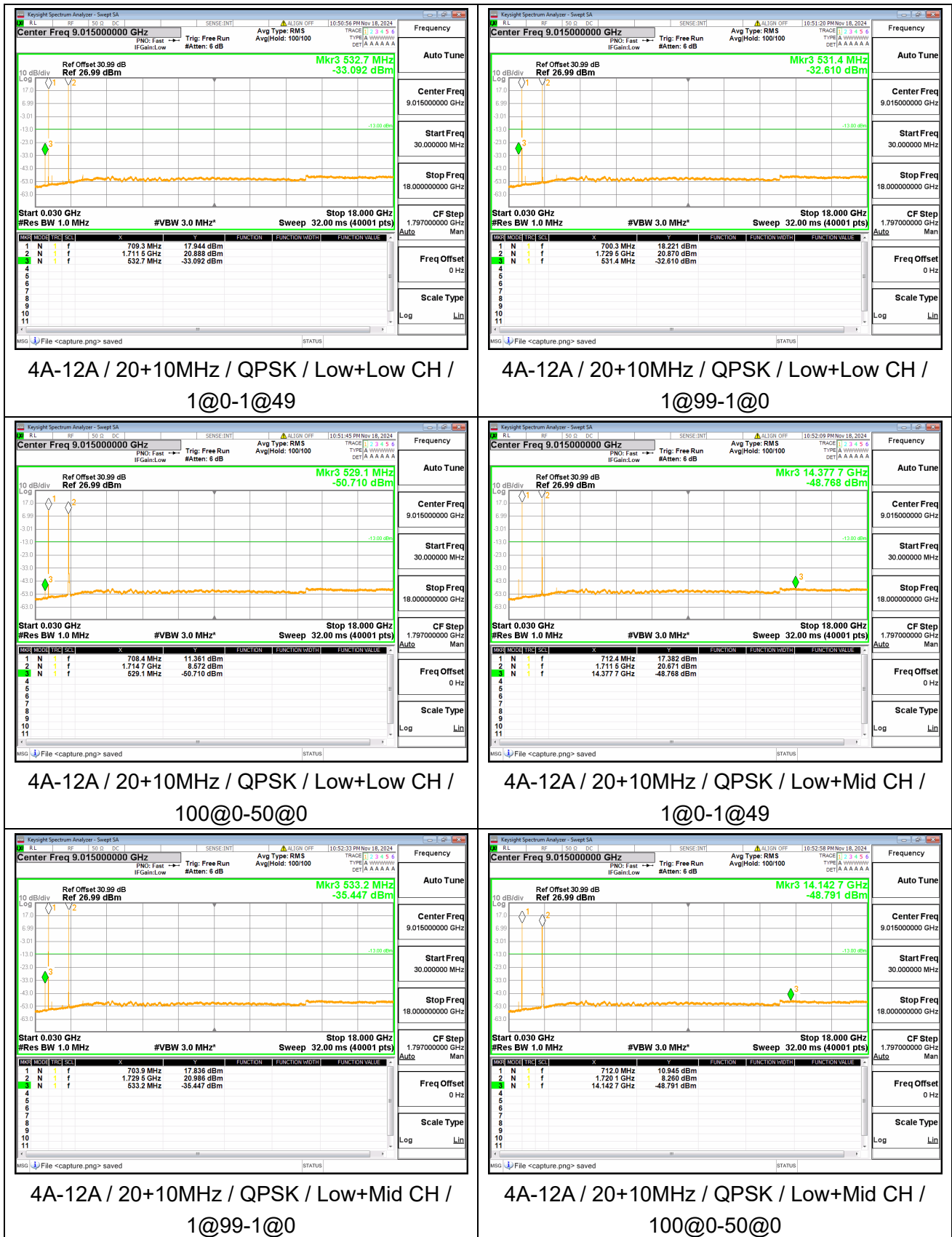


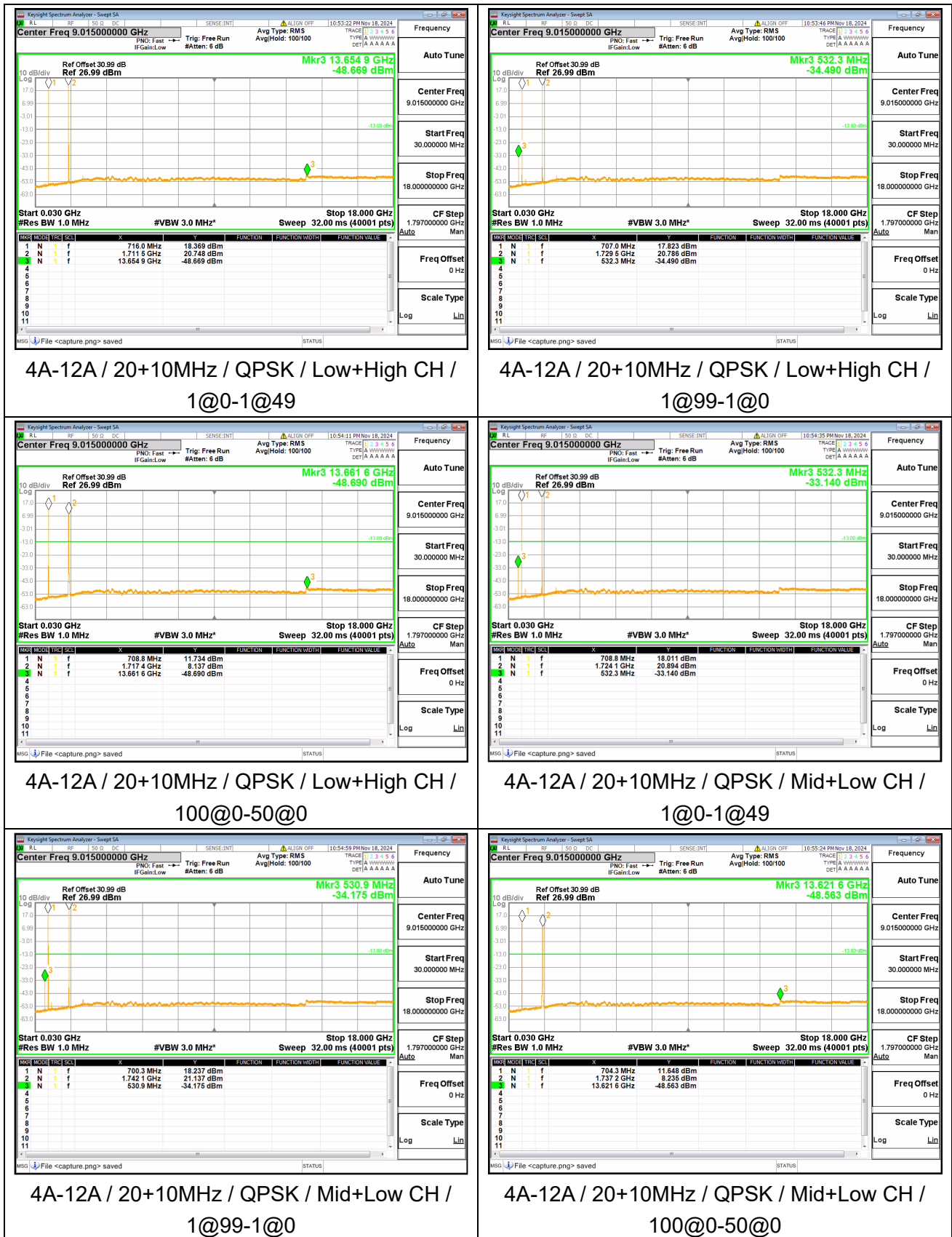
4A-5A / 10+10MHz / QPSK / High+Low CH /
1@49-1@0

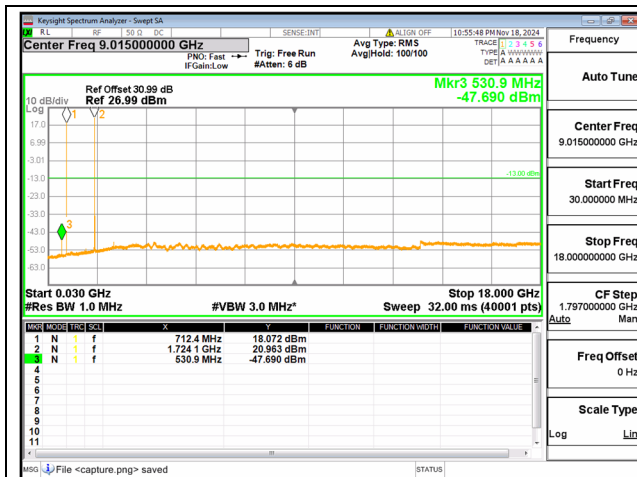


4A-5A / 10+10MHz / QPSK / High+Low CH /
50@0-50@0

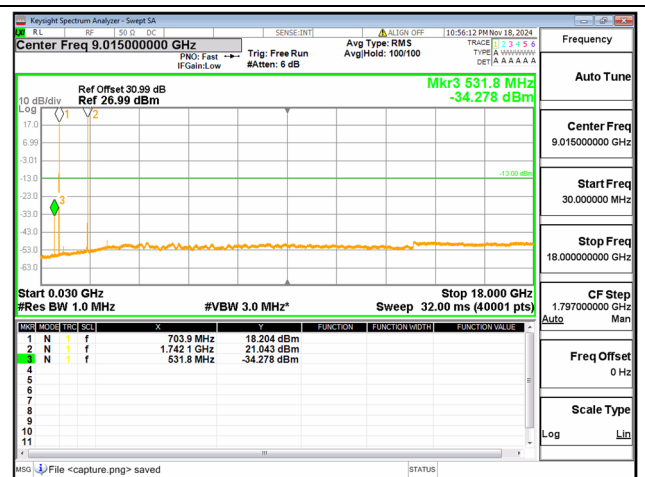




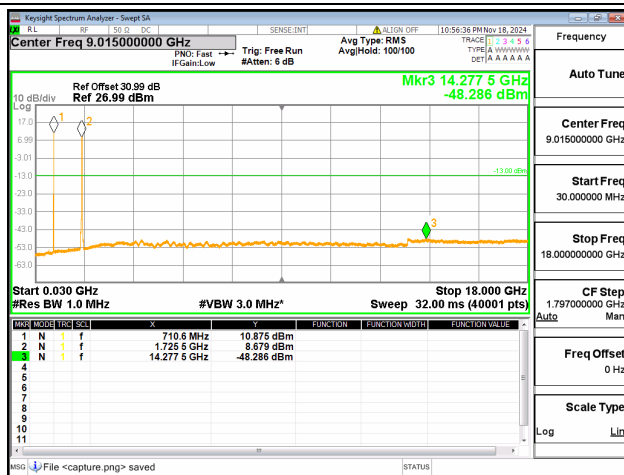




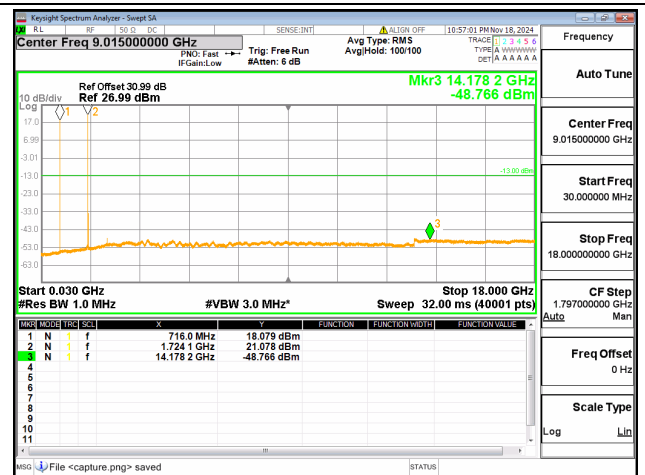
4A-12A / 20+10MHz / QPSK / Mid+Mid CH /
1@0-1@49



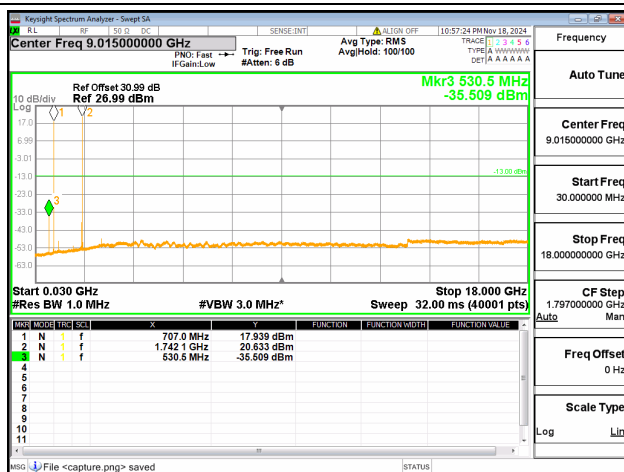
4A-12A / 20+10MHz / QPSK / Mid+Mid CH /
1@99-1@0



4A-12A / 20+10MHz / QPSK / Mid+Mid CH /
100@0-50@0



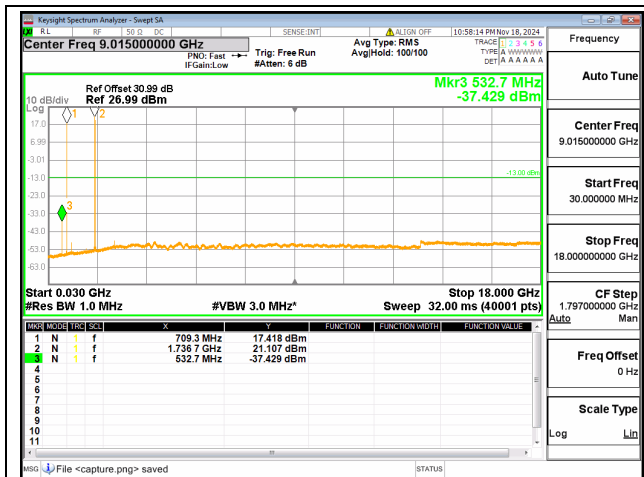
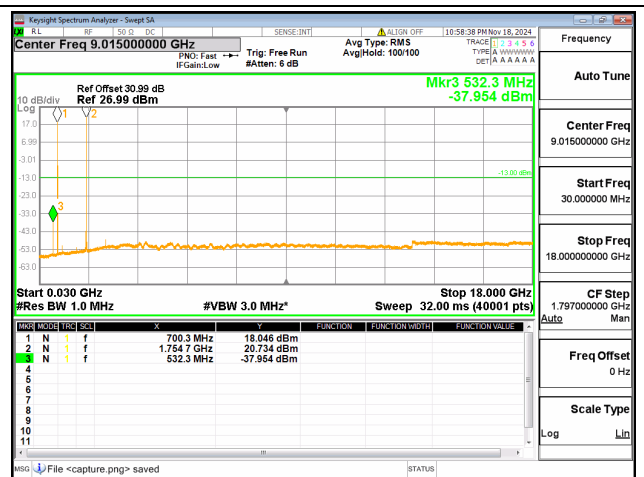
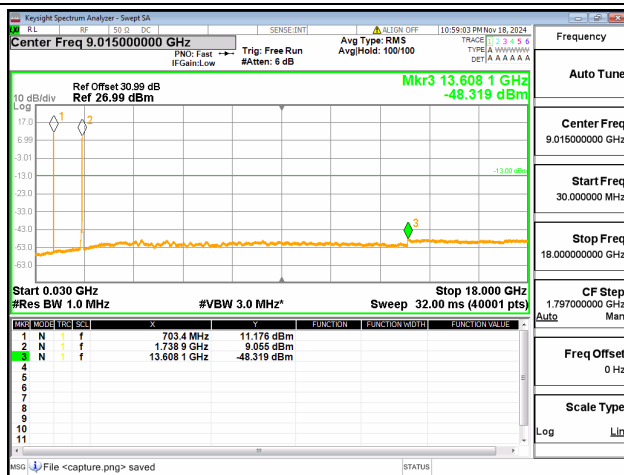
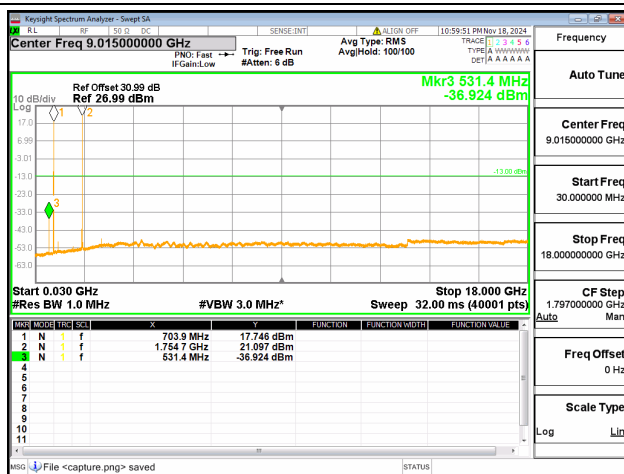
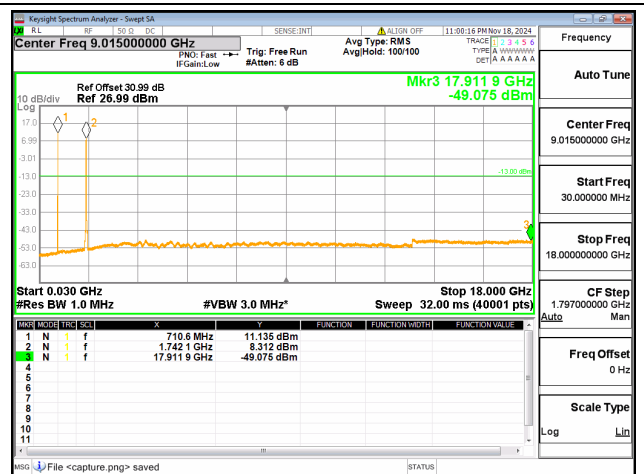
4A-12A / 20+10MHz / QPSK / Mid+High CH /
1@0-1@49

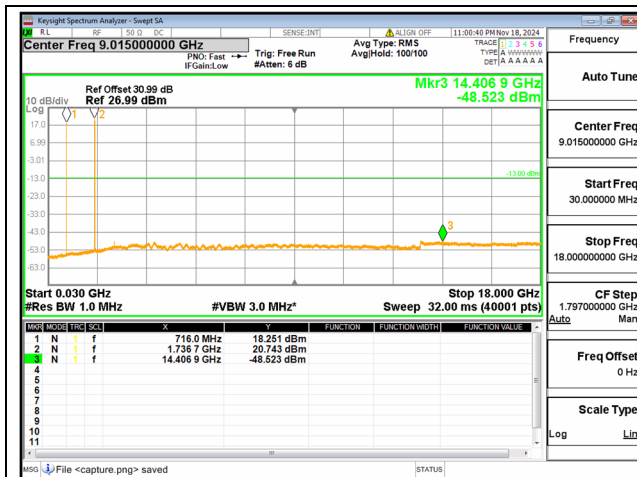
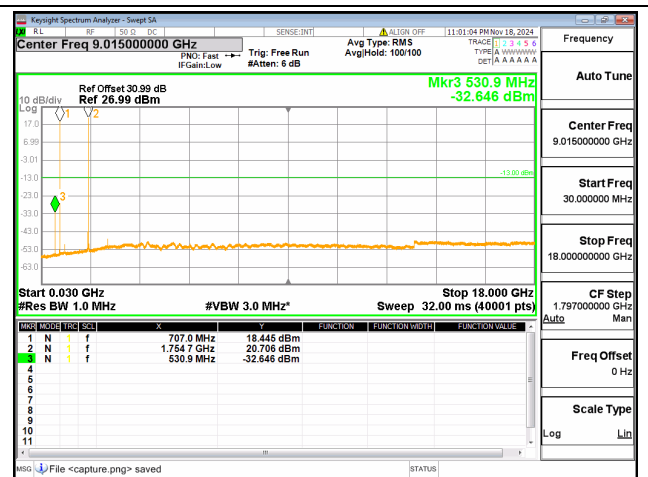
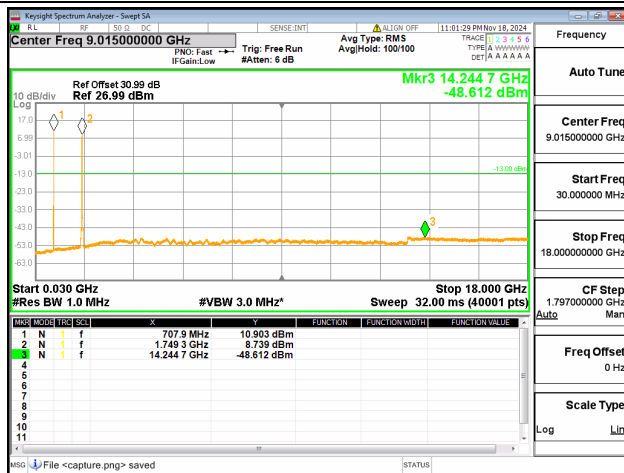
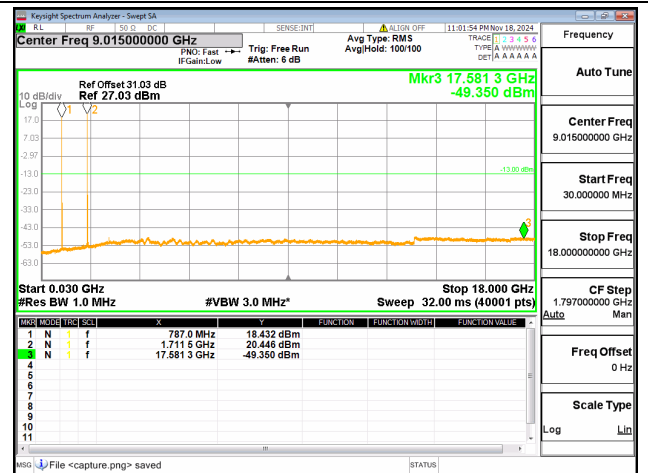
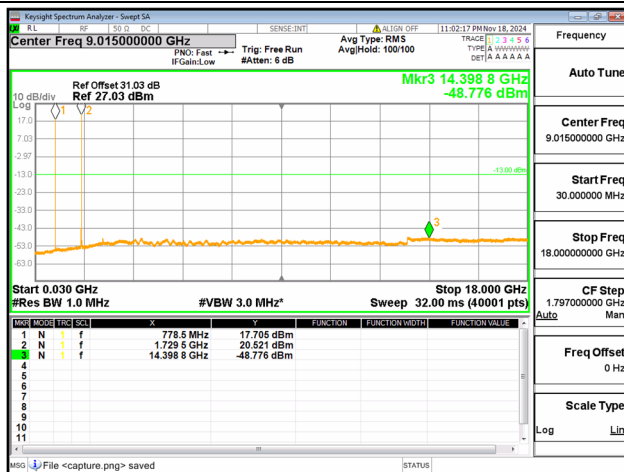


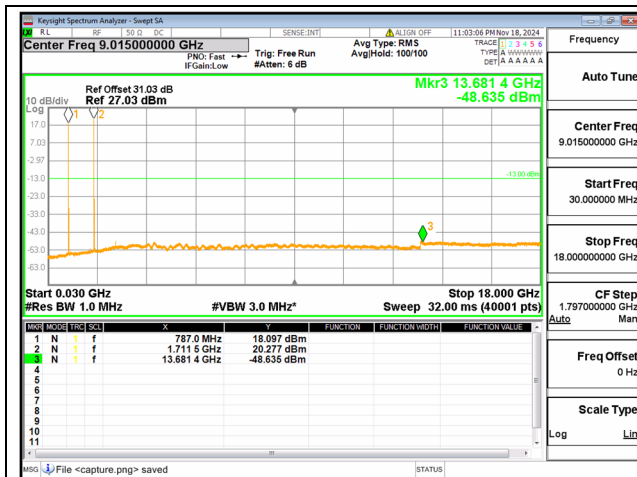
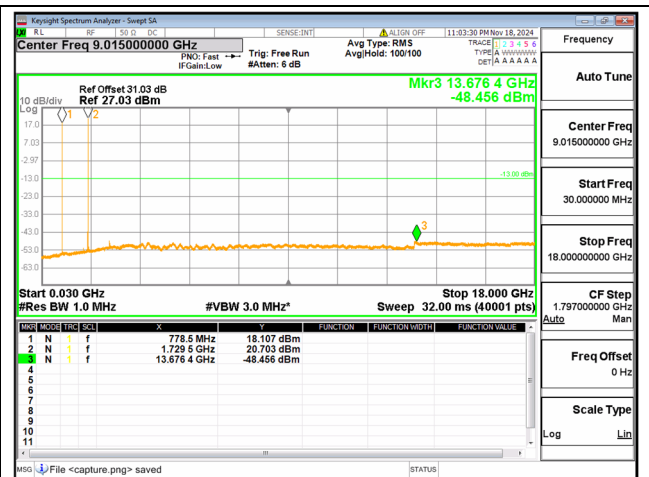
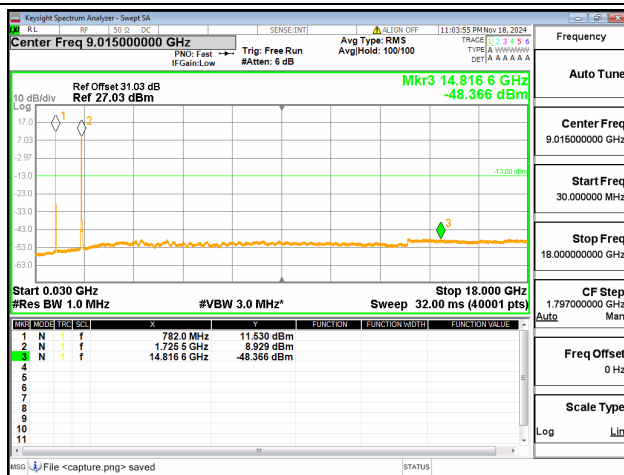
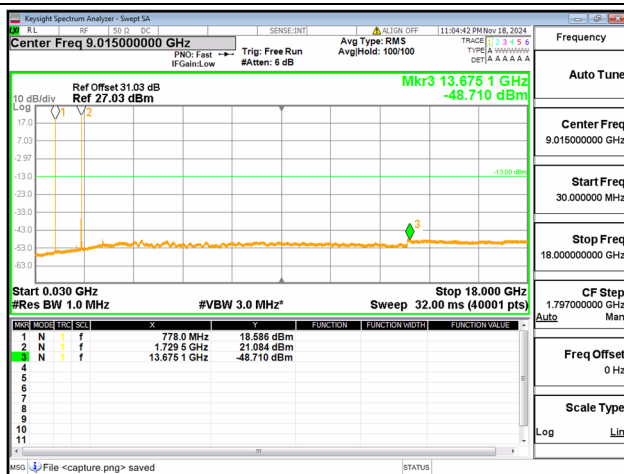
4A-12A / 20+10MHz / QPSK / Mid+High CH /
1@99-1@0

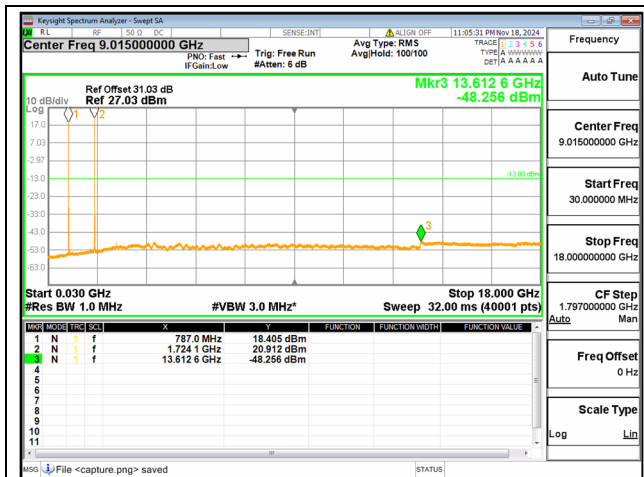
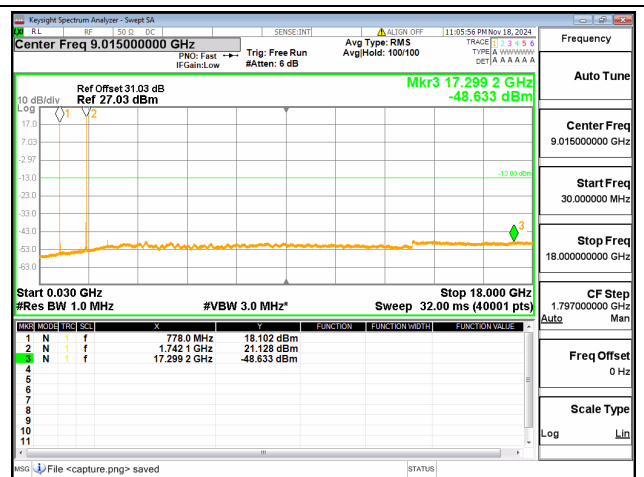
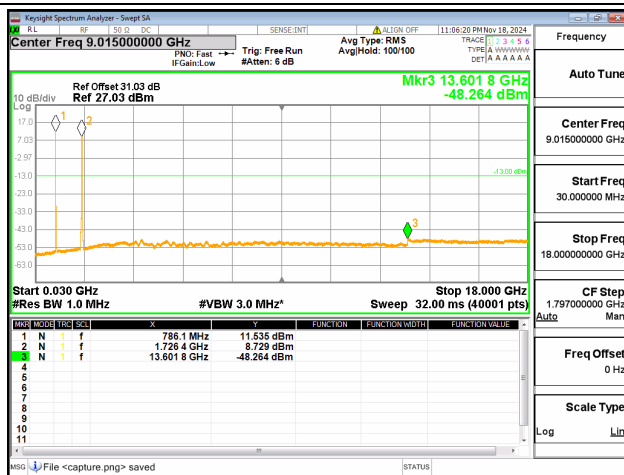
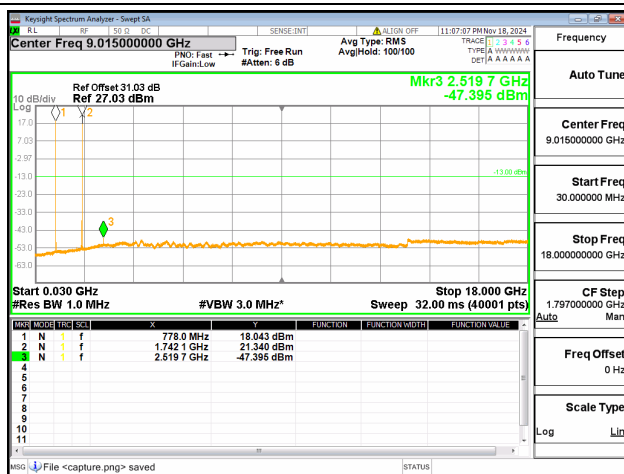


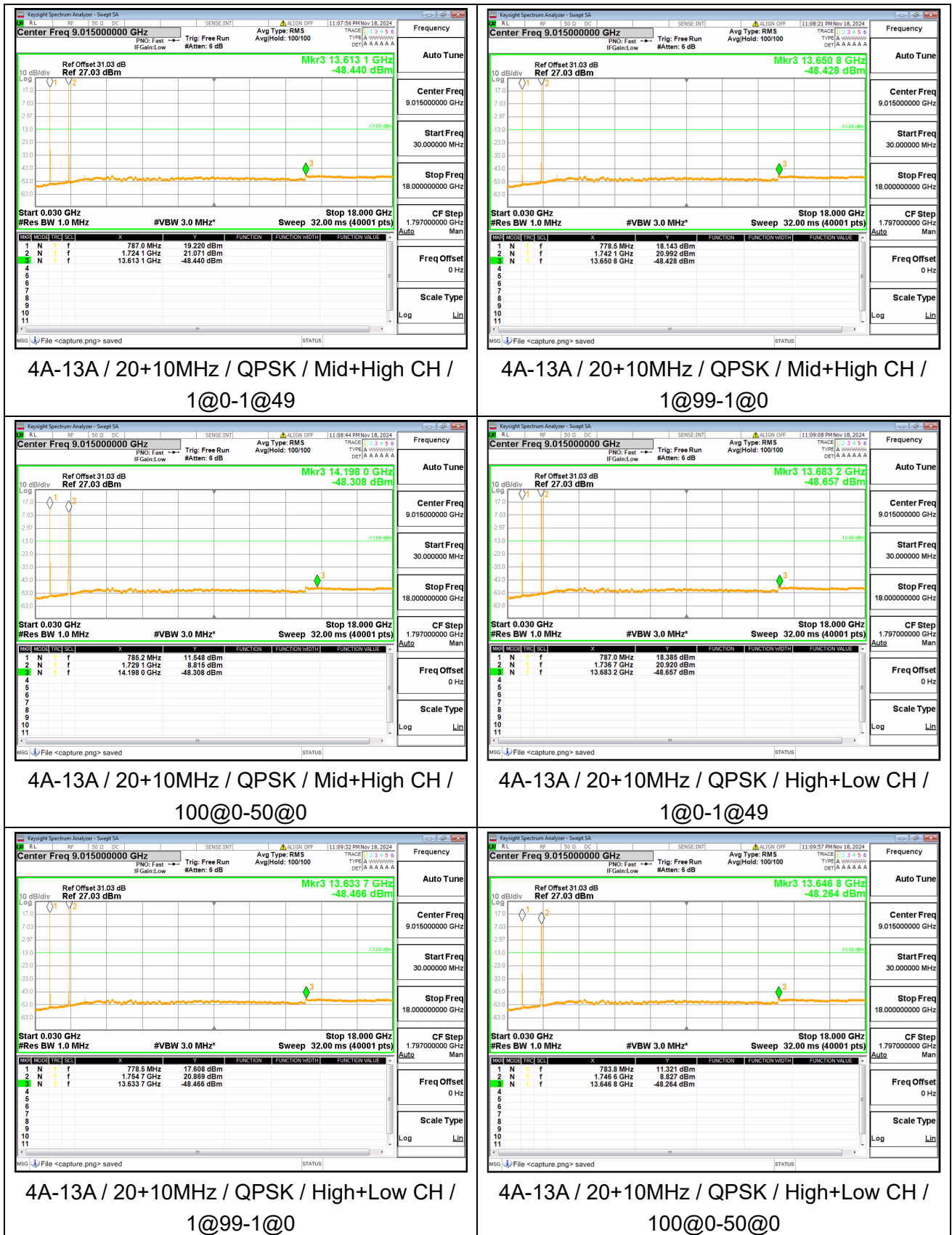
4A-12A / 20+10MHz / QPSK / Mid+High CH /
100@0-50@0

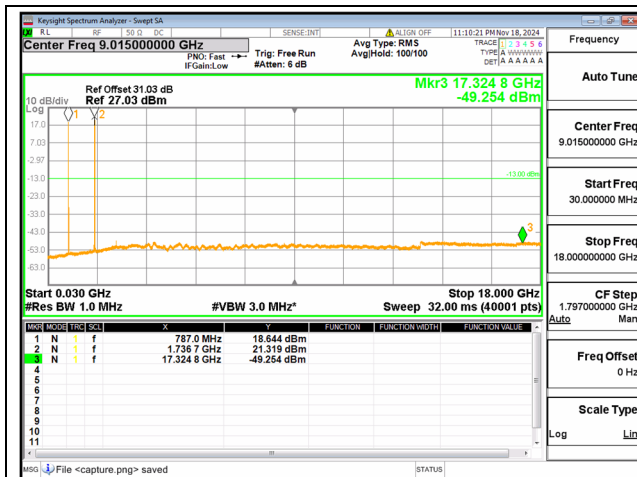
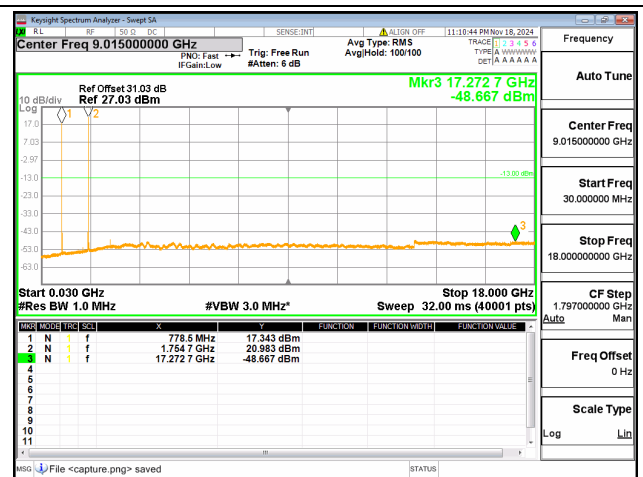
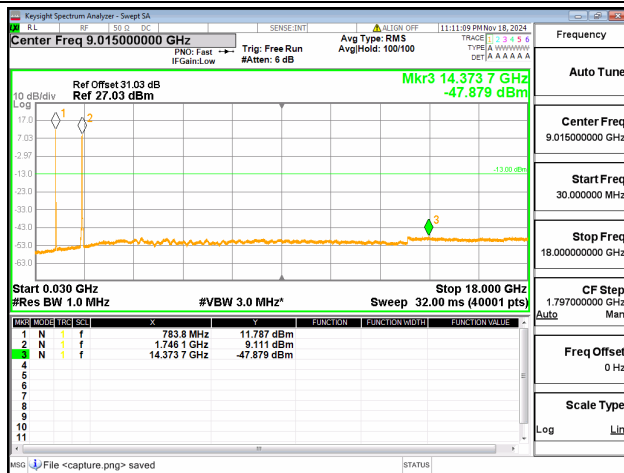
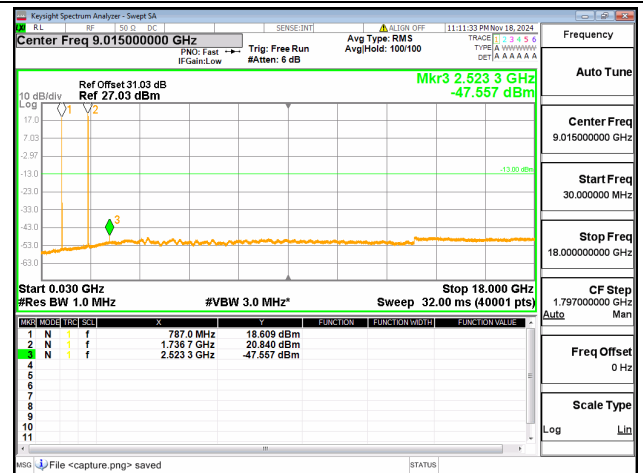
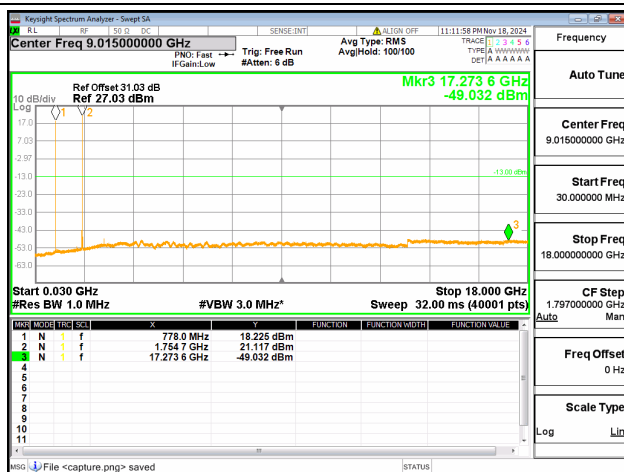
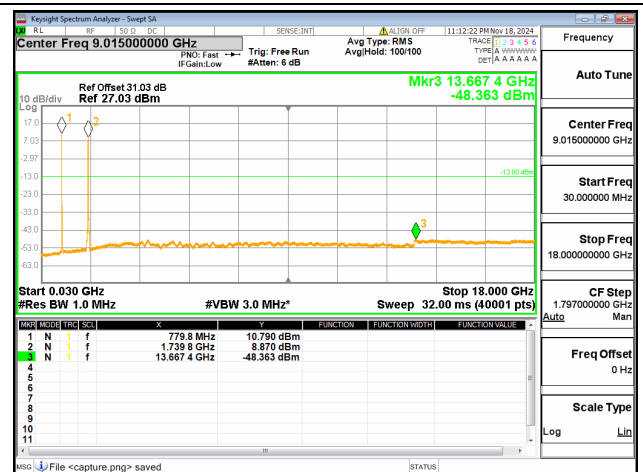
4A-12A / 20+10MHz / QPSK / High+Low CH /
1@0-1@494A-12A / 20+10MHz / QPSK / High+Low CH /
1@99-1@04A-12A / 20+10MHz / QPSK / High+Low CH /
100@0-50@04A-12A / 20+10MHz / QPSK / High+Mid CH /
1@0-1@494A-12A / 20+10MHz / QPSK / High+Mid CH /
1@99-1@04A-12A / 20+10MHz / QPSK / High+Mid CH /
100@0-50@0

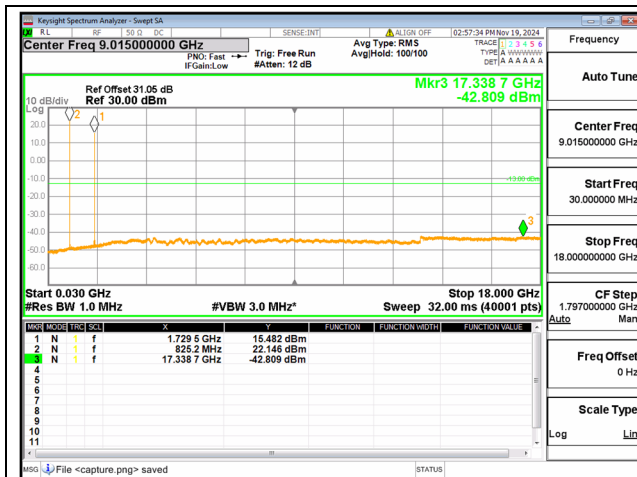
4A-12A / 20+10MHz / QPSK / High+High CH /
1@0-1@494A-12A / 20+10MHz / QPSK / High+High CH /
1@99-1@04A-12A / 20+10MHz / QPSK / High+High CH /
100@0-50@04A-13A / 20+10MHz / QPSK / Low+Low CH /
1@0-1@494A-13A / 20+10MHz / QPSK / Low+Low CH /
1@99-1@04A-13A / 20+10MHz / QPSK / Low+Low CH /
100@0-50@0

4A-13A / 20+10MHz / QPSK / Low+Mid CH /
1@0-1@494A-13A / 20+10MHz / QPSK / Low+Mid CH /
1@99-1@04A-13A / 20+10MHz / QPSK / Low+Mid CH /
100@0-50@04A-13A / 20+10MHz / QPSK / Low+High CH /
1@0-1@494A-13A / 20+10MHz / QPSK / Low+High CH /
1@99-1@04A-13A / 20+10MHz / QPSK / Low+High CH /
100@0-50@0

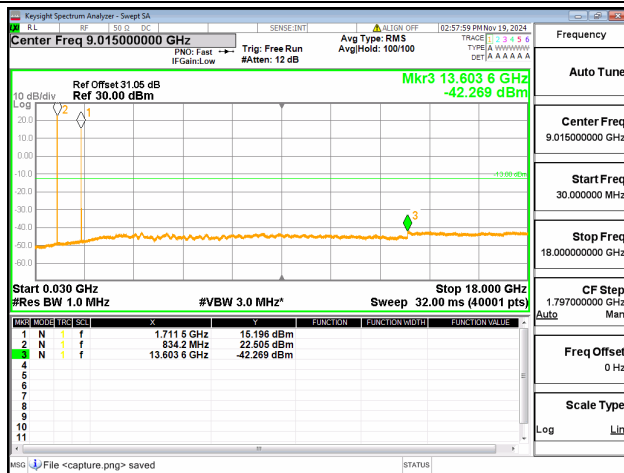
4A-13A / 20+10MHz / QPSK / Mid+Low CH /
1@0-1@494A-13A / 20+10MHz / QPSK / Mid+Low CH /
1@99-1@04A-13A / 20+10MHz / QPSK / Mid+Low CH /
100@0-50@04A-13A / 20+10MHz / QPSK / Mid+Mid CH /
1@0-1@494A-13A / 20+10MHz / QPSK / Mid+Mid CH /
1@99-1@04A-13A / 20+10MHz / QPSK / Mid+Mid CH /
100@0-50@0



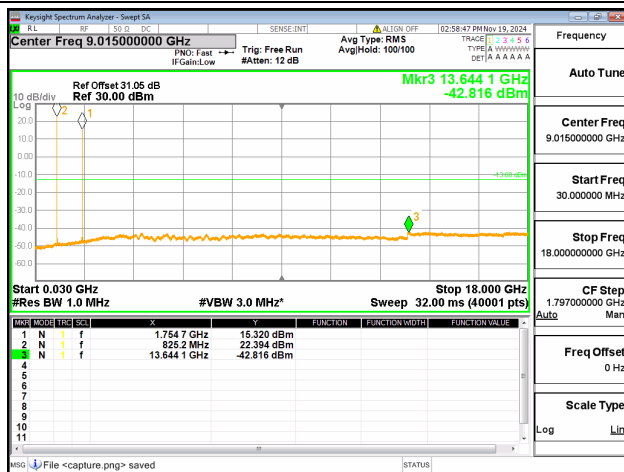
4A-13A / 20+10MHz / QPSK / High+Mid CH /
1@0-1@494A-13A / 20+10MHz / QPSK / High+Mid CH /
1@99-1@04A-13A / 20+10MHz / QPSK / High+Mid CH /
100@0-50@04A-13A / 20+10MHz / QPSK / High+High CH /
1@0-1@494A-13A / 20+10MHz / QPSK / High+High CH /
1@99-1@04A-13A / 20+10MHz / QPSK / High+High CH /
100@0-50@0



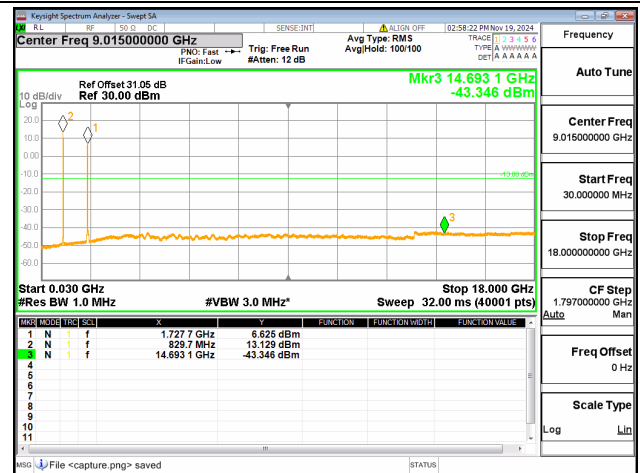
5A-66A / 10+20MHz / QPSK / Low+Low CH /
1@0-1@99



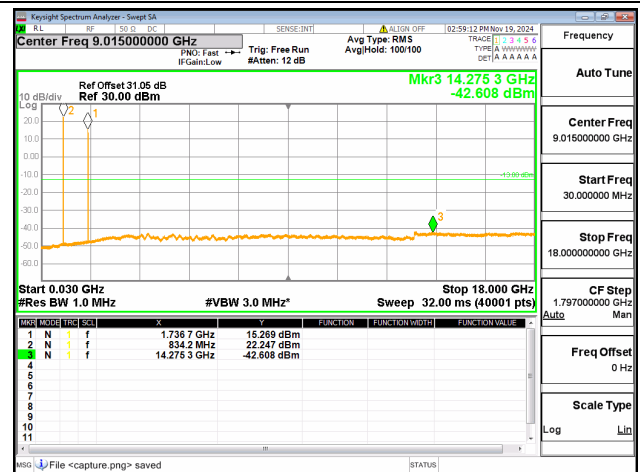
5A-66A / 10+20MHz / QPSK / Low+Low CH /
1@49-1@0



5A-66A / 10+20MHz / QPSK / Low+Mid CH /
1@0-1@99

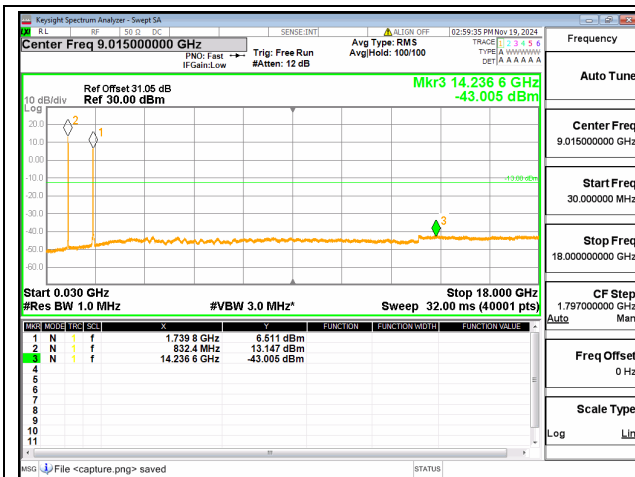
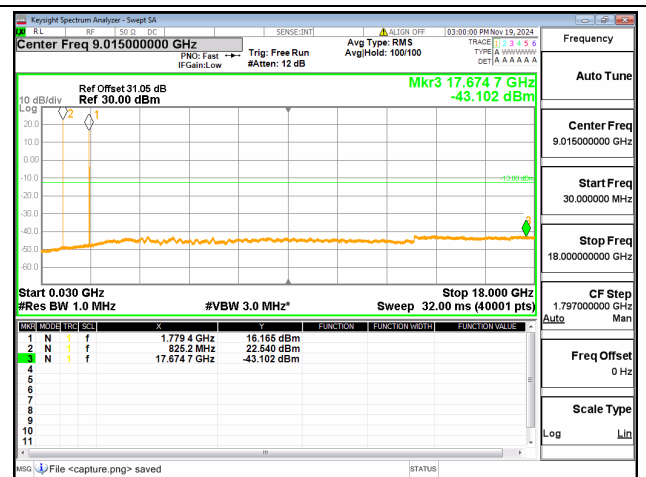
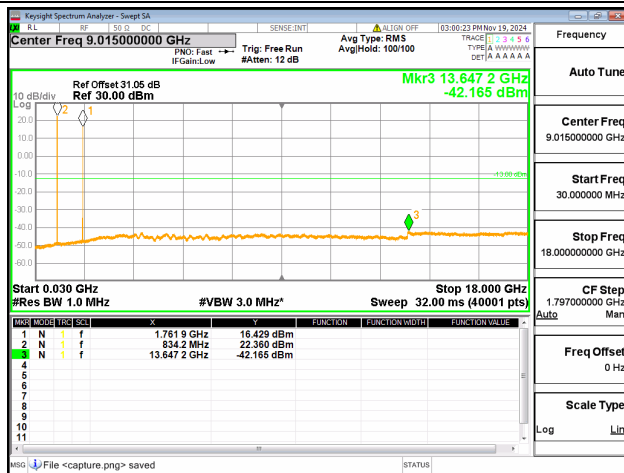
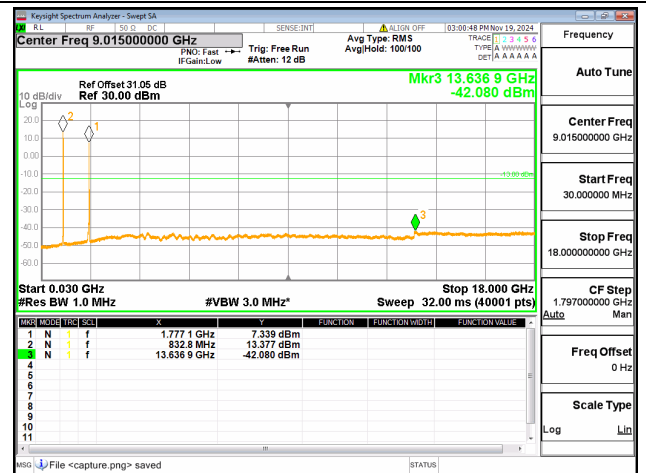
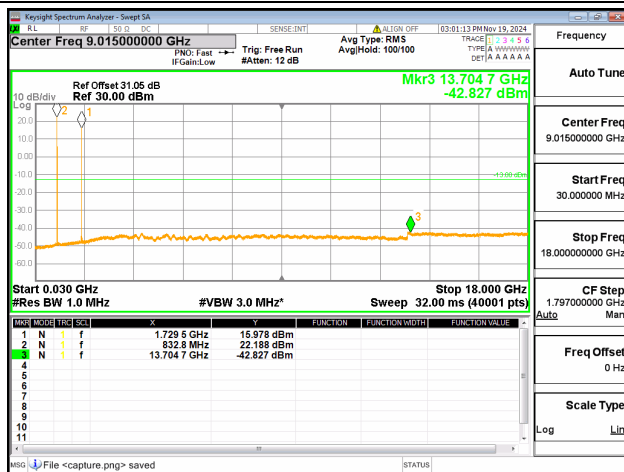


5A-66A / 10+20MHz / QPSK / Low+Low CH /
50@0-100@0



5A-66A / 10+20MHz / QPSK / Low+Mid CH /
1@49-1@0

N/A

5A-66A / 10+20MHz / QPSK / Low+Mid CH /
50@0-100@05A-66A / 10+20MHz / QPSK / Low+High CH /
1@0-1@995A-66A / 10+20MHz / QPSK / Low+High CH /
1@49-1@05A-66A / 10+20MHz / QPSK / Low+High CH /
50@0-100@05A-66A / 10+20MHz / QPSK / Mid+Low CH /
1@0-1@995A-66A / 10+20MHz / QPSK / Mid+Low CH /
1@49-1@0