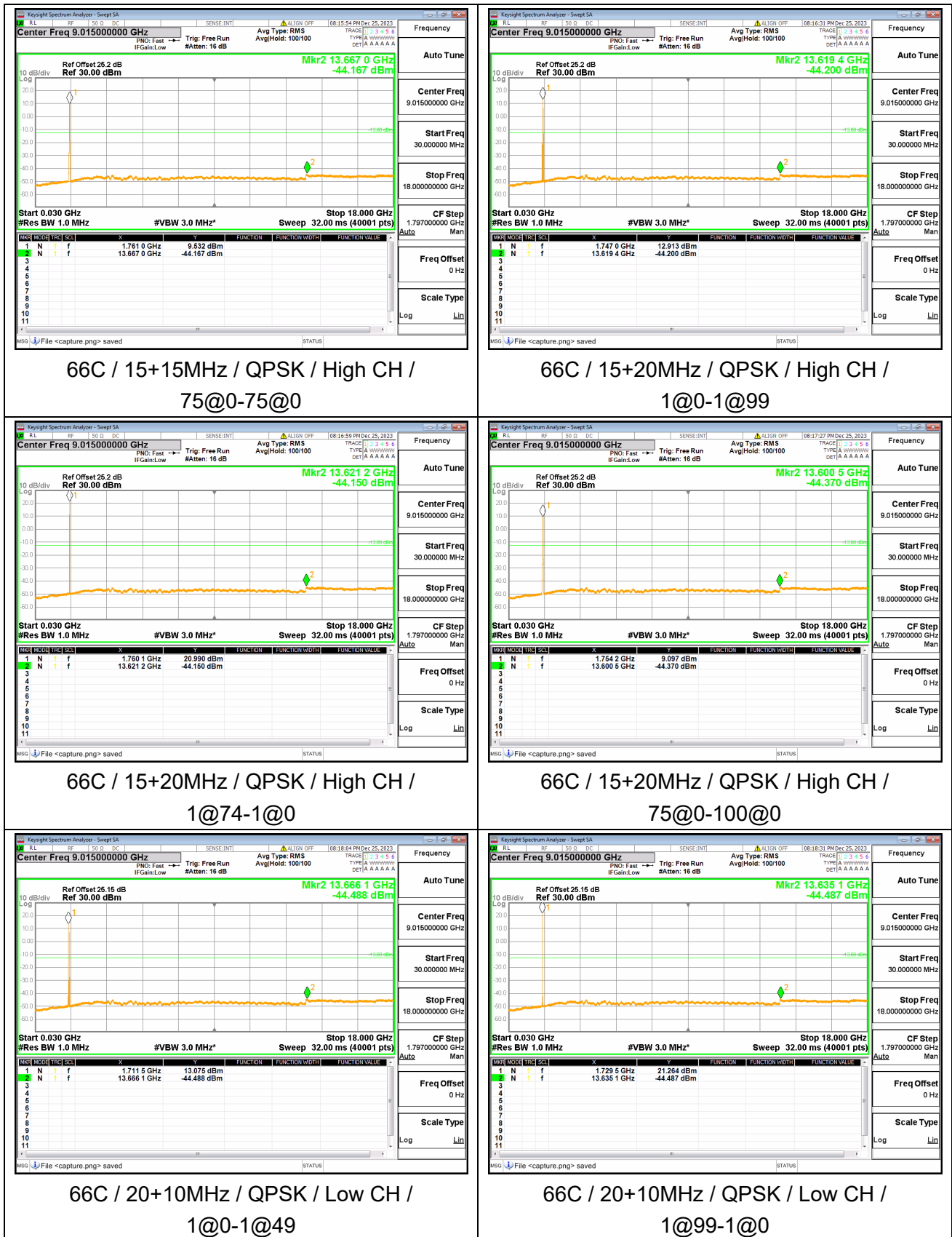
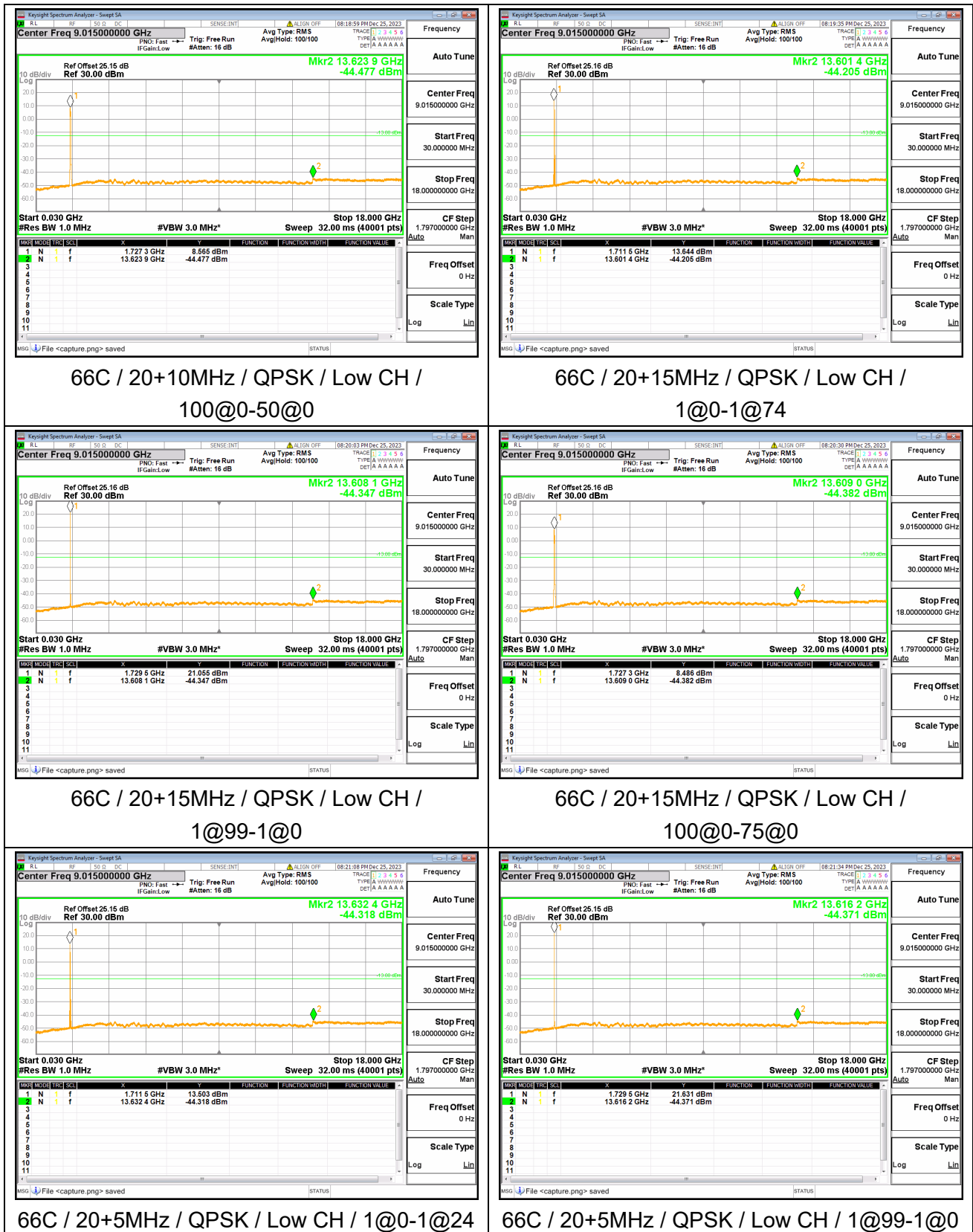
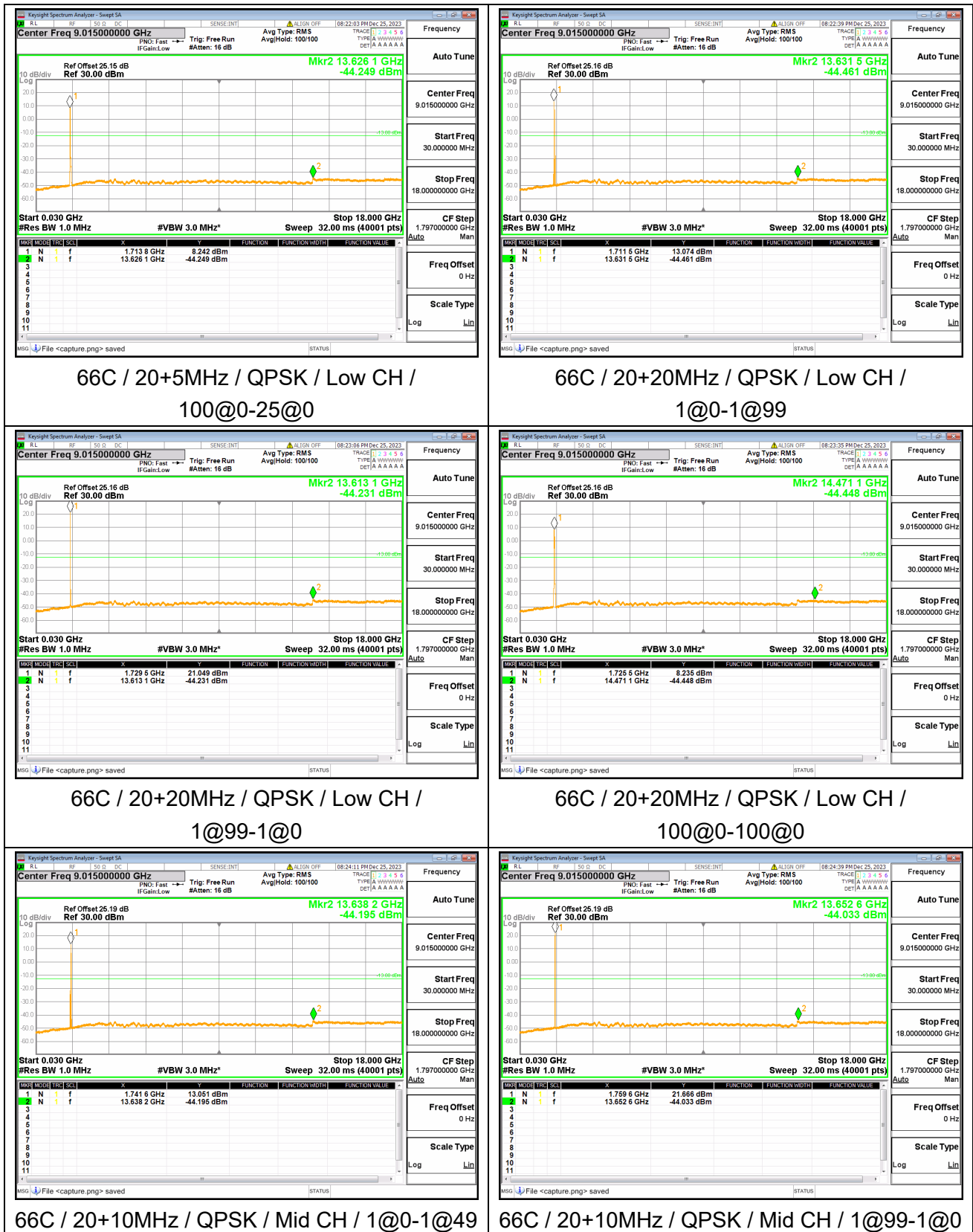
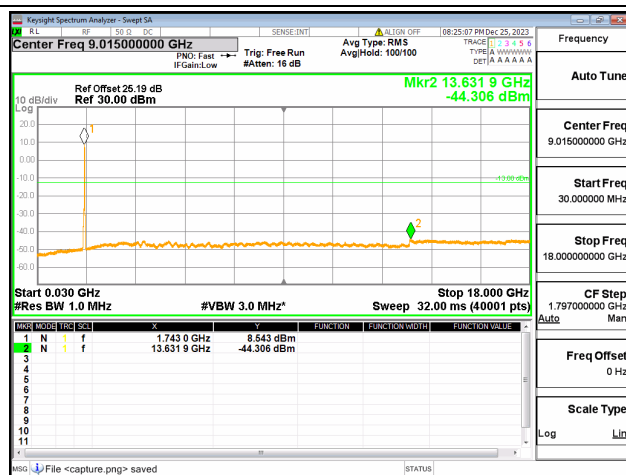


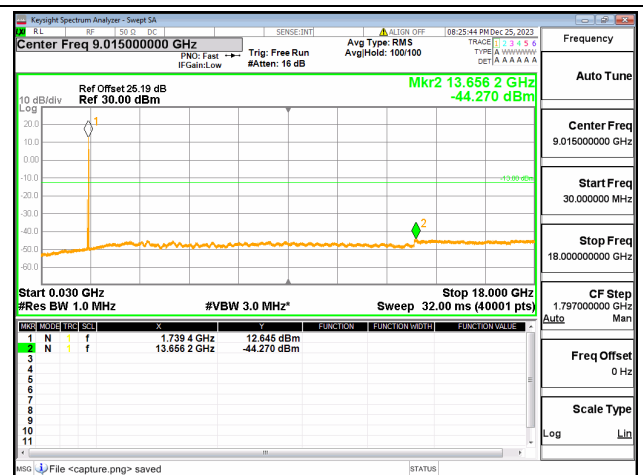




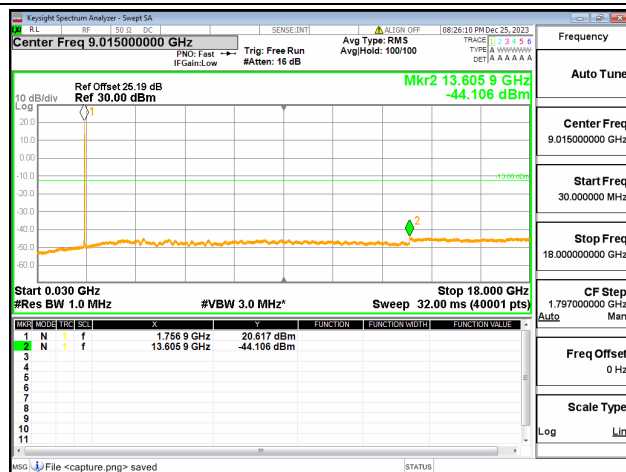




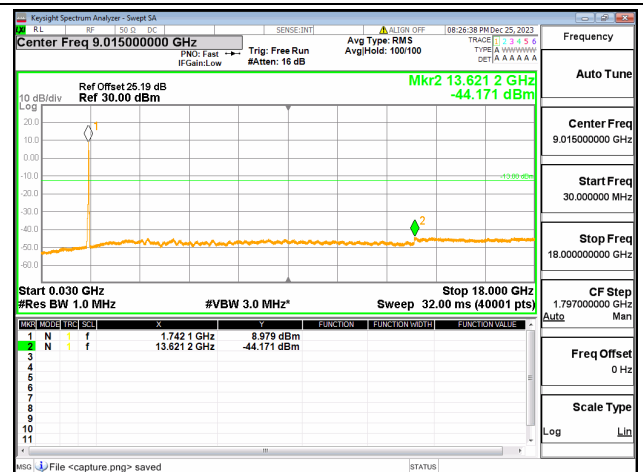
66C / 20+10MHz / QPSK / Mid CH /  
100@0-50@0



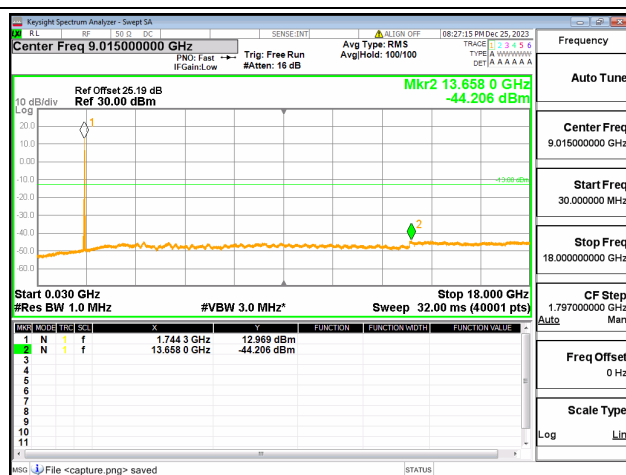
66C / 20+15MHz / QPSK / Mid CH / 1@0-1@74



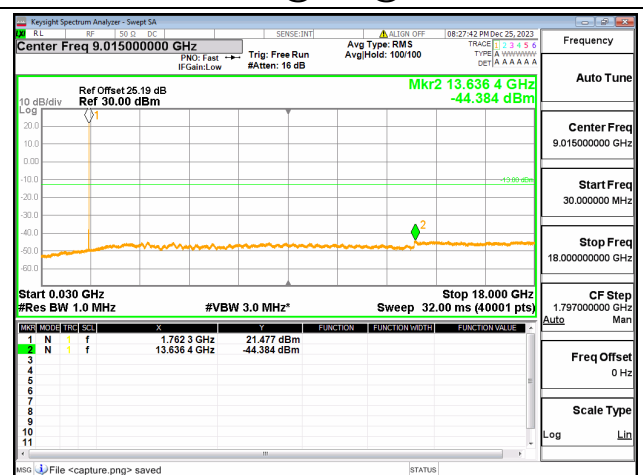
66C / 20+15MHz / QPSK / Mid CH / 1@99-1@0



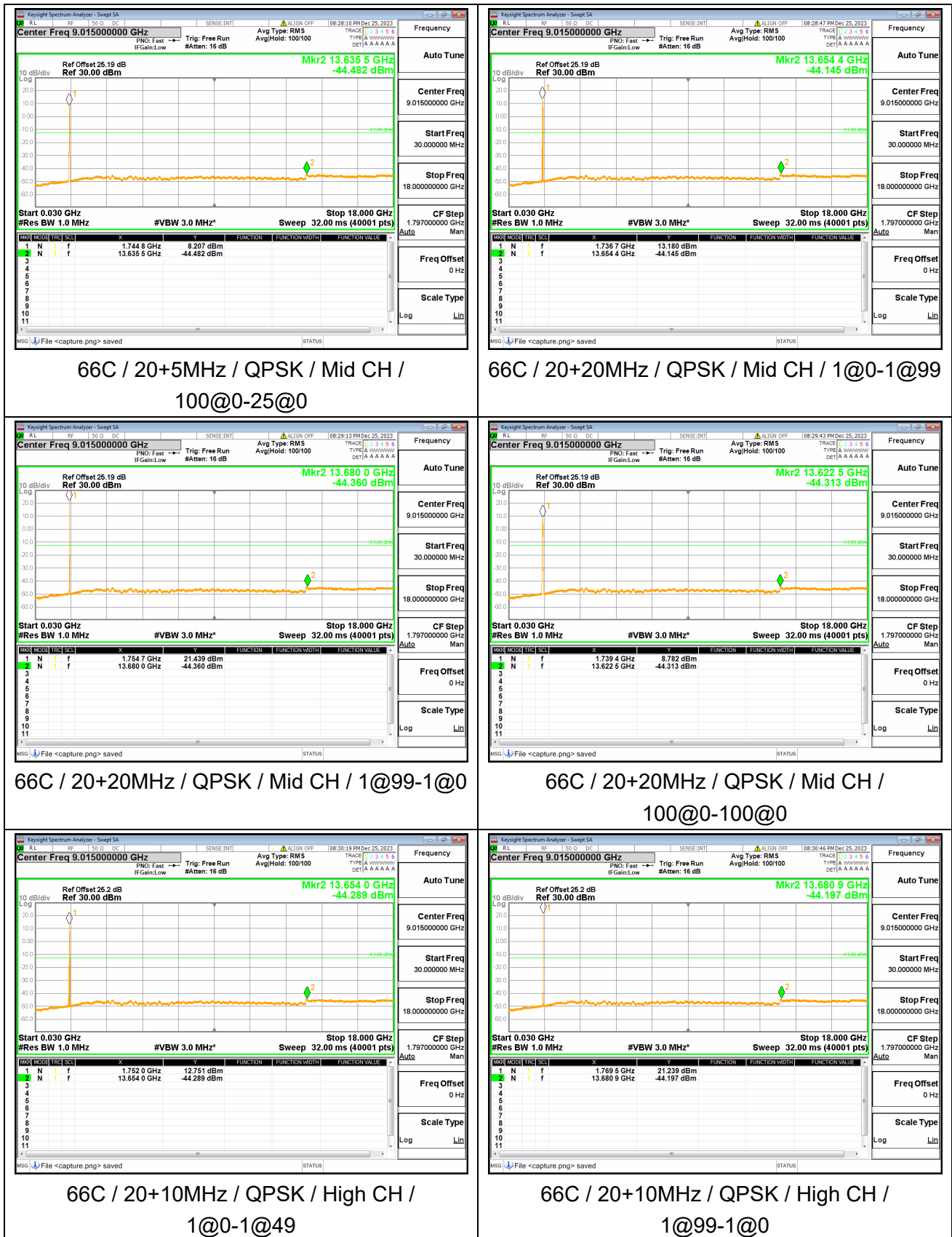
66C / 20+15MHz / QPSK / Mid CH /  
100@0-75@0

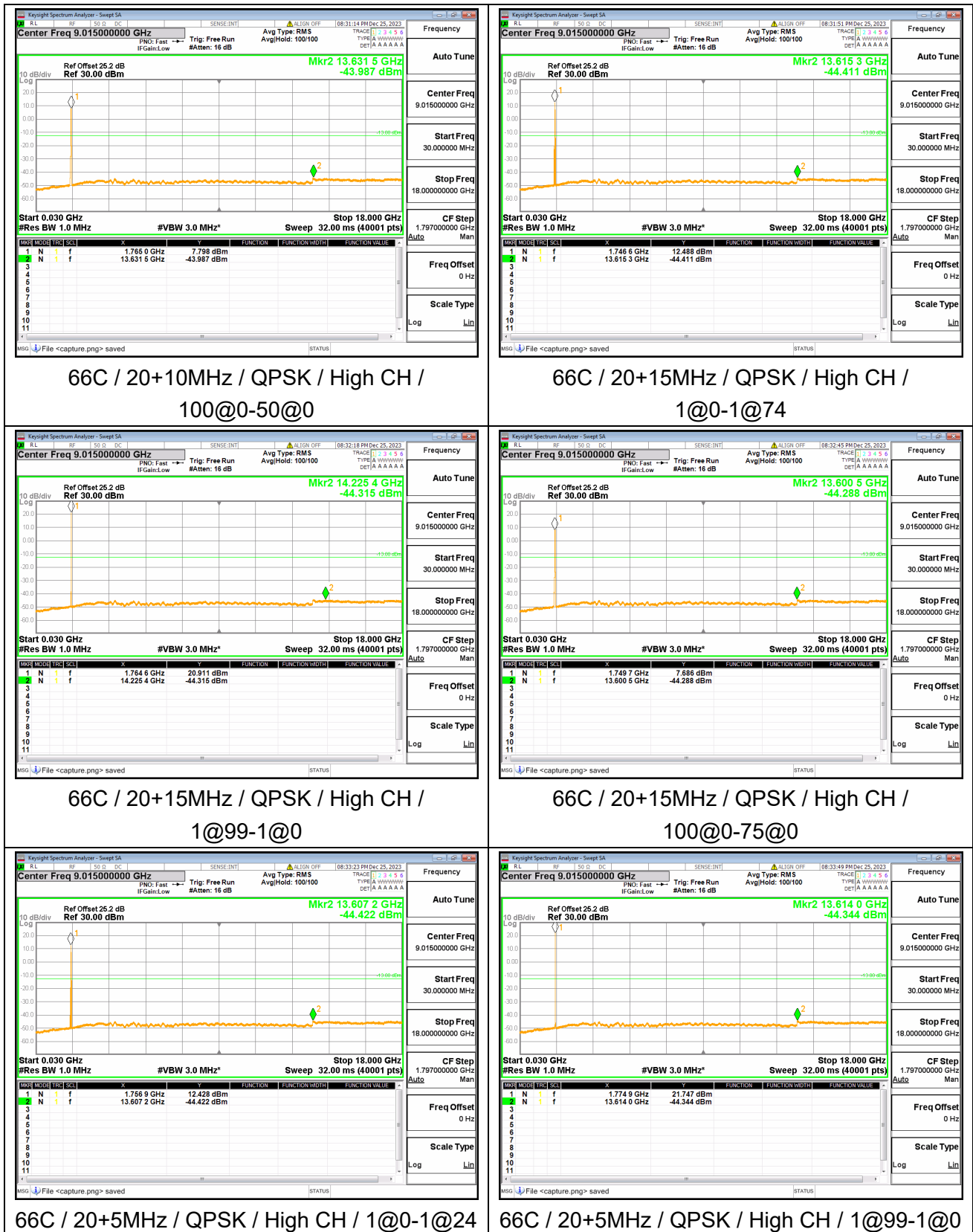


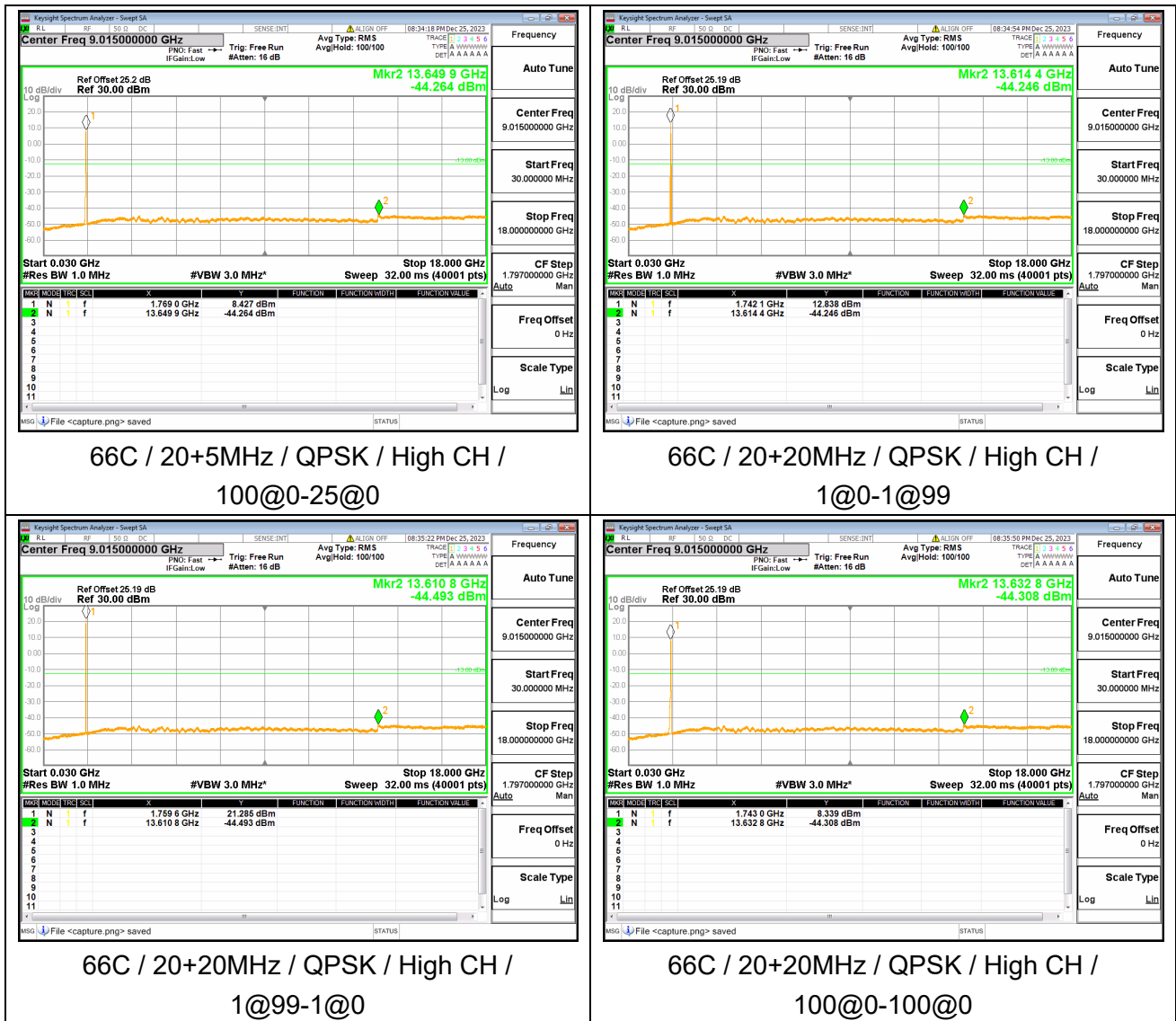
66C / 20+5MHz / QPSK / Mid CH / 1@0-1@24

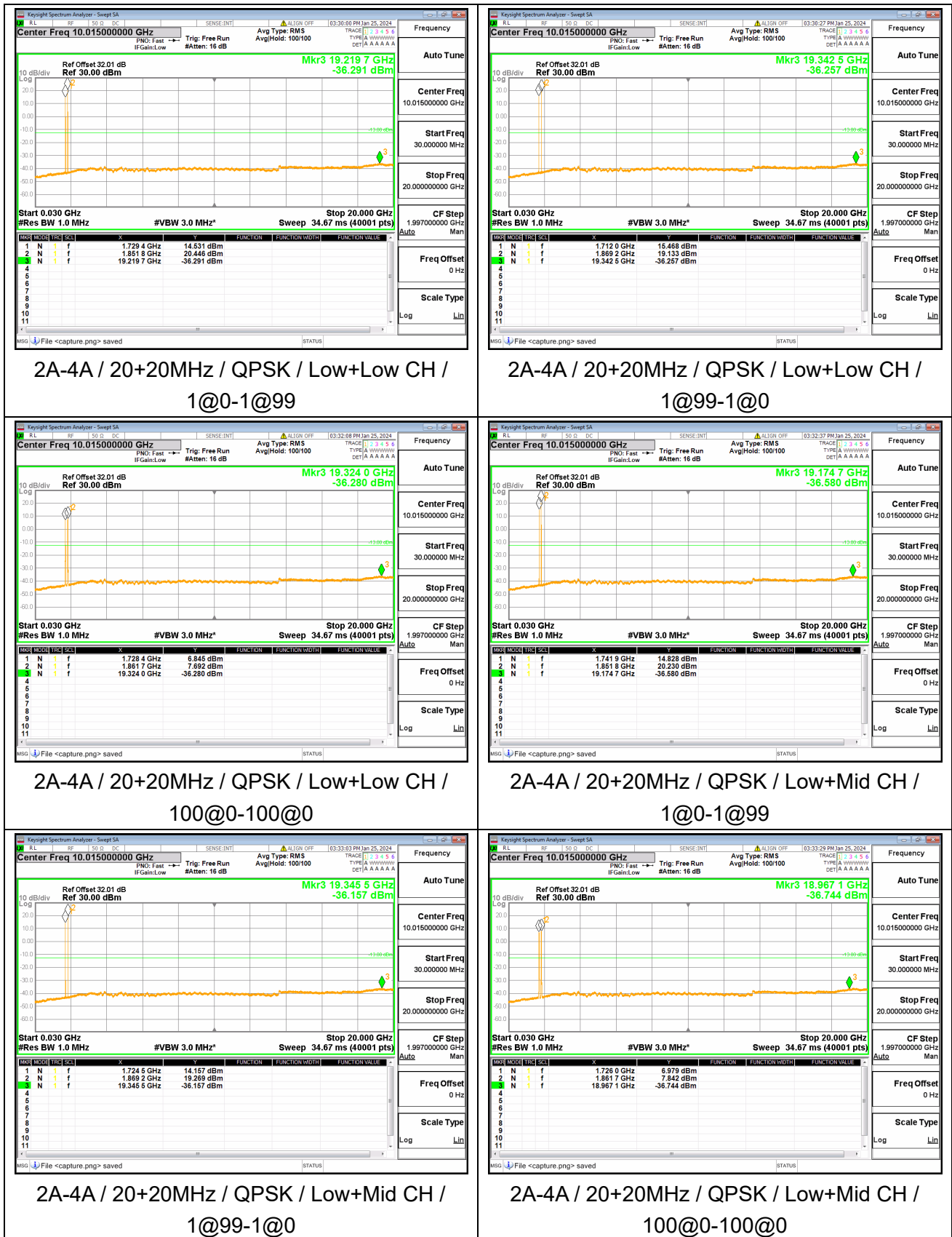


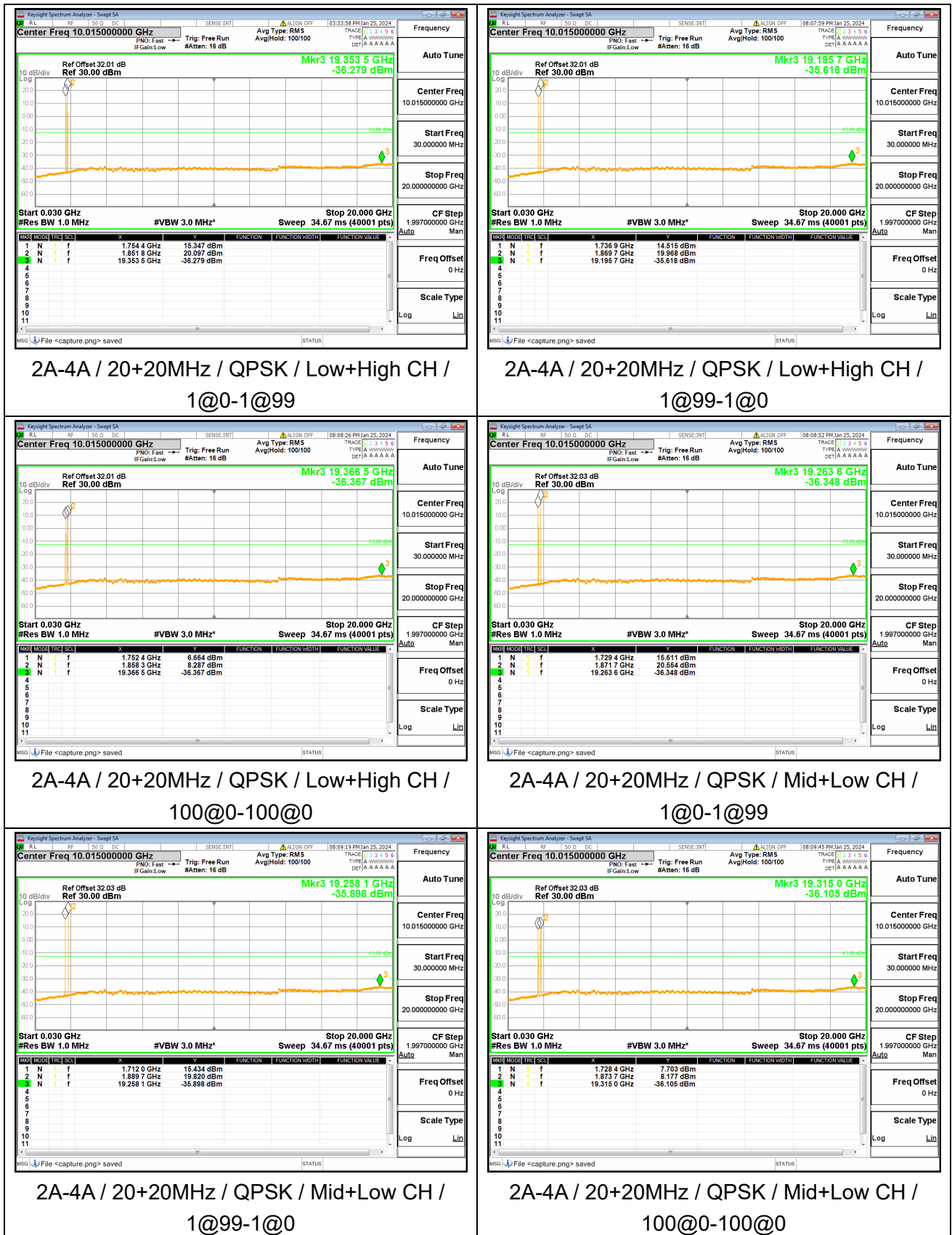
66C / 20+5MHz / QPSK / Mid CH / 1@99-1@0

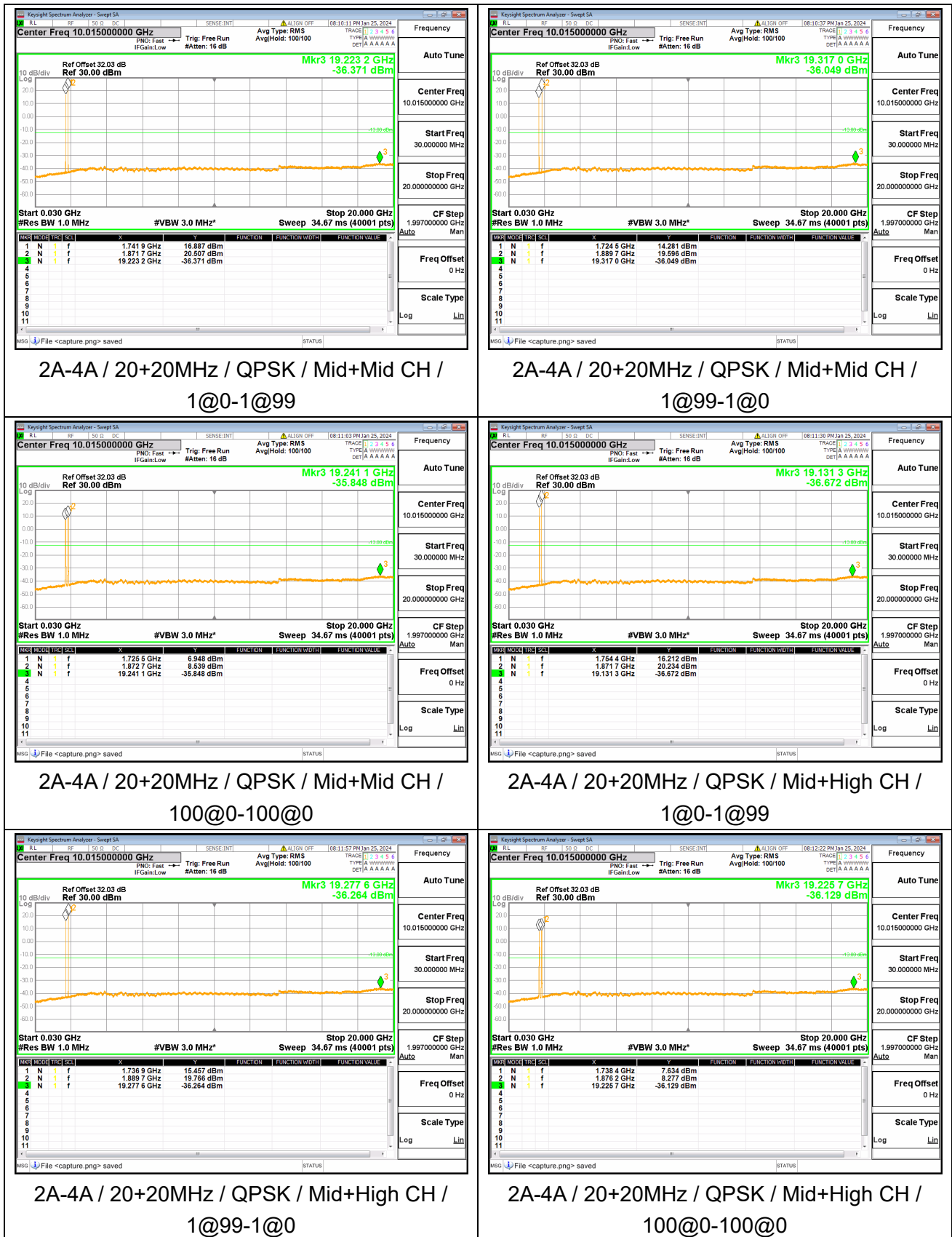


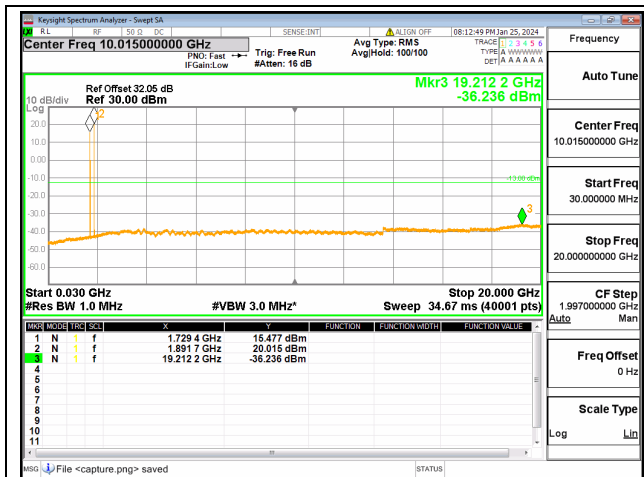




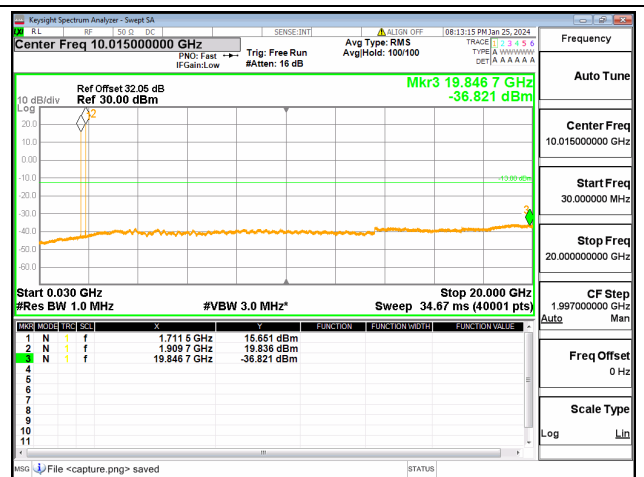




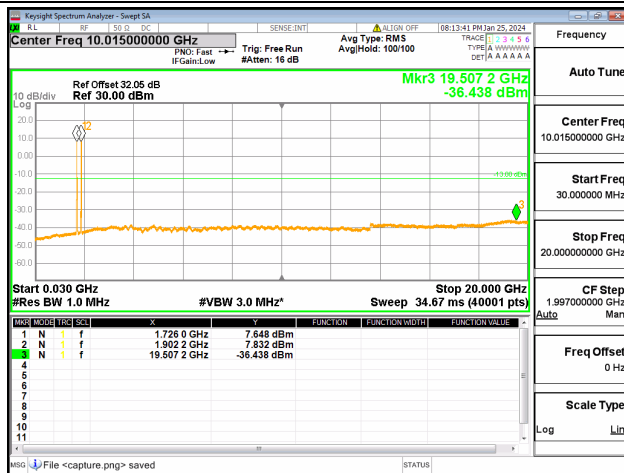




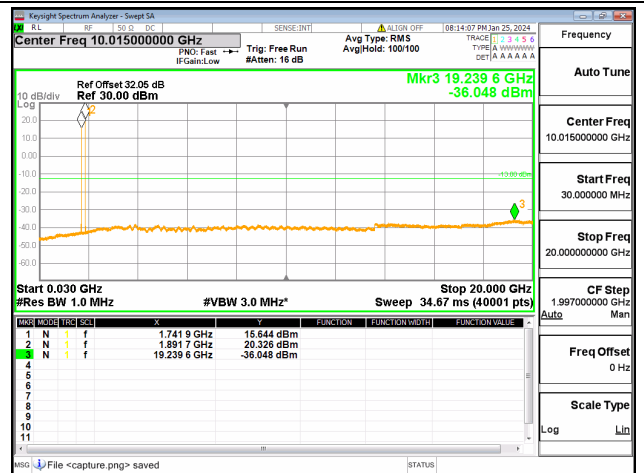
2A-4A / 20+20MHz / QPSK / High+Low CH /  
1@0-1@99



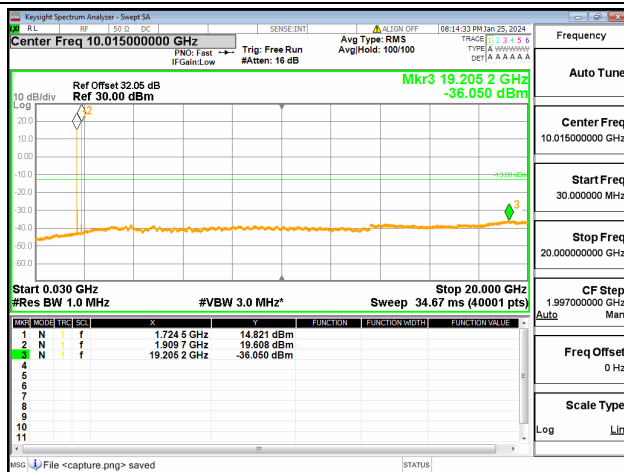
2A-4A / 20+20MHz / QPSK / High+Low CH /  
1@99-1@0



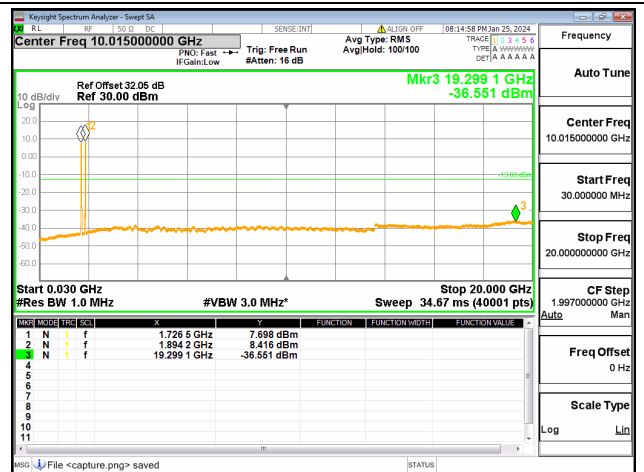
2A-4A / 20+20MHz / QPSK / High+Low CH /  
100@0-100@0



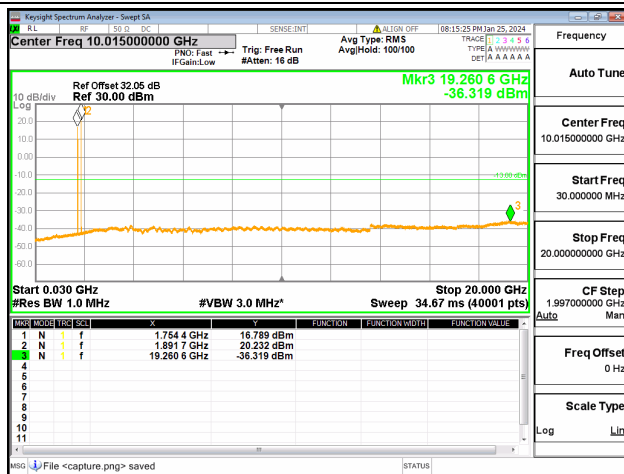
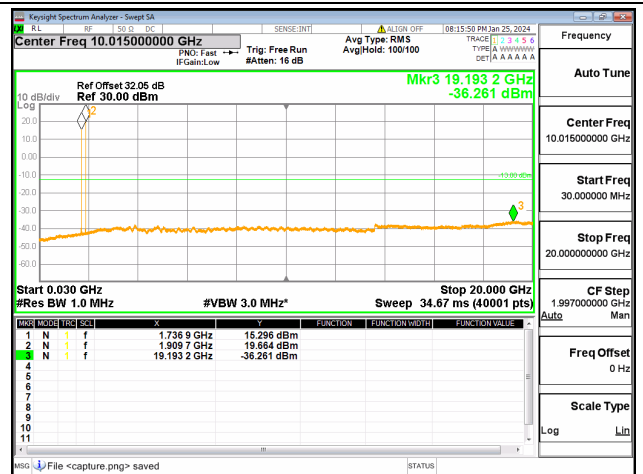
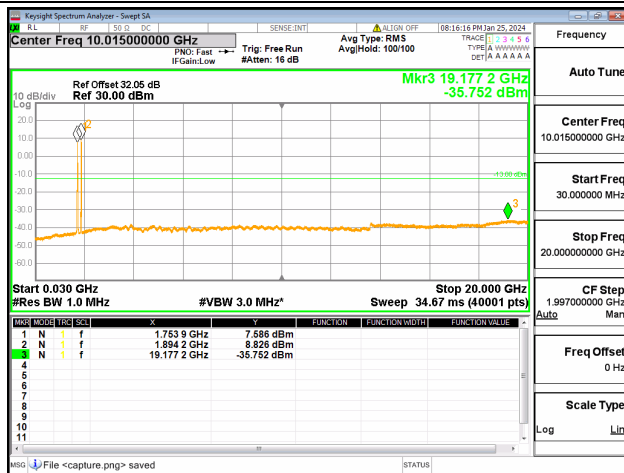
2A-4A / 20+20MHz / QPSK / High+Mid CH /  
1@0-1@99



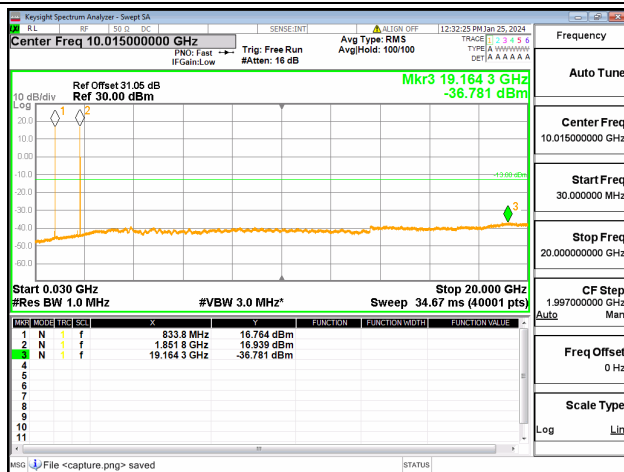
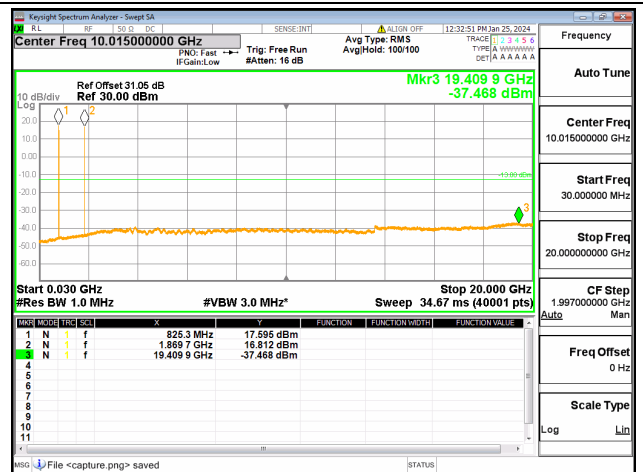
2A-4A / 20+20MHz / QPSK / High+Mid CH /  
1@99-1@0

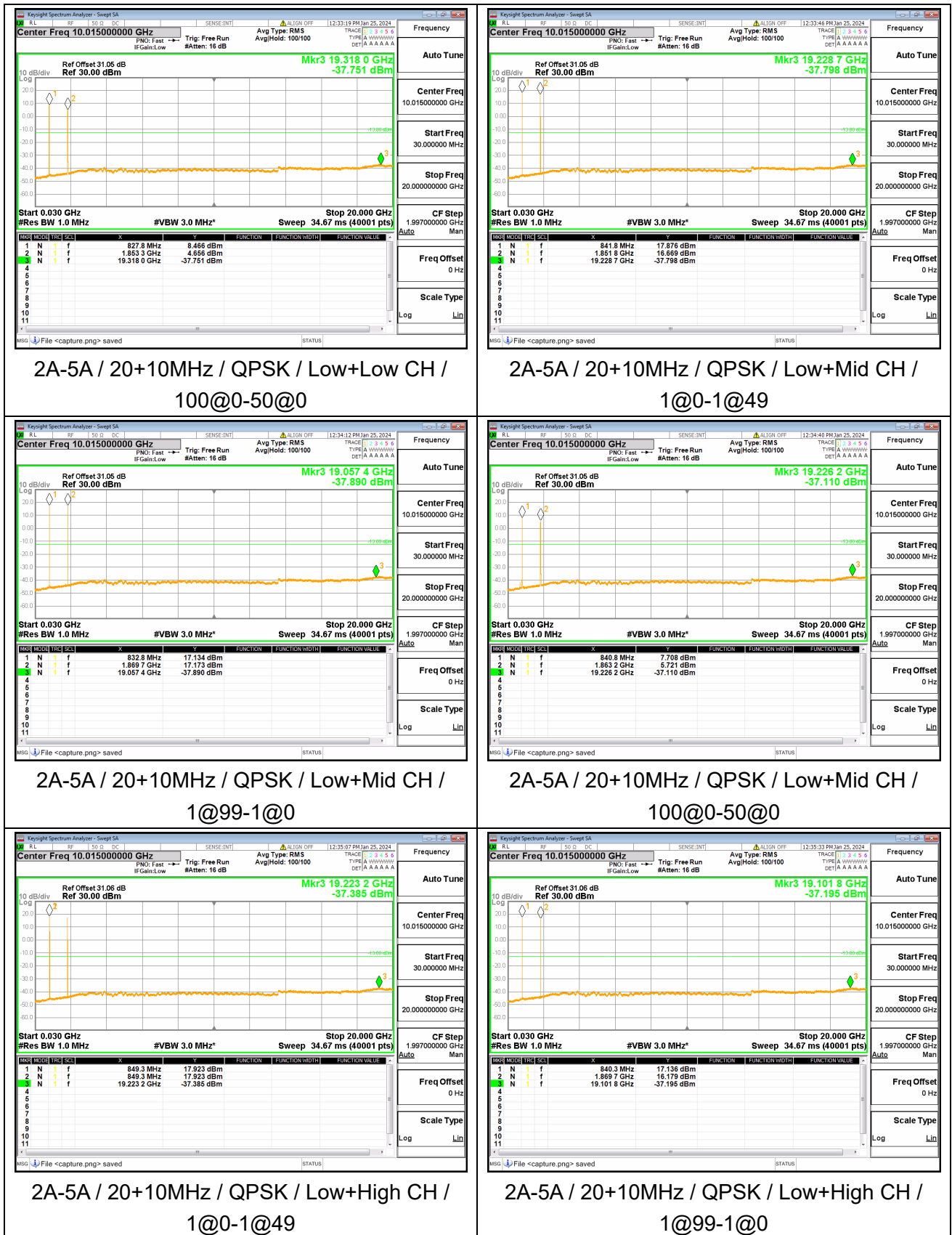


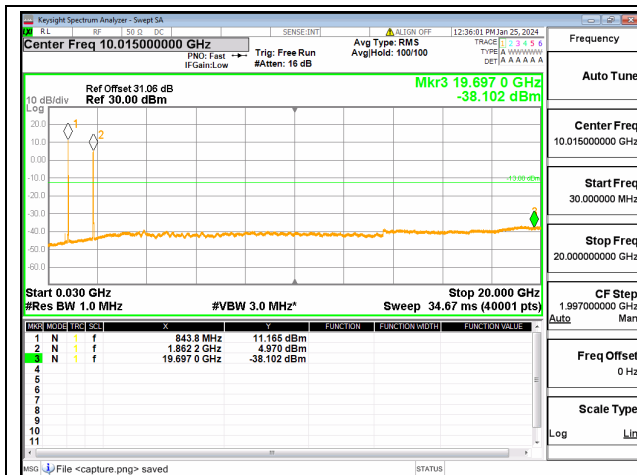
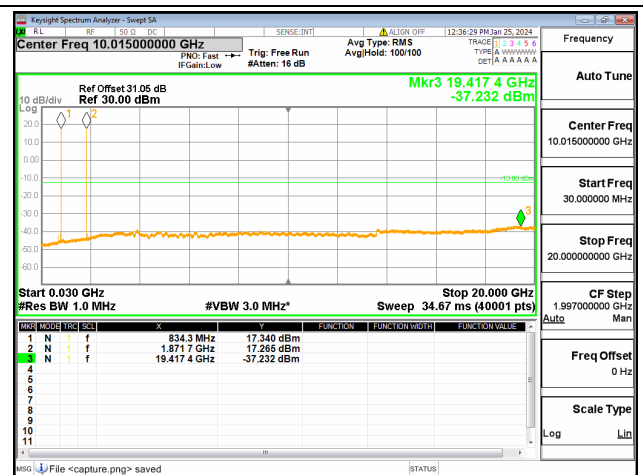
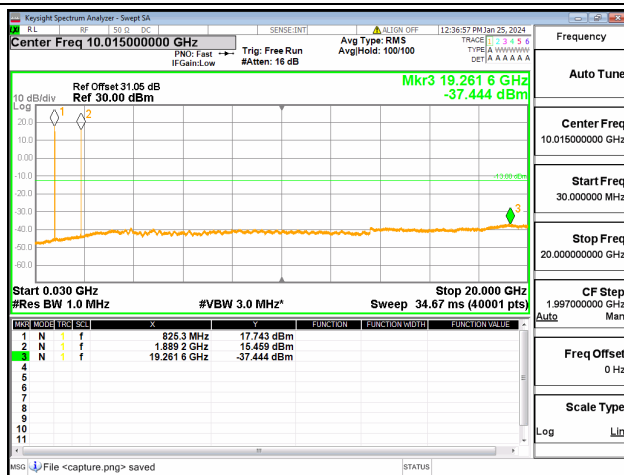
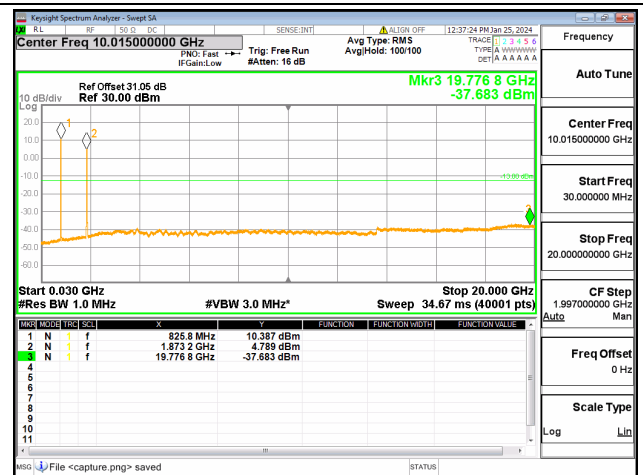
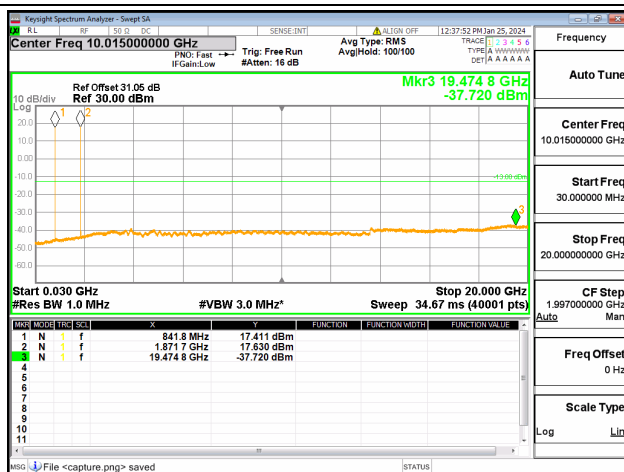
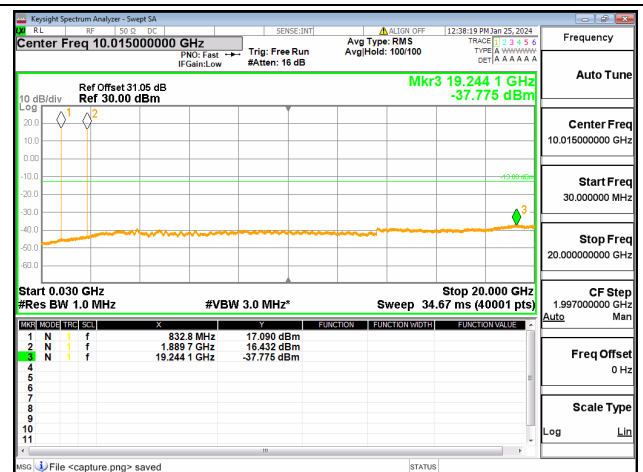
2A-4A / 20+20MHz / QPSK / High+Mid CH /  
100@0-100@0

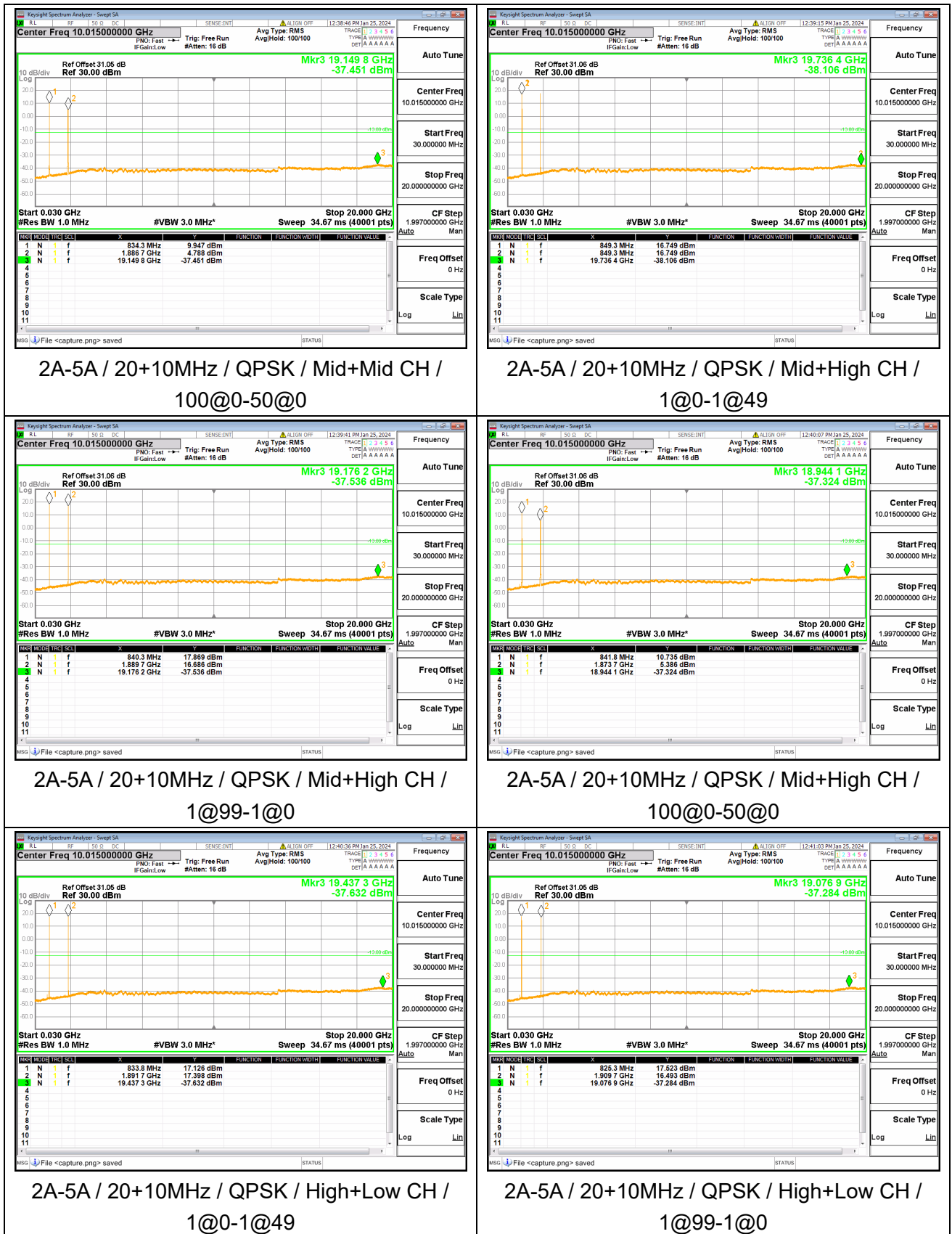
2A-4A / 20+20MHz / QPSK / High+High CH /  
1@0-1@992A-4A / 20+20MHz / QPSK / High+High CH /  
1@99-1@02A-4A / 20+20MHz / QPSK / High+High CH /  
100@0-100@0

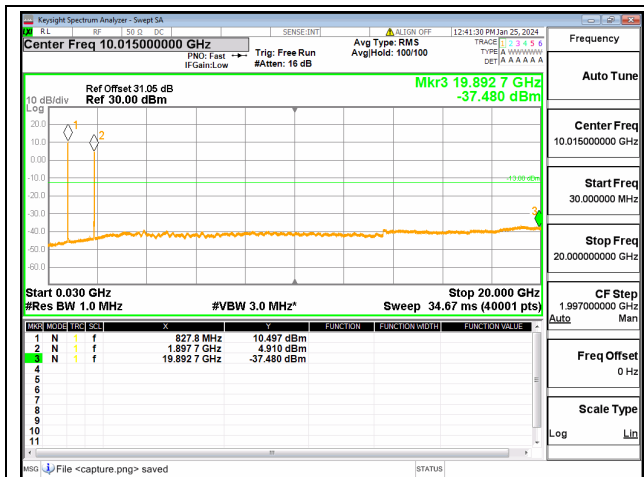
N/A

2A-5A / 20+10MHz / QPSK / Low+Low CH /  
1@0-1@492A-5A / 20+10MHz / QPSK / Low+Low CH /  
1@99-1@0

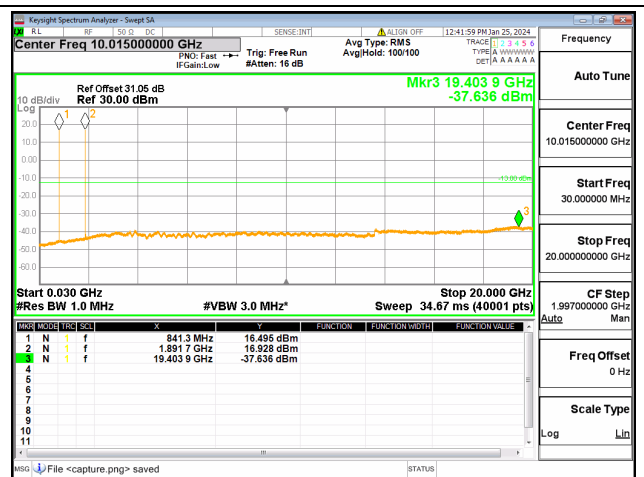


2A-5A / 20+10MHz / QPSK / Low+High CH /  
100@0-50@02A-5A / 20+10MHz / QPSK / Mid+Low CH /  
1@0-1@492A-5A / 20+10MHz / QPSK / Mid+Low CH /  
1@99-1@02A-5A / 20+10MHz / QPSK / Mid+Low CH /  
100@0-50@02A-5A / 20+10MHz / QPSK / Mid+Mid CH /  
1@0-1@492A-5A / 20+10MHz / QPSK / Mid+Mid CH /  
1@99-1@0

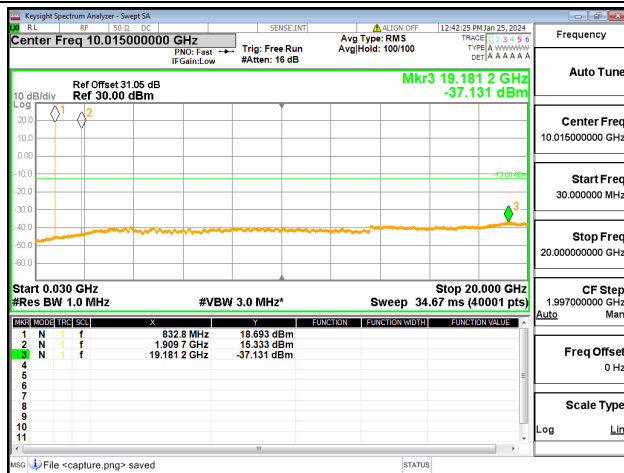




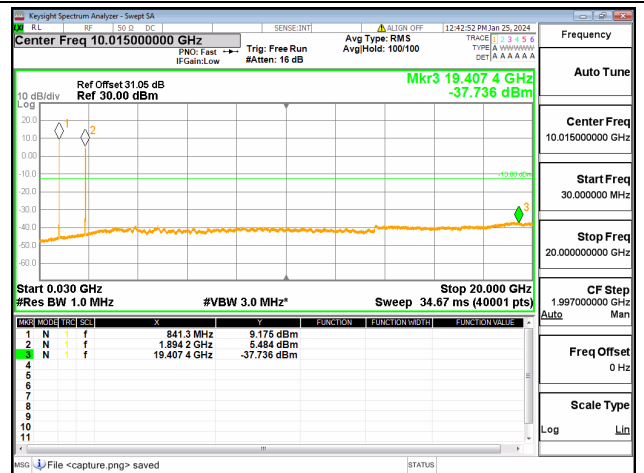
2A-5A / 20+10MHz / QPSK / High+Low CH /  
100@0-50@0



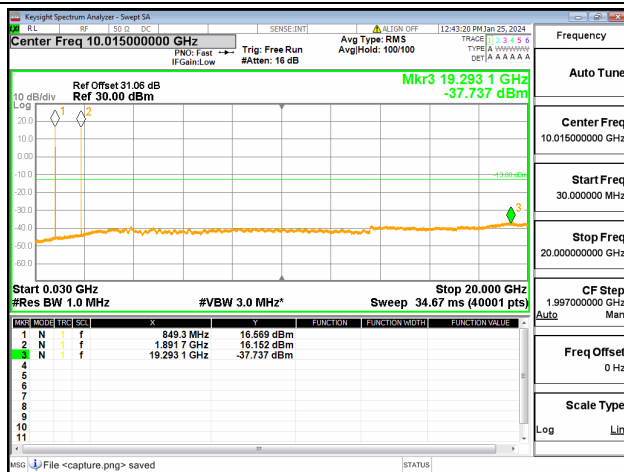
2A-5A / 20+10MHz / QPSK / High+Mid CH /  
1@0-1@49



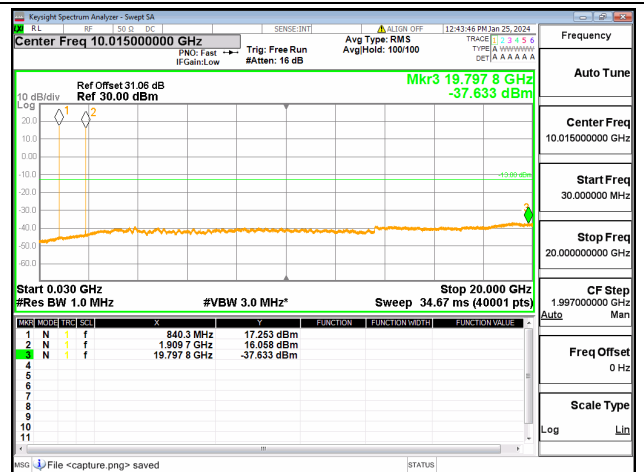
2A-5A / 20+10MHz / QPSK / High+Mid CH /  
1@99-1@0



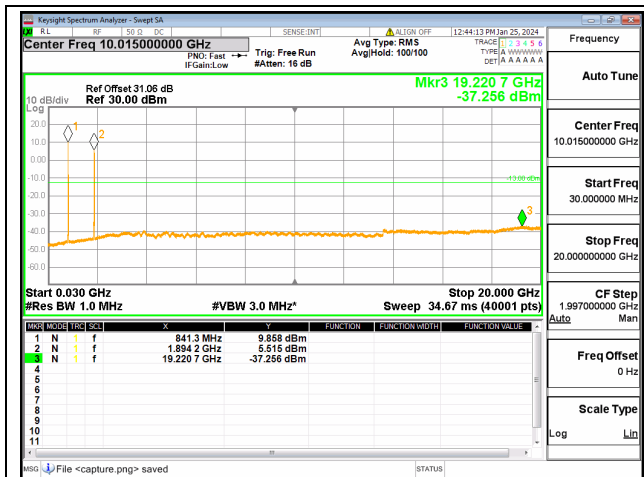
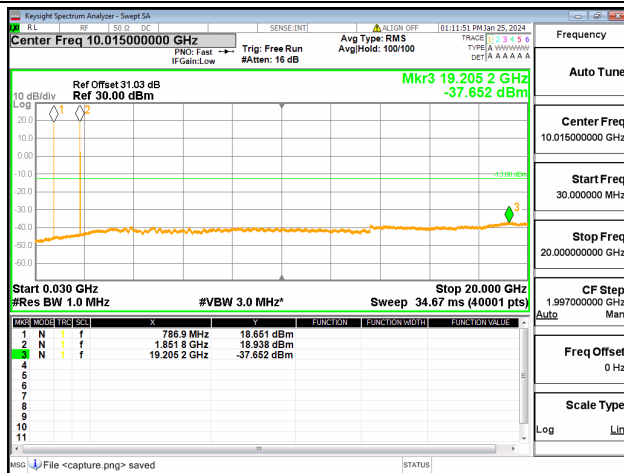
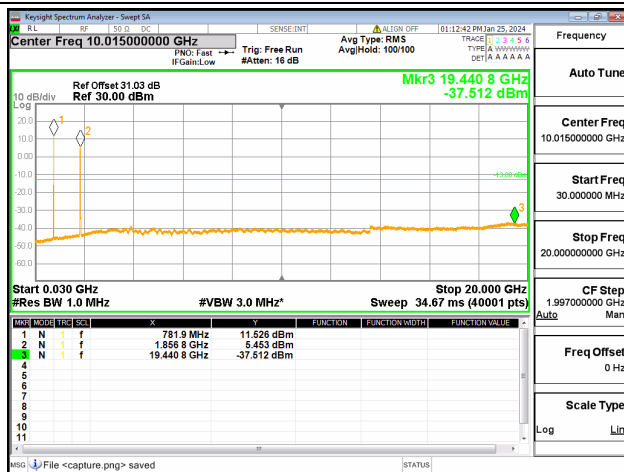
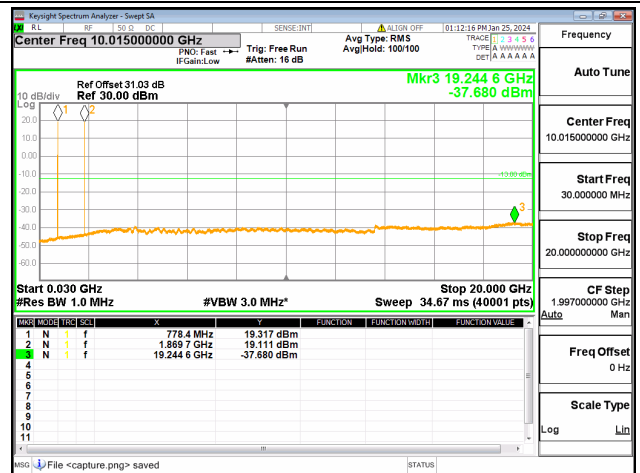
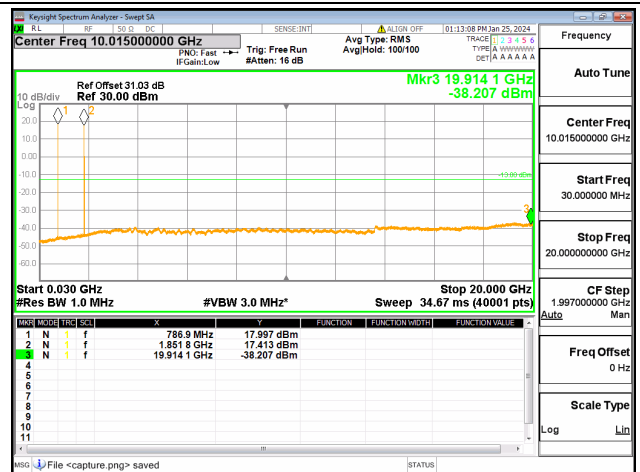
2A-5A / 20+10MHz / QPSK / High+Mid CH /  
100@0-50@0



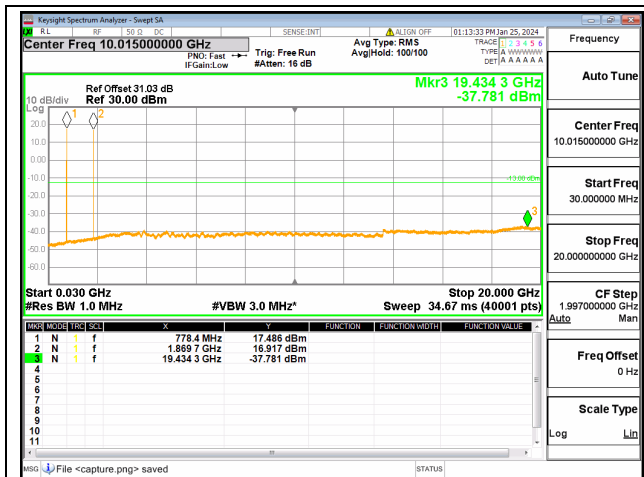
2A-5A / 20+10MHz / QPSK / High+High CH /  
1@0-1@49



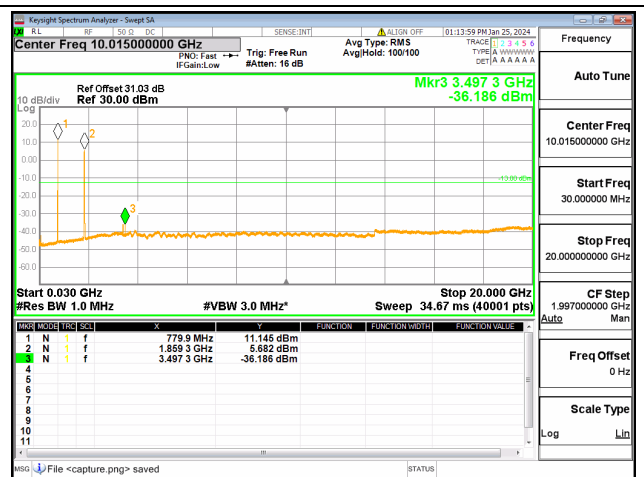
2A-5A / 20+10MHz / QPSK / High+High CH /  
1@99-1@0

2A-5A / 20+10MHz / QPSK / High+High CH /  
100@0-50@02A-13A / 20+10MHz / QPSK / Low+Low CH /  
1@0-1@492A-13A / 20+10MHz / QPSK / Low+Low CH /  
100@0-50@02A-13A / 20+10MHz / QPSK / Low+Low CH /  
1@99-1@02A-13A / 20+10MHz / QPSK / Low+Mid CH /  
1@0-1@49

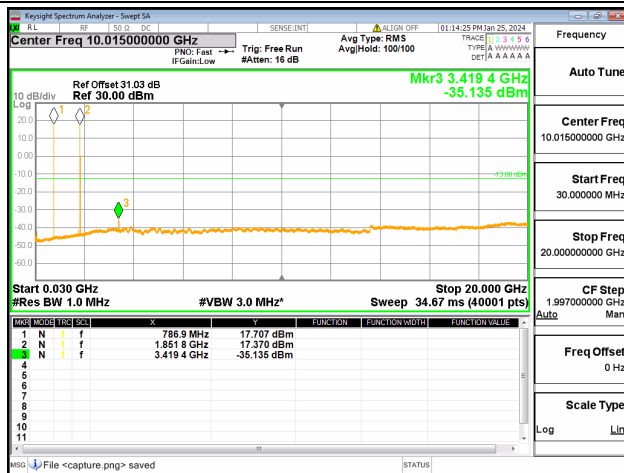
N/A



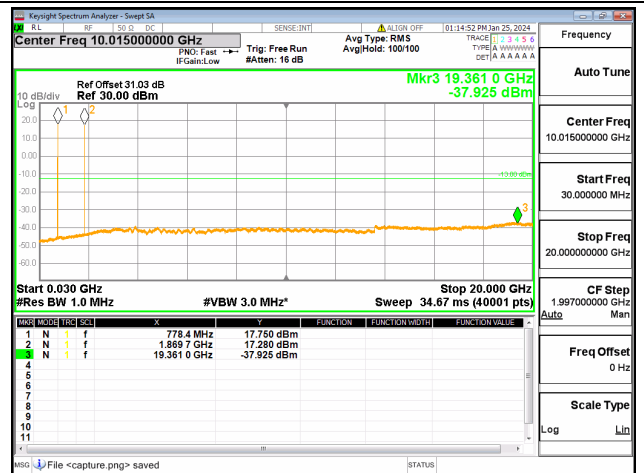
2A-13A / 20+10MHz / QPSK / Low+Mid CH /  
1@99-1@0



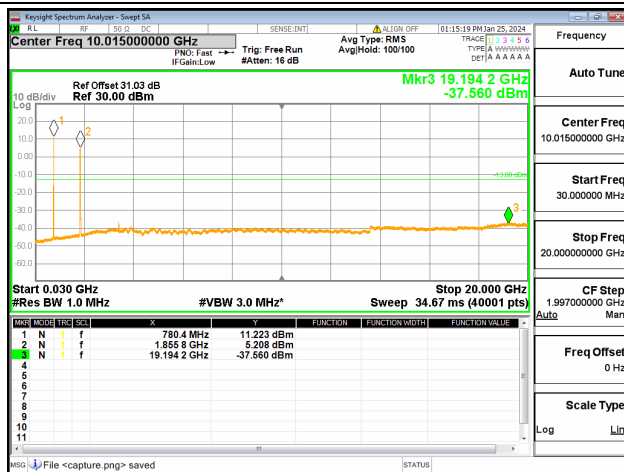
2A-13A / 20+10MHz / QPSK / Low+Mid CH /  
100@0-50@0



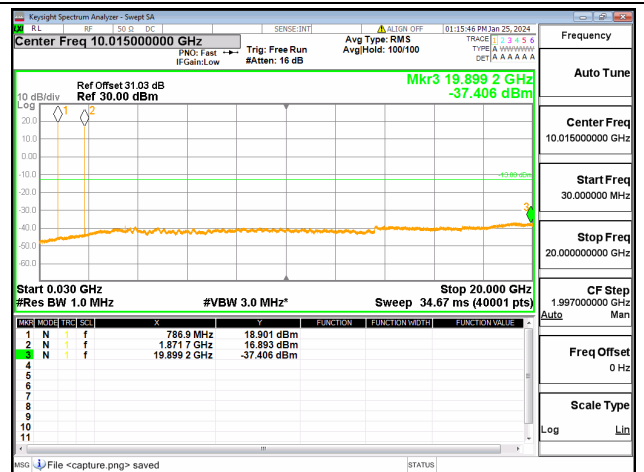
2A-13A / 20+10MHz / QPSK / Low+High CH /  
1@0-1@49



2A-13A / 20+10MHz / QPSK / Low+High CH /  
1@99-1@0



2A-13A / 20+10MHz / QPSK / Low+High CH /  
100@0-50@0



2A-13A / 20+10MHz / QPSK / Mid+Low CH /  
1@0-1@49

