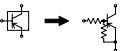


(SYSCON PCB)



NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE. NOTE: THE DC VOLTAGE AT EACH PART WAS MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK. CAUTION: DIGITAL TRANSISTOR

- RECORD COLOR SIGNAL
- RECORD LUMINANCE SIGNAL
- AUDIO SIGNAL(REC)
- AUDIO SIGNAL(PB)
- PLAYBACK COLOR SIGNAL
- PLAYBACK LUMINANCE SIGNAL
- TUNER VIDEO SIGNAL

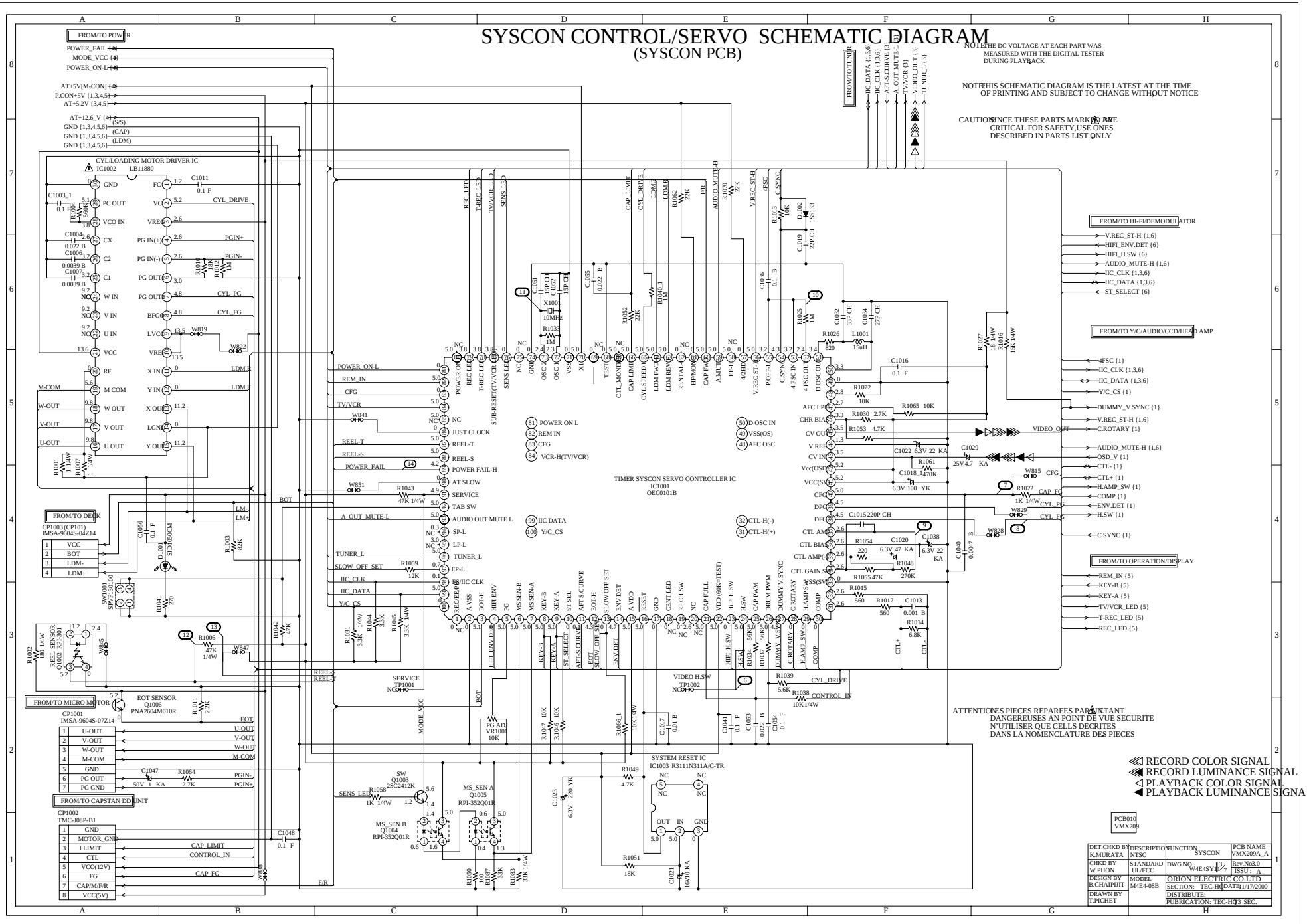
DET.CHKD BY K.MURATA	DESCRIPTION NTSC	FUNCTION Y/C/AUDIO/CCD/H.S.	PCB NAME REV.X209A_A
CHKD BY W.PHON	STANDARD UL/FCC	DWG.NO. W4E4YC127	Rev.No.0 ISSU: A
DESIGN BY B.CHAIPUJIT	MODEL M4E4-08B	ORION ELECTRIC CO.,LTD	
DRAWN BY T.PICHET	SECTION: TEC-HQ/DATE:1/17/2001		
DISTRIBUTE:			
PUBICATION: TEC-HQ/1 SEC.			

SYSCON CONTROL/SERVO SCHEMATIC DIAGRAM (SYSCON PCB)

NOT THE DC VOLTAGE AT EACH PART WAS
MEASURED WITH THE DIGITAL TESTER
DURING PLAYBACK

NOT THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME
OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

CAUTION SINCE THESE PARTS MARKED ARE
CRITICAL FOR SAFETY, USE ONES
DESCRIBED IN PARTS LIST ONLY



- ▶ RECORD COLOR SIGNAL
- ▶ RECORD LUMINANCE SIGNAL
- ▶ PLAYBACK COLOR SIGNAL
- ▶ PLAYBACK LUMINANCE SIGNAL

DET CHD BY	K. MURATA	DESCRIPTION	SYSCON	P.C.B NAME	VMX209A_A
CHD BY	STANDARD	DWG. NO.	W4EASY4	Rev. No.	8.0
W. PHON	UL/FCC	MODEL	ORION ELECTRIC CO. LTD	TSU	A
DESIGN BY	B. CHAIPIIT	SECTION	TEC-HQ3	DATE	1/77/2000
DRAWN BY	T. PICHET	DISTRIBUTE		PUBLICATION	TEC-HQ3 SEC.

TUNER SCHEMATIC DIAGRAM
(SYSCON PCB)

CAUTION: DIGITAL TRANSISTOR. THE DC VOLTAGE AT EACH PART MUST BE MEASURED WITH THE DIGITAL TESTER OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.

DANGER! THESE PARTS MARKED WITH THIS SYMBOL ARE DANGEROUS. PLEASE READ THE INSTRUCTIONS CAREFULLY.

ATTENTION: REPARER PAR UN TECHNICIEN QUALIFIE. NE PAS TOUCHER LES POINTS MARQUES AVEC CE SYMBOLE.

RECORD LUMINANCE SIGNAL
RECORD COLOR SIGNAL
AUDIO SIGNAL (PB)
TUNER VIDEO SIGNAL
PLAYBACK LUMINANCE SIGNAL
PLAYBACK COLOR SIGNAL

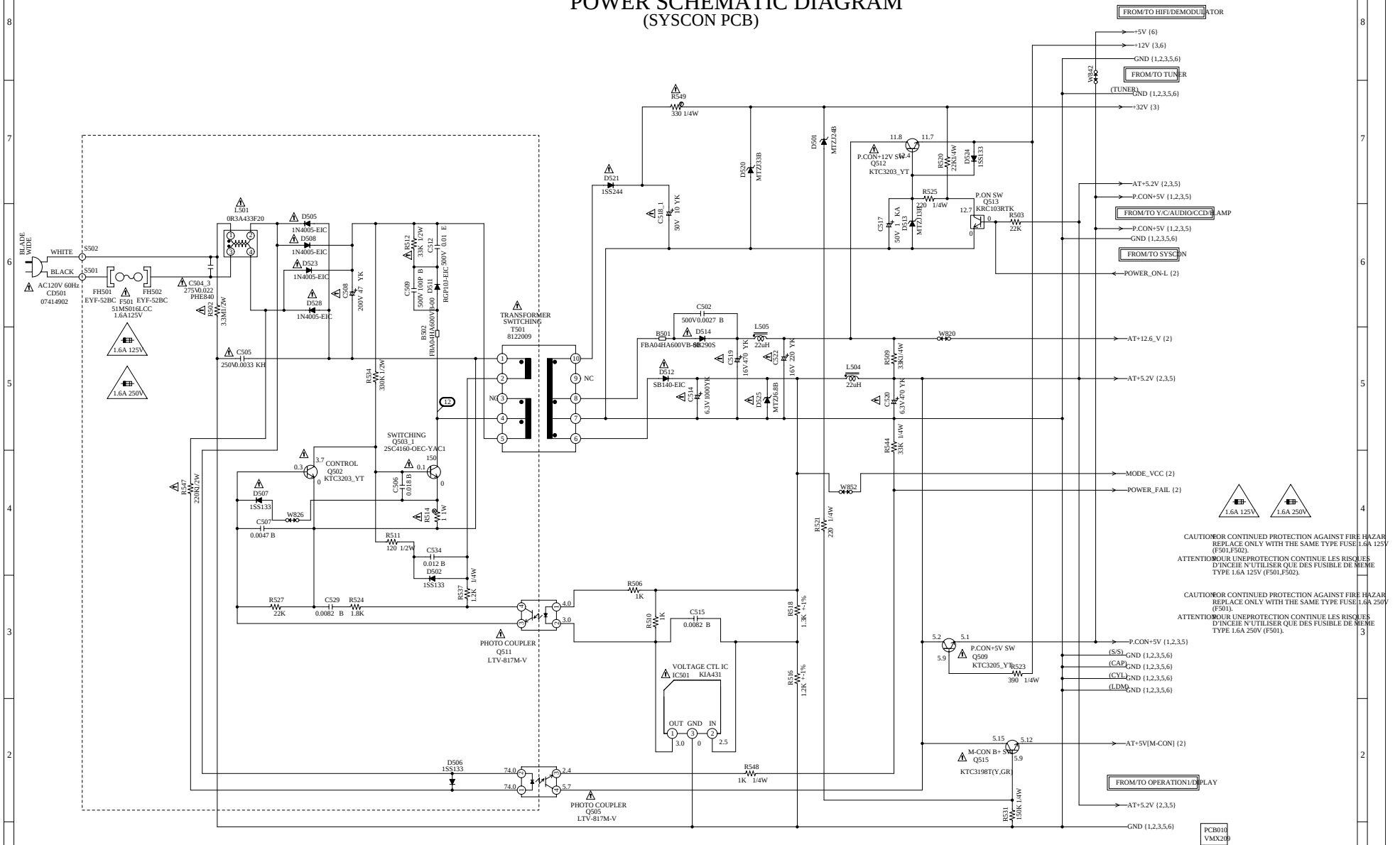
DET. CHKD BY	DESCRIPTION	FUNCTION	PCB NAME
K. MURATA	NTSC	TUNER	VMX209A, A
CHKD BY	STANDARD		
W. PHON	UL/FCC		
MODEL	M4E-4-08B		
DRAWN BY	T. PICHE		

ATTENTION: LES PIÈCES RÉPARÉES PAR UN
DANGEREUSES AN POINT DE VUE SECURITE
N'UTILISER QUE CELLES DECRITES
DANS LA NOMENCLATURE DES PIÈCES

RECORD LUMINANCE SIGNAL (Y)
 RECORD COLOR SIGNAL (C)
 AUDIO SIGNAL (REC)
 AUDIO SIGNAL (PB)
 TUNER VIDEO SIGNAL
 PLAYBACK LUMINANCE SIGNAL (Y)
 PLAYBACK COLOR SIGNAL (C)

DET.CHKD BY K.MURATA	DESCRIPTION NTSC	FUNCTION TUNER	PCB NAME VMX209A
CHKD BY W.PHON	STANDARD UL/FCC	DWG.NO. W4E-4TU117	Rev.No.03 ISSU: A
DRAWN BY B.CHAIPUJIT	MODEL M4E4-08B	ORION ELECTRIC CO.LTD SECTION: TEC-HQ DATE: 11/17/2001 DISTRIBUTE: PUBICATION: TEC-HQ 3C.SEC.	

POWER SCHEMATIC DIAGRAM (SYSCON PCB)



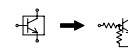
NOTE: THE DC VOLTAGE AT EACH PART WAS MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK.

NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

TOUTES PIÈCES RÉPARÉES PAR UN
 DANGEREUSES AU POINT DE VUE SECURITE
 N'UTILISER QUE CELLES DÉCRITES
 DANS LA NOMENCLATURE DES PIÈCES

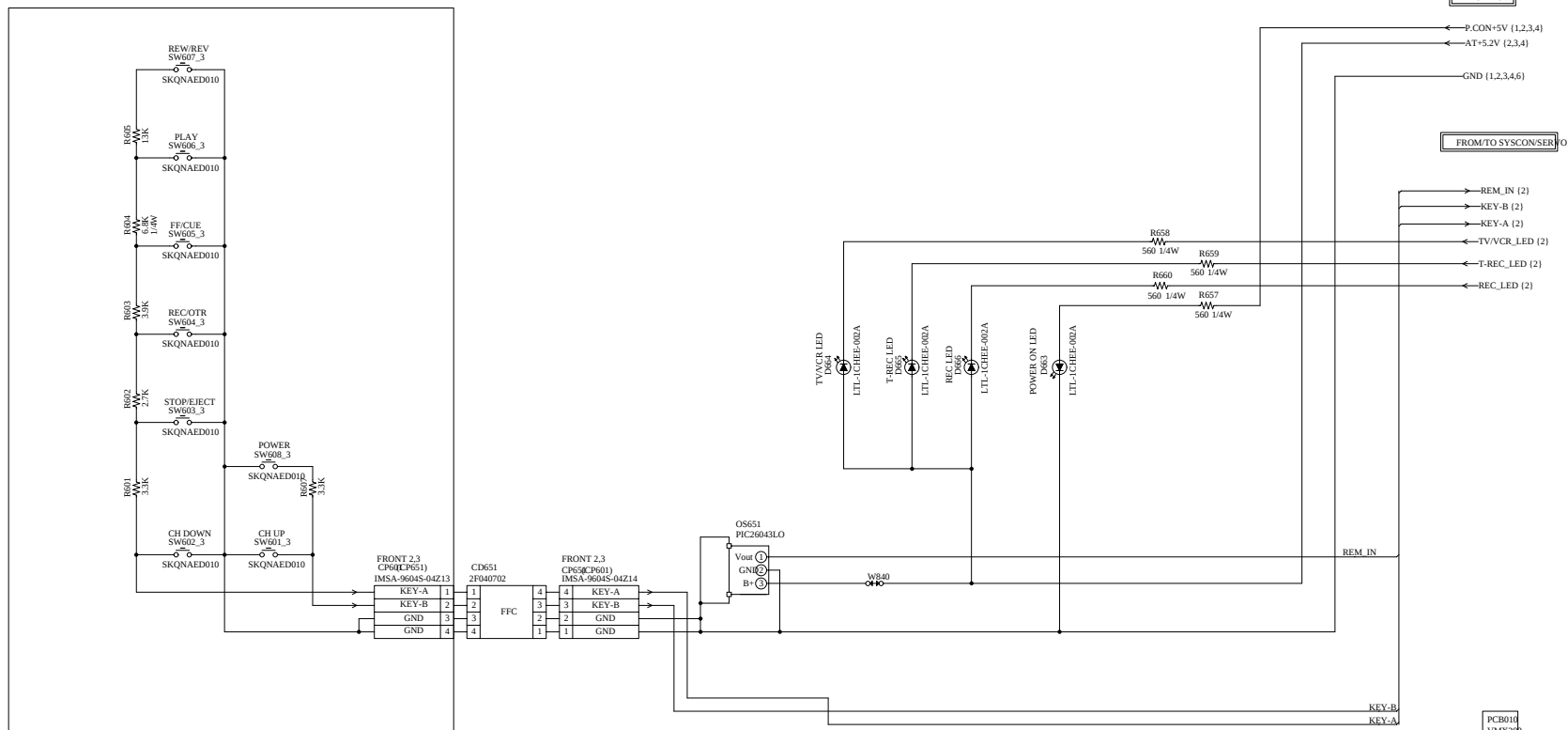
CAUTION SINCE THESE PARTS MARKED ARE CRITICAL FOR SAFETY, USE ONES DESCRIBED IN PARTS LIST ONLY

CAUTION: DIGITAL TRANSISTOR



DET.CHKD BY K.MURATA	DESCRIPTION NTSC	FUNCTION POWER	PCB NAME VMX209A_A
CHKD BY W.PHON	STANDARD UL/FCC	DWG.NO. W4E4P027	Rev.No.0 ISSU: A
DESIGN BY B.CHAIPUJIT	MODEL M4E4-08B	ORION ELECTRIC CO.LTD SECTION: TECH-DATE:11/7/2000	
DRAWN BY T.PICHET	DISTRIBUTE: PUBLICATION: TEC-HQ1 SEC.		

OPERATION/DISPLAY SCHEMATIC DIAGRAM
(SYSCON PCB)



PCB010
VMX20

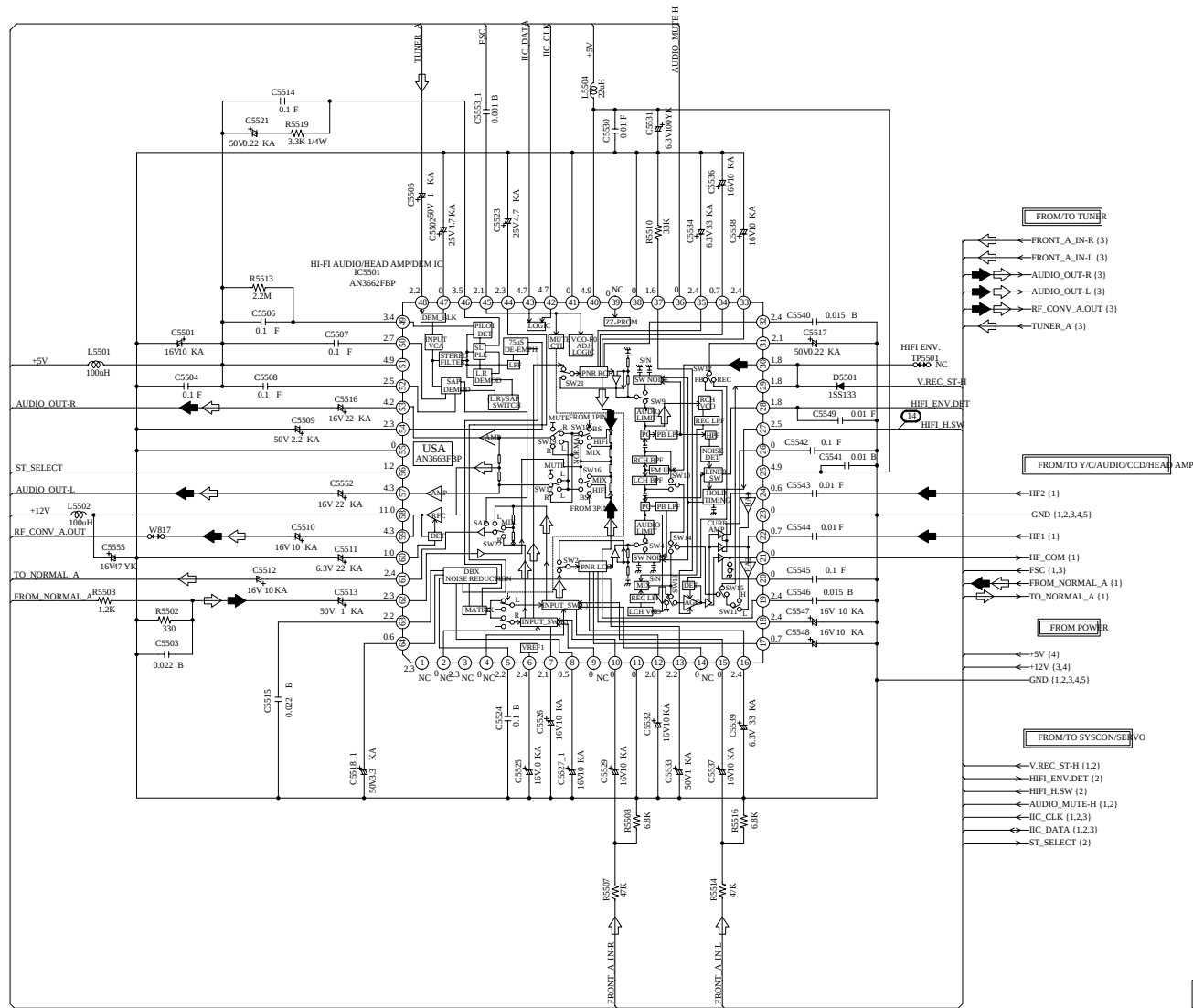
NOTE: THE DC VOLTAGE AT EACH PART WAS
MEASURED WITH THE DIGITAL TESTER
DURING PLAYBACK.

NOTE: THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME
OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE

PCB010
VMX20

DET. CHKD BY K. MURATA	DESCRIPTION NTSC	FUNCTION OPERATION/DISPLAY	P.C.B. NAME AX-209A_A
CHKD BY W. PHON	STANDARD UL/FCC	DWG. NO. W4E40P17	Rev. No. 3.0
DESIGN BY B. CHAIPIIT	MODEL M4E4-08B	ORION ELECTRIC CO., LTD.	TSU: A
DRAWN BY T. PICHET		SECTION: TEC-HQ1	DATE: 1/17/2000
		DISTRIBUTE:	
		PUBLICATION: TEC-HQ1 SEC.	

HI-FI/DEMODULATOR SCHEMATIC DIAGRAM
(SYSCON PCB)



NOTE: THE DC VOLTAGE AT EACH PART OF THIS SCHEMATIC DIAGRAM IS THE LATEST AT THE TIME OF PRINTING AND SUBJECT TO CHANGE WITHOUT NOTICE.
MEASURED WITH THE DIGITAL TESTER DURING PLAYBACK

PCB010	YMX219		
DET. CHKD BY	K. MURATA	FUNCTION	HI-FI/DEMODULATOR
CHKD BY	W. PHON	DWG. NO.	W4E4HF
DESIGN BY	B. CHAIPIIT	MODEL	M4E4-08B
DRAWN BY	T. PICHET	SECTION	TEC-HQ1/17/2000
		DISTRIBUTE	
		PUBLICATION	TEC-HQ1 SEC.

PCB NAME	PCB010
REV. NO.	YMX219
TSU	TSU: A
ORION ELECTRIC CO. LTD.	