

Technical Description

This model is a receiver for accepting the interpreted caller ID information from 43-940. The data is modulated with a carrier frequency of 418Mhz.

IC1 EM78860 is the CPU controlling the unit. It periodically pull low VX pin to enable 418 MHz receiving circuit. (IC2)

When there is RF data, the CPU will start a receive cycle to accept all bits of information from 43-940. In turn the same will be output to the LCD.

The record is stored in the non-volatile memory IC3 24C16 for further review. 60 records maximum can be stored.

Looking into the receive circuit, IC2, X1, ANT1, C30, C3, R12 forms the superheterodyne type (SH) circuit 418Mhz.

The raw data is passed to the CPU (DATAIN) via R21, Q1, for interpretation.