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CB801AS Operation Principle

1. The CB801AS project is the PCI interface for Wireless LAN IEEE 802.11b/g. It's protable device, used for 2.4GHz ISM band. Support 6,9,12,18,24,36,48,54 Mbps for OFDM; 11,5.5Mbps for CCK; 1,2 Mbps for Barker modulation; Atheros super G mode, offering up to 108 Mbps.
2. CPU, U1, AR2414 is an all CMOS, single chip solution for dual-mode, IEEE 802.11b/g wireless local area network (WLANs).
3. The AR2414 transceiver consists of the major functional blocks
 - Receiver
 - Transmitter
 - Frequency synthesizer
 - Associated bias / control

4. Components:

1. CPU	AR2414 WLAN 802.11 b/g MAC/BB/Radio processor.
2. Power Amplifier	GP1214 2.4GHz high performance power amplifier
3. Low Noise Amplifier	SIRENZA SGA-8343
4. PCB Layout	4 layers

5. LED part:

LED 1	LED 2	Description
Slow rate blink	off	Power save mode
Alternate blink	Alternate blink	Looking for network association
Blink	Blink	Associated or joined with network;
off	off	Power off or PCI/CardBus slot disabled

6. EEPROM, U3, M24C32, 32Kbits. It is supports 400KHz protocol.