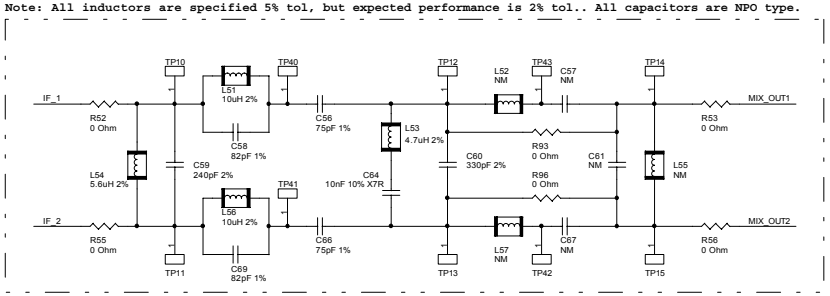
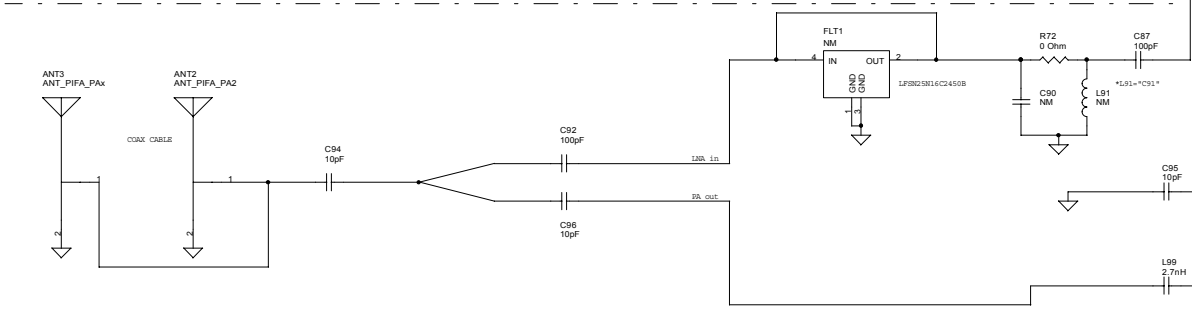
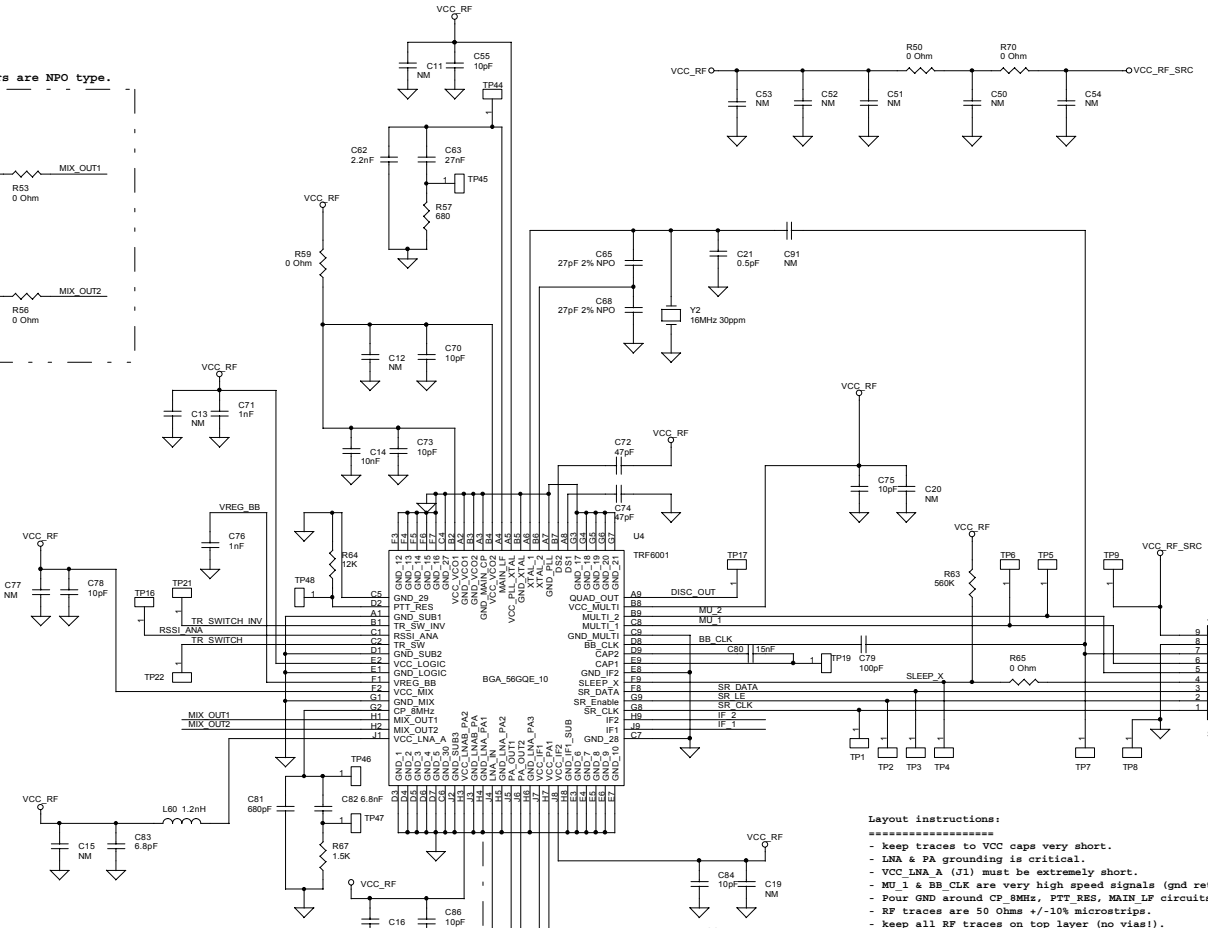
%%% WILL UPDATE COMPLETE IF FILTER, CHOOSE LOWER TOL. COMPONENTS %%%

Note: All inductors are specified 5% tol, but expected performance is 2% tol.. All capacitors are NPO type.







Layout instructions:
==========
- keep traces to VCC caps very short.
- LNA & PA grounding is critical.
- VCC_LNA_A (J1) must be extremely short.
- MU_1 & BB_CLK are very high speed signals (gnd return!).
- Pour GND around CP_8MHz, PTT_RES, MAIN_LF circuits.
- RF traces are 50 Ohms +/-10% microstrips.
- keep all RF traces on top layer (no vias!).
- layout PA_OUT lines symmetrically.
- Follow layout order for C51, C52, C53.

FOR FUTURE REVISIONS:
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- optimize design of impedance matching on LNA_IN & PA_OUT.
- determine which of the 10nF caps on VCC_RF are needed.

Ask TI:
======
- none.

R59 value is for 3.0V VCC_RF product version.

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Title: RF Module, Hagrid -- TRF6001			
Designed by: ES, PL	Project Name: HAGRID		
Size: B	Document Number: 221625-0000	PCB 7 part number: 201625-0000	Rev: **PA5.2**
Date: Tuesday, June 04, 2002			Sheet 1 of 1

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