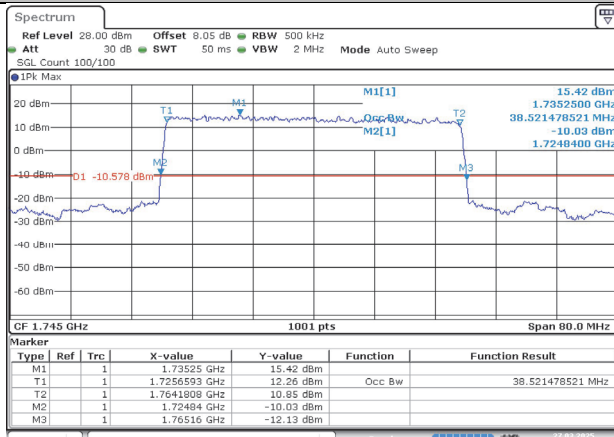


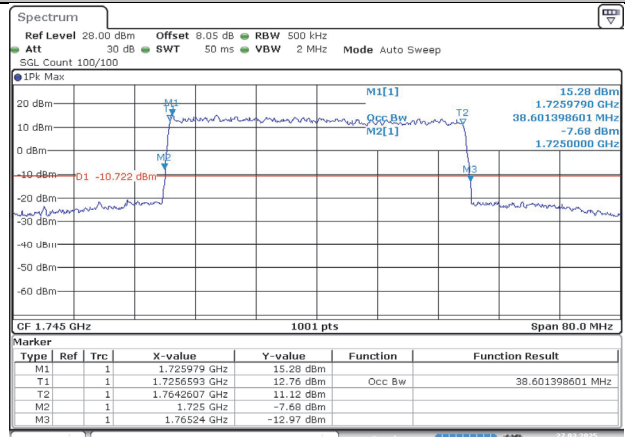
DC_5A_n66A / 15KHz / 40MHz

MCH / DFT-Pi2BPSK / Outer_Full



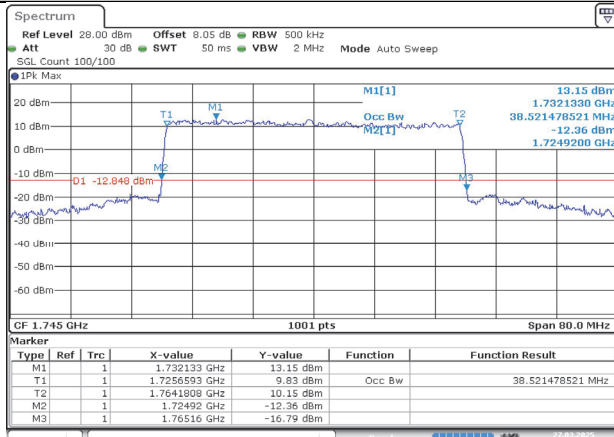
Date: 27.MAR.2025 09:01:16

MCH / DFT-QPSK / Outer_Full



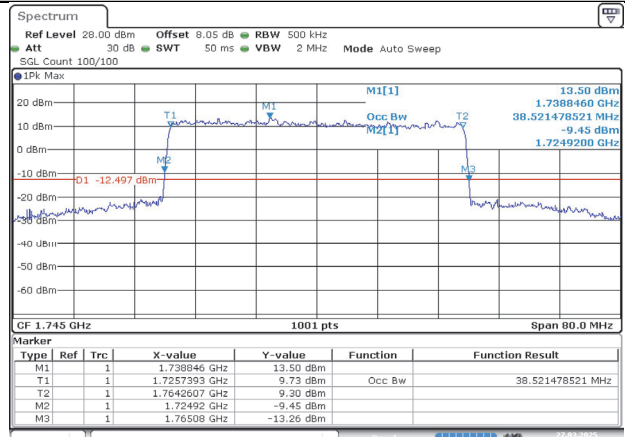
Date: 27.MAR.2025 09:01:25

MCH / CP-QPSK / Outer_Full



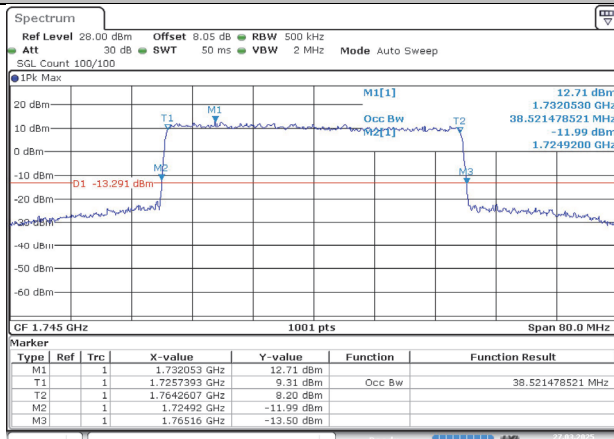
Date: 27.MAR.2025 09:02:00

MCH / CP-16QAM / Outer_Full



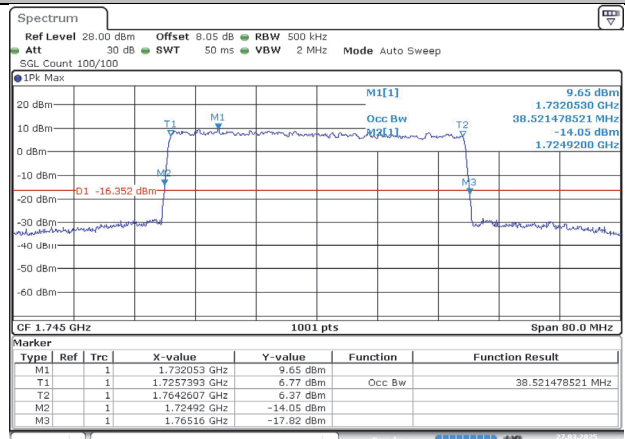
Date: 27.MAR.2025 09:02:08

MCH / CP-64QAM / Outer_Full



Date: 27.MAR.2025 09:02:17

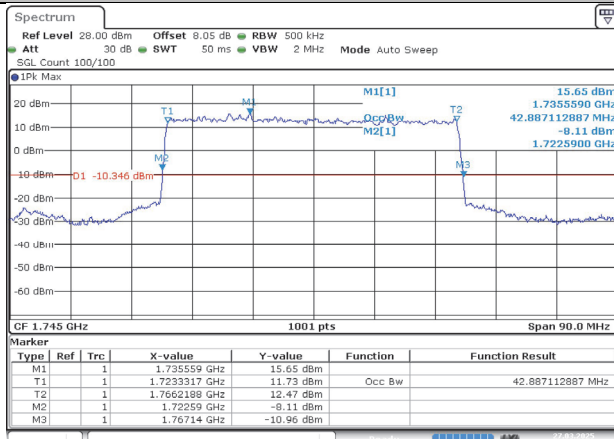
MCH / CP-256QAM / Outer_Full



Date: 27.MAR.2025 09:02:26

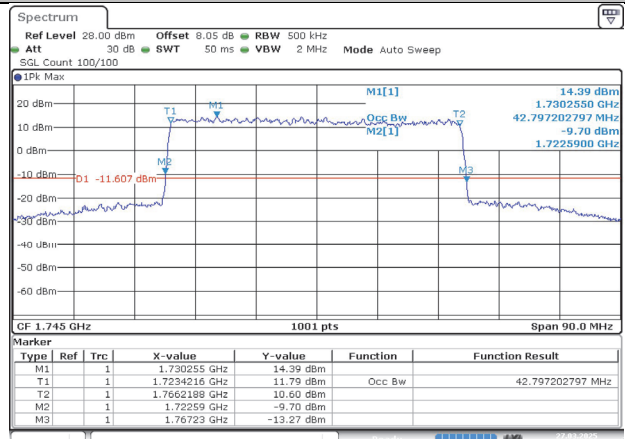
DC_5A_n66A / 15KHz / 45MHz

MCH / DFT-Pi2BPSK / Outer_Full



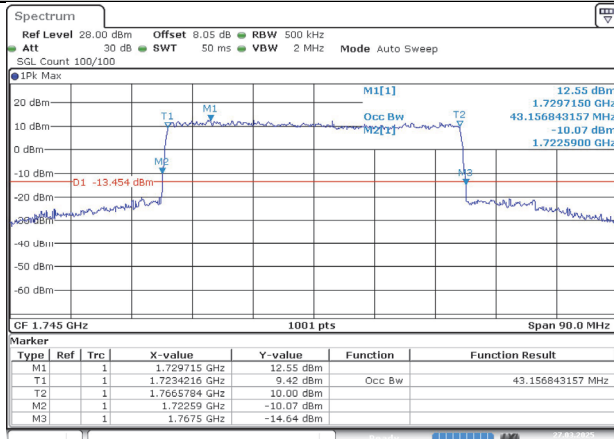
Date: 27 MAR 2025 09:02:38

MCH / DFT-QPSK / Outer_Full



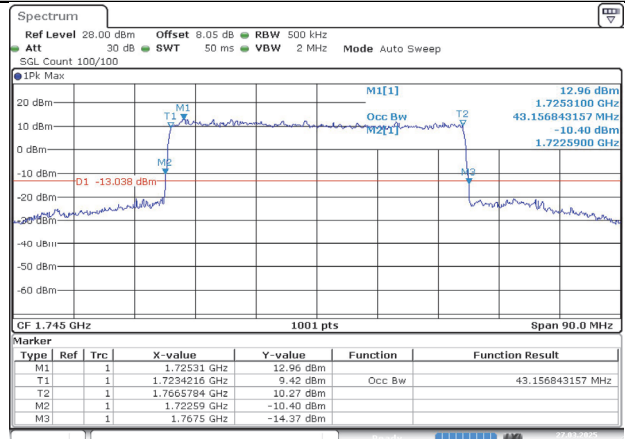
Date: 27 MAR 2025 09:02:47

MCH / CP-QPSK / Outer_Full



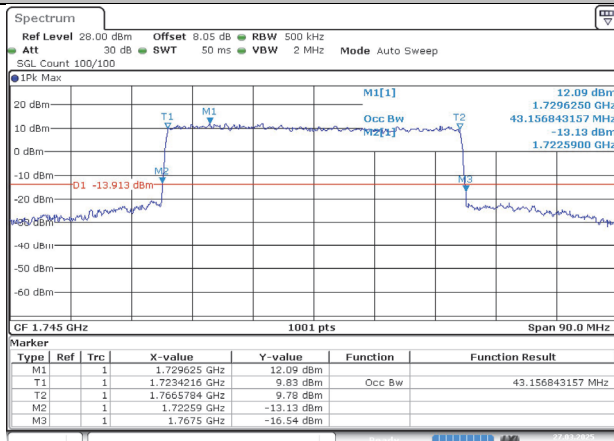
Date: 27 MAR 2025 09:03:23

MCH / CP-16QAM / Outer_Full



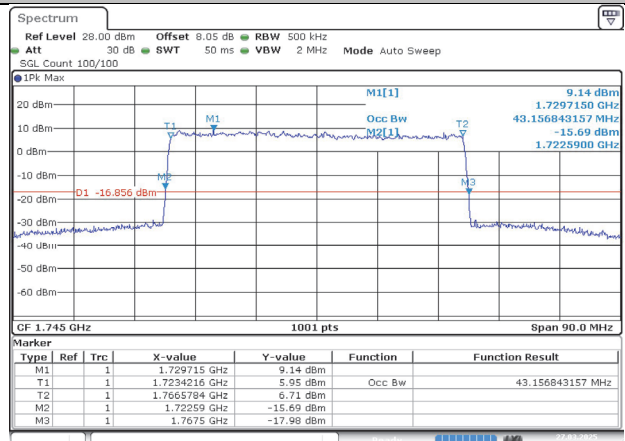
Date: 27 MAR 2025 09:03:31

MCH / CP-64QAM / Outer_Full



Date: 27 MAR 2025 09:03:39

MCH / CP-256QAM / Outer_Full



Date: 27 MAR 2025 09:03:48

5. Conducted Band Edges

5.1. Test Plots for SCS=15KHz

