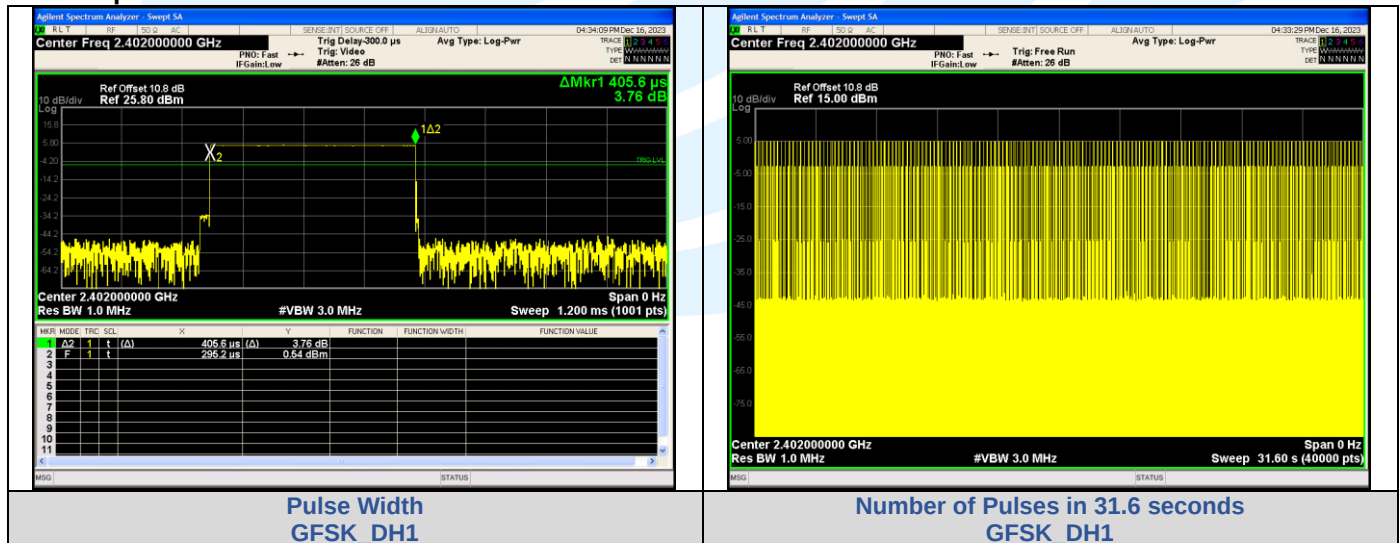


A.5 DWELL TIME

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH1	CH0 (2402MHz)	0.4056	319	129.39	< 400	PASS
	DH1	CH78 (2480MHz)	0.4056	316	128.17		PASS
	DH3	CH0 (2402MHz)	1.656	159	263.3		PASS
	DH3	CH78 (2480MHz)	1.656	160	264.96		PASS
	DH5	CH0 (2402MHz)	2.912	105	305.76		PASS
	DH5	CH78 (2480MHz)	2.912	112	326.14		PASS
π /4DQPSK	2-DH1	CH0 (2402MHz)	0.3984	318	126.69		PASS
	2-DH1	CH78 (2480MHz)	0.3984	316	125.89		PASS
	2-DH3	CH0 (2402MHz)	1.656	170	281.52		PASS
	2-DH3	CH78 (2480MHz)	1.656	157	259.99		PASS
	2-DH5	CH0 (2402MHz)	2.912	104	302.85		PASS
	2-DH5	CH78 (2480MHz)	2.896	109	315.66		PASS
8DPSK	3-DH1	CH0 (2402MHz)	0.3960	317	125.53		PASS
	3-DH1	CH78 (2480MHz)	0.3960	312	123.55		PASS
	3-DH3	CH0 (2402MHz)	1.656	159	263.3		PASS
	3-DH3	CH78 (2480MHz)	1.632	155	252.96		PASS
	3-DH5	CH0 (2402MHz)	2.896	108	312.77		PASS
	3-DH5	CH78 (2480MHz)	2.896	100	289.6		PASS

Test Graphs



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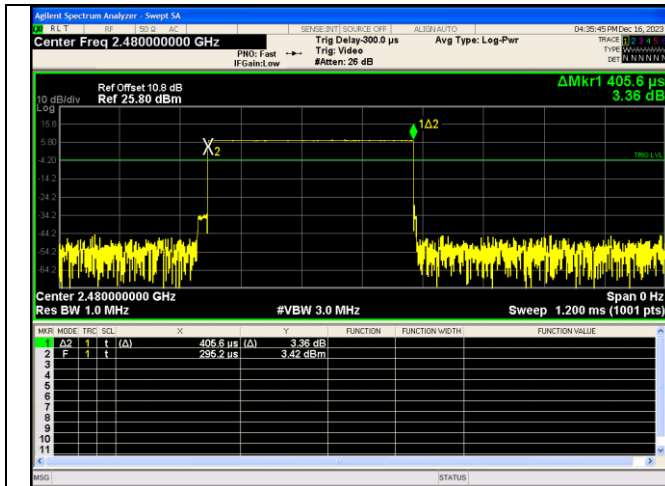
Tel: +86-755-28230888

Fax: +86-755-28230886

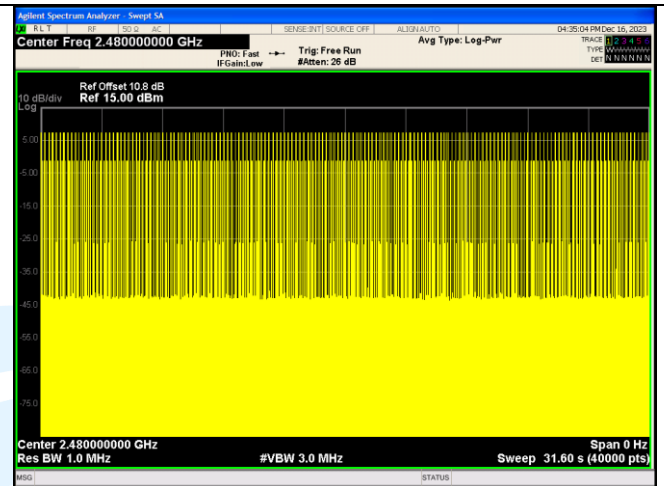
E-mail: info@uttlab.com

<http://www.uttlab.com>

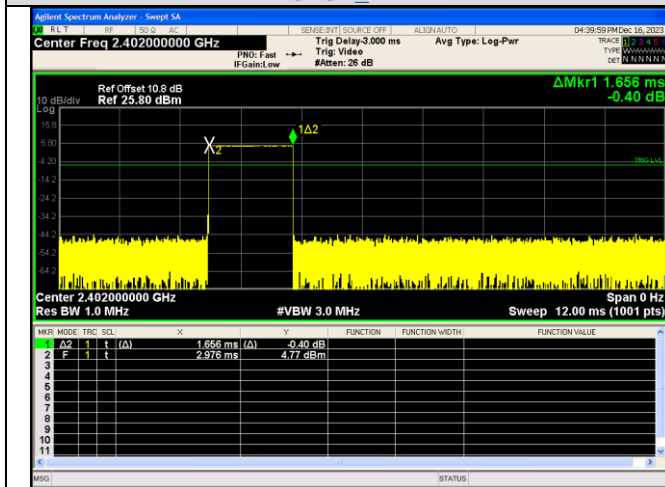
UTTR-RF-FCCPART15.247-V1.1



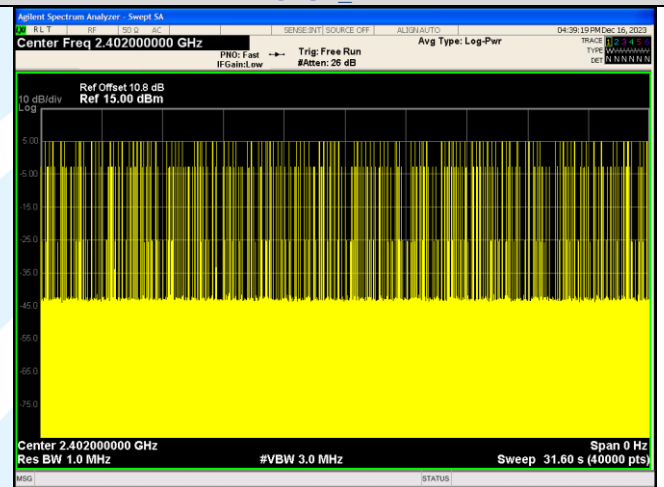
Pulse Width
GFSK_DH1



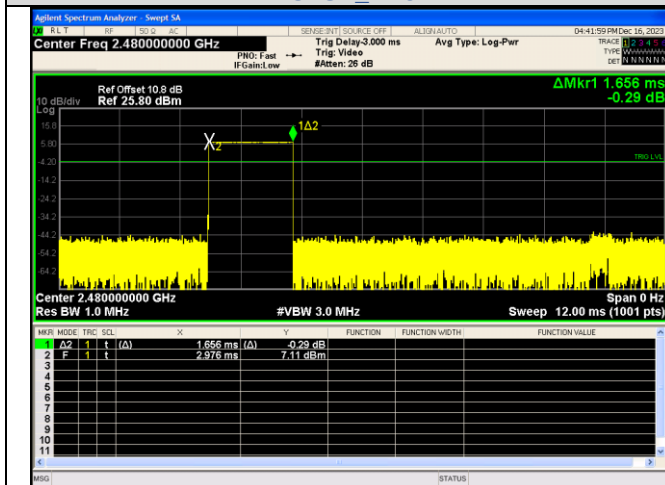
Number of Pulses in 31.6 seconds
GFSK_DH1



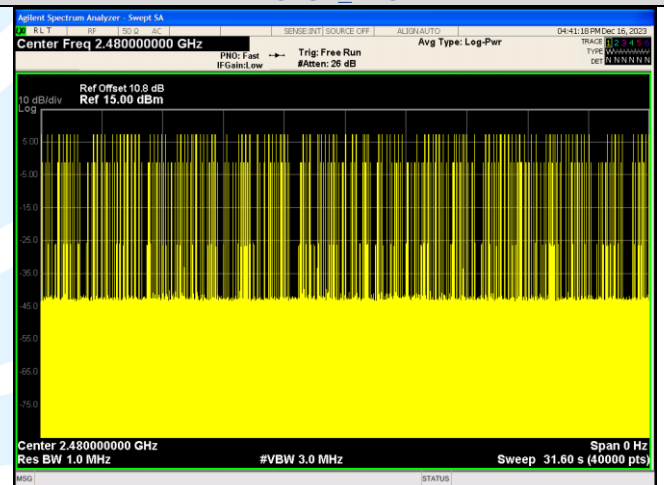
Pulse Width
GFSK_DH3



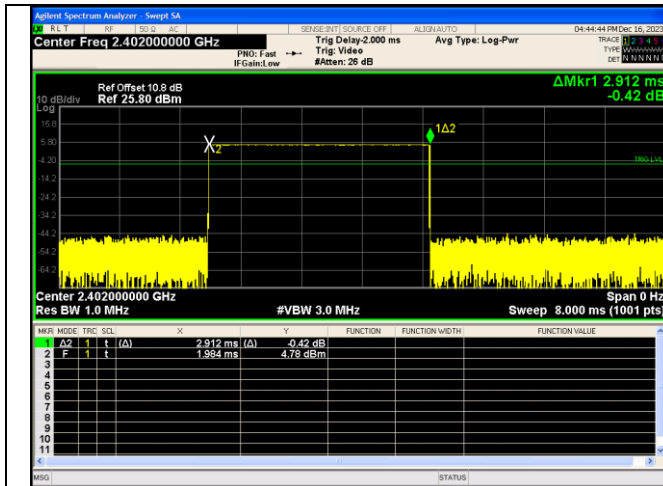
Number of Pulses in 31.6 seconds
GFSK_DH3



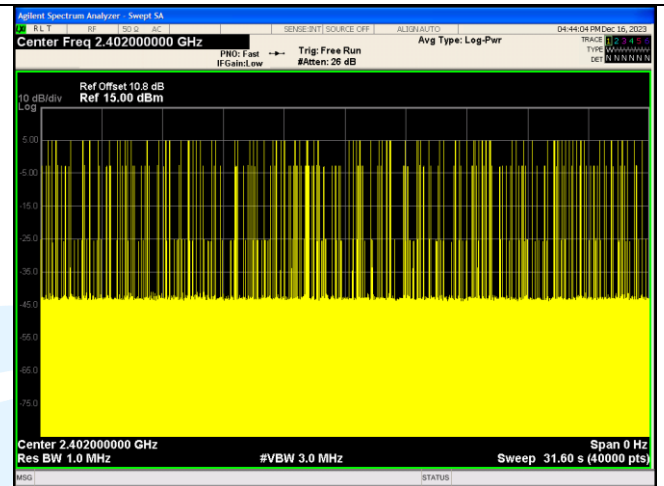
Pulse Width
GFSK_DH3



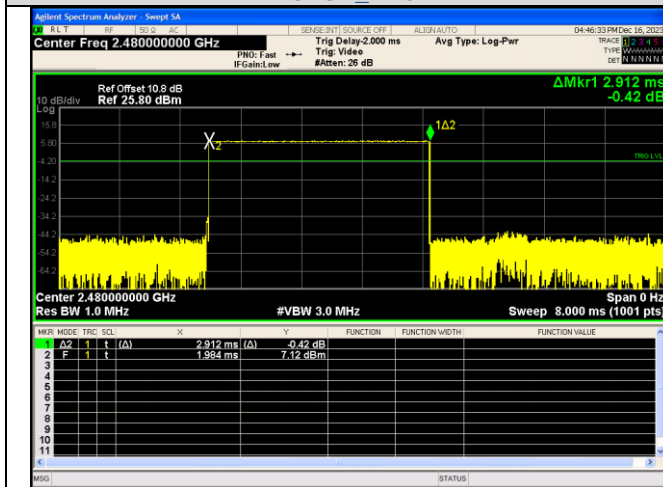
Number of Pulses in 31.6 seconds
GFSK_DH3



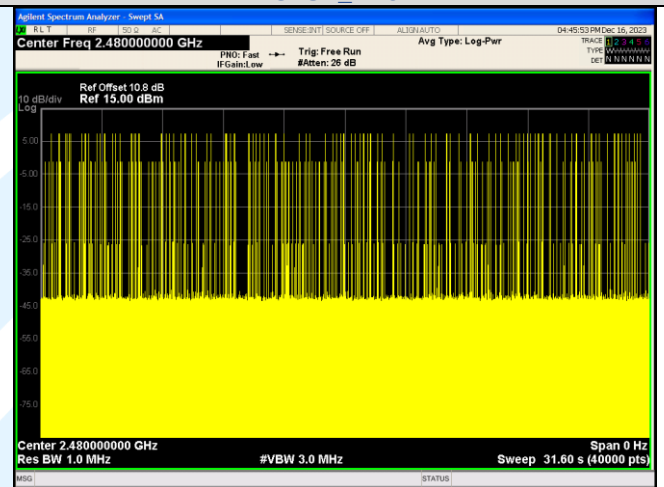
Pulse Width
GFSK_DH5



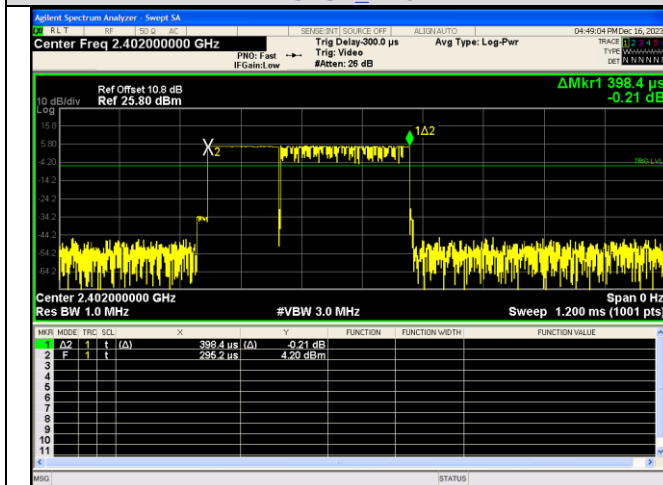
Number of Pulses in 31.6 seconds
GFSK_DH5



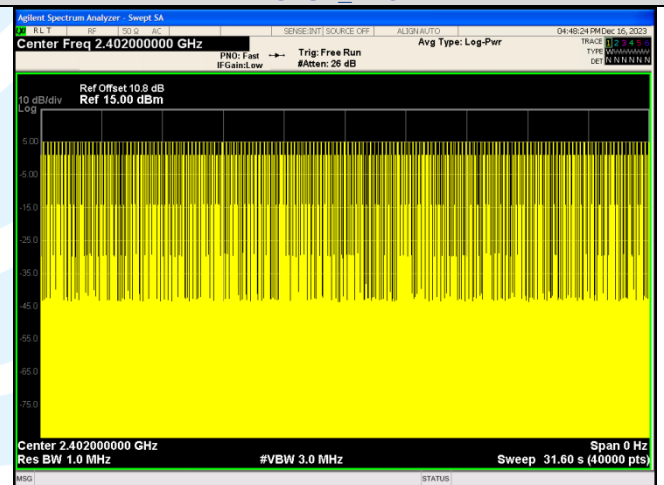
Pulse Width
GFSK_DH5



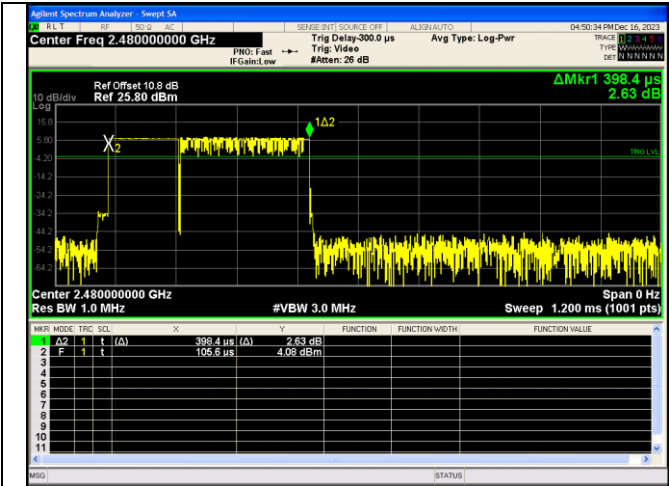
Number of Pulses in 31.6 seconds
GFSK_DH5



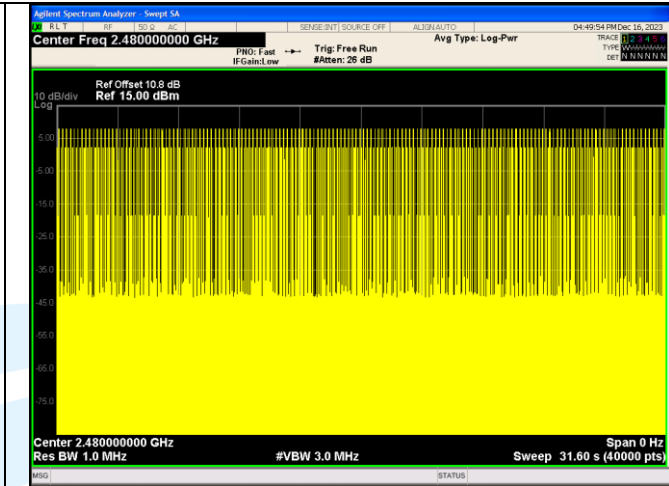
Pulse Width
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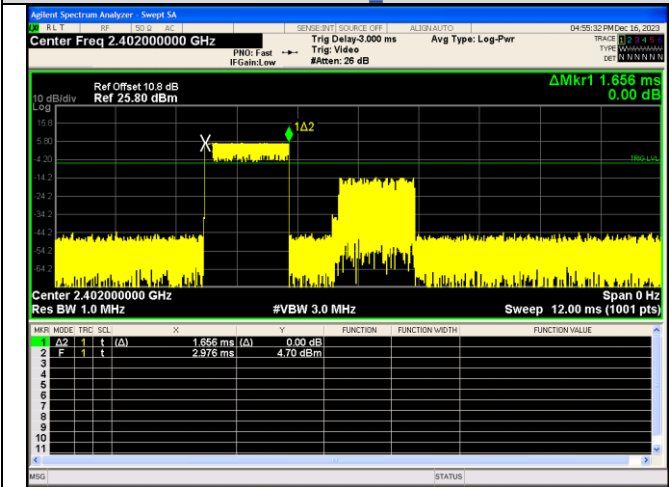
Number of Pulses in 31.6 seconds
π/4DQPSK_2-DH1



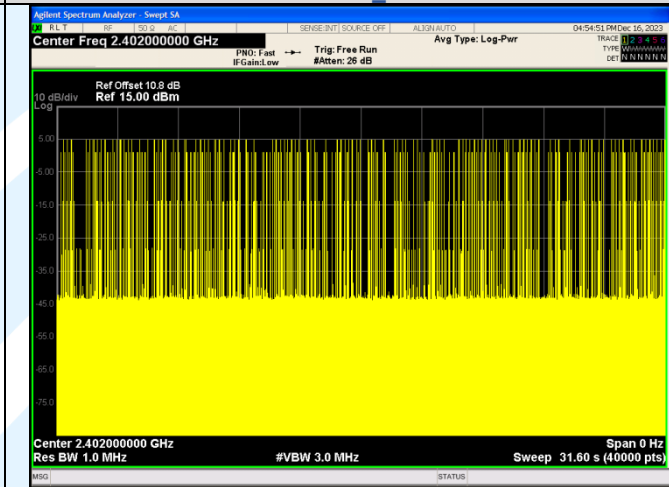
Pulse Width
 $\pi/4$ DQPSK_2-DH1



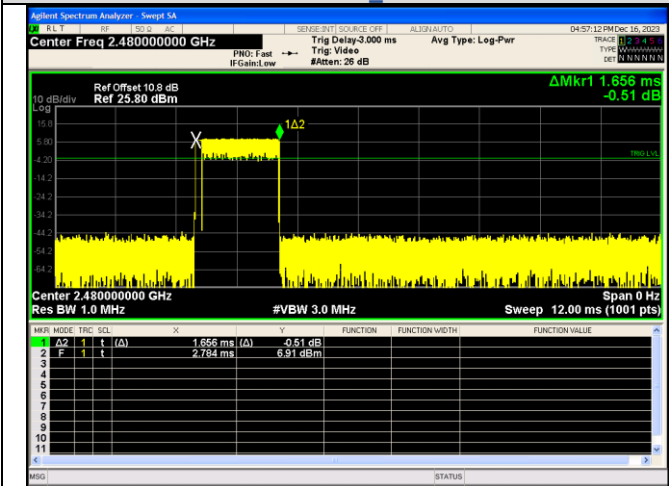
Number of Pulses in 31.6 seconds
 $\pi/4$ DQPSK_2-DH1



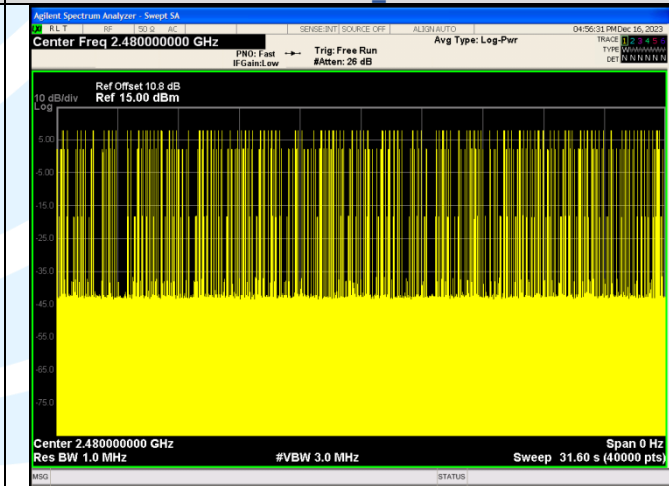
Pulse Width
 $\pi/4$ DQPSK_2-DH3



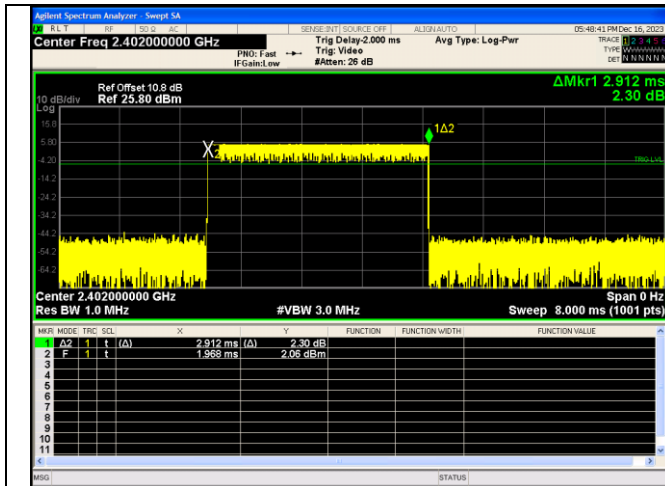
Number of Pulses in 31.6 seconds
 $\pi/4$ DQPSK_2-DH3



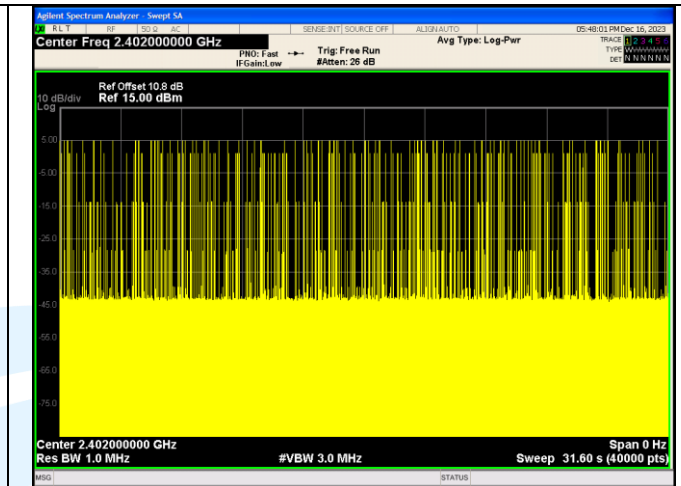
Pulse Width
 $\pi/4$ DQPSK_2-DH3



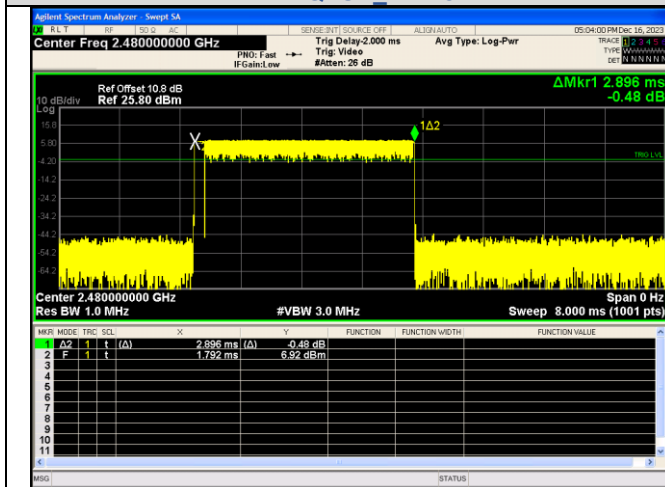
Number of Pulses in 31.6 seconds
 $\pi/4$ DQPSK_2-DH3



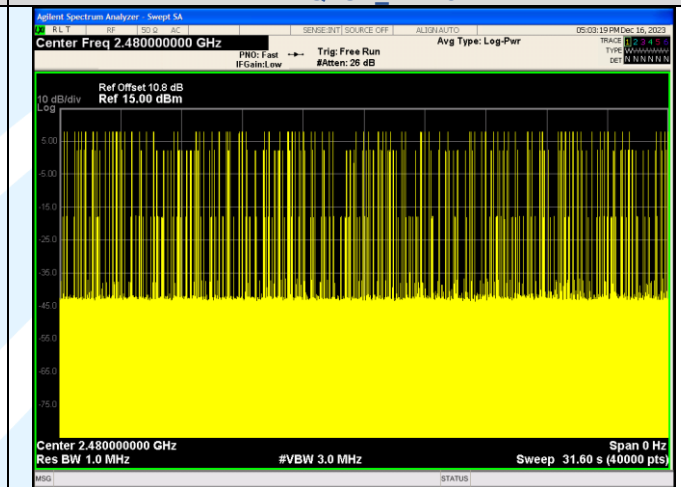
Pulse Width
 $\pi/4$ DQPSK_2-DH5



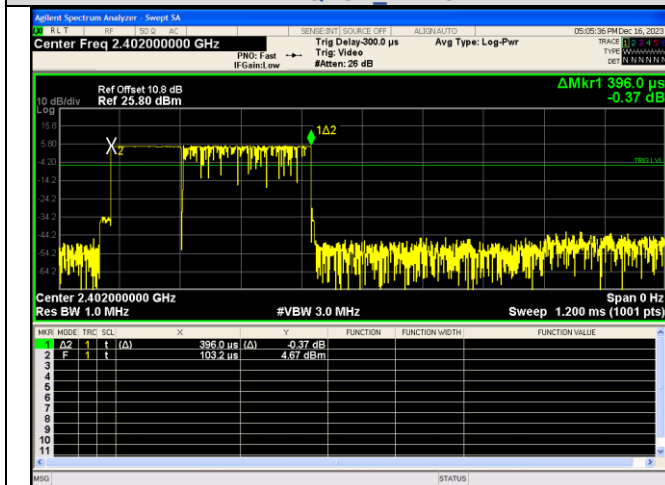
Number of Pulses in 31.6 seconds
 $\pi/4$ DQPSK_2-DH5



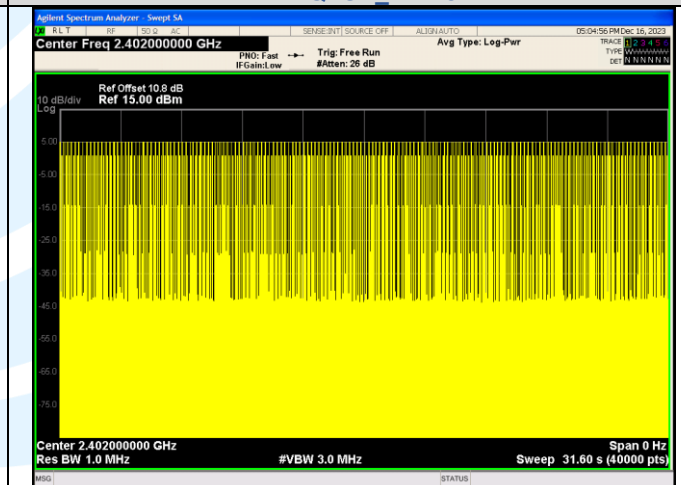
Pulse Width
 $\pi/4$ DQPSK_2-DH5



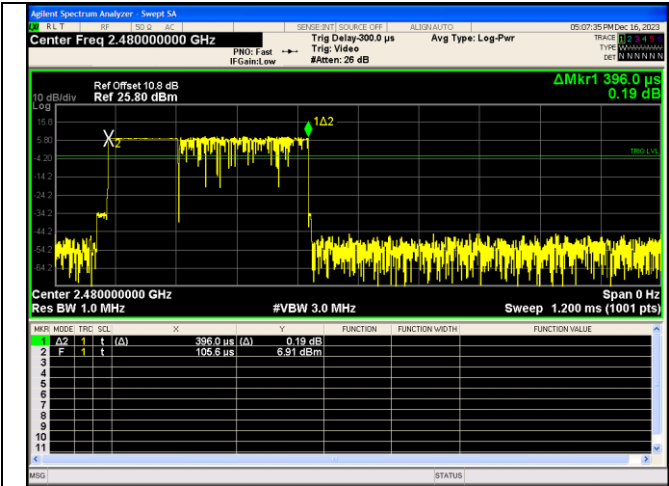
Number of Pulses in 31.6 seconds
 $\pi/4$ DQPSK_2-DH5



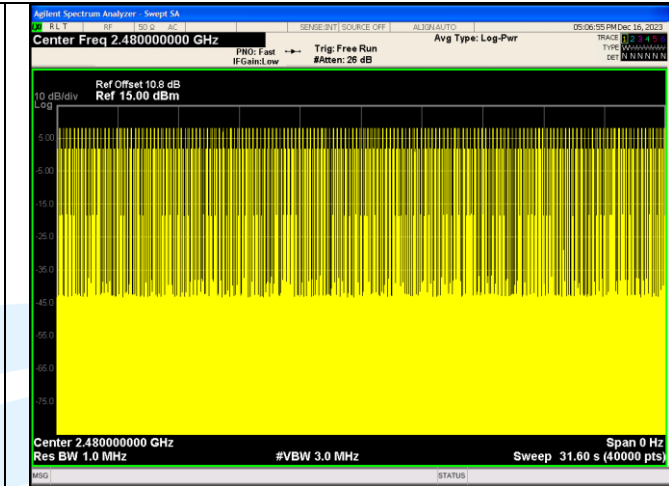
Pulse Width
8DPSK_3-DH1



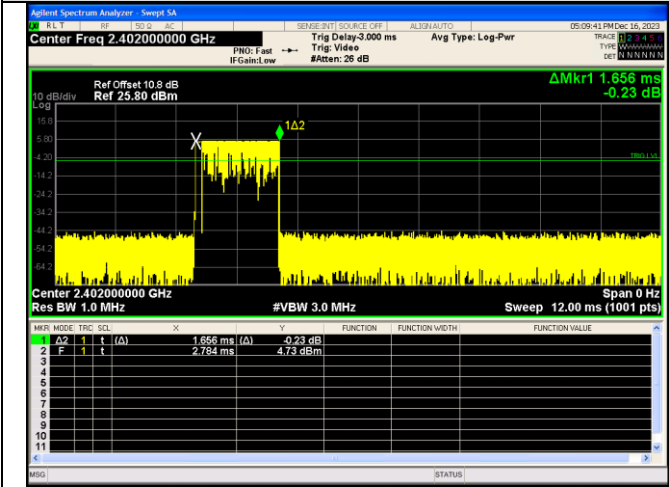
Number of Pulses in 31.6 seconds
8DPSK_3-DH1



Pulse Width
8DPSK_3-DH1



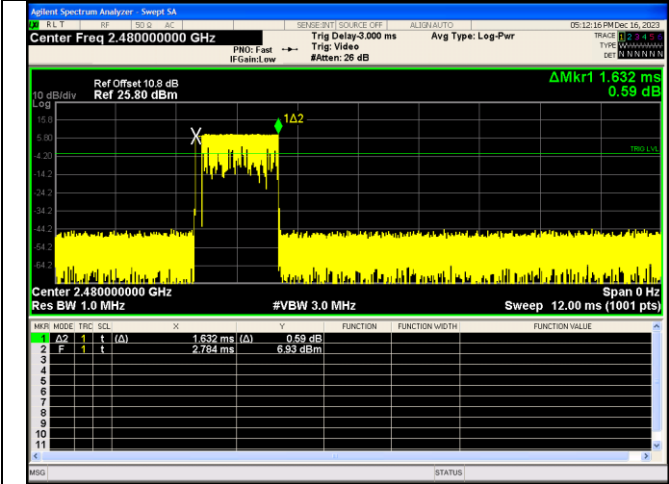
Number of Pulses in 31.6 seconds
8DPSK_3-DH1



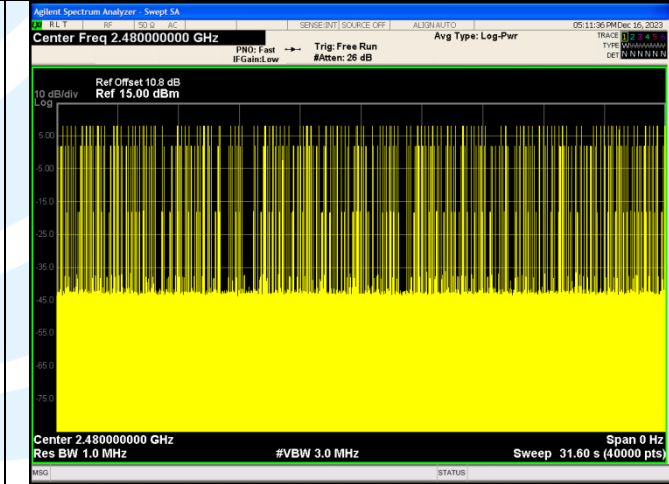
Pulse Width
8DPSK_3-DH3



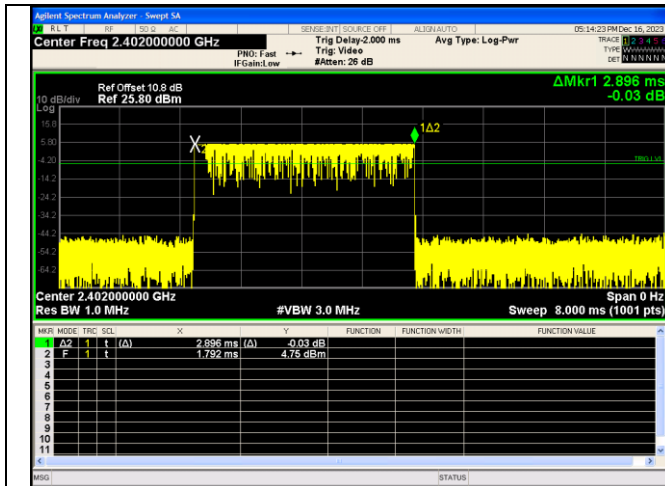
Number of Pulses in 31.6 seconds
8DPSK_3-DH3



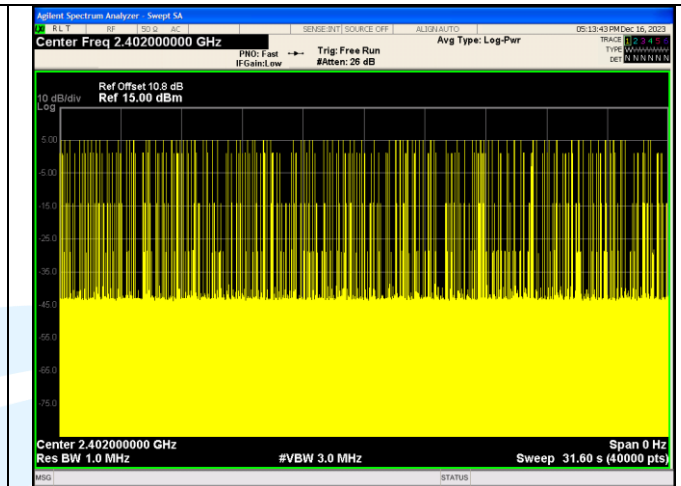
Pulse Width
8DPSK_3-DH3



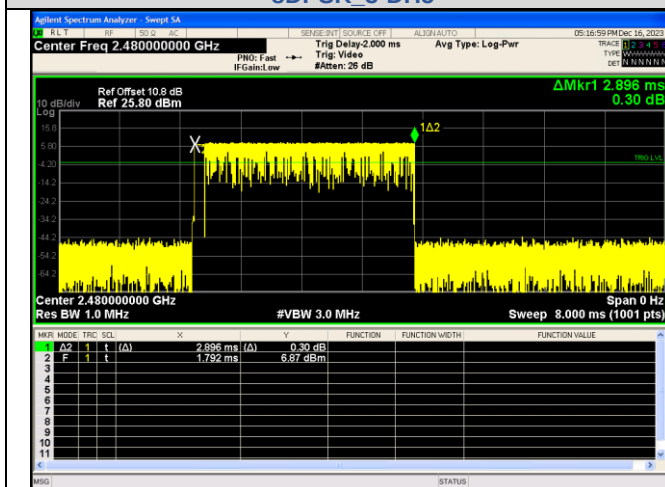
Number of Pulses in 31.6 seconds
8DPSK_3-DH3



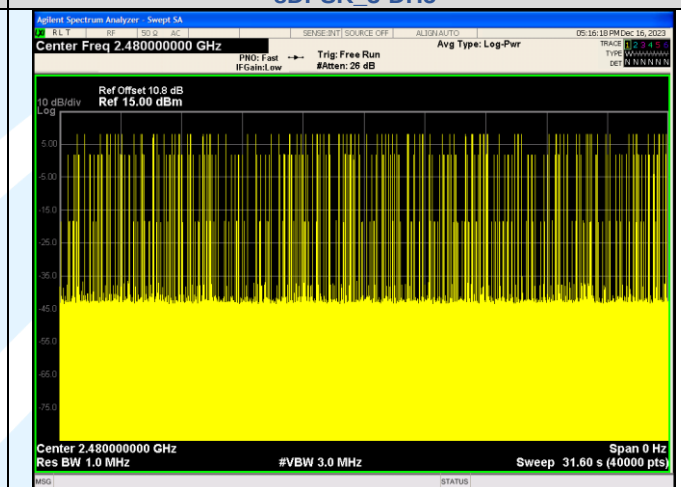
Pulse Width
8DPSK_3-DH5



Number of Pulses in 31.6 seconds
8DPSK_3-DH5



Pulse Width
8DPSK_3-DH5

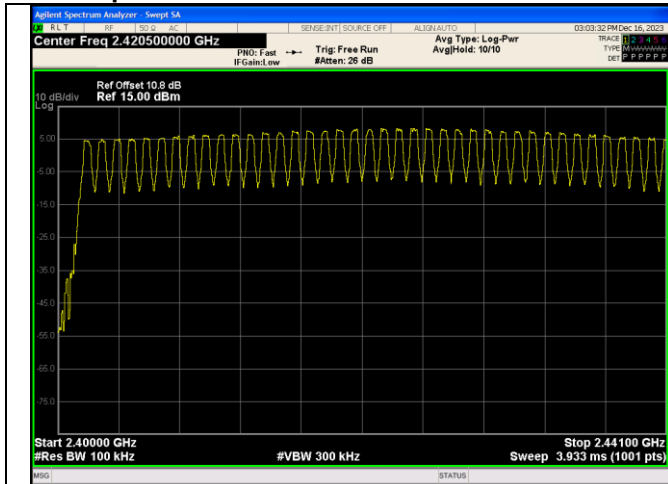


Number of Pulses in 31.6 seconds
8DPSK_3-DH5

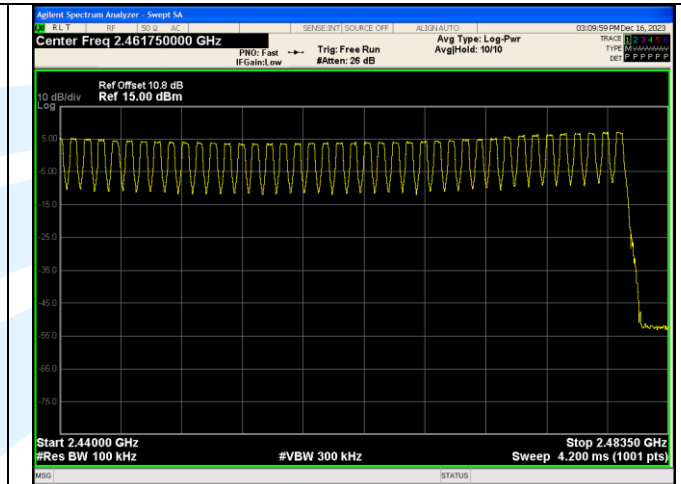
A.6 NUMBER OF HOPPING CHANNEL

Modulation	Packet	Number of Hopping Channel	Limit	Result
GFSK	DH5	79	15	PASS
$\pi/4$ DQPSK	2-DH5	79	15	PASS
8DPSK	3-DH5	79	15	PASS

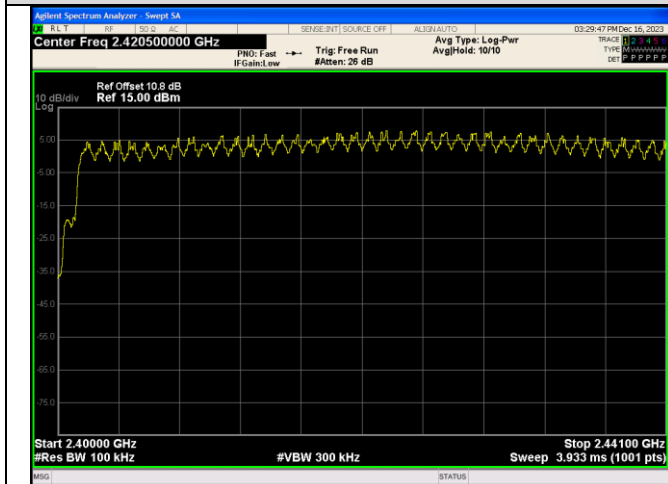
Test Graphs



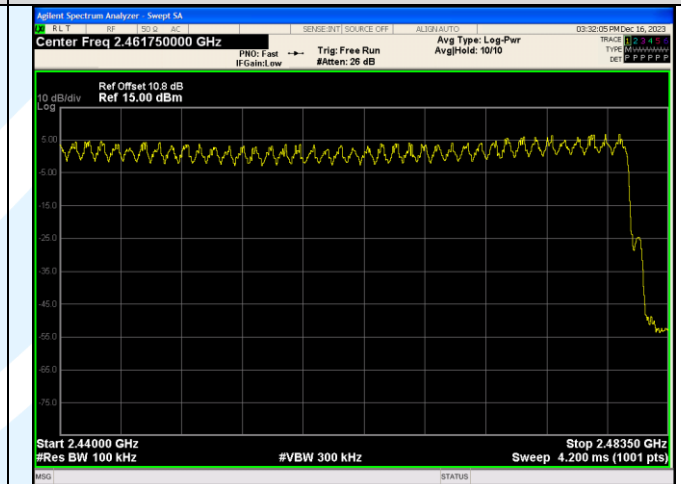
Low End Spectrum Channel Hopping Plot
GFSK



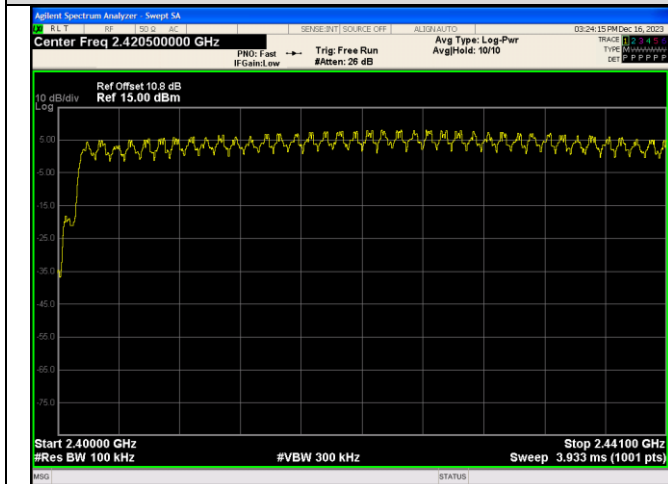
High End Spectrum Channel Hopping Plot
GFSK



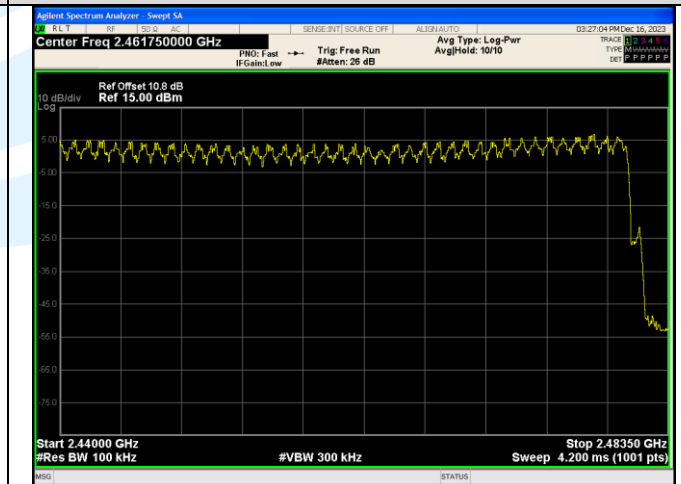
Low End Spectrum Channel Hopping Plot
 $\pi/4$ DQPSK



High End Spectrum Channel Hopping Plot
 $\pi/4$ DQPSK



Low End Spectrum Channel Hopping Plot
8DPSK



High End Spectrum Channel Hopping Plot
8DPSK

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APPENDIX 1 PHOTOS OF TEST SETUP

See test photos attached in Appendix 1 for the actual connections between Product and support equipment.

APPENDIX 2 PHOTOS OF EUT CONSTRUCTIONAL DETAILS

Refer to Appendix 2 for EUT external and internal photos.

*** End of Report ***

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