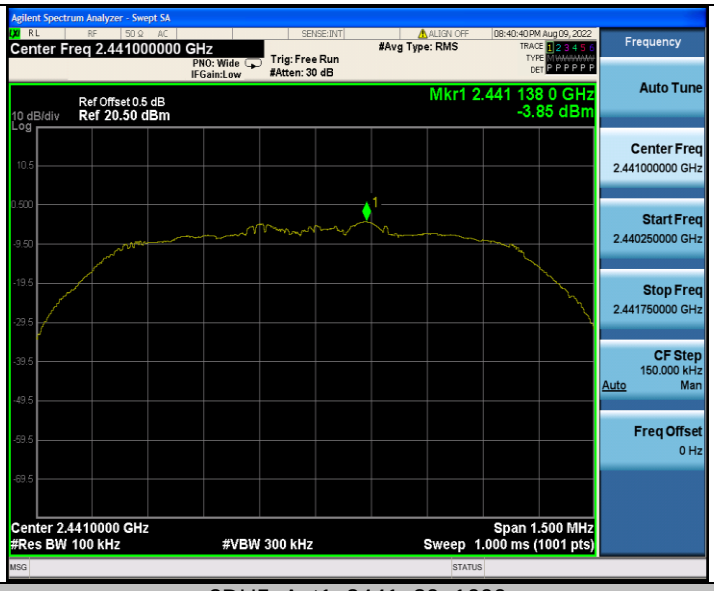
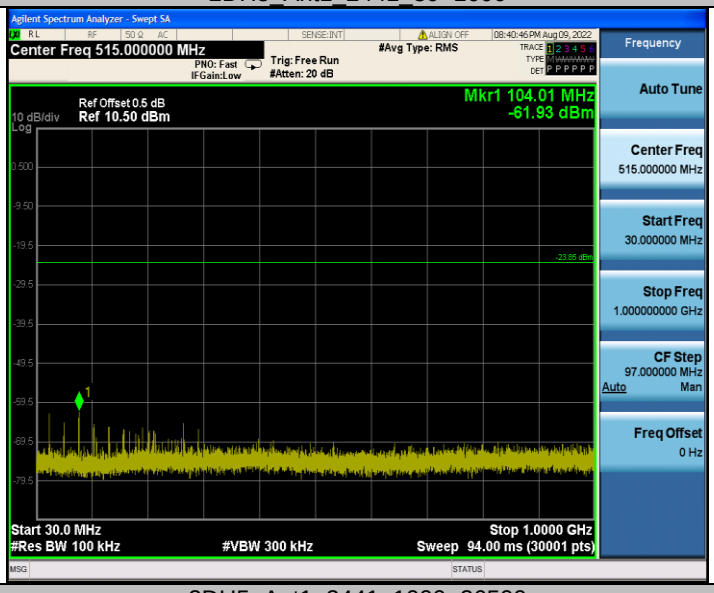
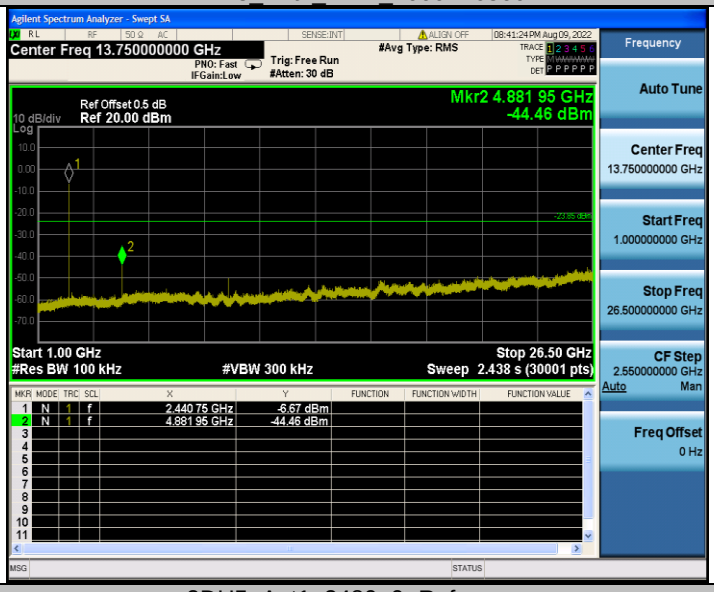


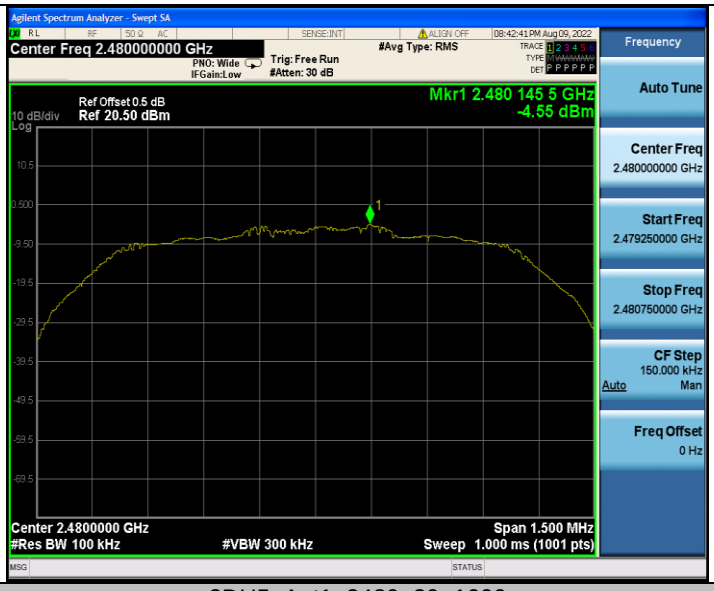
2DH5_Ant1_2441_30~1000



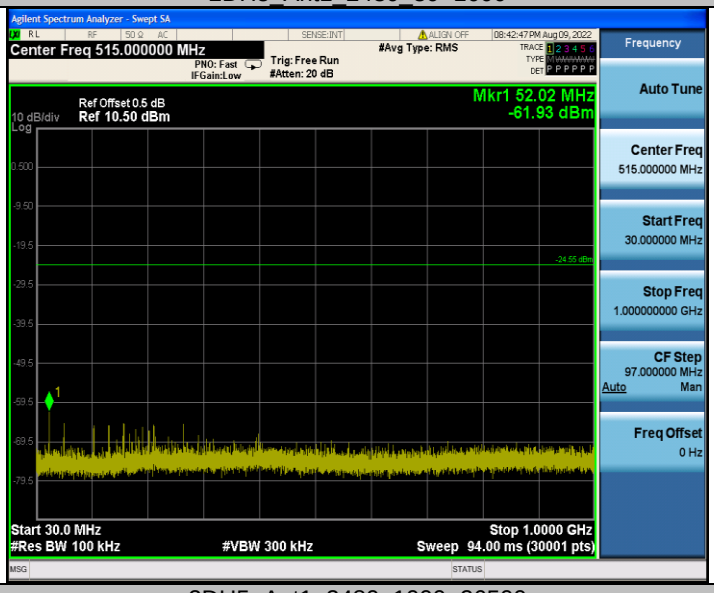
2DH5_Ant1_2441_1000~26500



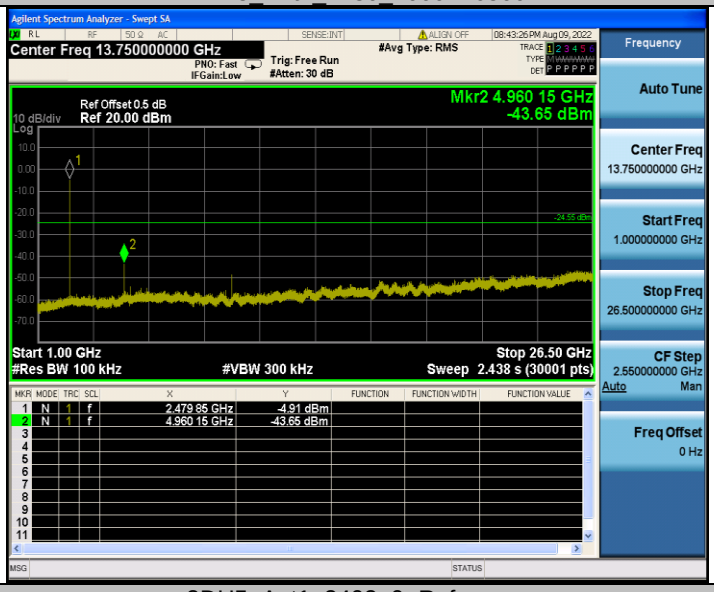
2DH5_Ant1_2480_0~Reference



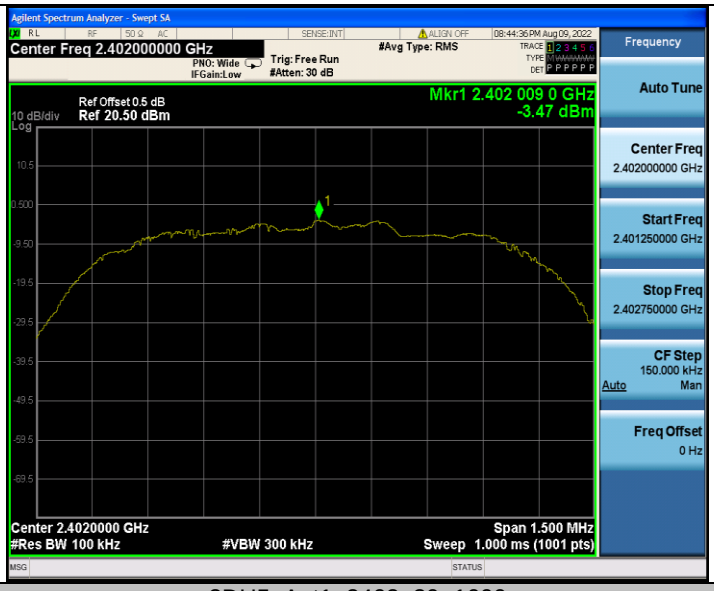
2DH5_Ant1_2480_30~1000



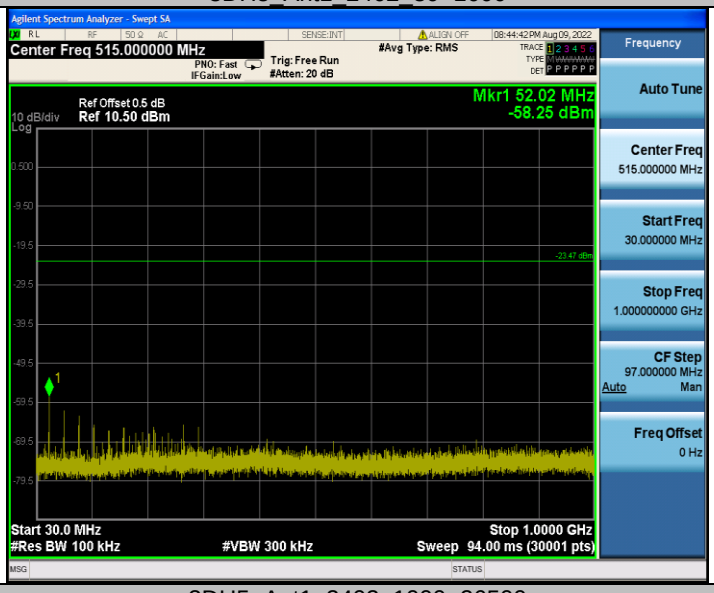
2DH5_Ant1_2480_1000~26500



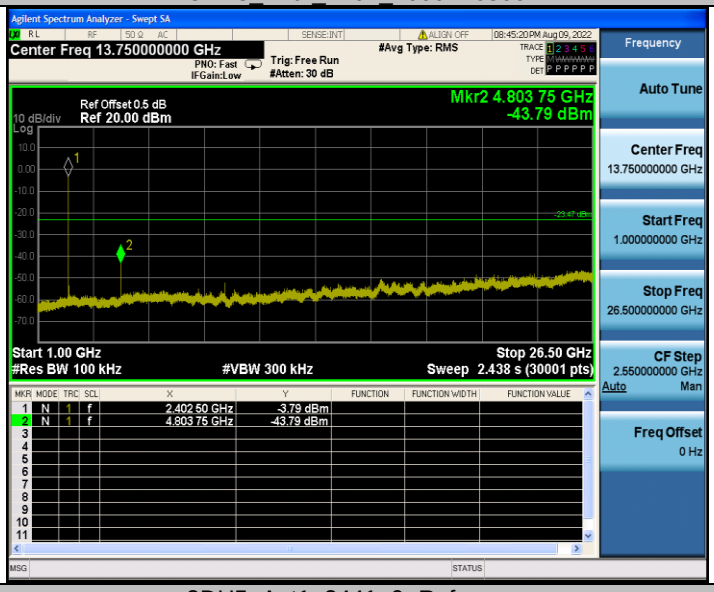
3DH5_Ant1_2402_0~Reference



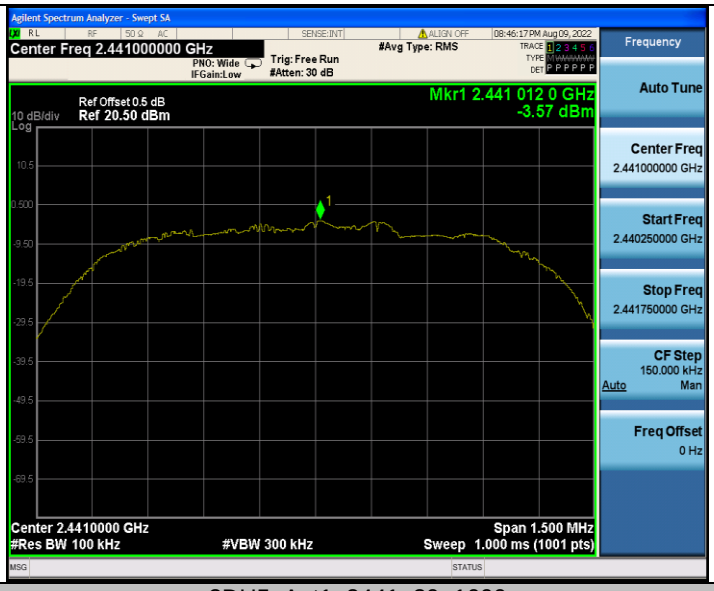
3DH5_Ant1_2402_30~1000



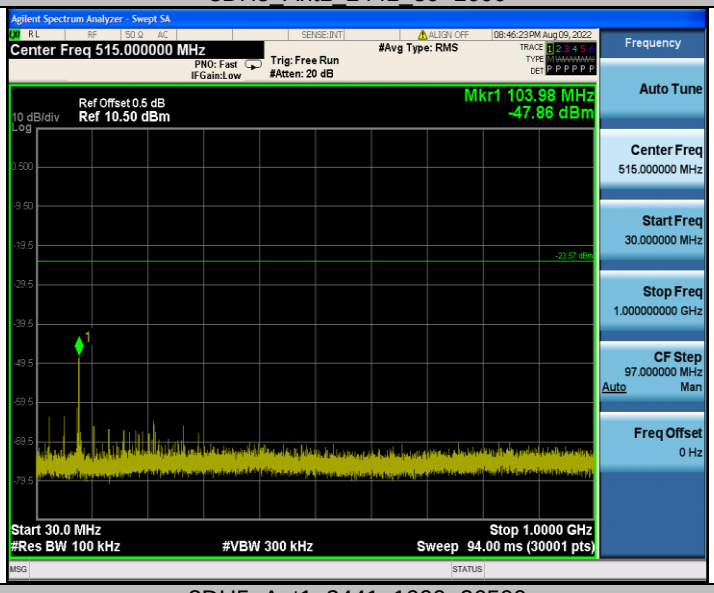
3DH5_Ant1_2402_1000~26500



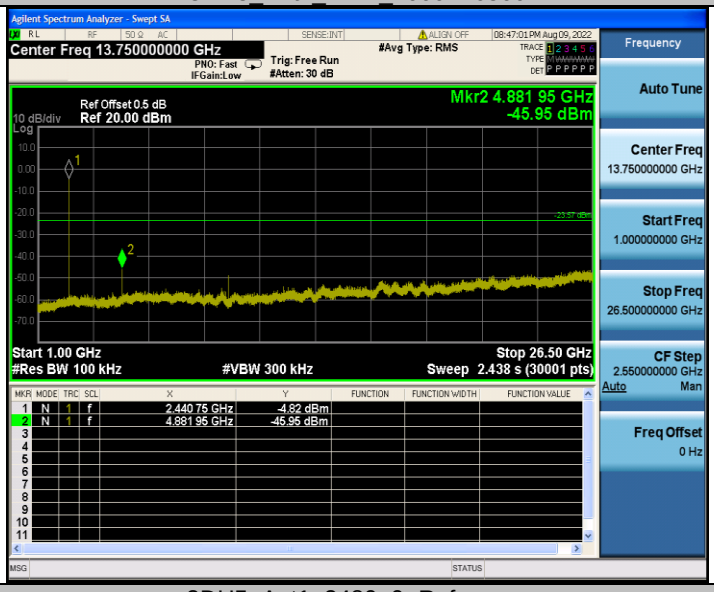
3DH5_Ant1_2441_0~Reference



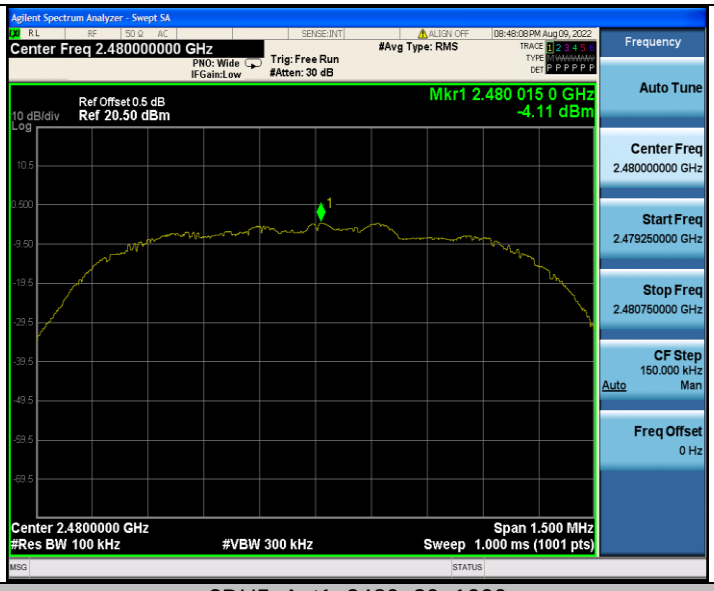
3DH5_Ant1_2441_30~1000



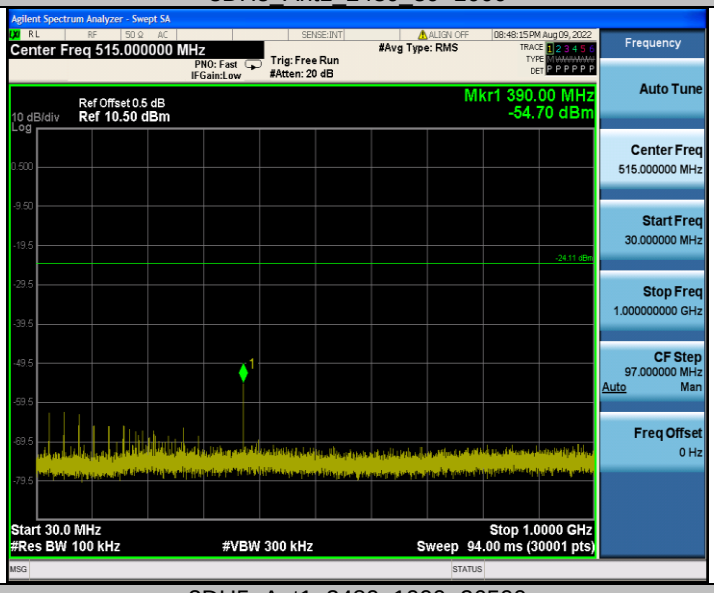
3DH5_Ant1_2441_1000~26500



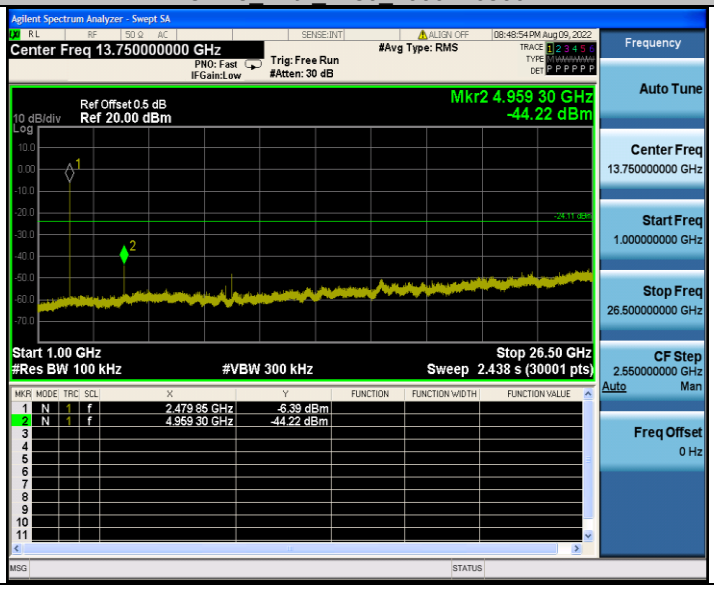
3DH5_Ant1_2480_0~Reference



3DH5_Ant1_2480_30~1000



3DH5_Ant1_2480_1000~26500

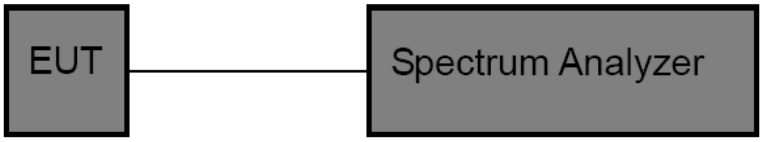


3.5. Bandwidth

Limit

N/A

Test Configuration



Test Procedure

1. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
2. OCB and 20dB Spectrum Setting:
   (1) Set RBW = 1% ~ 5% occupied bandwidth.
   (2) Set the video bandwidth (VBW) ≥ 3 RBW.
   (3) Detector = Peak.
   (4) Trace mode = Max hold.
   (5) Sweep = Auto couple.

Note: The EUT was set to continuously transmitting in each mode and low, Middle and high channel for the test.

Test Mode

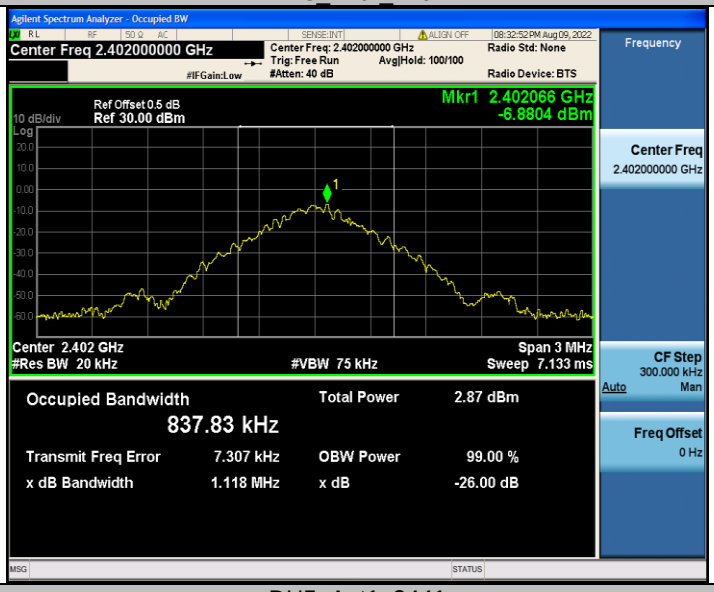
Please refer to the clause 2.4.

Test Results

Modulation type	Channel	99% Bandwidth (MHz)	20dB Bandwidth (MHz)	20dB Bandwidth *2/3 (MHz)
GFSK	00	0.838	1.089	0.726
	39	0.825	1.092	0.728
	78	0.816	1.089	0.726
π/4-DQPSK	00	1.178	1.392	0.928
	39	1.187	1.392	0.928
	78	1.186	1.386	0.924
8-DPSK	00	1.193	1.380	0.920
	39	1.181	1.389	0.926
	78	1.194	1.392	0.928

Occupied Bandwidth:

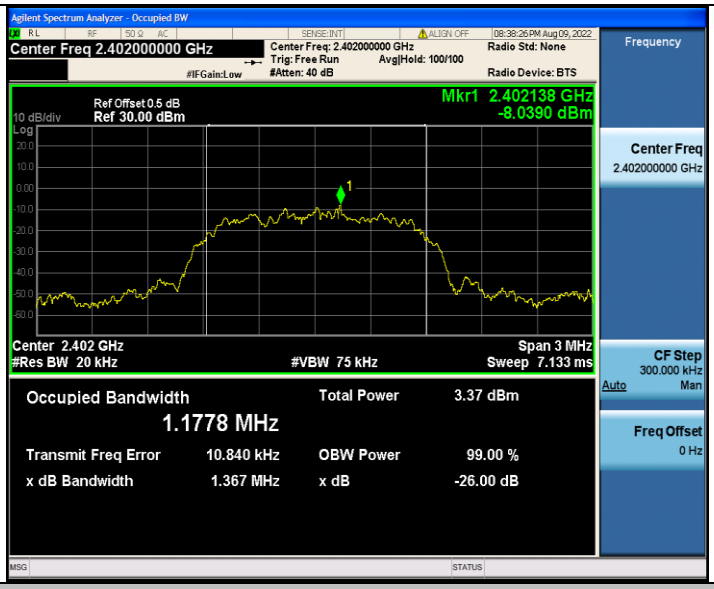
DH5_Ant1_2402



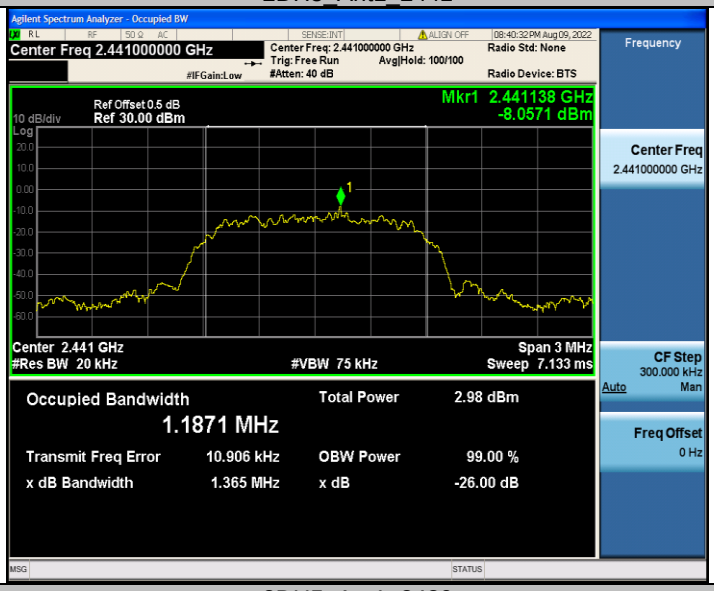
DH5_Ant1_2441

DH5_Ant1_2480

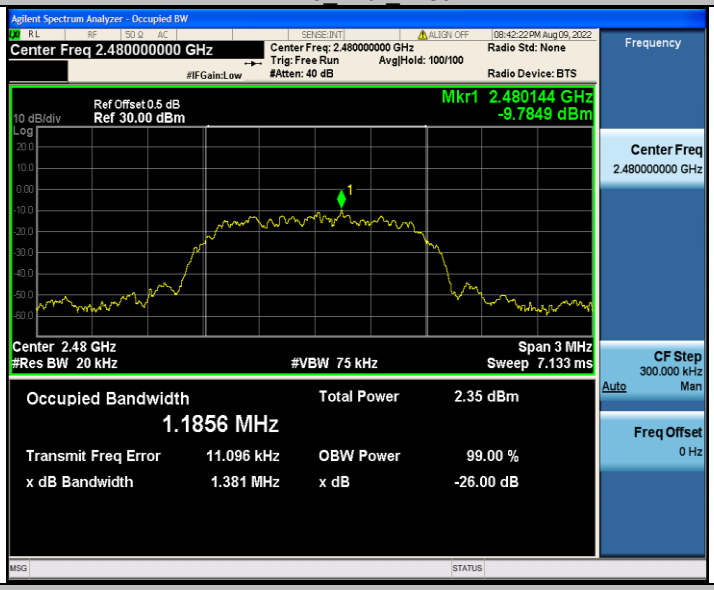
2DH5_Ant1_2402



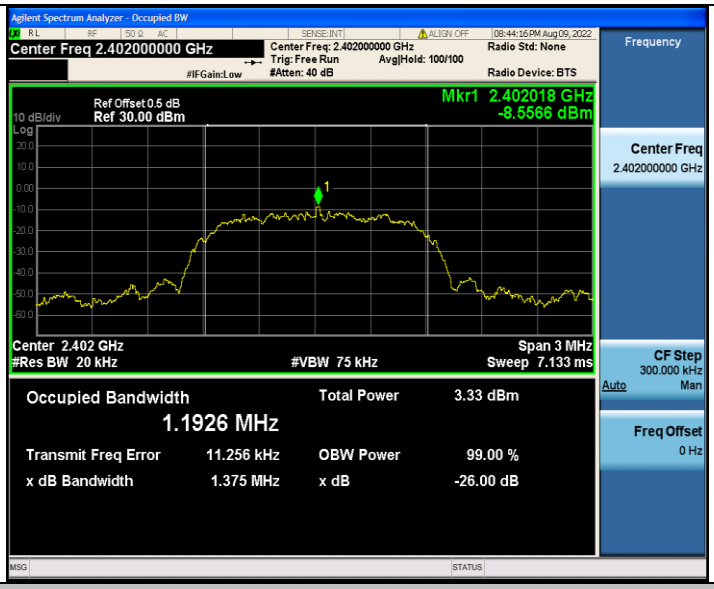
2DH5_Ant1_2441



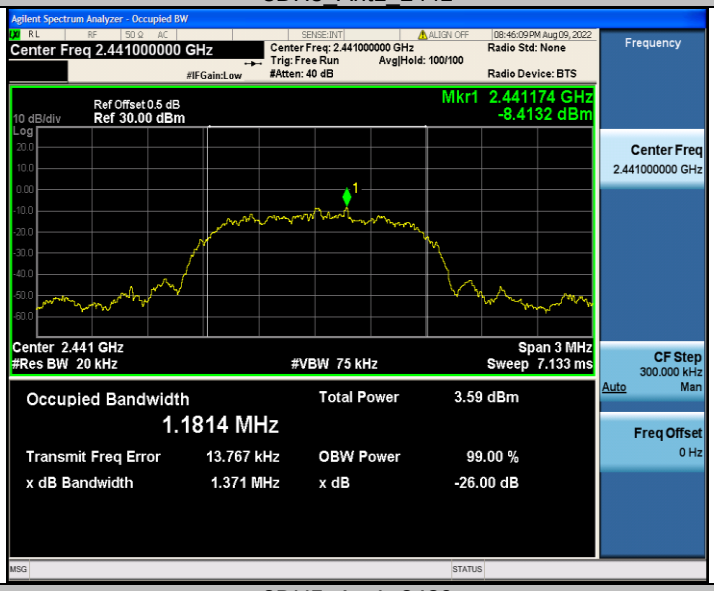
2DH5_Ant1_2480



3DH5_Ant1_2402



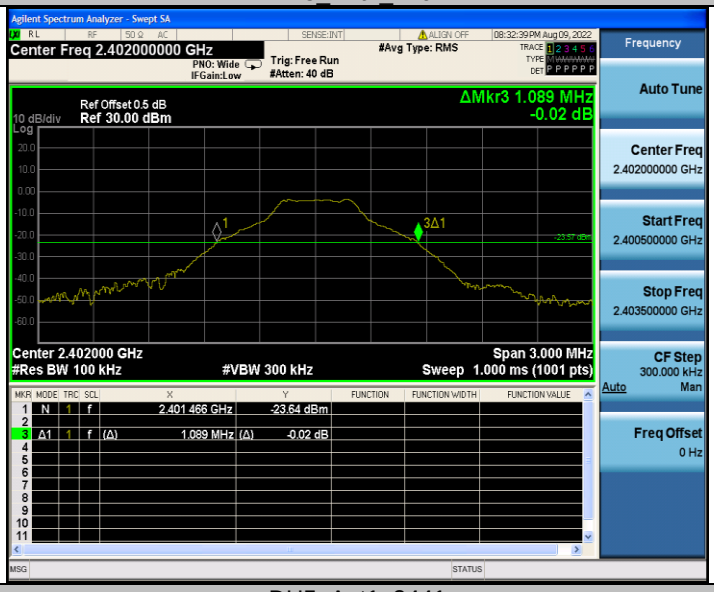
3DH5_Ant1_2441



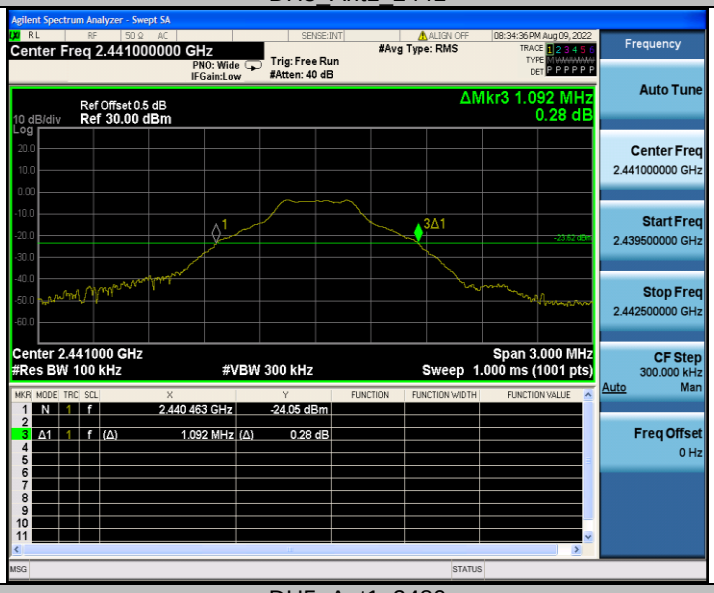
3DH5_Ant1_2480

20dB Bandwidth:

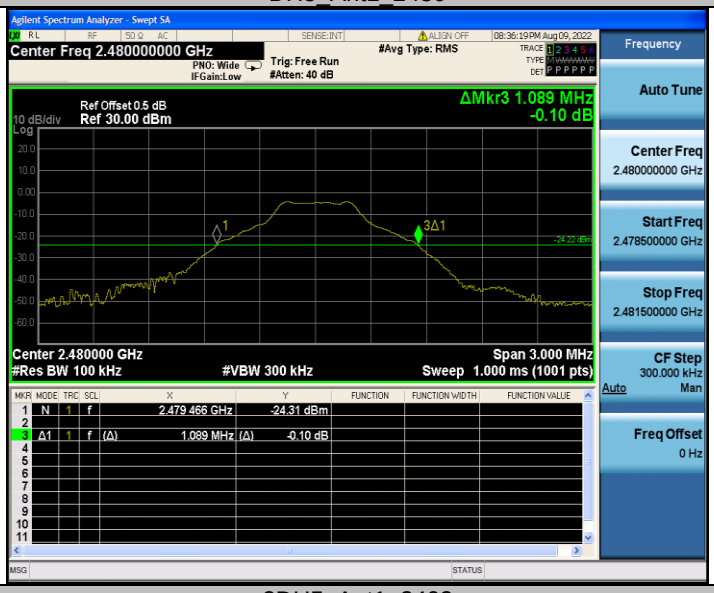
DH5_Ant1_2402



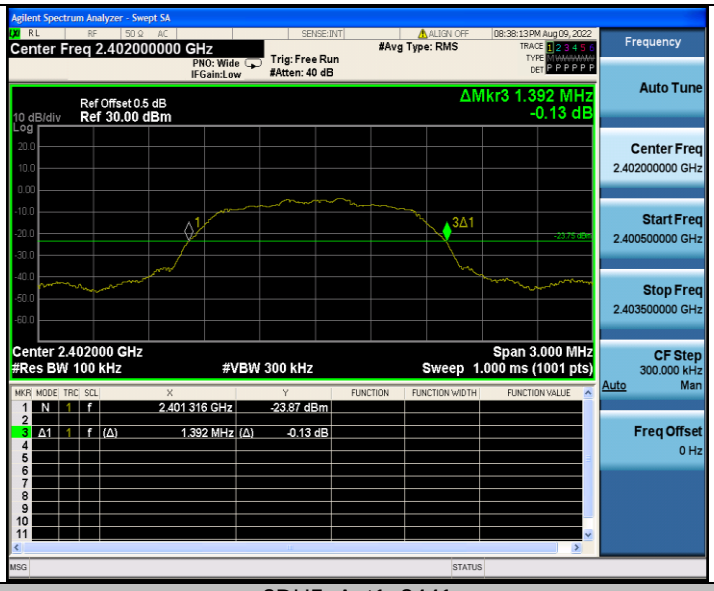
DH5_Ant1_2441



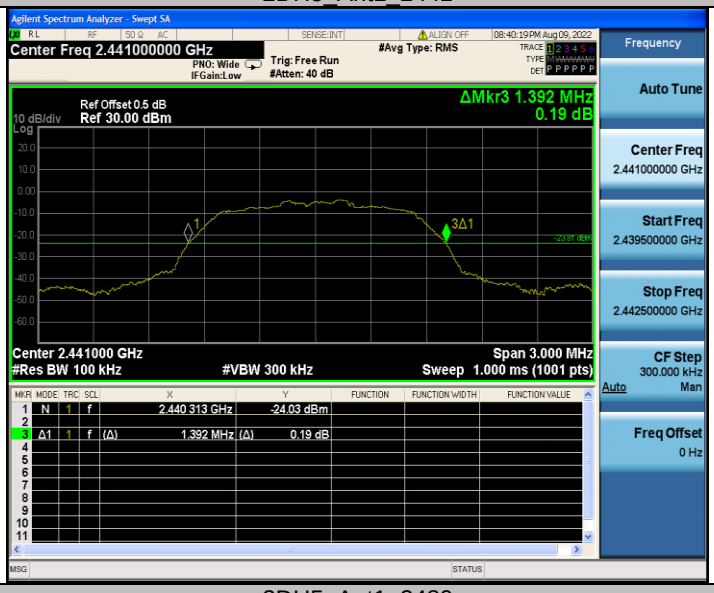
DH5_Ant1_2480



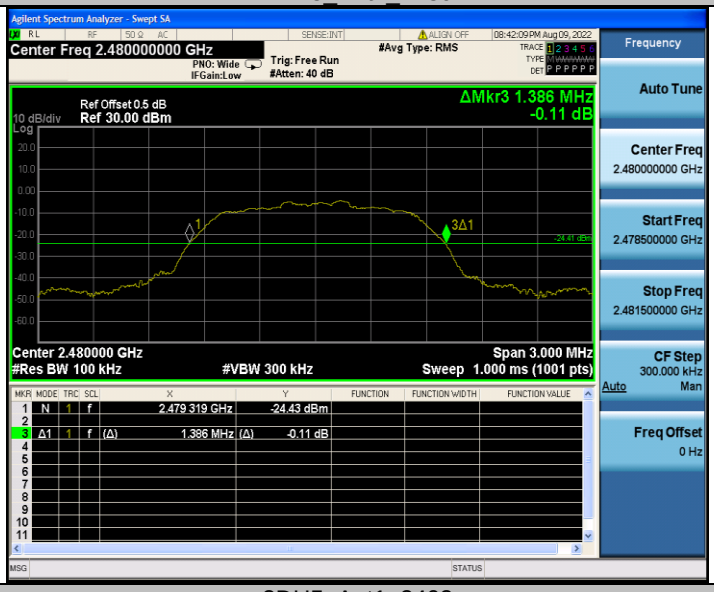
2DH5_Ant1_2402



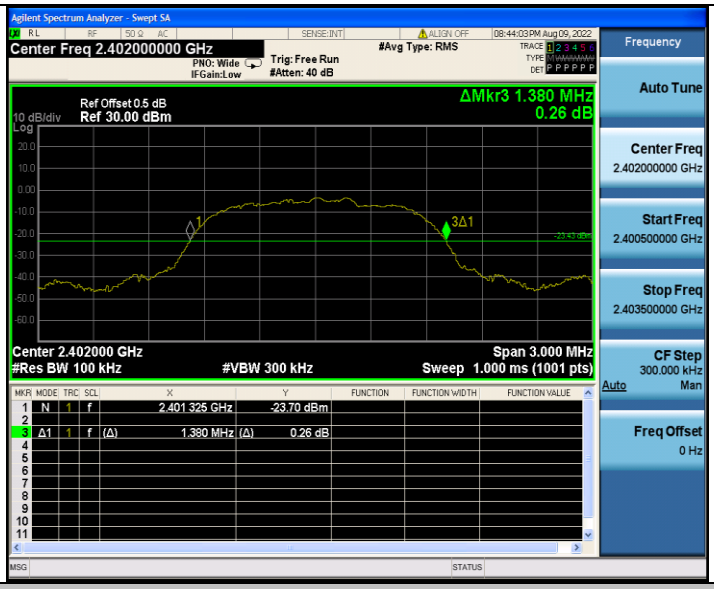
2DH5_Ant1_2441



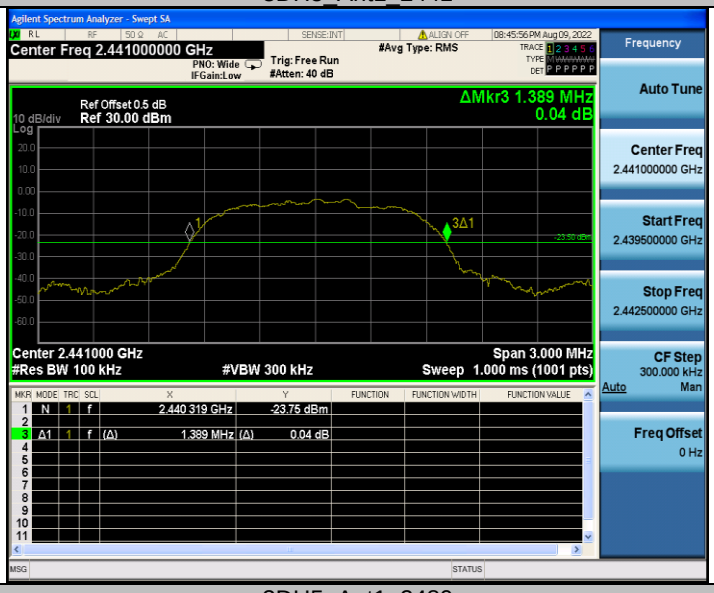
2DH5_Ant1_2480



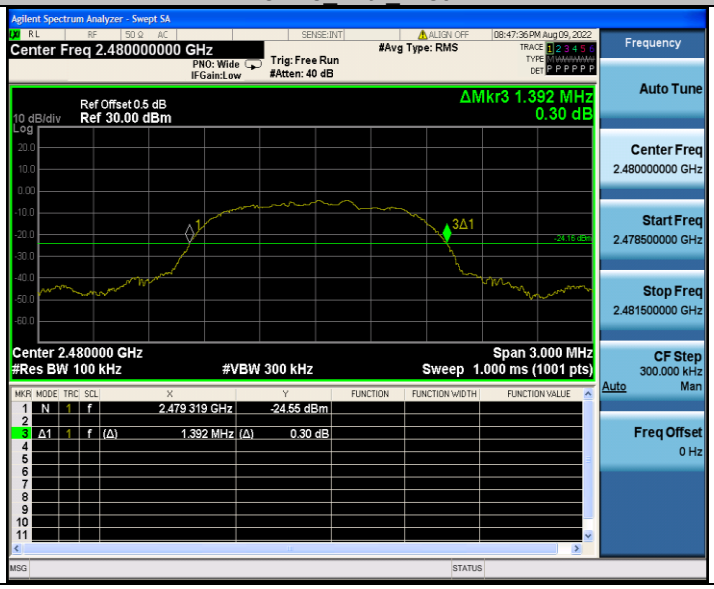
3DH5_Ant1_2402



3DH5_Ant1_2441



3DH5_Ant1_2480



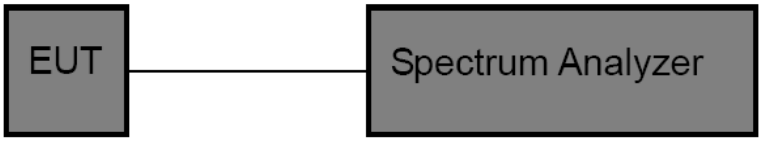
3.6. Channel Separation

Limit

FCC CFR Title 47 Part 15 Subpart C Section 15.247 (a)(1)/ RSS-247 5.1 b :

Test Item	Limit	Frequency Range(MHz)
Channel Separation	>25KHz or >two-thirds of the 20 dB bandwidth Which is greater	2400~2483.5

Test Configuration



Test Procedure

1. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
2. Spectrum Setting:
   (1) Set RBW = 100 kHz.
   (2) Set the video bandwidth (VBW) ≥ 3 RBW.
   (3) Detector = Peak.
   (4) Trace mode = Max hold.
   (5) Sweep = Auto couple.

Test Mode

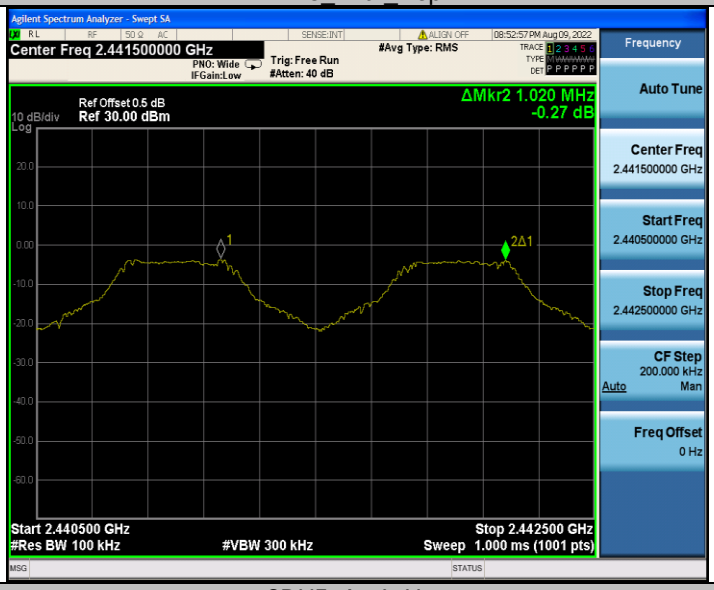
Please refer to the clause 2.4.

Test Results

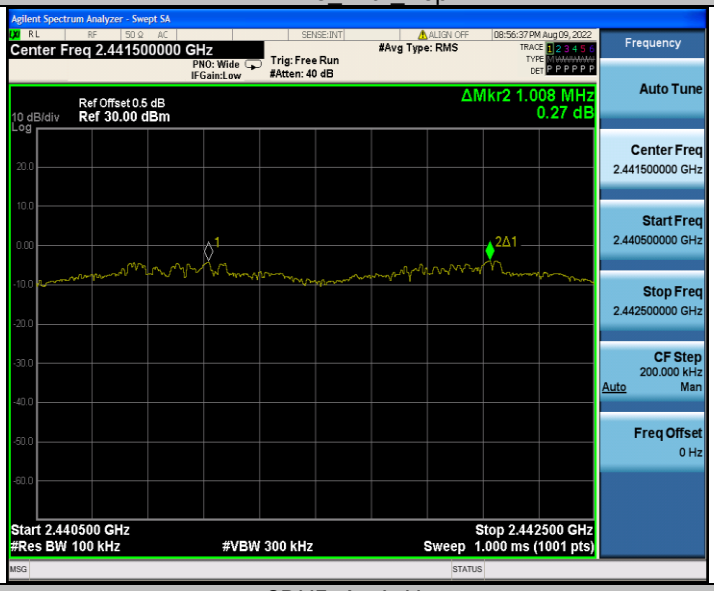
Modulation type	Channel	Carrier Frequencies Separation (MHz)	Limit (MHz)	Result
GFSK	39	1.020	>0.728	Pass
π/4-DQPSK	39	1.008	>0.928	Pass
8-DPSK	39	0.998	>0.926	Pass

Test plot as follows:

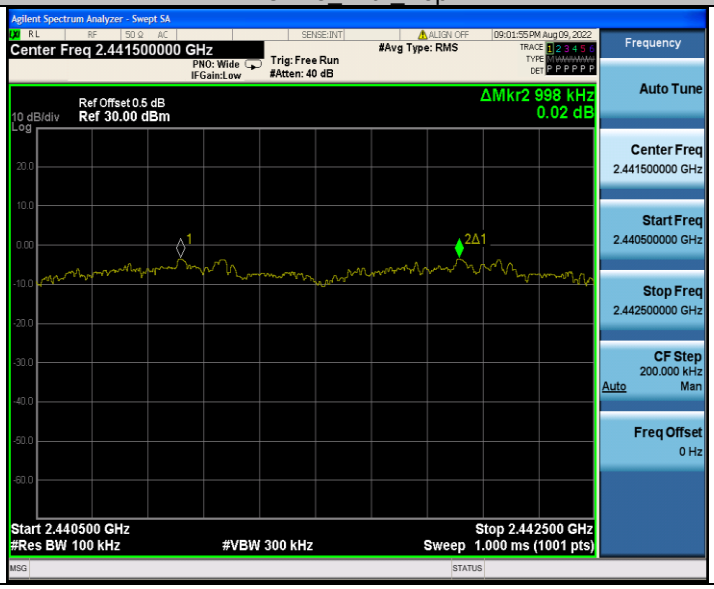
DH5_Ant1_Hop



2DH5_Ant1_Hop



3DH5_Ant1_Hop



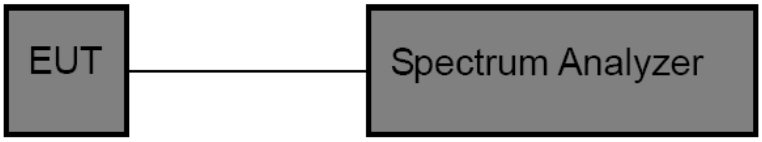
3.7. Number of Hopping Channel

Limit

FCC CFR Title 47 Part 15 Subpart C Section 15.247 (a)(iii)/ RSS-247 5.1 d:

Section	Test Item	Limit
15.247 (a)(iii)/ RSS-247 5.1 d:	Number of Hopping Channel	>15

Test Configuration



Test Procedure

1. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
2. Spectrum Setting:
   (1) Peak Detector: RBW=100 kHz, VBW≥RBW, Sweep time= Auto.

Test Mode

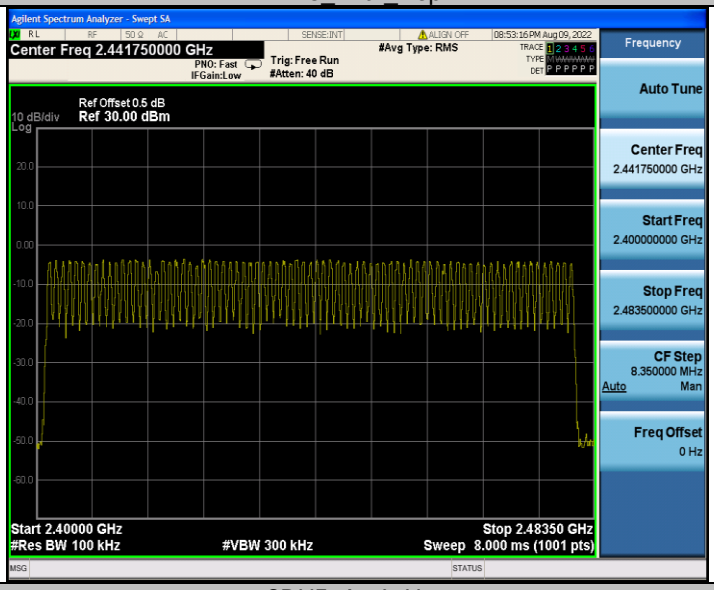
Please refer to the clause 2.4.

Test Result

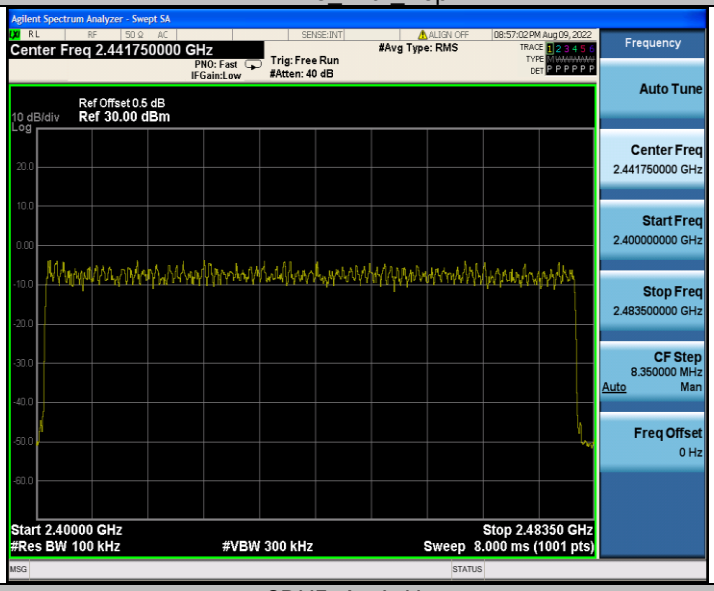
Modulation type	Channel number	Limit	Result
GFSK	79	≥15.00	Pass
π/4-DQPSK	79		
8DPSK	79		

Test plot as follows:

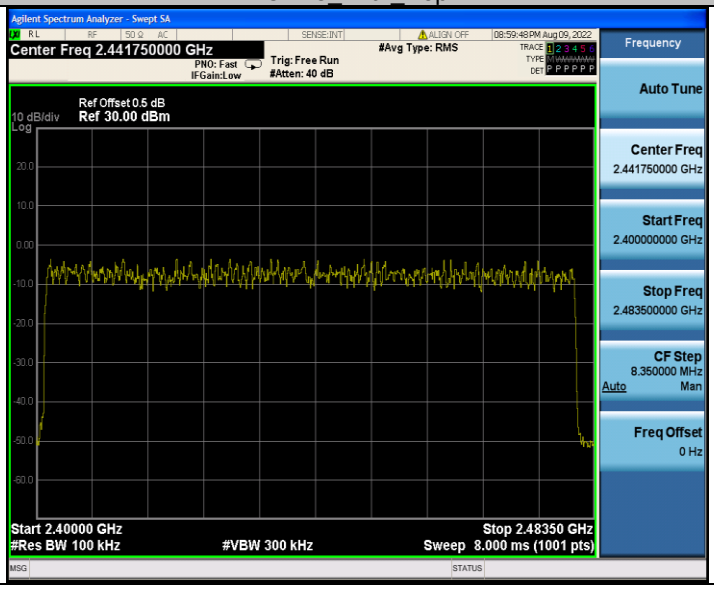
DH5_Ant1_Hop



2DH5_Ant1_Hop



3DH5_Ant1_Hop

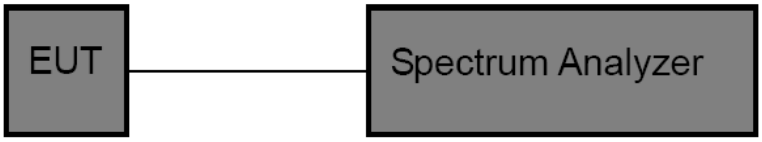


3.8. Dwell Time

Limit

Section	Test Item	Limit
15.247(a)(iii)/ RSS-247 5.1 d	Average Time of Occupancy	0.4 sec

Test Configuration



Test Procedure

1. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
2. Spectrum Setting:
   (1) Spectrum Setting: RBW=1MHz, VBW≥RBW.
   (2) Use video trigger with the trigger level set to enable triggering only on full pulses.
   (3) Sweep Time is more than once pulse time.
   (4) Set the center frequency on any frequency would be measure and set the frequency span to zero.
   (5) Measure the maximum time duration of one single pulse.
   (6) Set the EUT for packet transmitting.

Test Mode

Please refer to the clause 2.4.

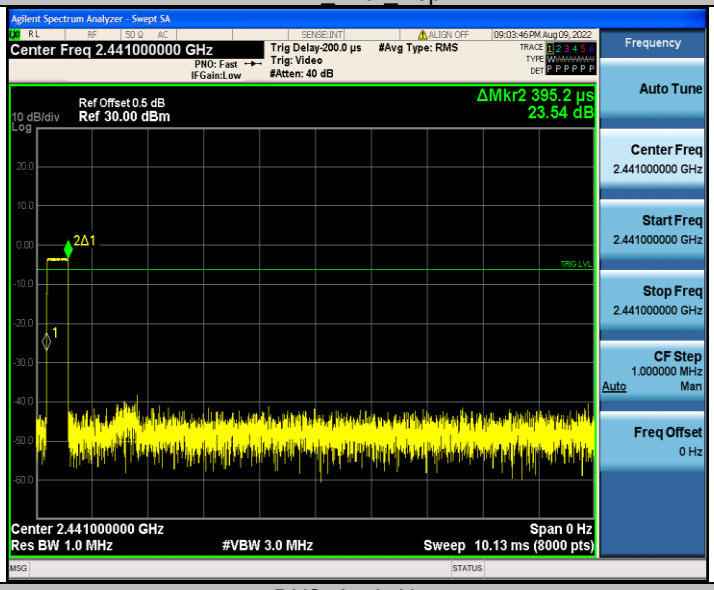
Test Result

Modulation type	Channel	Channel (MHz)	Pulse Time (ms)	Total of Dwell (ms)	Period Time (ms)	Limit (Second)	Result
GFSK	DH1	2441	0.40	128.00	31.60	≤ 0.40	Pass
	DH3	2441	1.65	264.00	31.60		
	DH5	2441	2.90	309.33	31.60		
π/4-DQPSK	2DH1	2441	0.40	128.00	31.60	≤ 0.40	Pass
	2DH3	2441	1.66	265.60	31.60		
	2DH5	2441	2.90	309.33	31.60		
8-DPSK	3DH1	2441	0.41	131.20	31.60	≤ 0.40	Pass
	3DH3	2441	1.66	265.60	31.60		
	3DH5	2441	2.91	310.40	31.60		

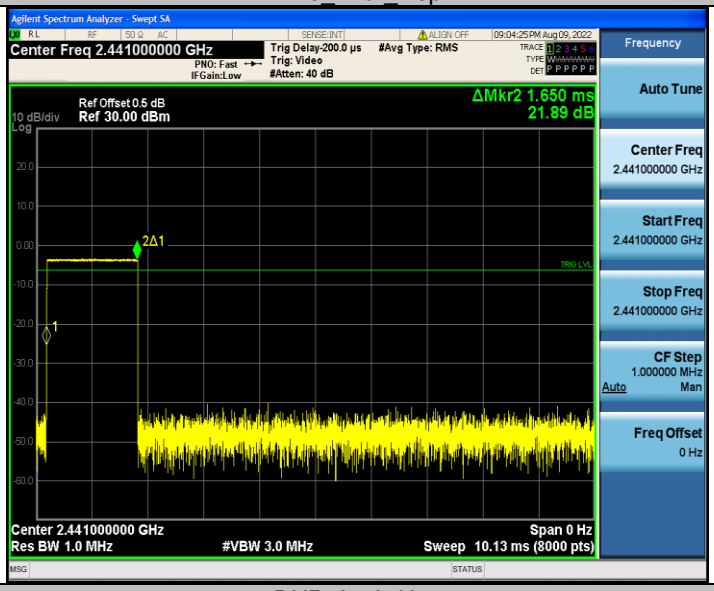
Note: 1DH1/2DH1/3DH1 Total of Dwell = Pulse Time(1600/2)*31.6/79*
1DH3/2DH3/3DH3 Total of Dwell = Pulse Time(1600/4)*31.6/79*
1DH5/2DH5/3DH5 Total of Dwell = Pulse Time(1600/6)*31.6/79*

Test plot as follows:

DH1_Ant1_Hop

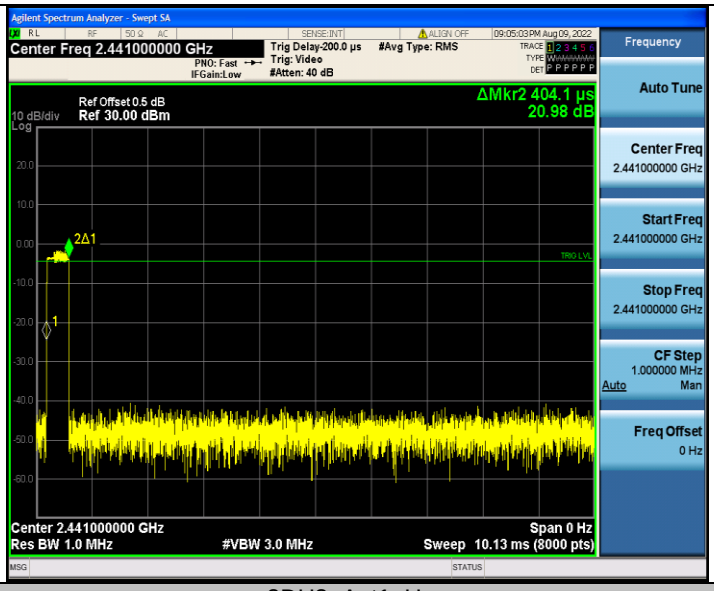


DH3_Ant1_Hop

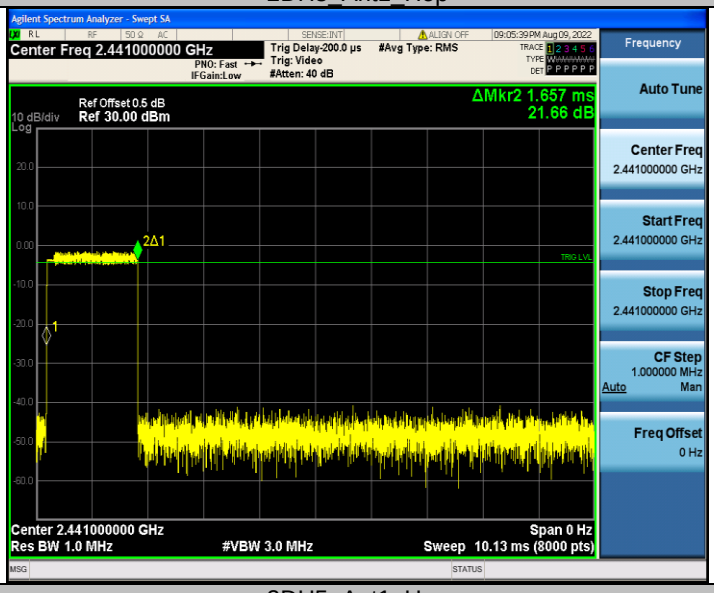


DH5_Ant1_Hop

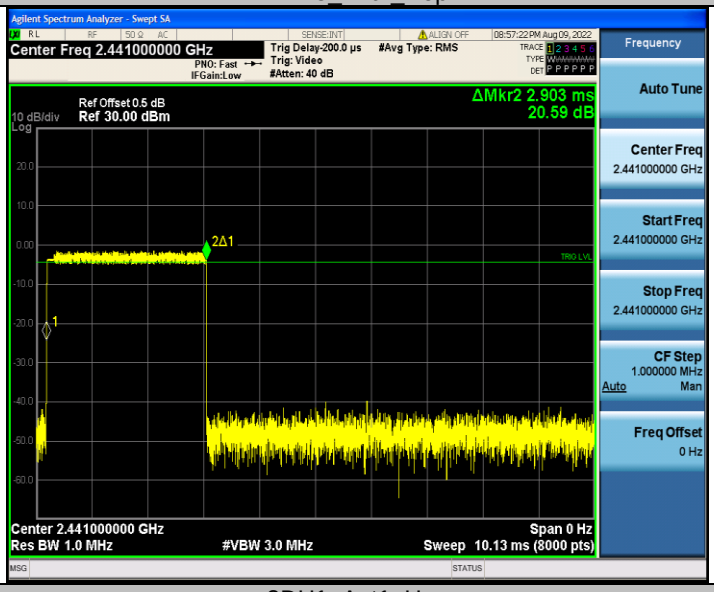
2DH1_Ant1_Hop



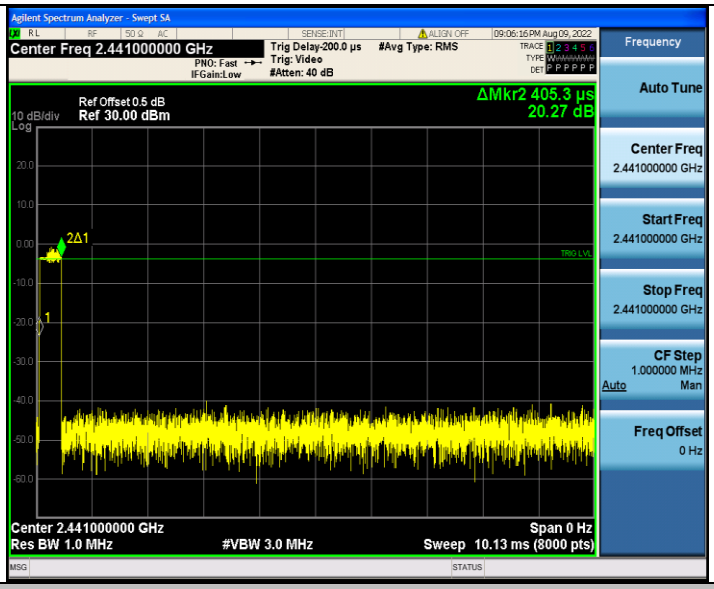
2DH3_Ant1_Hop



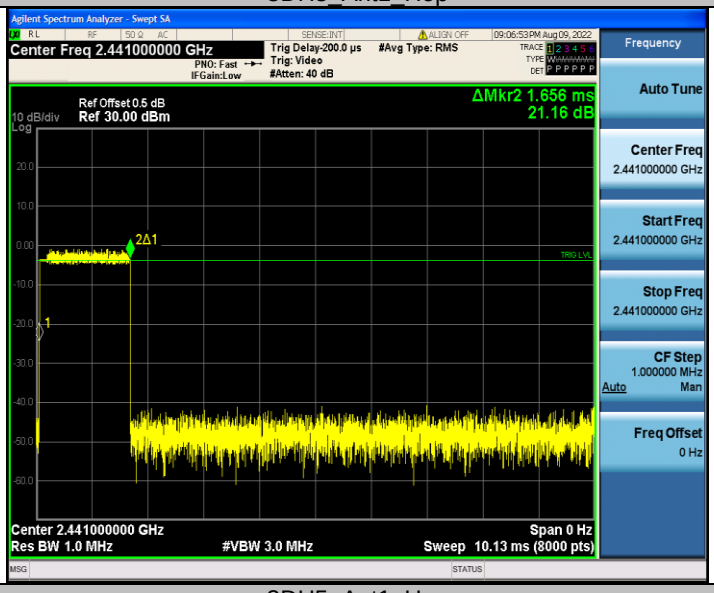
2DH5_Ant1_Hop



3DH1_Ant1_Hop



3DH3_Ant1_Hop



3DH5_Ant1_Hop

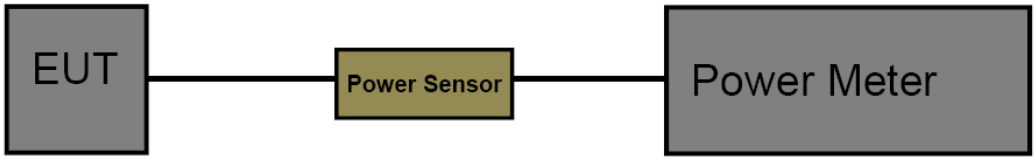
3.9. Peak Output Power

Limit

FCC CFR Title 47 Part 15 Subpart C Section 15.247 (b)(1) / RSS-247 5.4 b:

Test Item	Limit	Frequency Range(MHz)
Peak Output Power	Hopping Channels>75 Power<1W(30dBm) Other <125mW(21dBm)	2400~2483.5

Test Configuration



Test Procedure

1. The maximum conducted output power may be measured using a broadband Peak RF power meter.
2. Peak power measurements were performed only when the EUT was transmitting at its maximum power control level using a broadband power meter with a pulse sensor.
3. The power meter implemented triggering and gating capabilities which were set up such that power measurements were recorded only during the ON time of the transmitter.
4. Record the measurement data.

Test Mode

Please refer to the clause 2.4.

Test Result

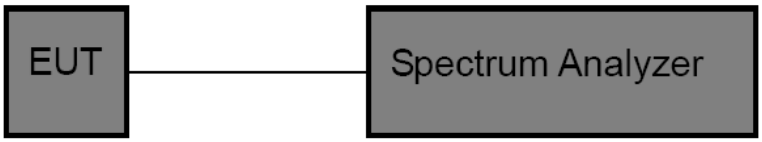
Modulation type	Channel	Output power (dBm)	Limit (dBm)	Result
GFSK	00	-3.29	≤ 21.00	Pass
	39	-3.32		
	78	-3.90		
π/4-DQPSK	00	-0.75	≤ 21.00	Pass
	39	-0.88		
	78	-1.55		
8-DPSK	00	-0.06	≤ 21.00	Pass
	39	-0.20		
	78	-0.90		

3.10. Duty Cycle

Limit

None, for report purposes only.

Test Configuration



Test Procedure

1. The EUT was directly connected to the spectrum analyzer and antenna output port as show in the block diagram above.
2. The EUT was directly connected to the Spectrum Analyzer and antenna output port as show in the block diagram above. The measurement according to section 10.2 of KDB 558074 D01 DTS Meas Guidance v05r02.
3. Spectrum Setting:

Set analyzer center frequency to test channel center frequency.
Set the span to 0Hz
Set the RBW to 10MHz
Set the VBW to 10MHz
Detector: Peak
Sweep time: Auto

Allow trace to fully stabilize. Then use the peak marker function to determine the maximum amplitude level.

Test Mode

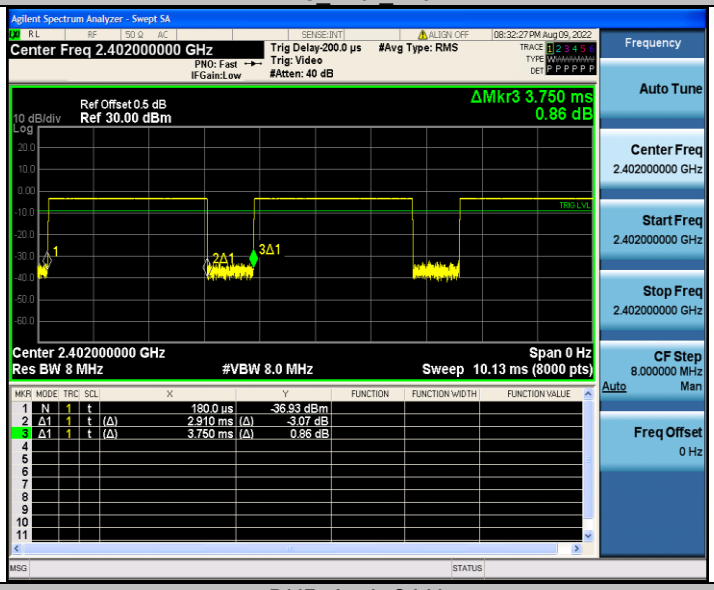
Please refer to the clause 2.4.

Test Result

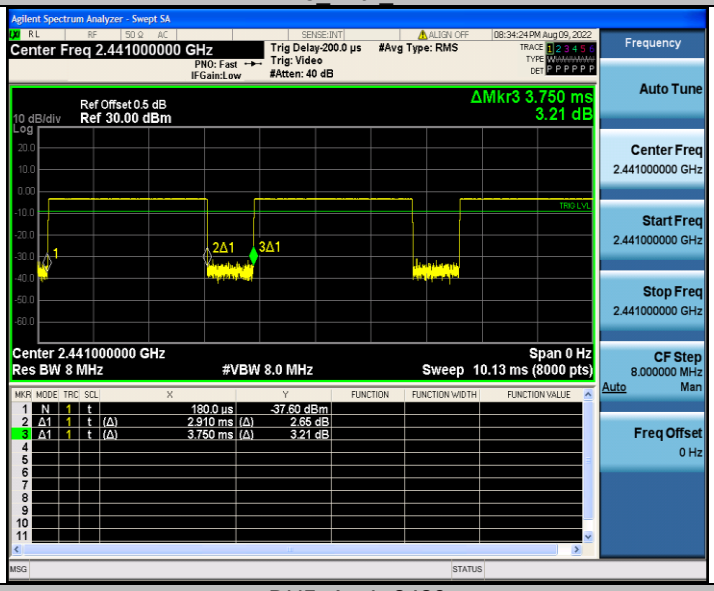
Test Mode	Frequency [MHz]	Transmission Duration [ms]	Transmission Period [ms]	Duty Cycle [%]	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
GFSK	2402	2.91	3.75	77.60	0.34	1
	2441	2.91	3.75	77.60	0.34	1
	2480	2.91	3.76	77.39	0.34	1
π/4-DQPSK	2402	2.90	3.75	77.33	0.34	1
	2441	2.91	3.75	77.60	0.34	1
	2480	2.90	3.75	77.33	0.34	1
8-DPSK	2402	2.91	3.75	77.60	0.34	1
	2441	2.91	3.75	77.60	0.34	1
	2480	2.91	3.75	77.60	0.34	1

Test plot as follows:

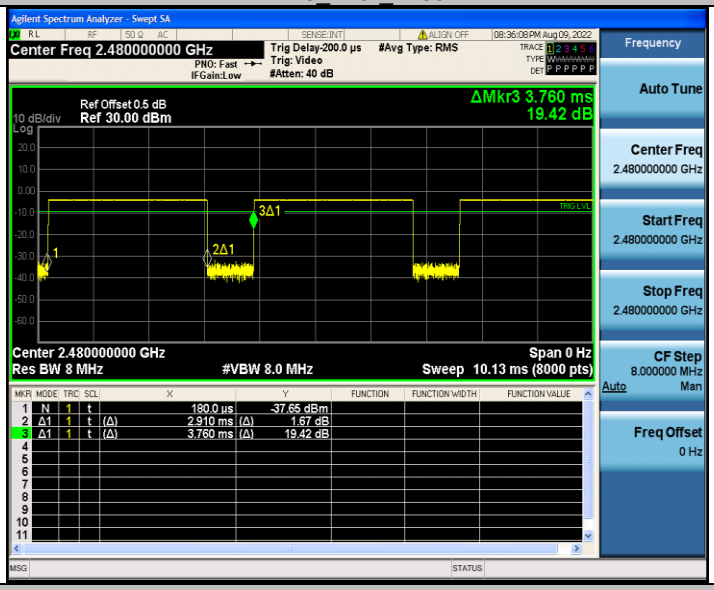
DH5_Ant1_2402



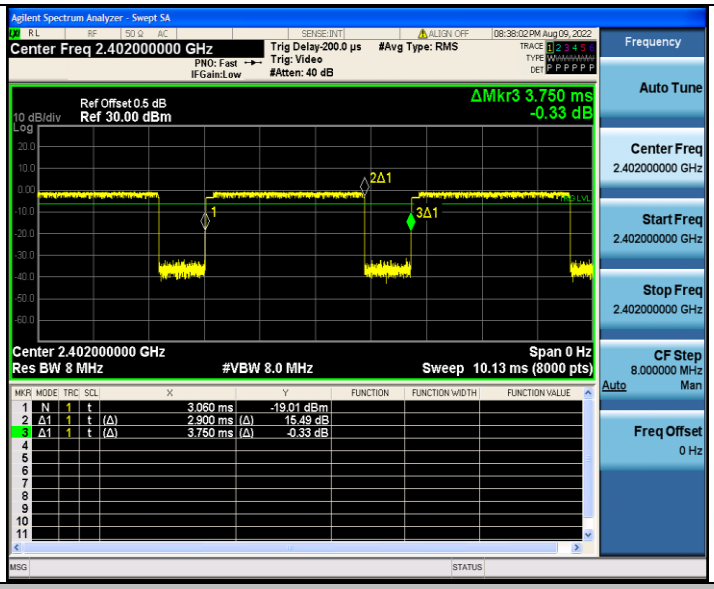
DH5_Ant1_2441



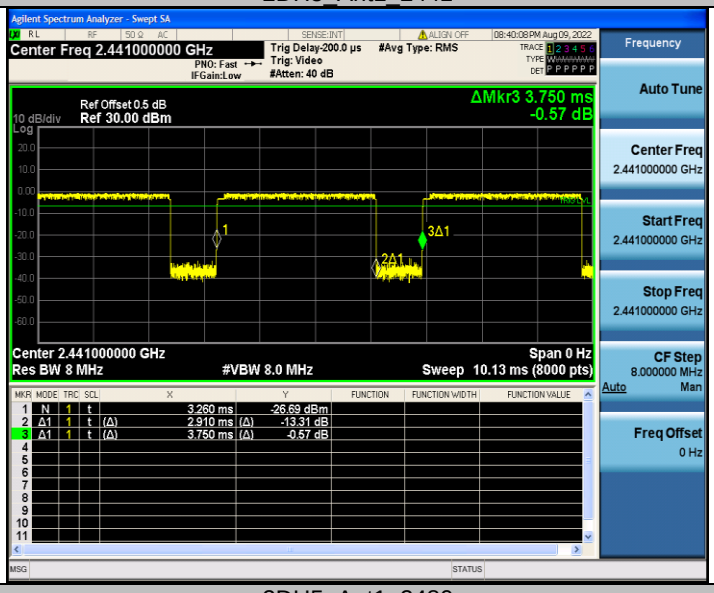
DH5_Ant1_2480



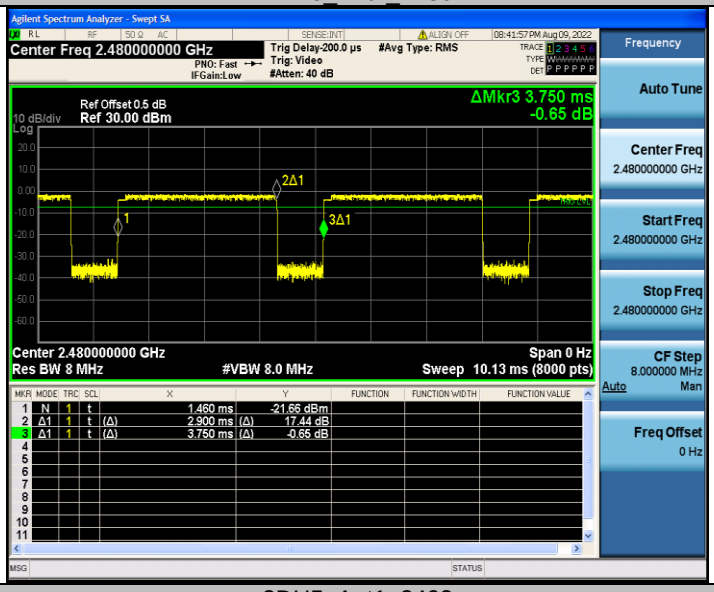
2DH5_Ant1_2402



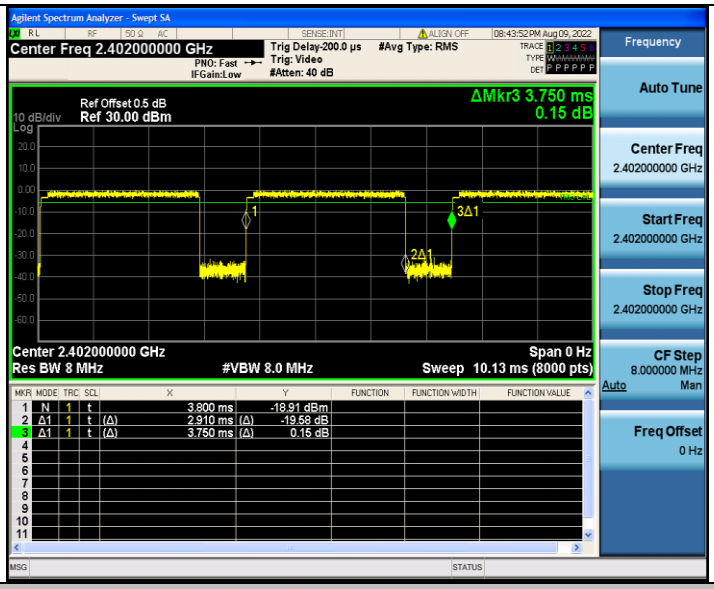
2DH5_Ant1_2441



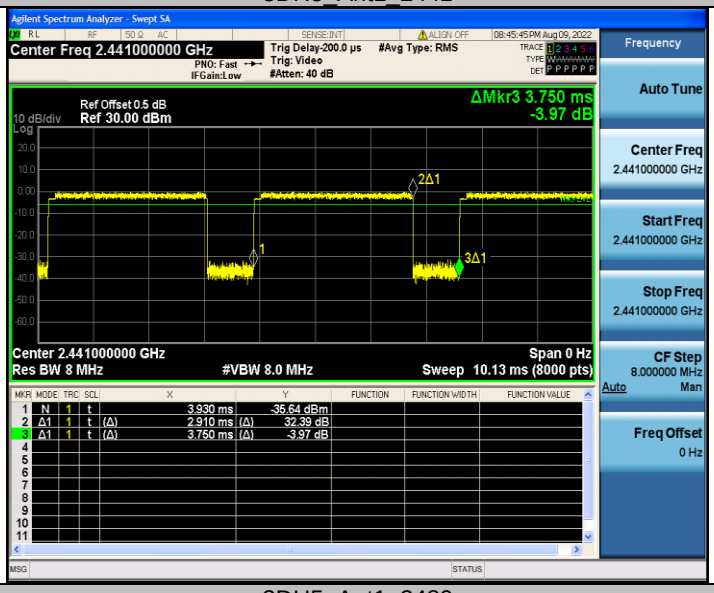
2DH5_Ant1_2480



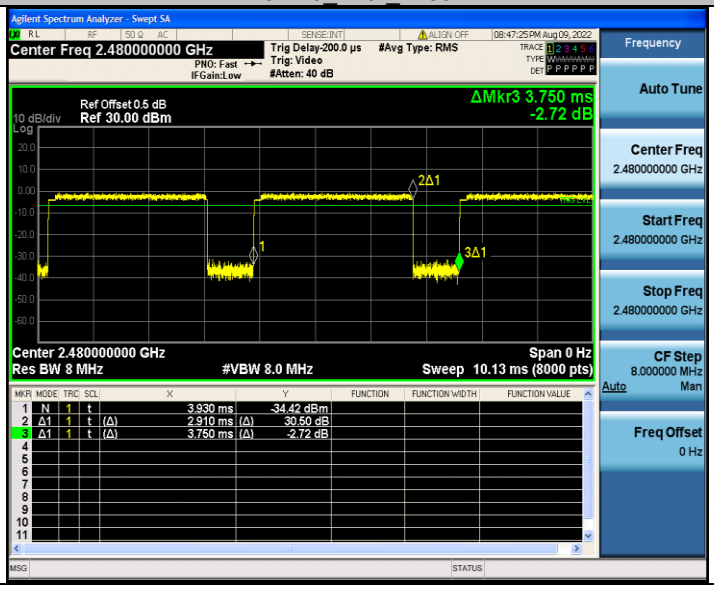
3DH5_Ant1_2402



3DH5_Ant1_2441



3DH5_Ant1_2480



3.11. Antenna Requirement

Requirement

FCC CFR Title 47 Part 15 Subpart C Section 15.203:

An intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of antenna that uses a unique coupling to the intentional radiator, the manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.

FCC CFR Title 47 Part 15 Subpart C Section 15.247(c) (1)(i):

(i) Systems operating in the 2400~2483.5 MHz band that is used exclusively for fixed. Point-to-point operations may employ transmitting antennas with directional gain greater than 6dBi provided the maximum conducted output power of the intentional radiator is reduced by 1 dB for every 3 dB that the directional gain of the antenna exceeds 6dBi.

Test Result

The directional gain of the antenna less than 6dBi, please refer to the EUT internal photographs antenna photo.

**************************THE END**************************