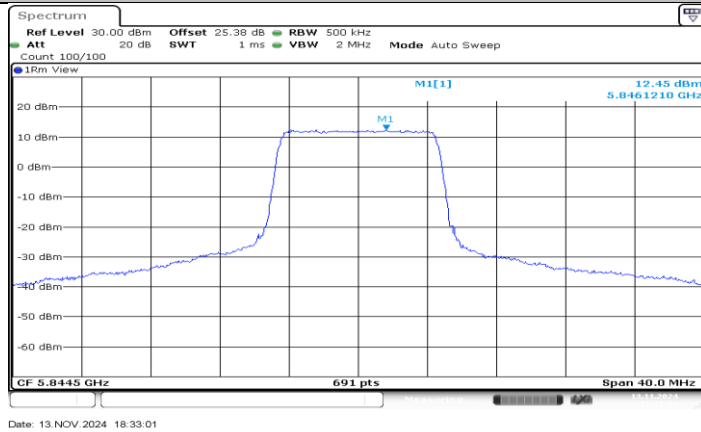
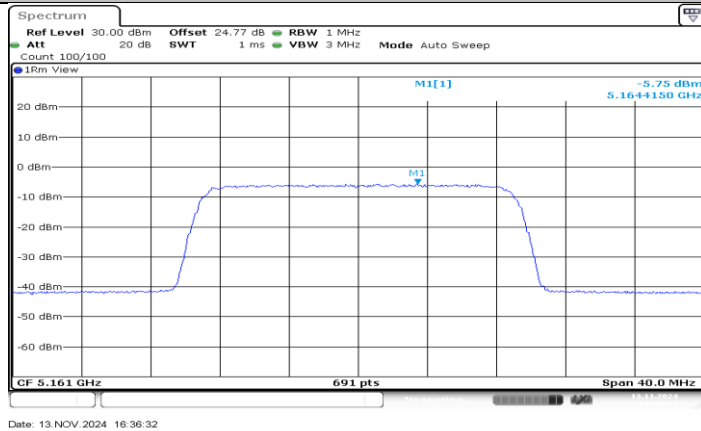


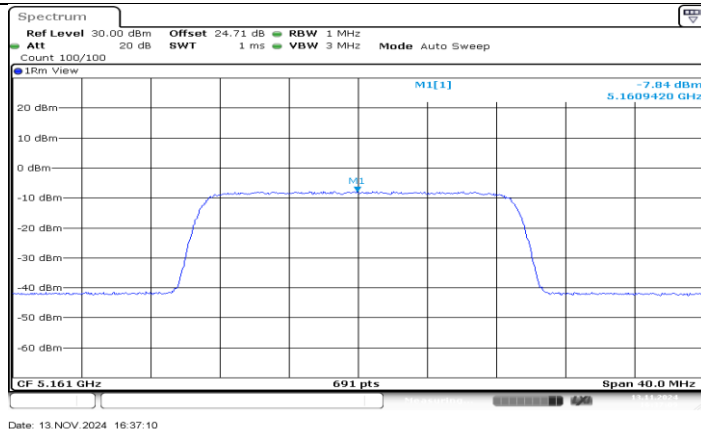
SDR 10M_Ant1_5844.5



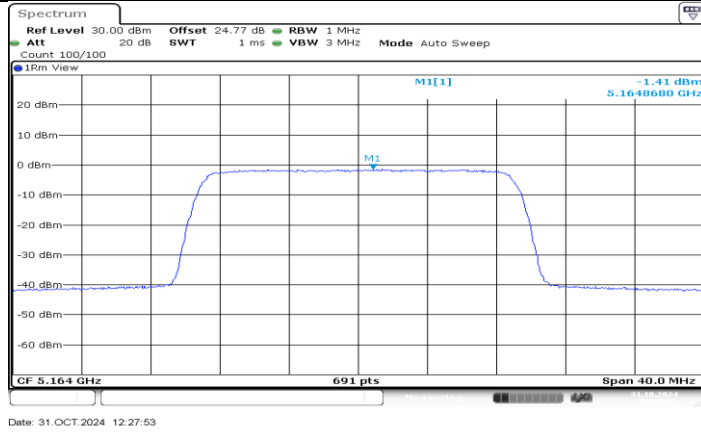
SDR 10M_Ant2_5844.5



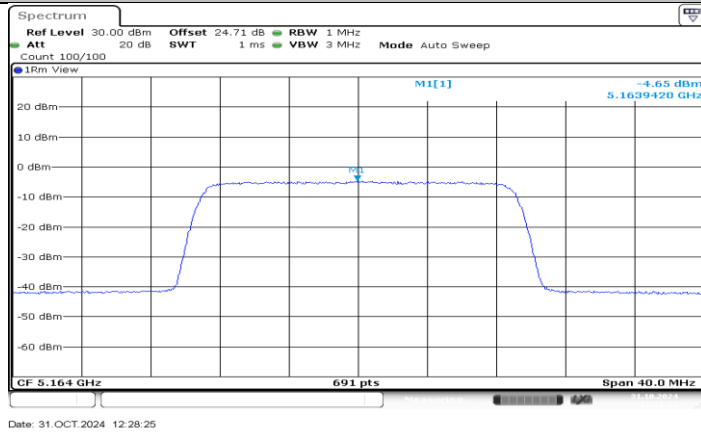
SDR 20M_Ant1_5161



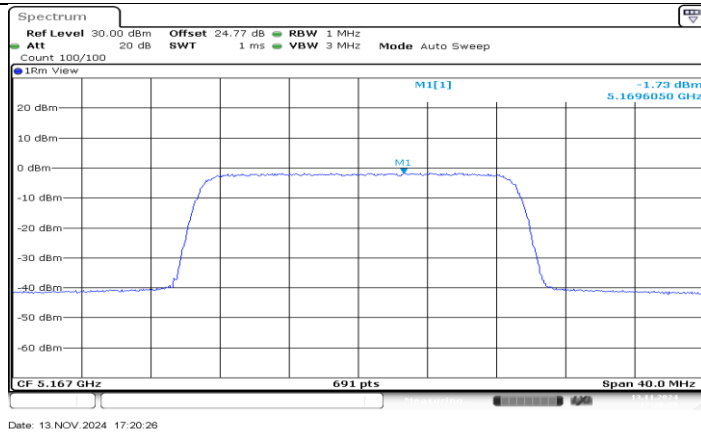
SDR 20M_Ant2_5161



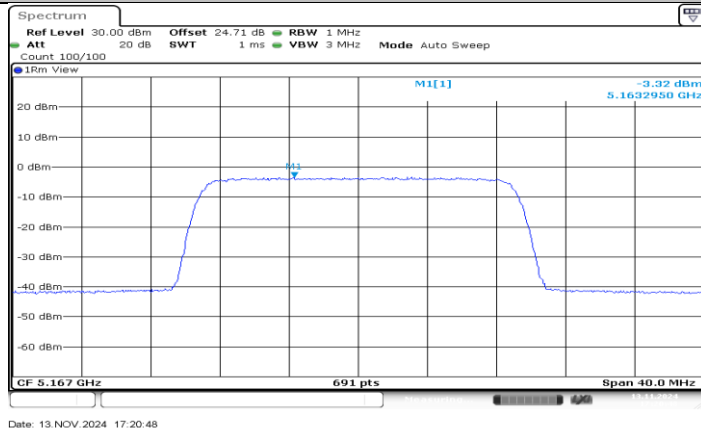
SDR 20M_Ant1_5164



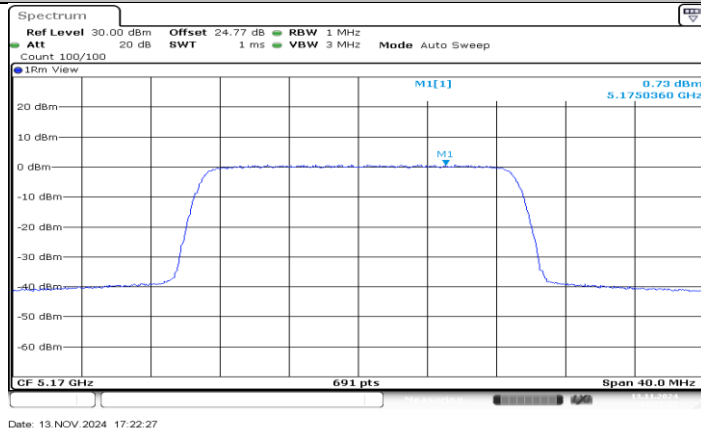
SDR 20M_Ant2_5164



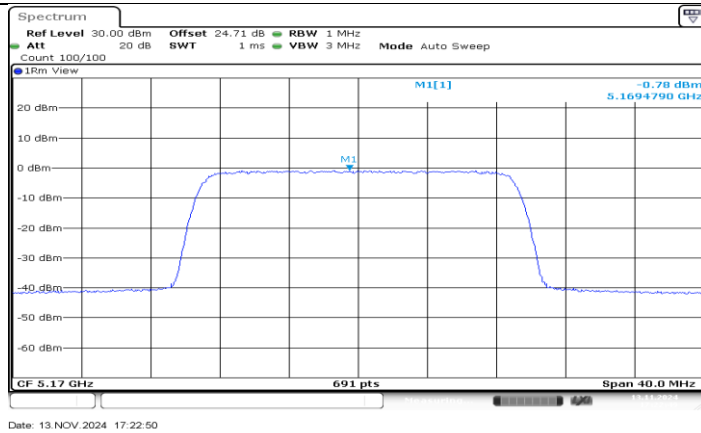
SDR 20M_Ant1_5167



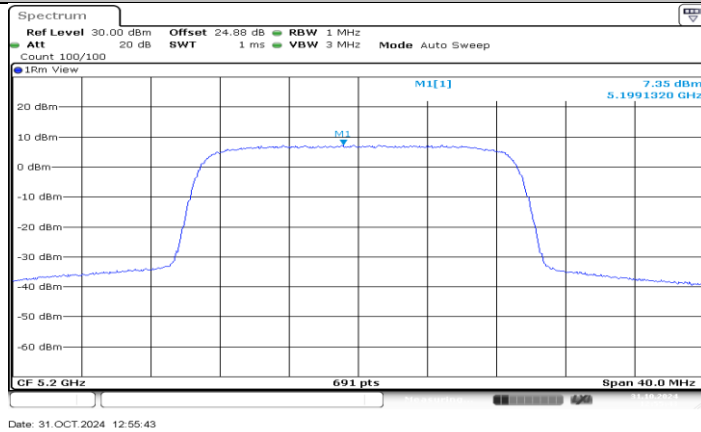
SDR 20M_Ant2_5167



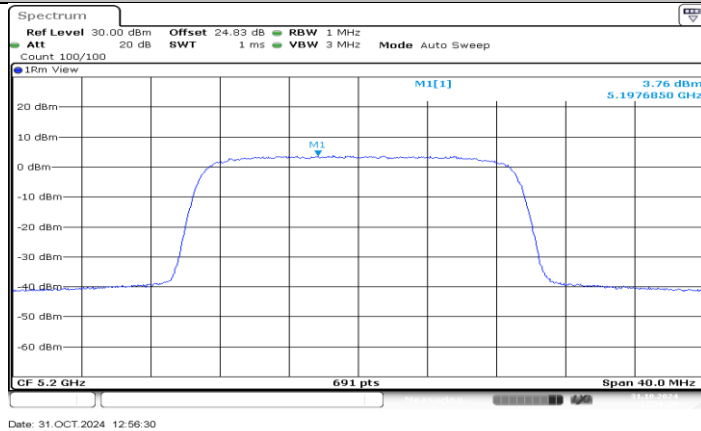
SDR 20M_Ant1_5170



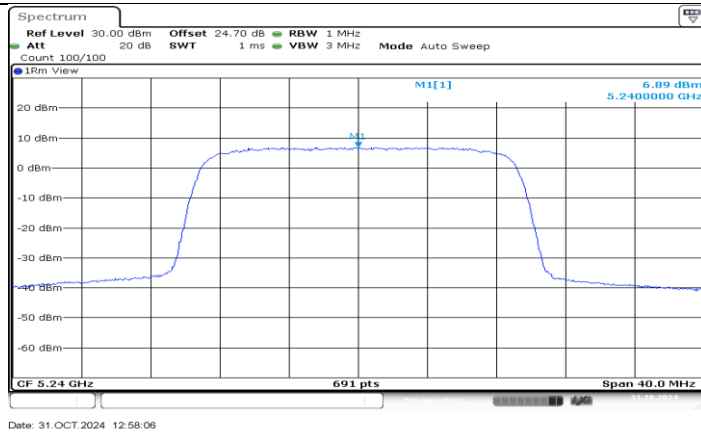
SDR 20M_Ant2_5170



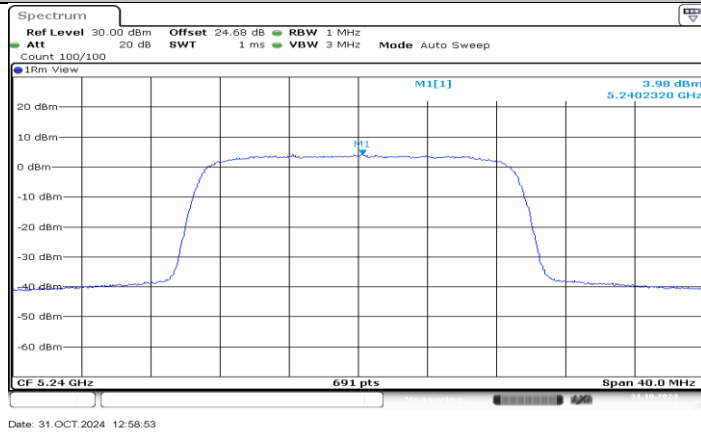
SDR 20M_Ant1_5200



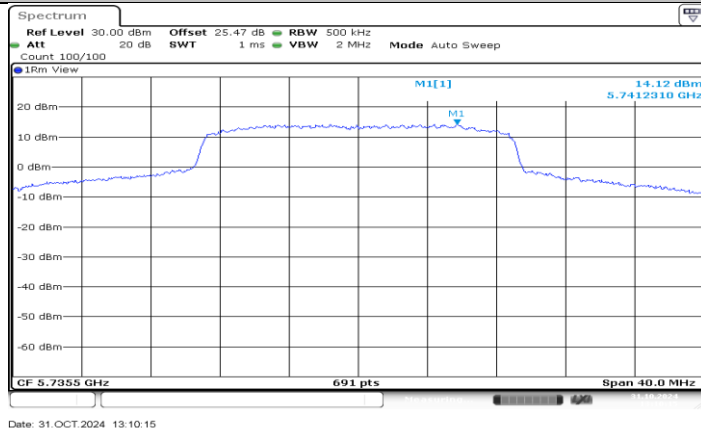
SDR 20M_Ant2_5200



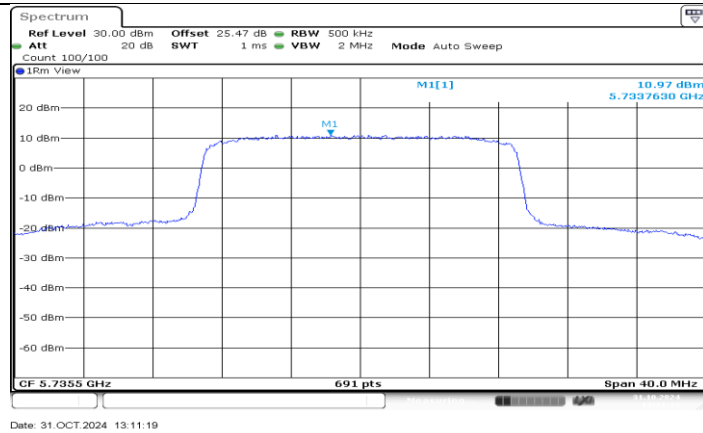
SDR 20M_Ant1_5240



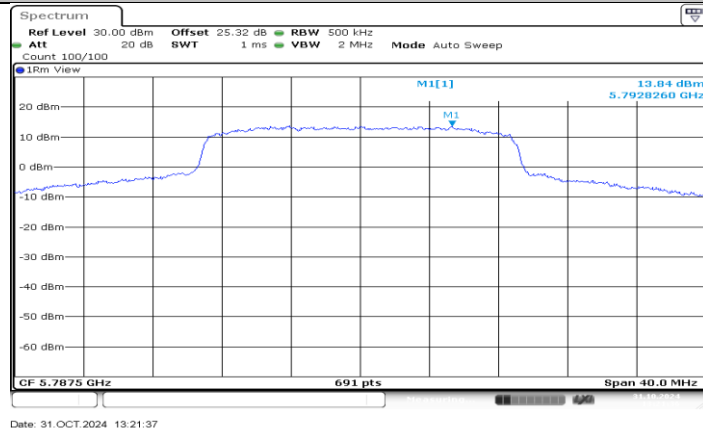
SDR 20M_Ant2_5240



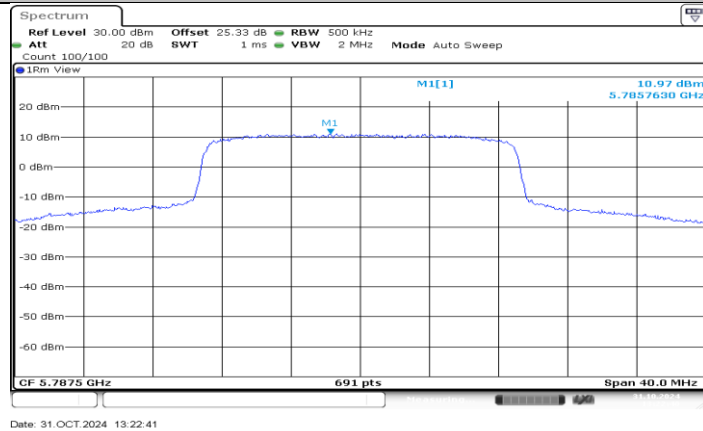
SDR 20M_Ant1_5735.5



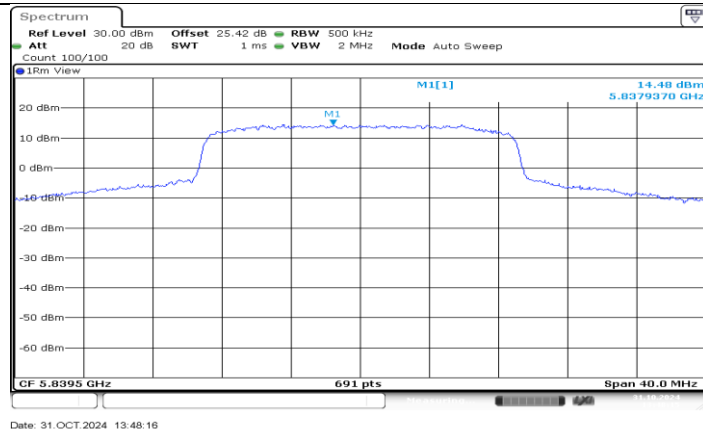
SDR 20M_Ant2_5735.5



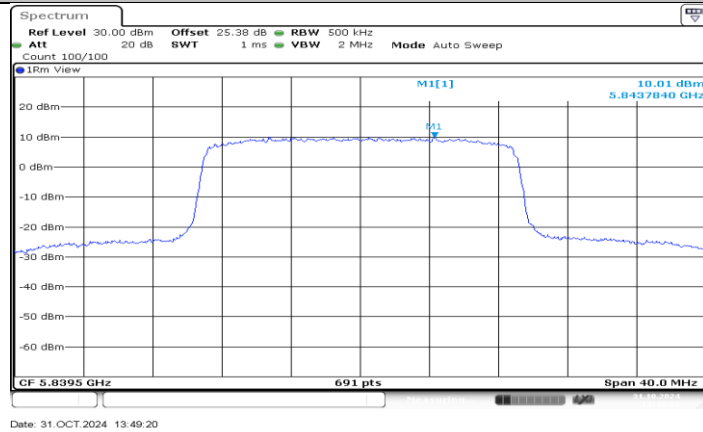
SDR 20M_Ant1_5787.5



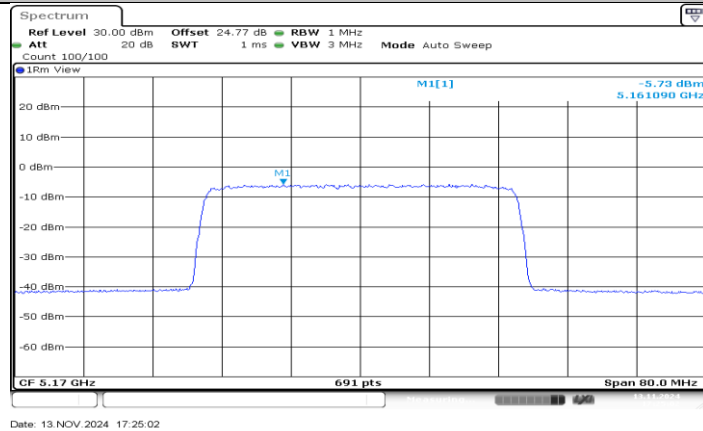
SDR 20M_Ant2_5787.5



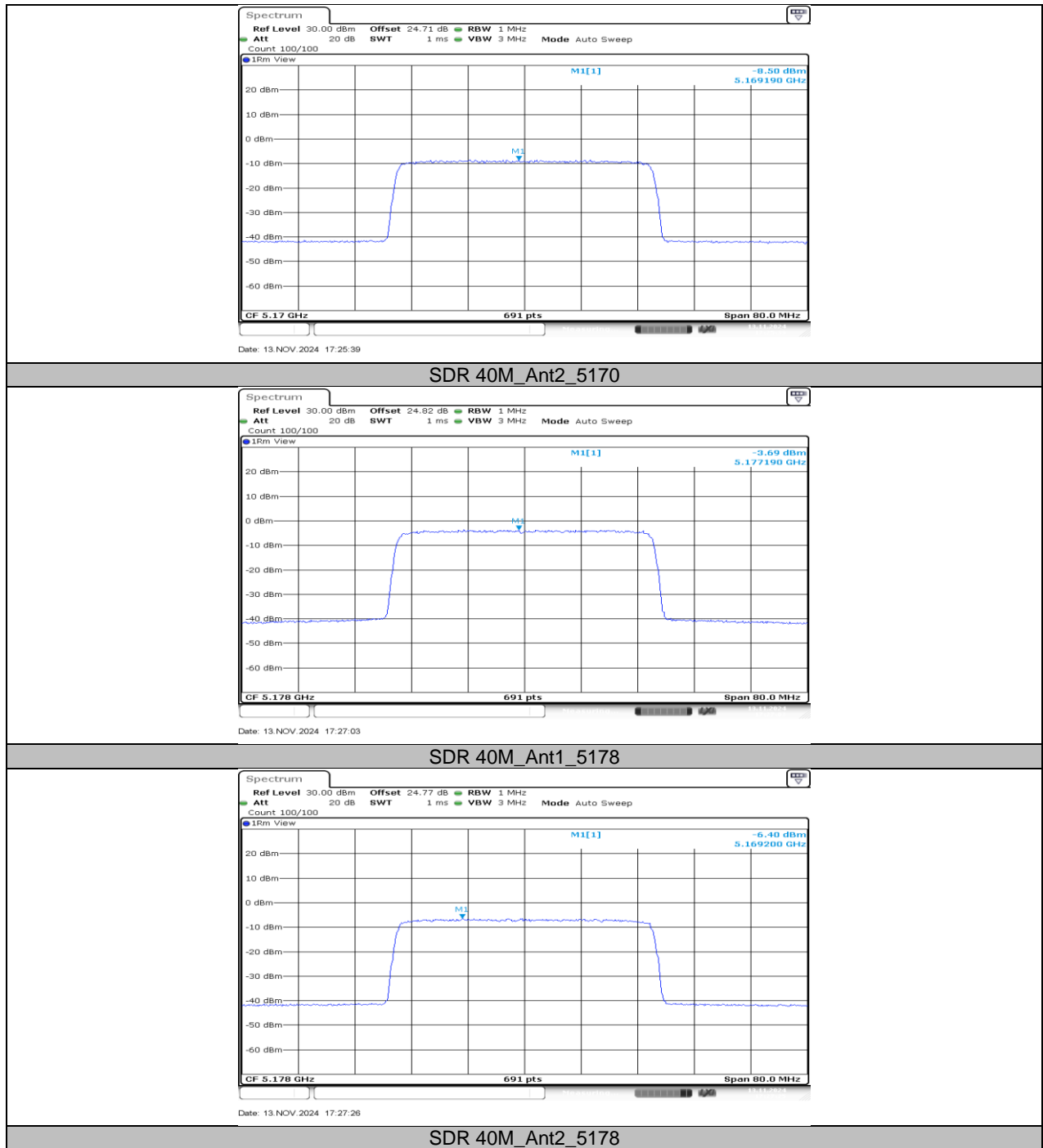
SDR 20M_Ant1_5839.5

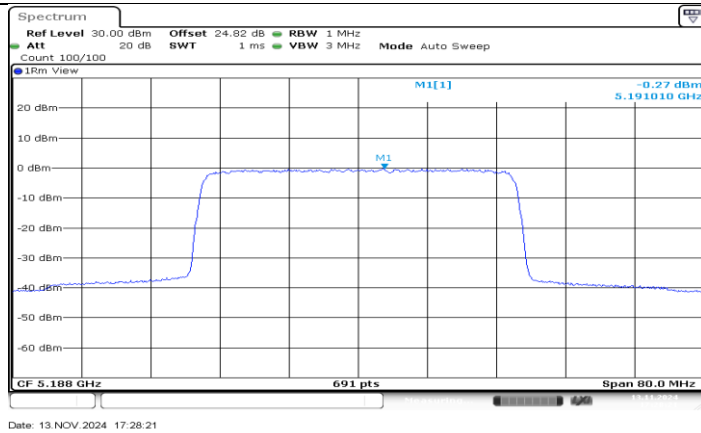


SDR 20M_Ant2_5839.5

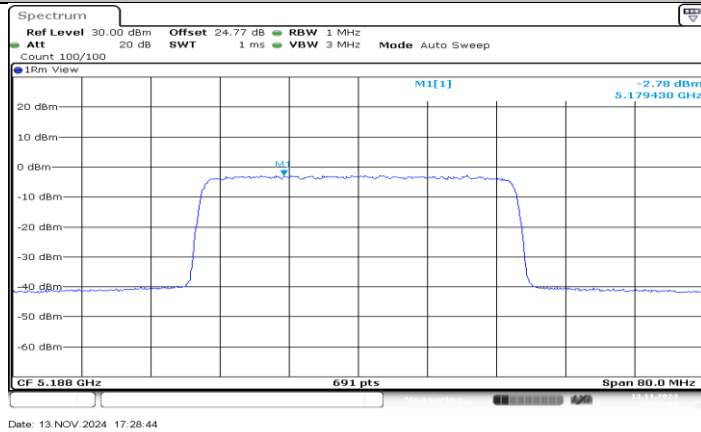


SDR 40M_Ant1_5170

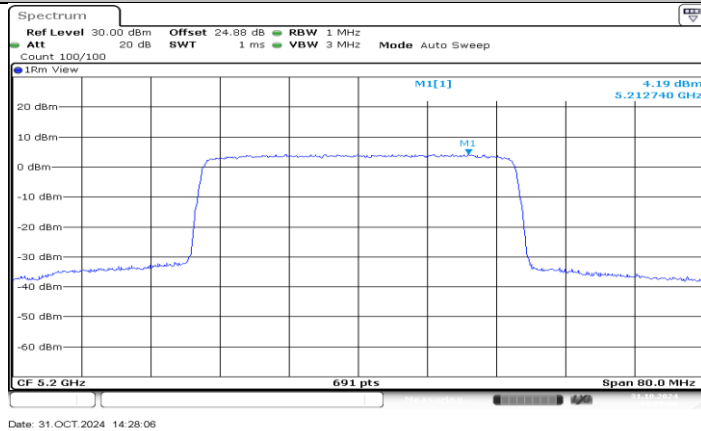




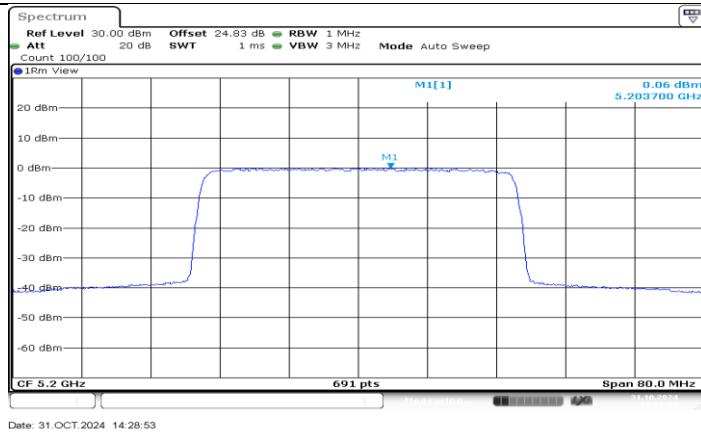
SDR 40M_Ant1_5188



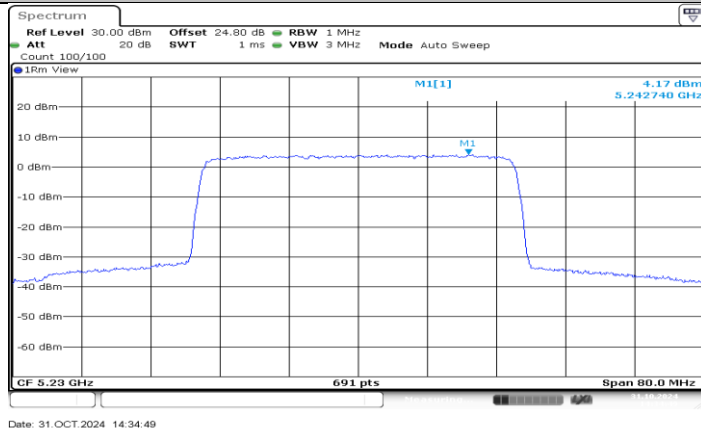
SDR 40M_Ant2_5188



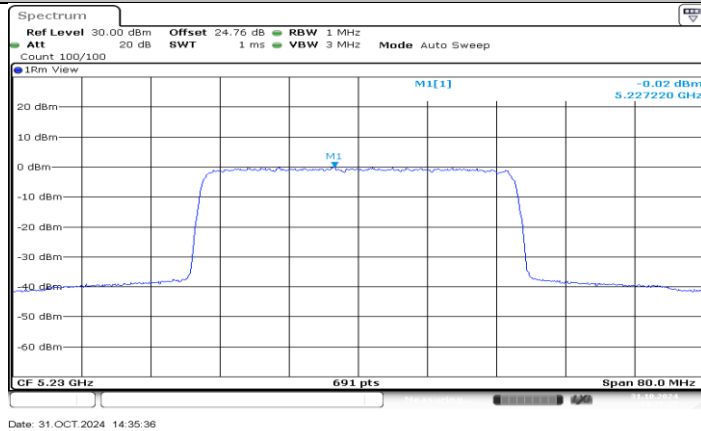
SDR 40M_Ant1_5200



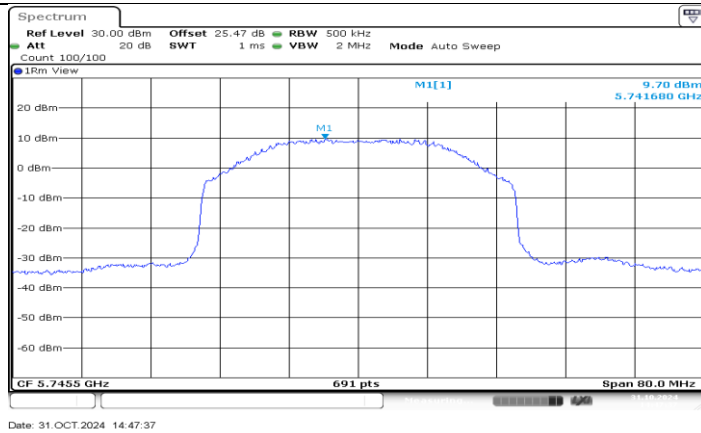
SDR 40M_Ant2_5200



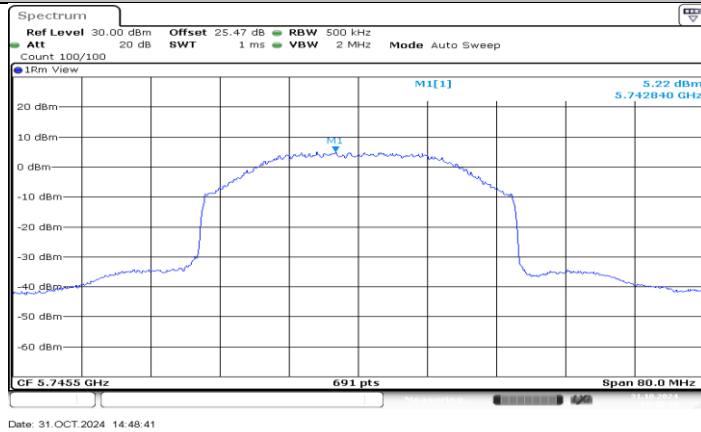
SDR 40M_Ant1_5230



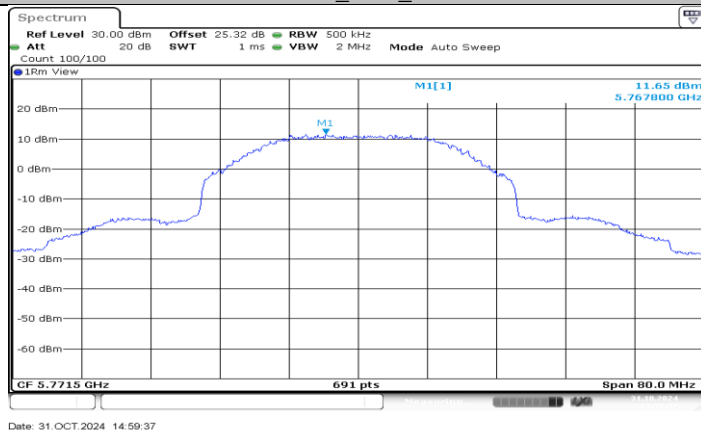
SDR 40M_Ant2_5230



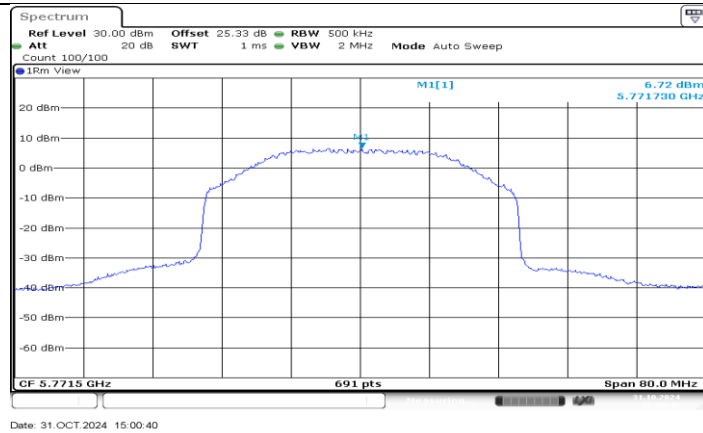
SDR 40M_Ant1_5745.5



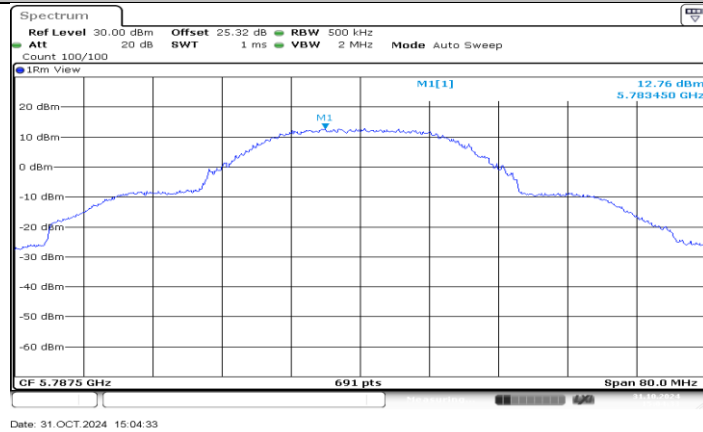
SDR 40M_Ant2_5745.5



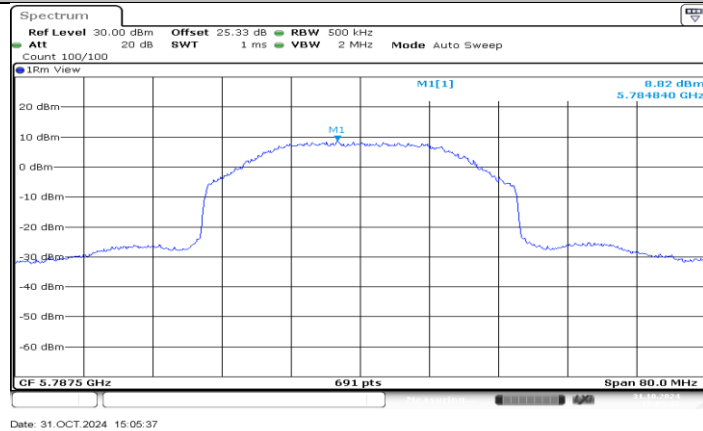
SDR 40M_Ant1_5771.5



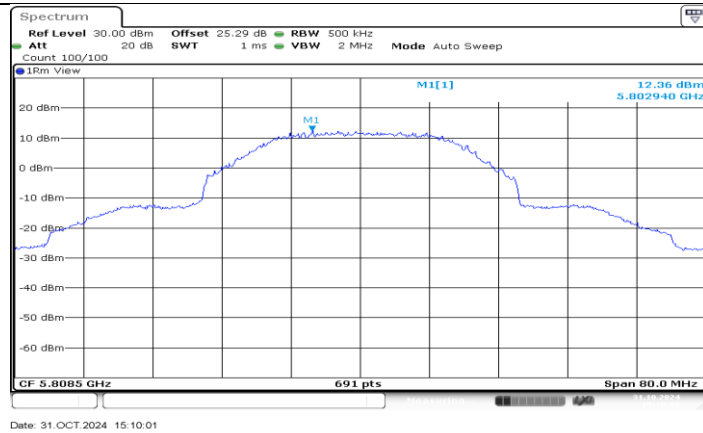
SDR 40M_Ant2_5771.5



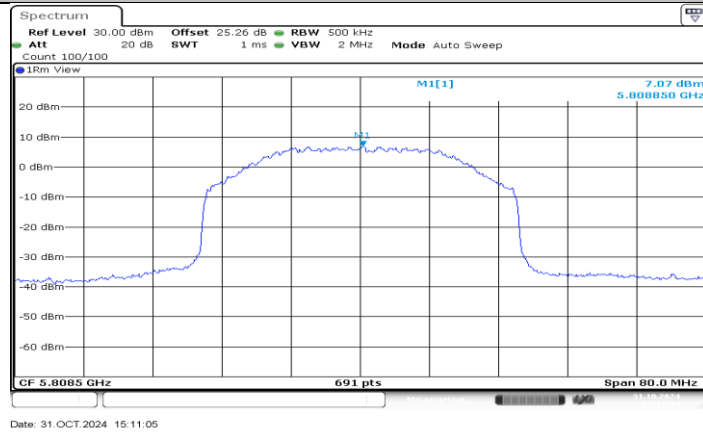
SDR 40M_Ant1_5787.5



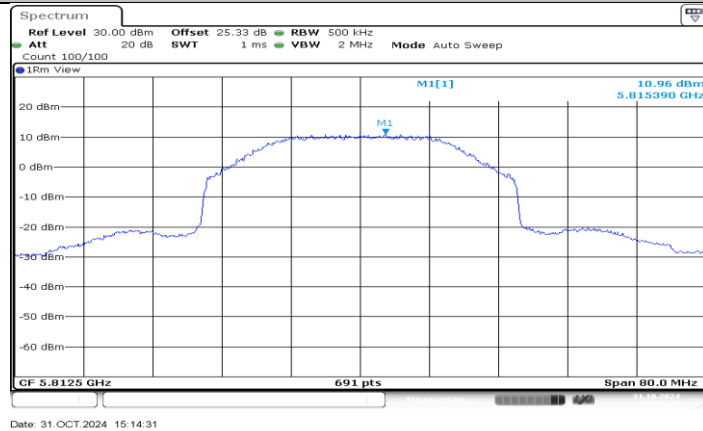
SDR 40M_Ant2_5787.5



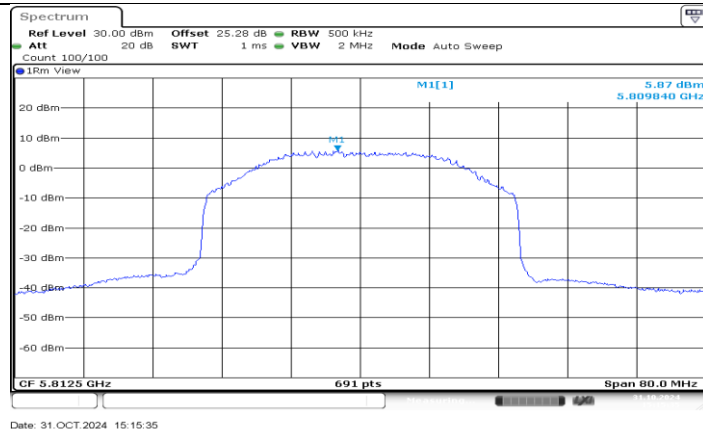
SDR 40M_Ant1_5808.5



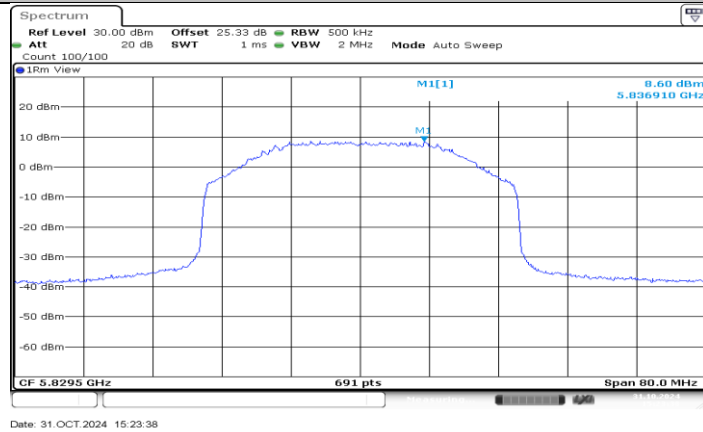
SDR 40M_Ant2_5808.5



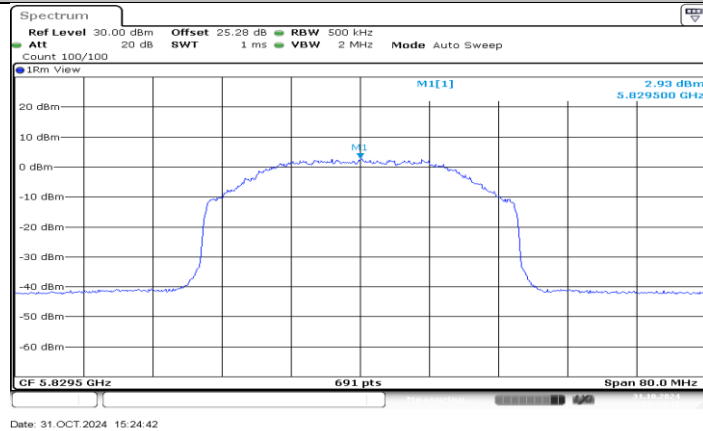
SDR 40M_Ant1_5812.5



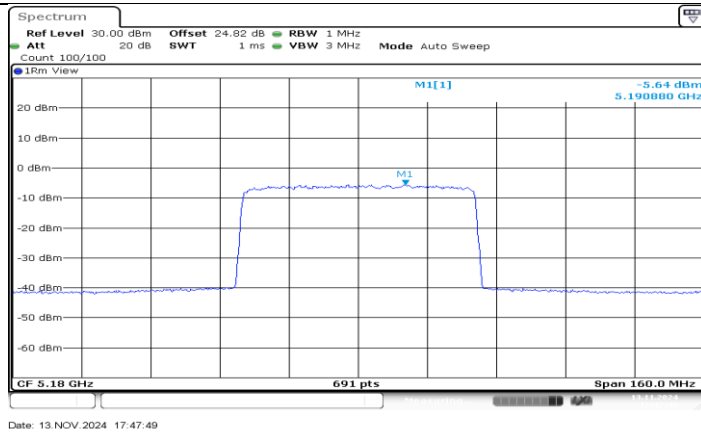
SDR 40M_Ant2_5812.5



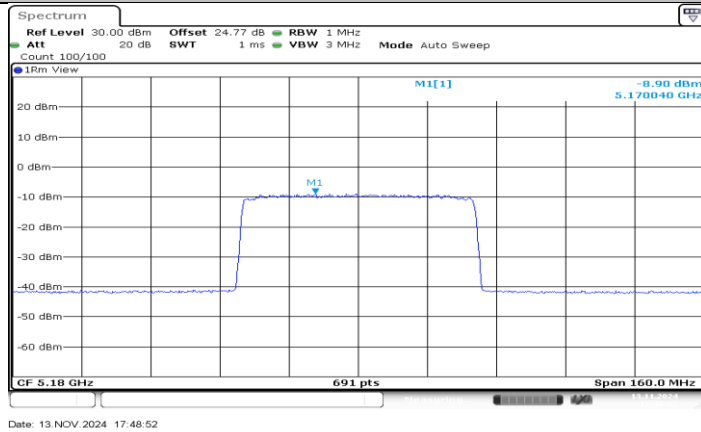
SDR 40M_Ant1_5829.5



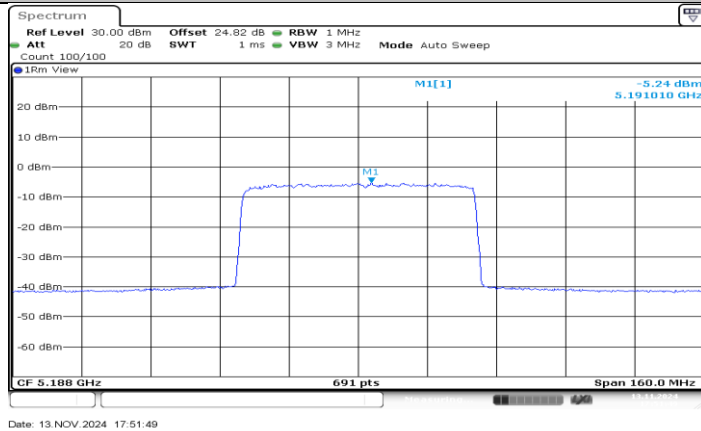
SDR 40M_Ant2_5829.5



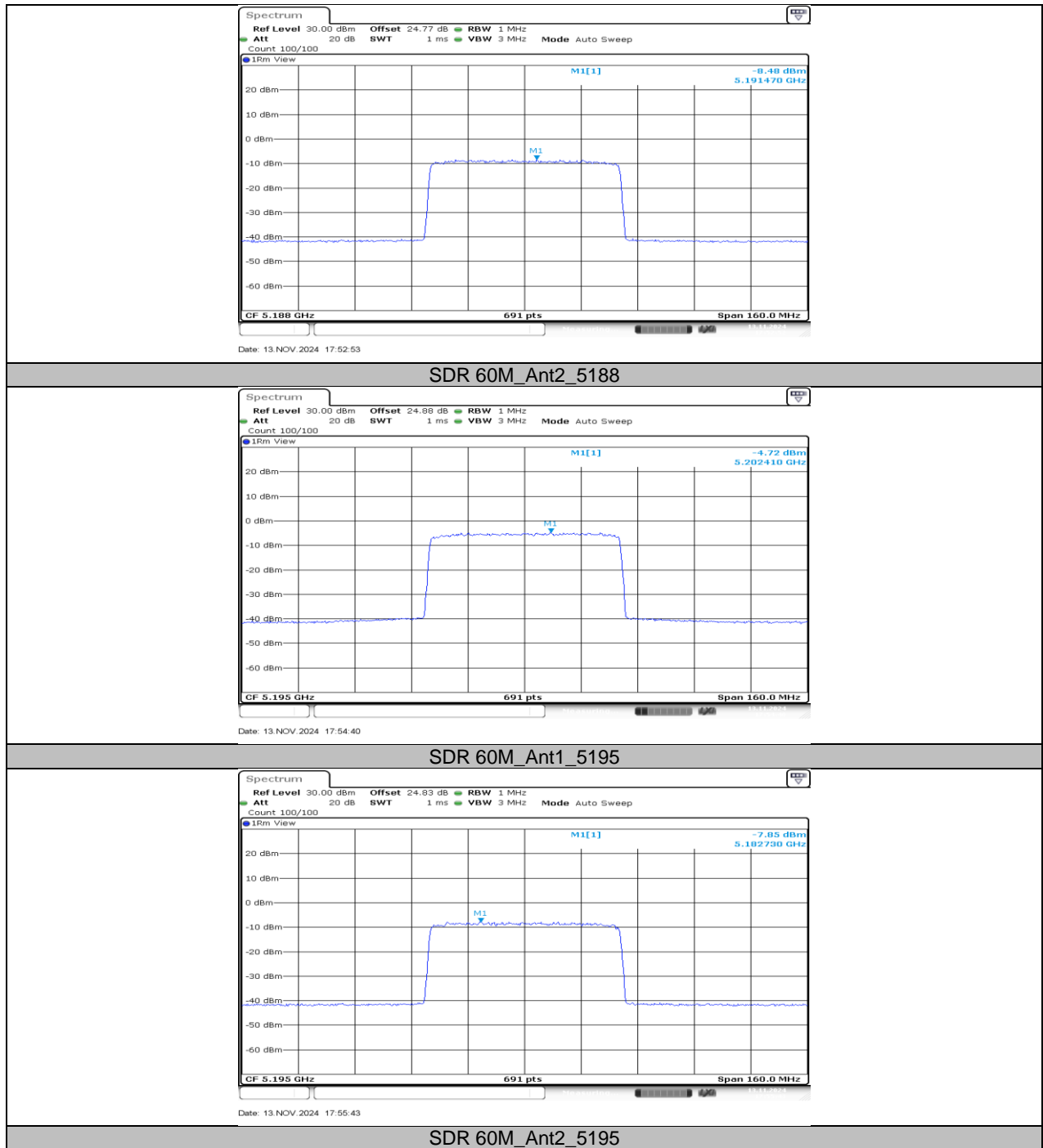
SDR 60M_Ant1_5180

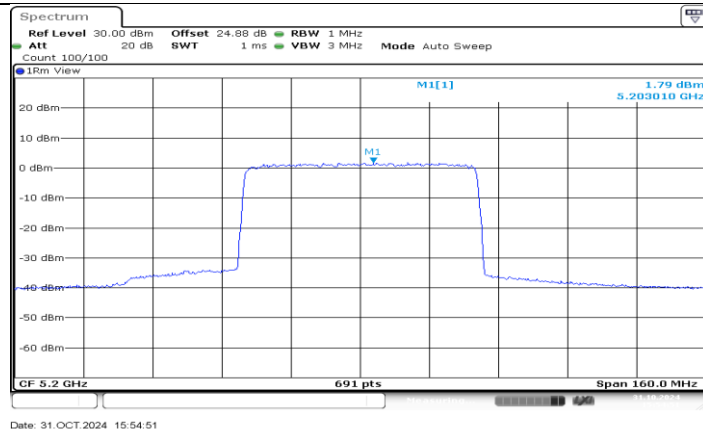


SDR 60M_Ant2_5180

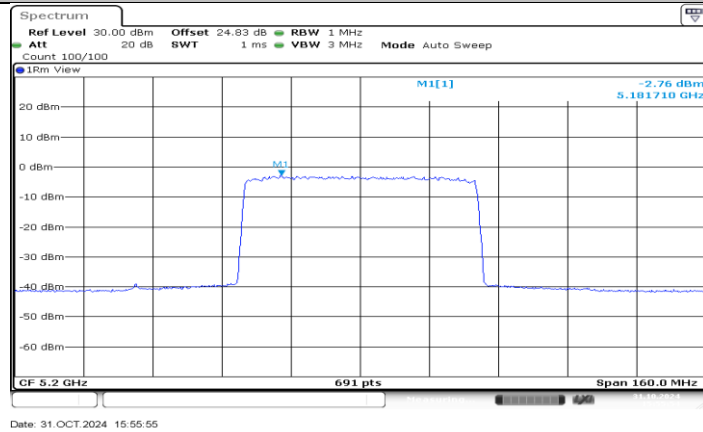


SDR 60M_Ant1_5188

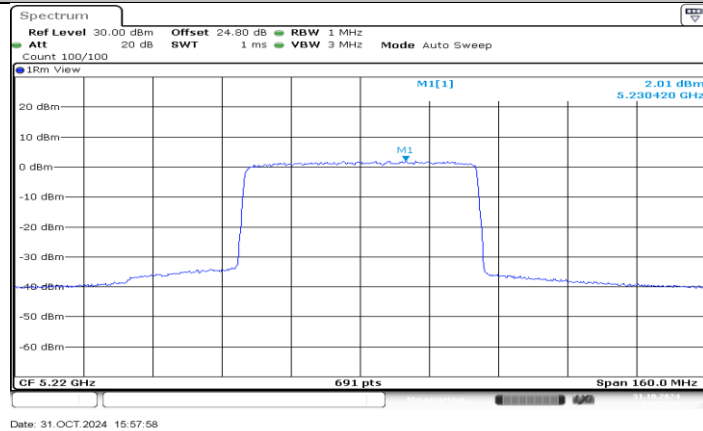




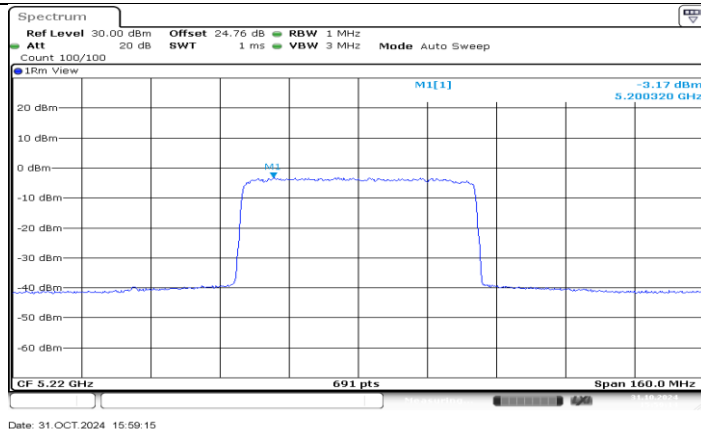
SDR 60M_Ant1_5200



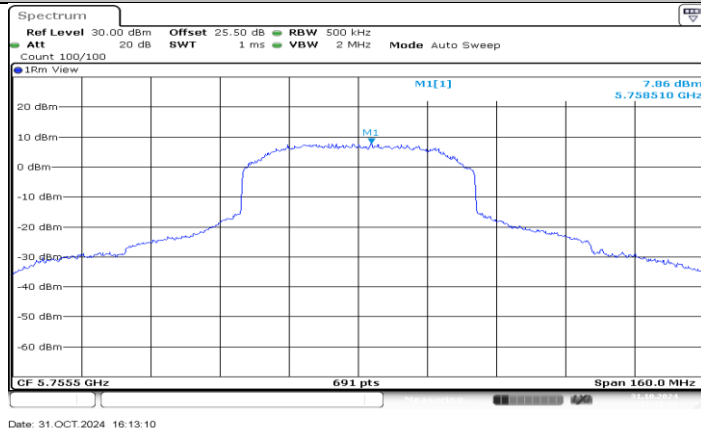
SDR 60M_Ant2_5200



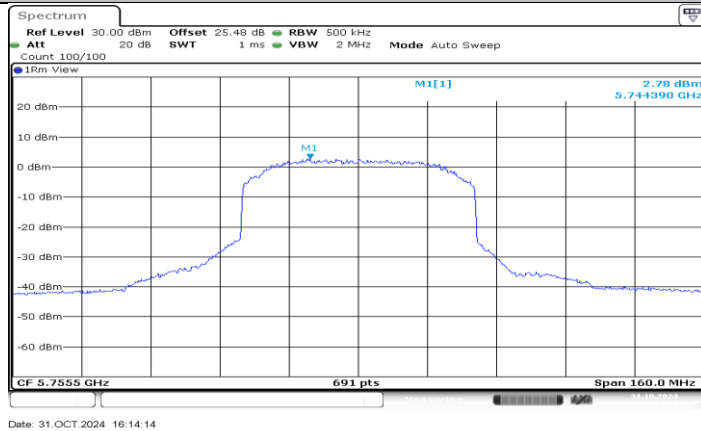
SDR 60M_Ant1_5220



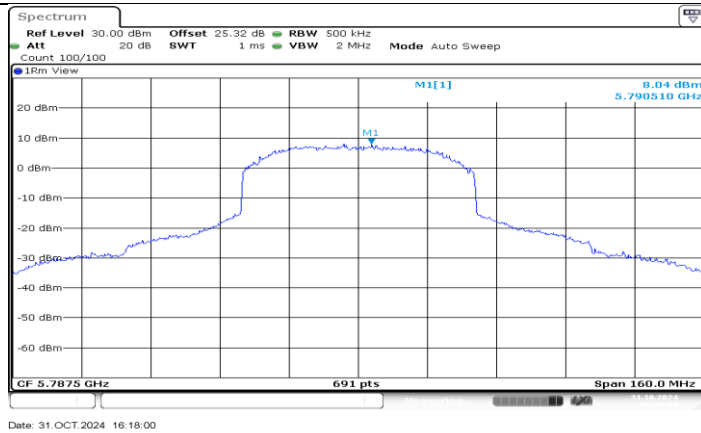
SDR 60M_Ant2_5220



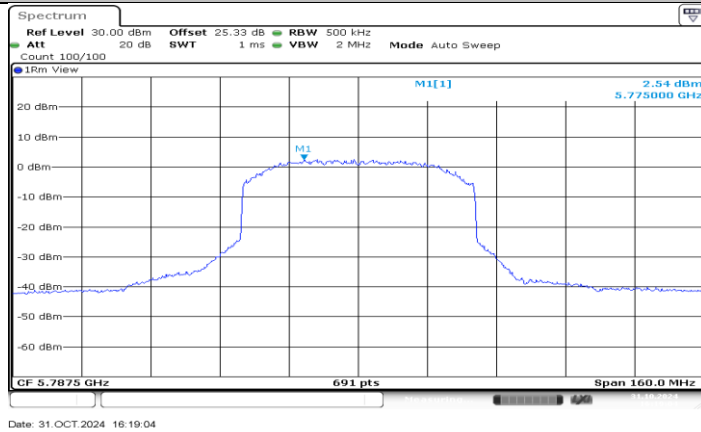
SDR 60M_Ant1_5755.5



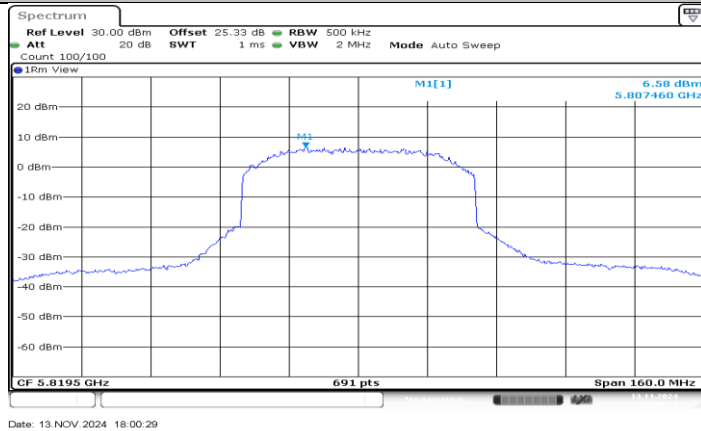
SDR 60M_Ant2_5755.5



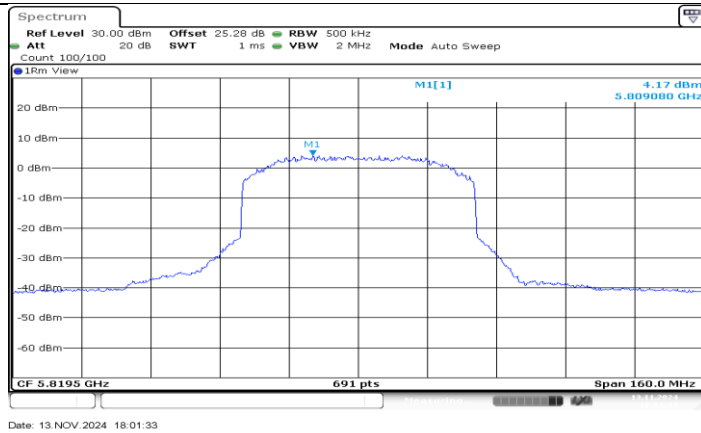
SDR 60M_Ant1_5787.5



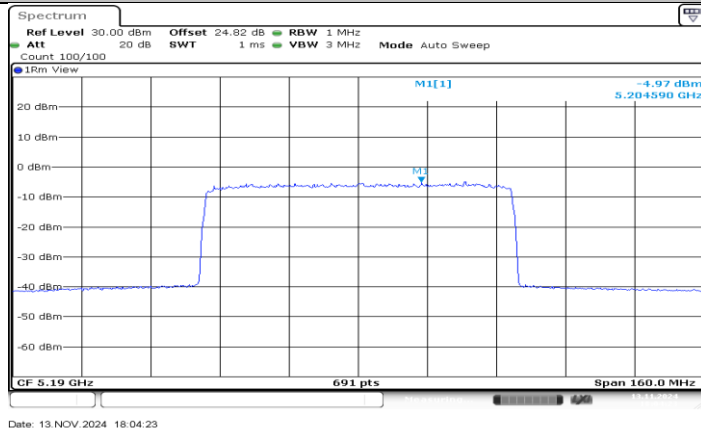
SDR 60M_Ant2_5787.5



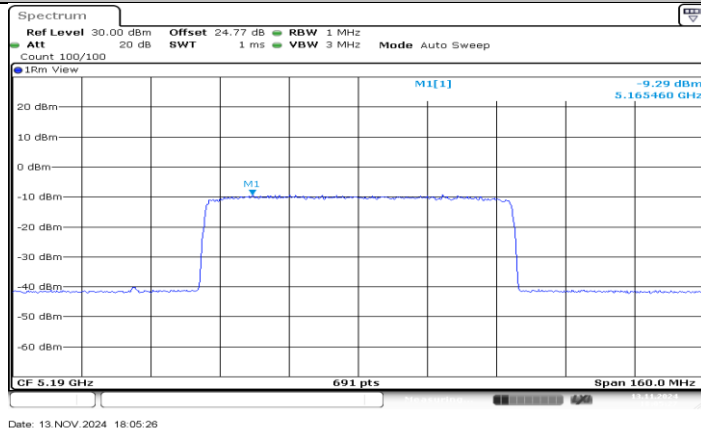
SDR 60M_Ant1_5819.5



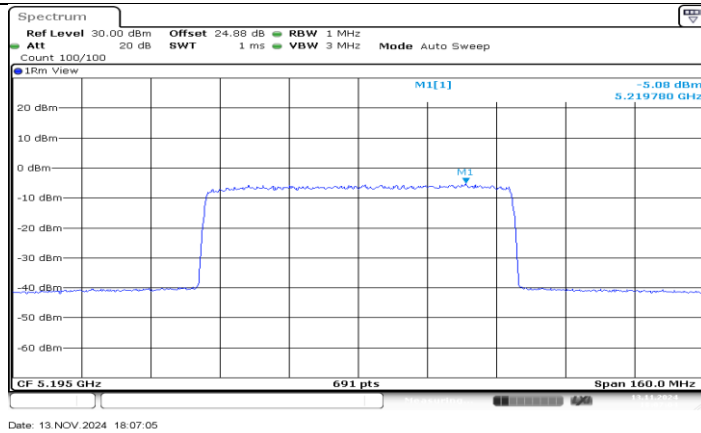
SDR 60M_Ant2_5819.5



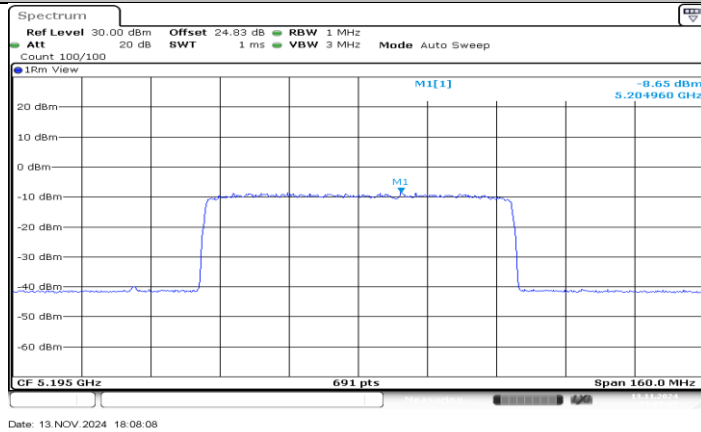
SDR 80M_Ant1_5190



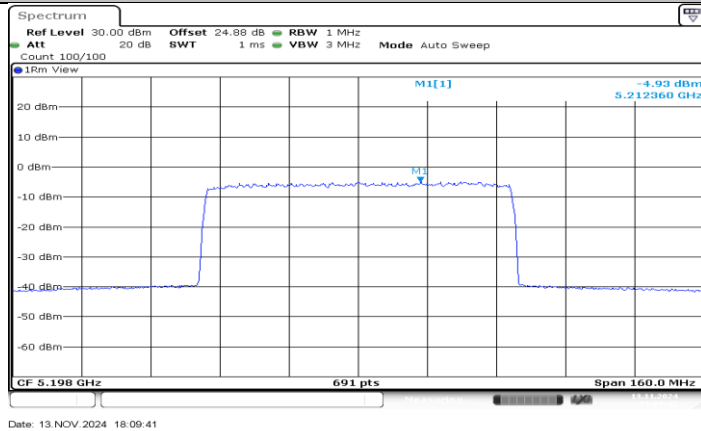
SDR 80M_Ant2_5190



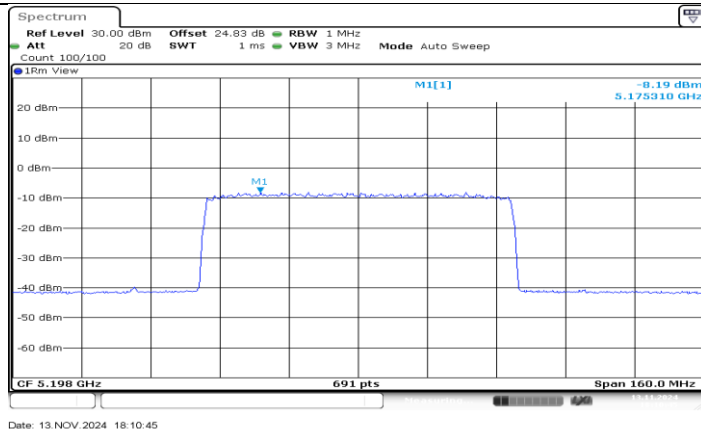
SDR 80M_Ant1_5195



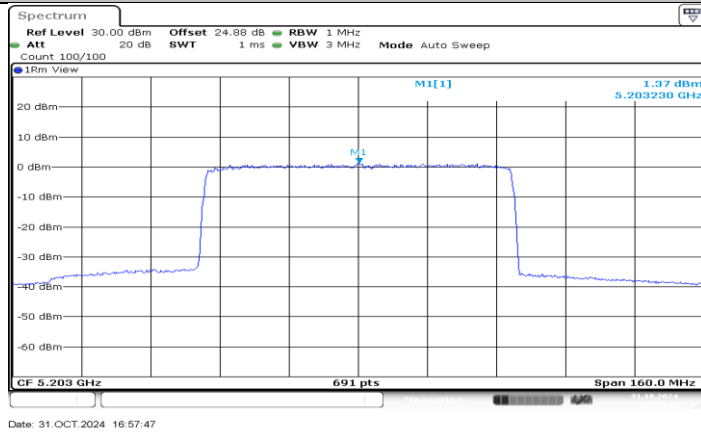
SDR 80M_Ant2_5195



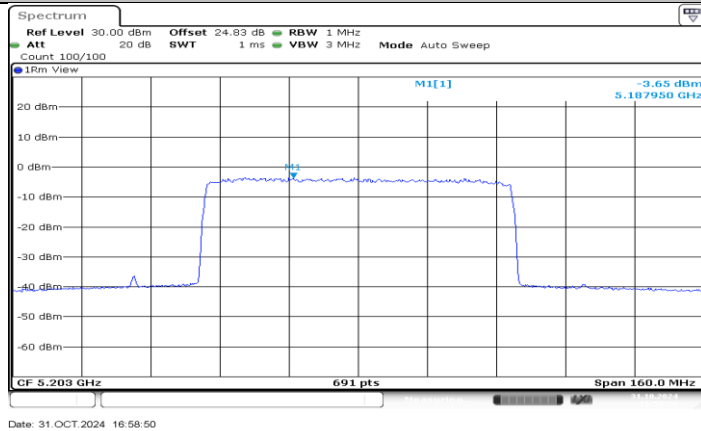
SDR 80M_Ant1_5198



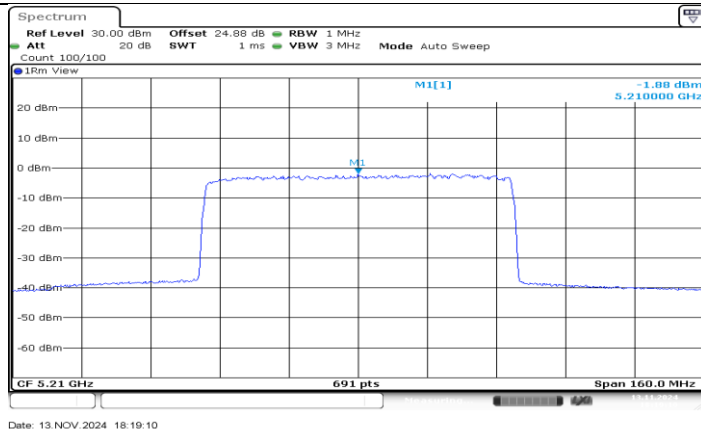
SDR 80M_Ant2_5198



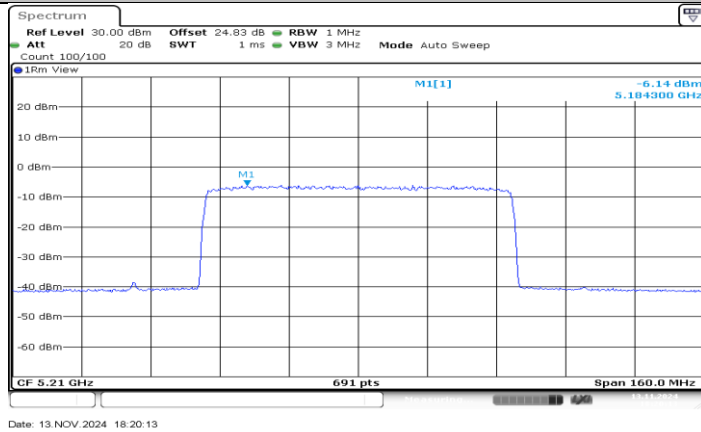
SDR 80M_Ant1_5203



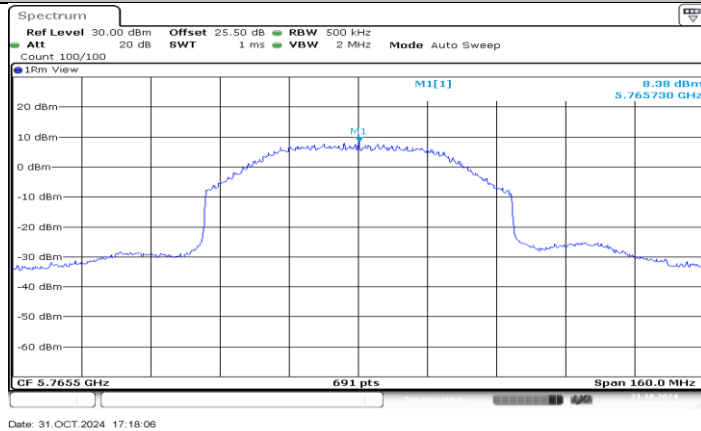
SDR 80M_Ant2_5203



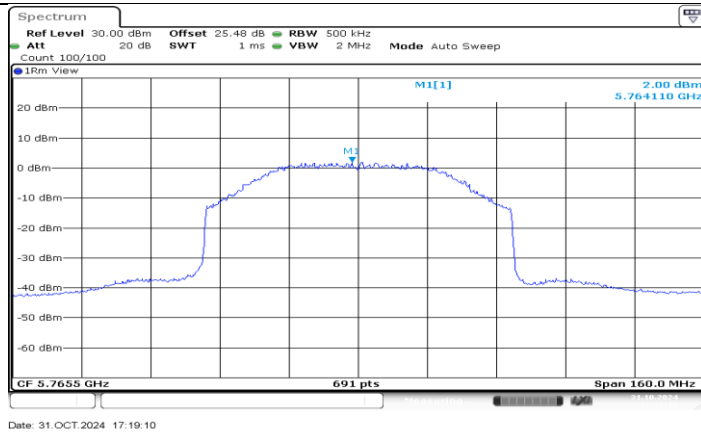
SDR 80M_Ant1_5210



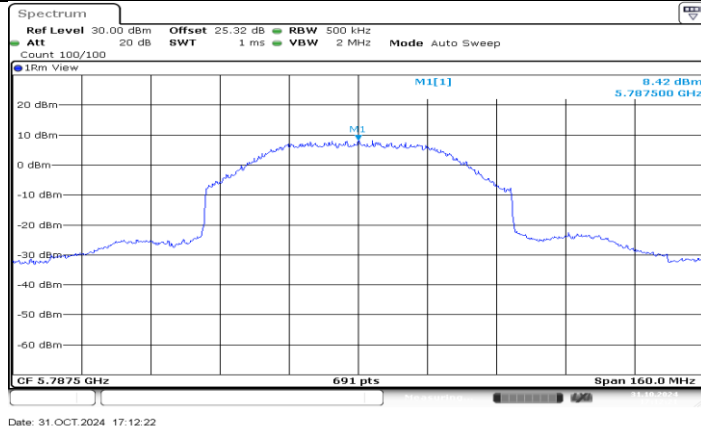
SDR 80M_Ant2_5210



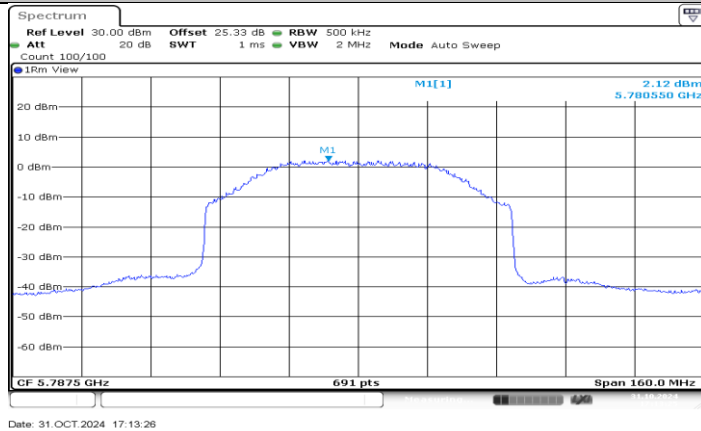
SDR 80M_Ant1_5765.5



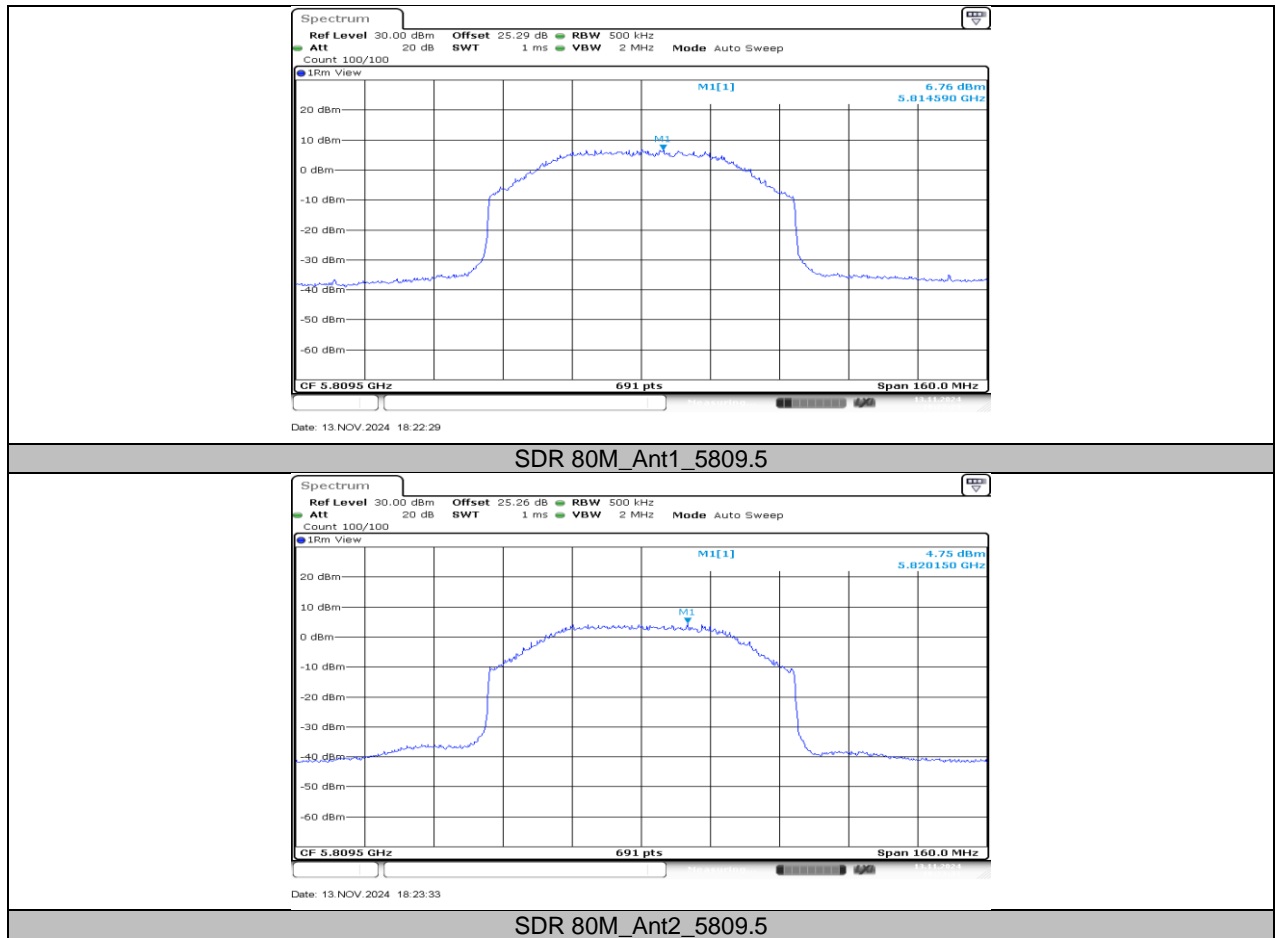
SDR 80M_Ant2_5765.5



SDR 80M_Ant1_5787.5



SDR 80M_Ant2_5787.5



10.6. APPENDIX F: FREQUENCY STABILITY

10.6.1. Test Result

Frequency Error vs. Voltage									
SRD 10M:5201MHz									
Temp.	Volt.	0 Minute		2 Minute		5 Minute		10 Minute	
		Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)
TN	VL	5200.9766	-4.51	5200.9967	-0.64	5201.0214	4.12	5201.0021	0.40
TN	VN	5200.9835	-3.18	5201.0195	3.74	5200.9891	-2.10	5200.9818	-3.50
TN	VH	5200.9807	-3.71	5201.0206	3.96	5200.9931	-1.34	5200.9902	-1.88
Frequency Error vs. Temperature									
802.11a:5200MHz									
Temp.	Volt.	0 Minute		2 Minute		5 Minute		10 Minute	
		Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)	Freq.Error (MHz)	Tolerance (ppm)
40	VN	5201.0231	4.44	5201.0088	1.69	5201.0167	3.20	5200.9910	-1.73
30	VN	5200.9875	-2.40	5200.9818	-3.50	5201.0201	3.87	5200.9891	-2.10
20	VN	5200.9753	-4.74	5200.9944	-1.08	5200.9794	-3.95	5201.0057	1.10
10	VN	5200.9759	-4.63	5201.0077	1.48	5201.0145	2.78	5201.0150	2.89
0	VN	5200.9969	-0.61	5200.9754	-4.73	5201.0247	4.75	5200.9925	-1.43
-10	VN	5201.0157	3.01	5201.0068	1.31	5201.0131	2.52	5200.9774	-4.34

Note:

1. All antennas, test modes and test channels have been tested, only the worst data record in the report.
2. For the detail Test Conditions, please refer to section 7.5 TEST ENVIRONMENT.

10.7. APPENDIX G: DUTY CYCLE**10.7.1. Test Result**

Test Mode	On Time (msec)	Period (msec)	Duty Cycle x (Linear)	Duty Cycle (%)	Duty Cycle Correction Factor (dB)	1/T Minimum VBW (kHz)	Final setting For VBW (kHz)
SRD 10M	100.00	100.00	1.0000	100.00	0.00	0.01	0.01
SRD 20M	100.00	100.00	1.0000	100.00	0.00	0.01	0.01
SRD 40M	100.00	100.00	1.0000	100.00	0.00	0.01	0.01
SRD 60M	100.00	100.00	1.0000	100.00	0.00	0.01	0.01
SRD 80M	100.00	100.00	1.0000	100.00	0.00	0.01	0.01

Note:

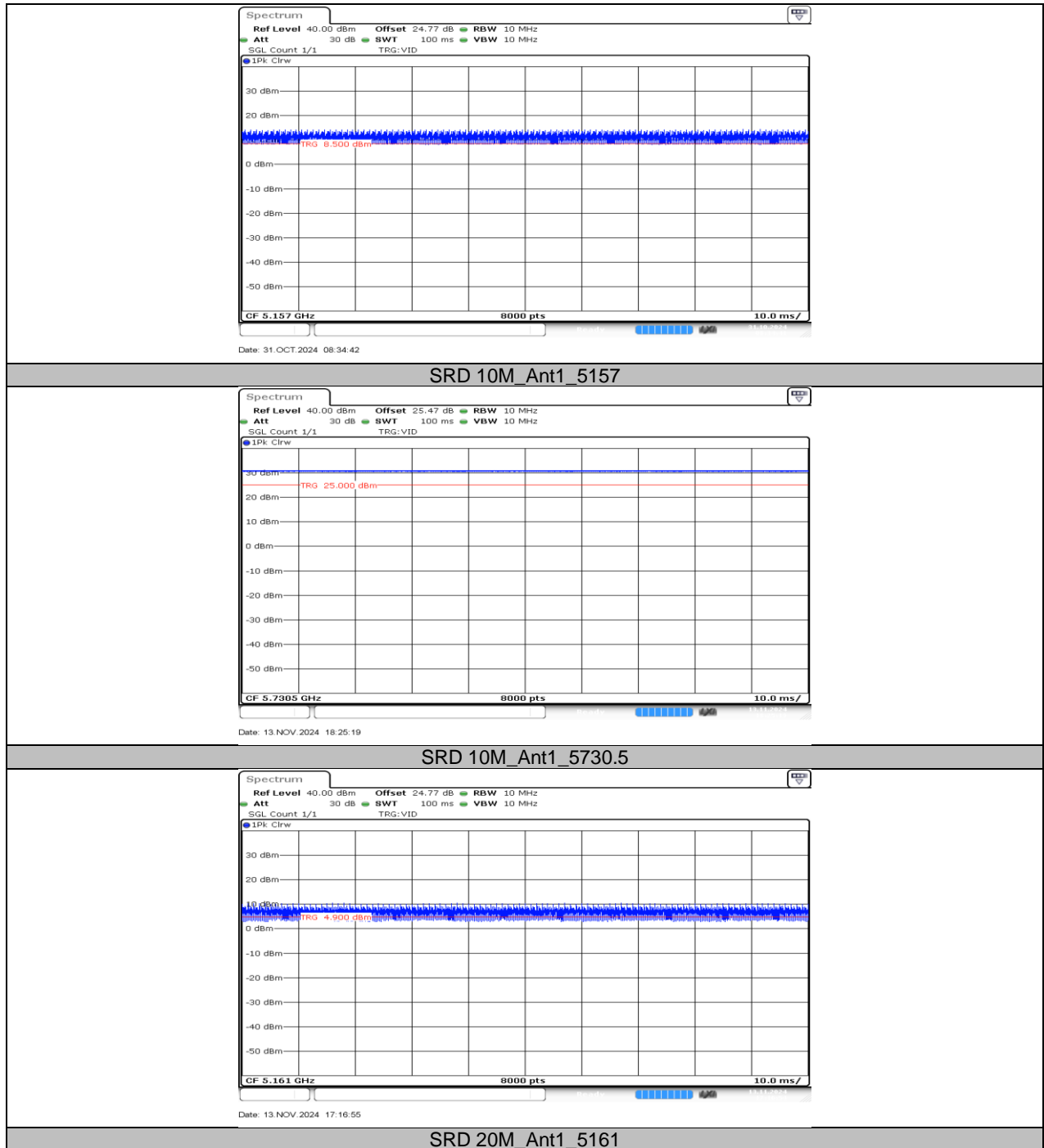
Duty Cycle Correction Factor= $10\log(1/x)$.

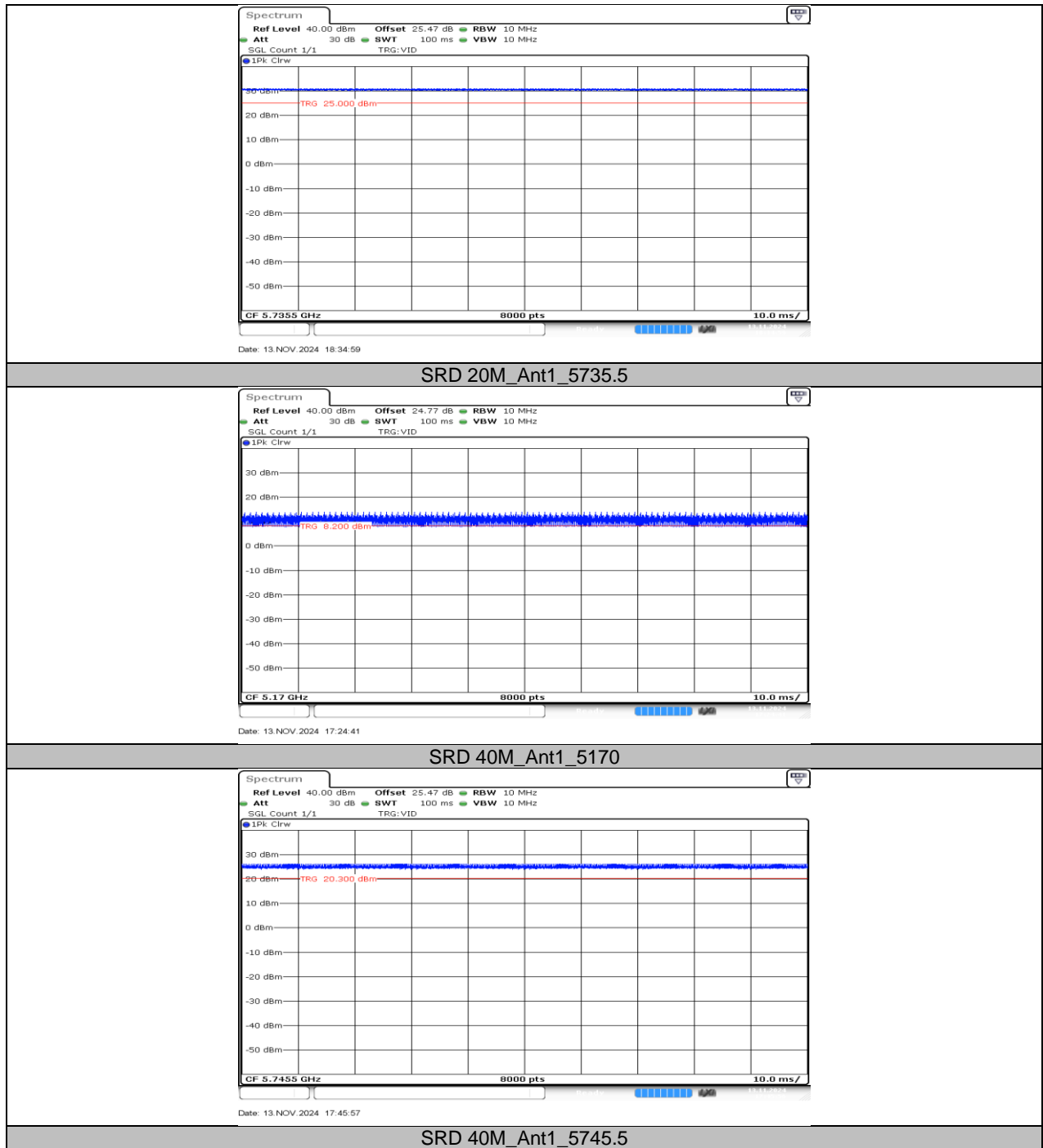
Where: x is Duty Cycle (Linear)

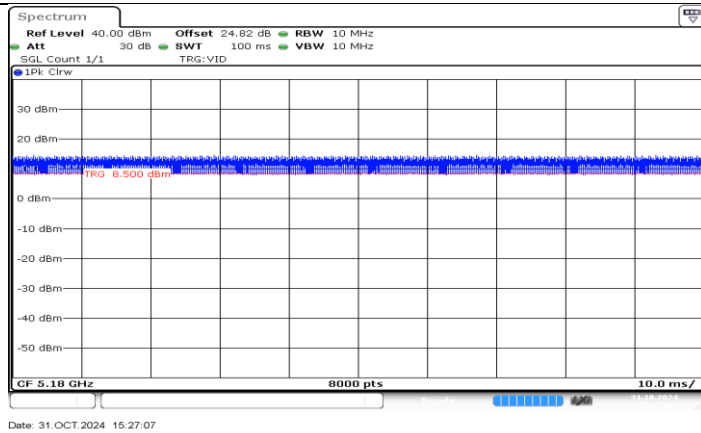
Where: T is On Time

If that calculated VBW is not available on the analyzer then the next higher value should be used.

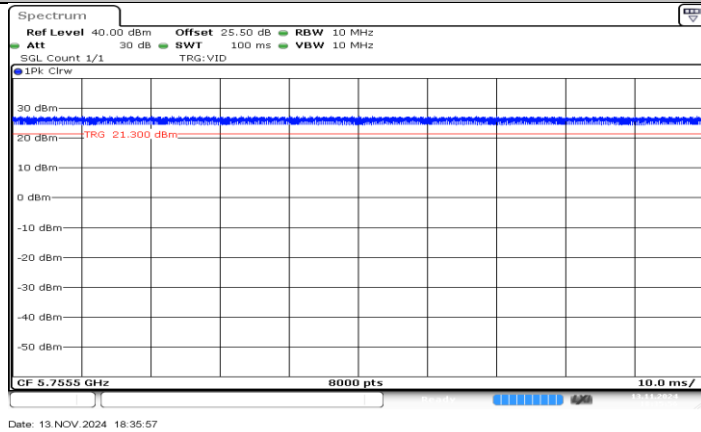
10.7.2. Test Graphs



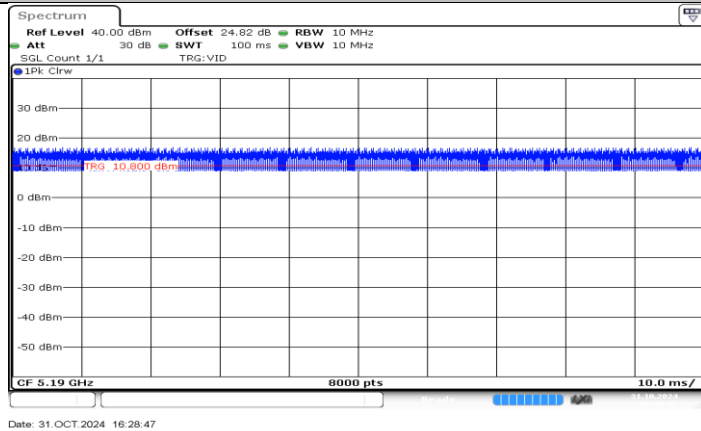




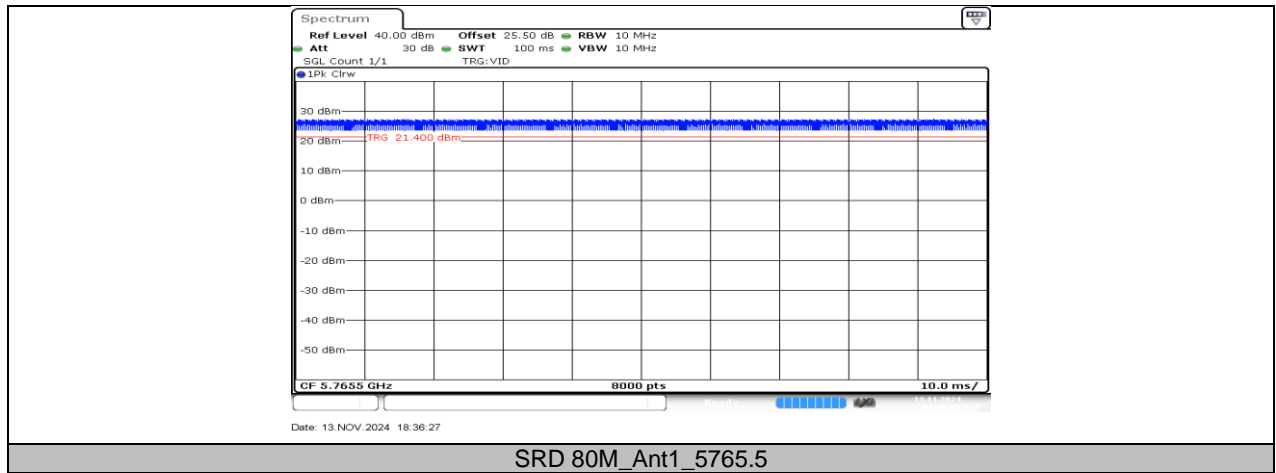
SRD 60M_Ant1_5180



SRD 60M_Ant1_5755.5



SRD 80M_Ant1_5190



END OF REPORT