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CherryTrail -T Title01_BLOCK DIAGRAM SizeCustomDocument NumberRevV1.0 Date:Monday, August 28, 2017Sheet1 of 38				BPAGE DRAWING		<table><tr><td>BOM_RELEASE_DATE</td><td><BOM_RELEASE_DATE></td><td colspan="2">PB_NUMBERH80659-001</td></tr><tr><td>SIGNATURE</td><td>DATE</td><td colspan="2">INTEL CORPORATION2200 MISSION BLVD SANTA CLARA, CA 95054</td></tr><tr><td>DRN_BY<DRN_BY></td><td><DRN_BY_DATE></td><td colspan="2">TITLE</td></tr><tr><td>CHK_BY<CHK_BY></td><td><CHK_BY_DATE></td><td colspan="2">CHT-CR_MRD</td></tr><tr><td>ENGR_APVD<ENGR_APVD></td><td><ENGR_APVD_DATE></td><td colspan="2"></td></tr><tr><td>VARIANT:<VARIANT></td><td></td><td>INTEL CONFIDENTIAL</td><td>DOCUMENT_NUMBER<DOCUMENT_NUMBER></td></tr><tr><td colspan="2"></td><td></td><td>REV Op9</td></tr><tr><td colspan="2"></td><td></td><td>PAGE 1/38</td></tr></table>		BOM_RELEASE_DATE	<BOM_RELEASE_DATE>	PB_NUMBERH80659-001		SIGNATURE	DATE	INTEL CORPORATION2200 MISSION BLVD SANTA CLARA, CA 95054		DRN_BY<DRN_BY>	<DRN_BY_DATE>	TITLE		CHK_BY<CHK_BY>	<CHK_BY_DATE>	CHT-CR_MRD		ENGR_APVD<ENGR_APVD>	<ENGR_APVD_DATE>			VARIANT:<VARIANT>		INTEL CONFIDENTIAL	DOCUMENT_NUMBER<DOCUMENT_NUMBER>				REV Op9				PAGE 1/38	1
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CherryTrail -T

Title

02_BLOCK DIAGRAM

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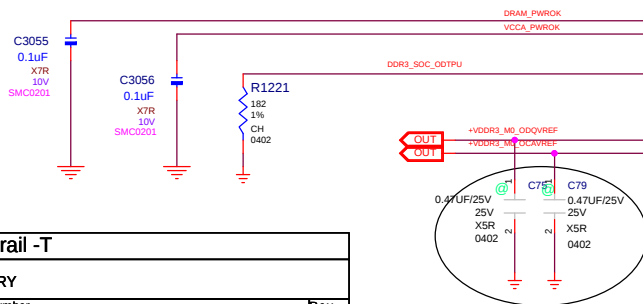
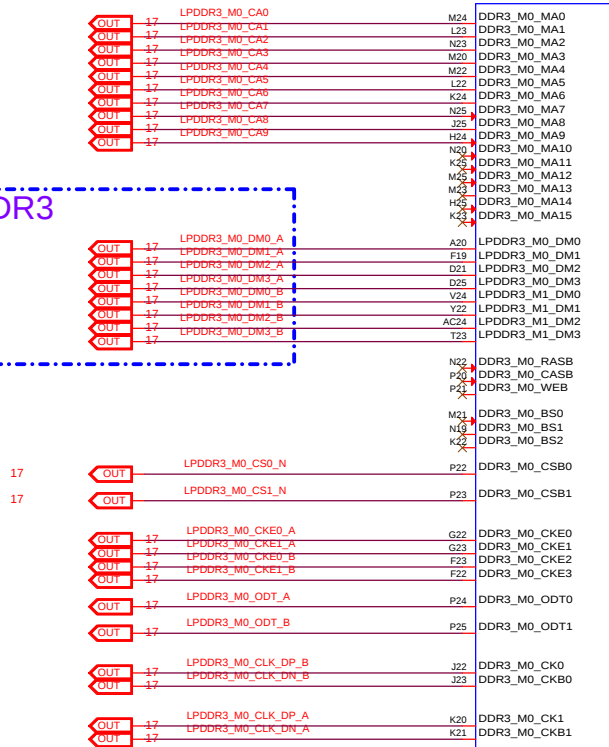
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5	SOC : CAMERA & DISPLAY
6	SOC: STORAGE AUDIO & SATA
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8	SOC : SPI UART GPIO MISC
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BPAGE DRAWING

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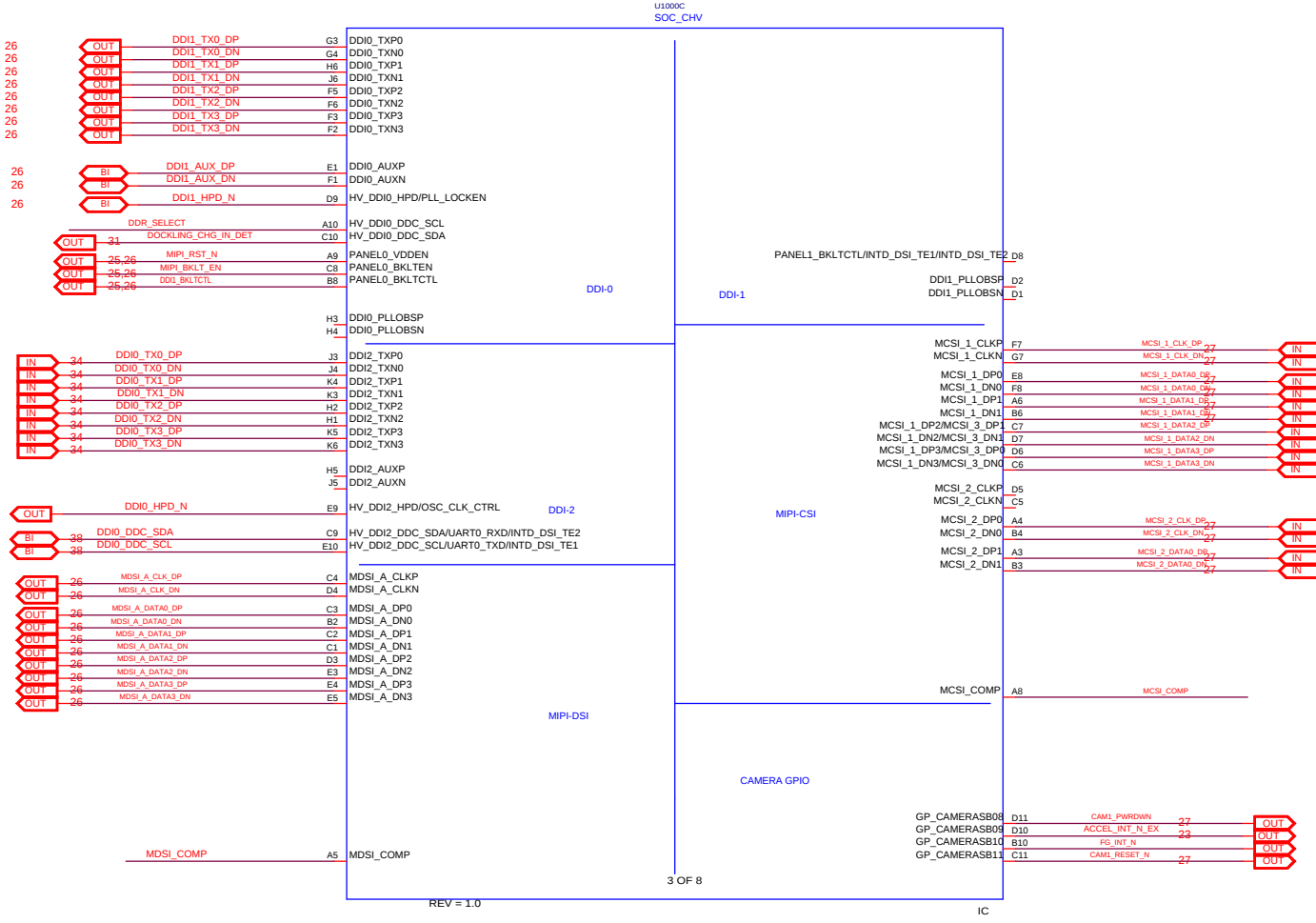
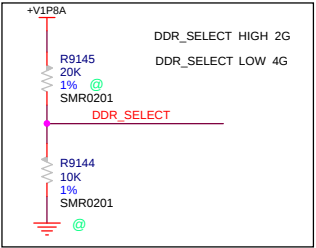


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SOC:CAMERA & DISPLAY

EDP SIGNALS

TP'S TO BE PLACED NEAR DISP CONNECTOR



HDMI SIGNALS

TP'S TO BE PLACED NEAR DISP CONNECTOR

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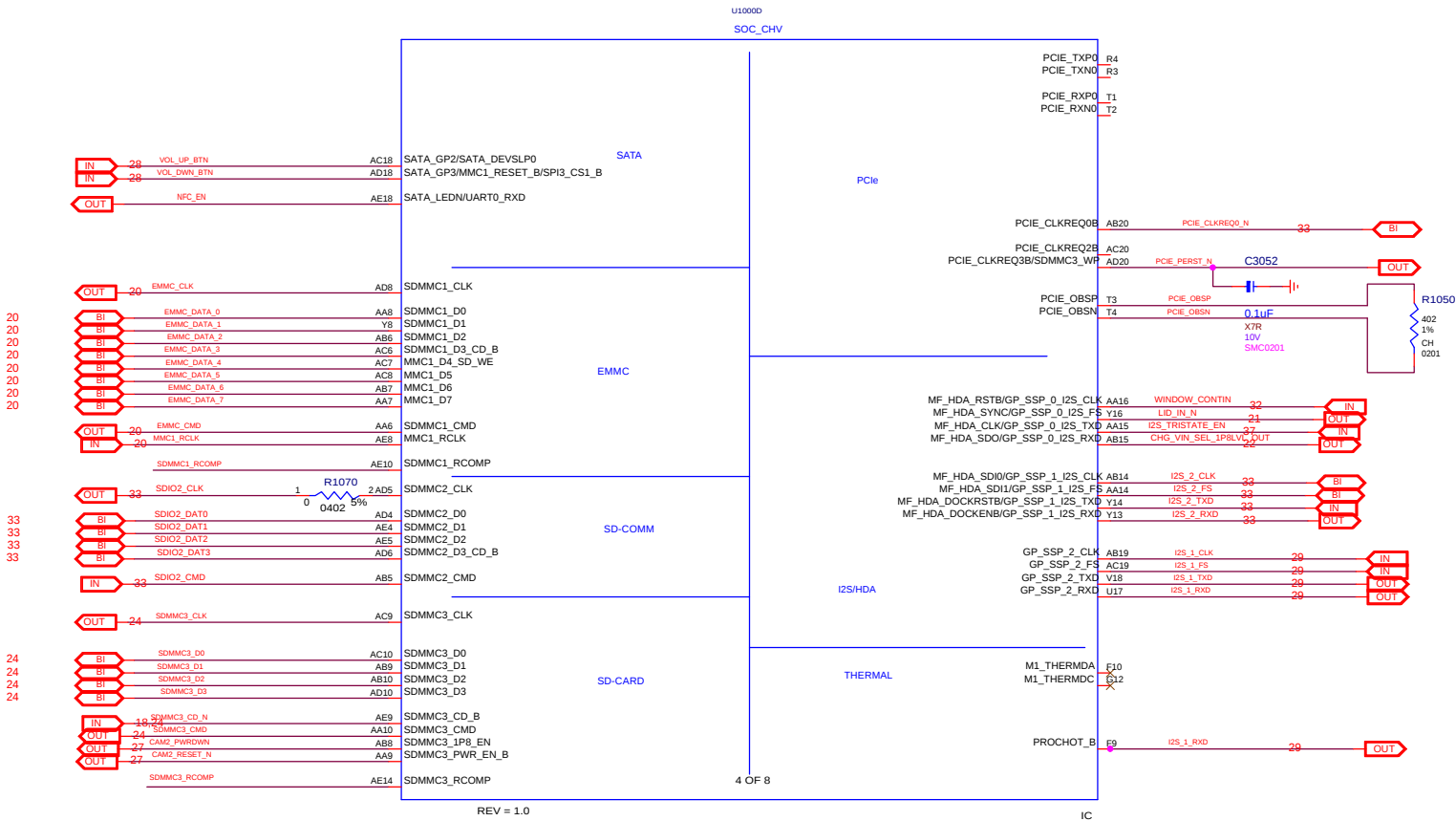
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SOC DISPLAY CAMERA

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SOC:STORAGE,AUDIO & SATA



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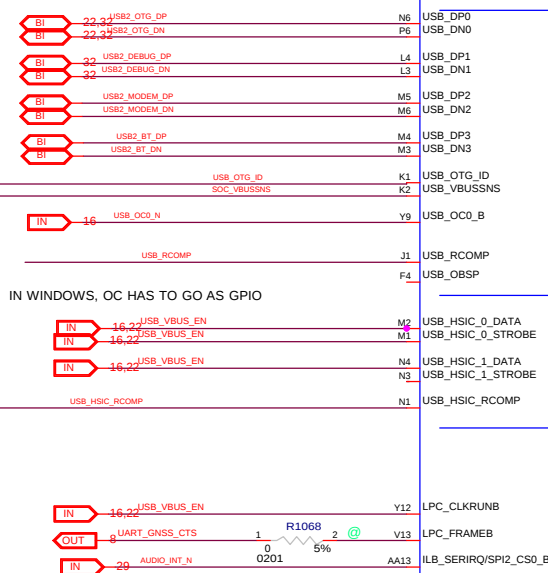
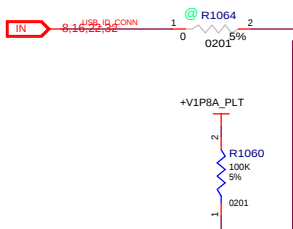
SOC STORAGE I2S

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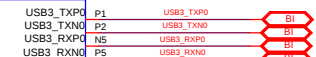
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TEST POINTS AND COMPONENTS
ON SOC PAGES SHOULD BE PLACED CLOSE TO RELEVANT COMPONENTS

VBUS_DIV AND OTG
MIGHT NOT BE SUPPORTED

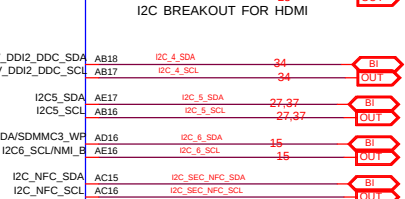
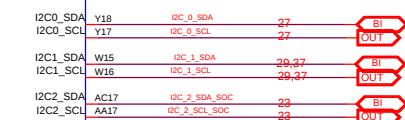
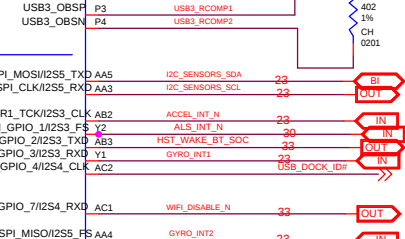


REV = 1.0

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CHANGE REFDES



I2C MAPPING TABLE
I2C_0 - CAMERA
I2C_1 - AUDIO,DEBUG,CHARGER,FG
I2C_2 - SENSORS
I2C_4 - NFC AND HDMI
I2C_5 - TOUCH
I2C_6 - PMIC

SOC USB I2C GPIO

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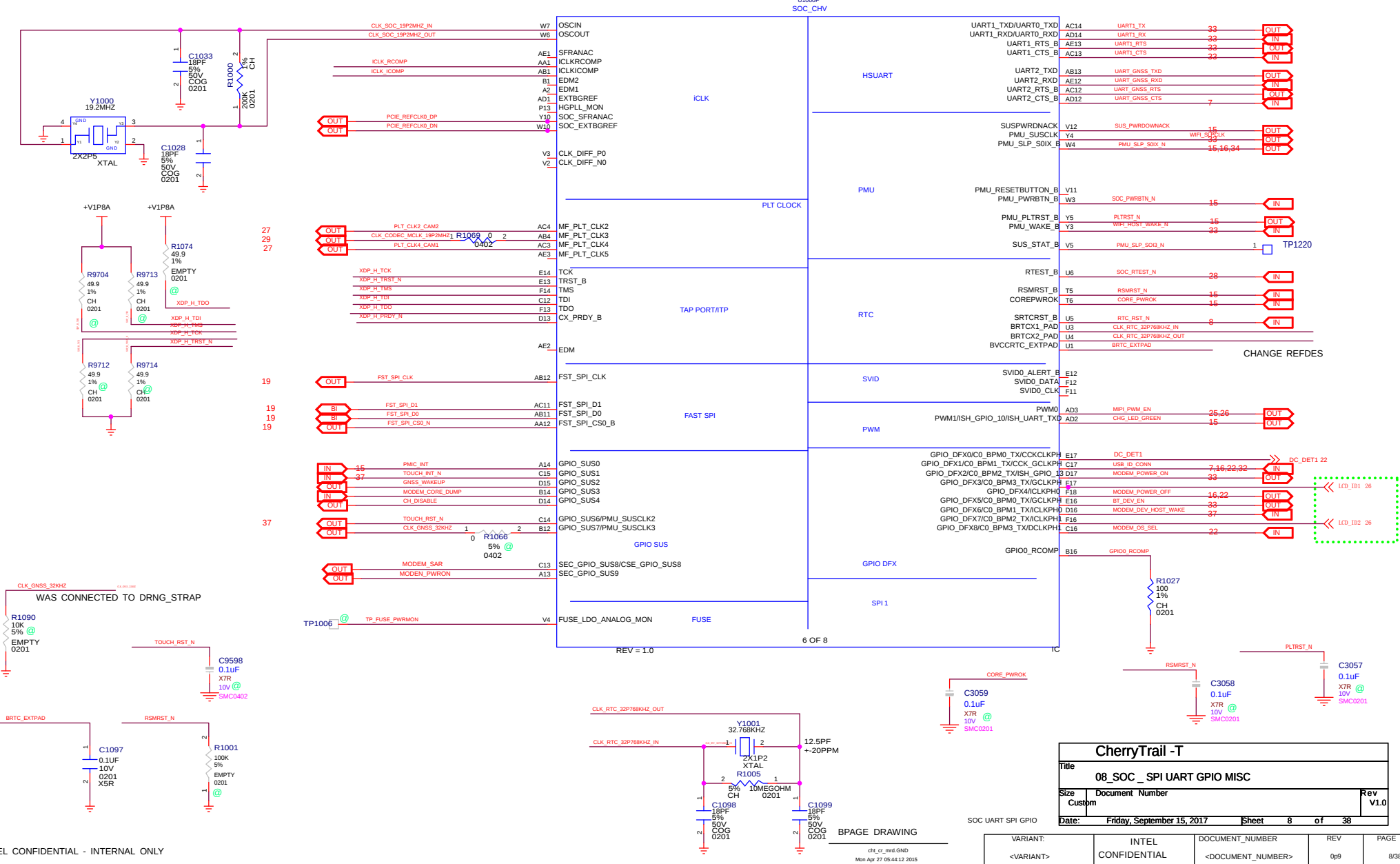
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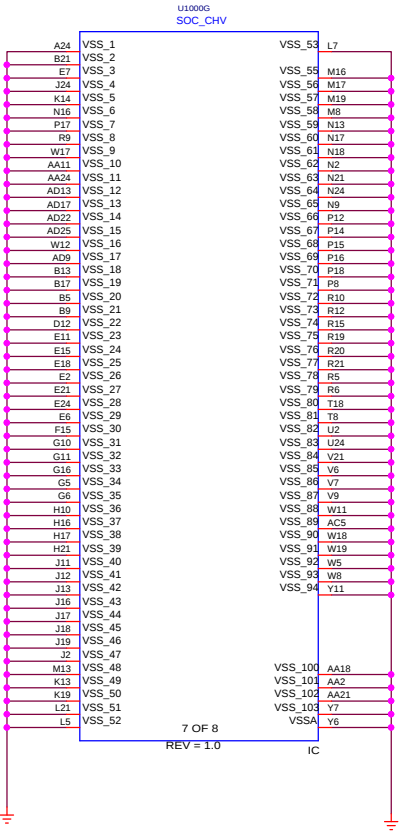
SOC:SPI,UART,GPIO,MISC



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SOC: GND



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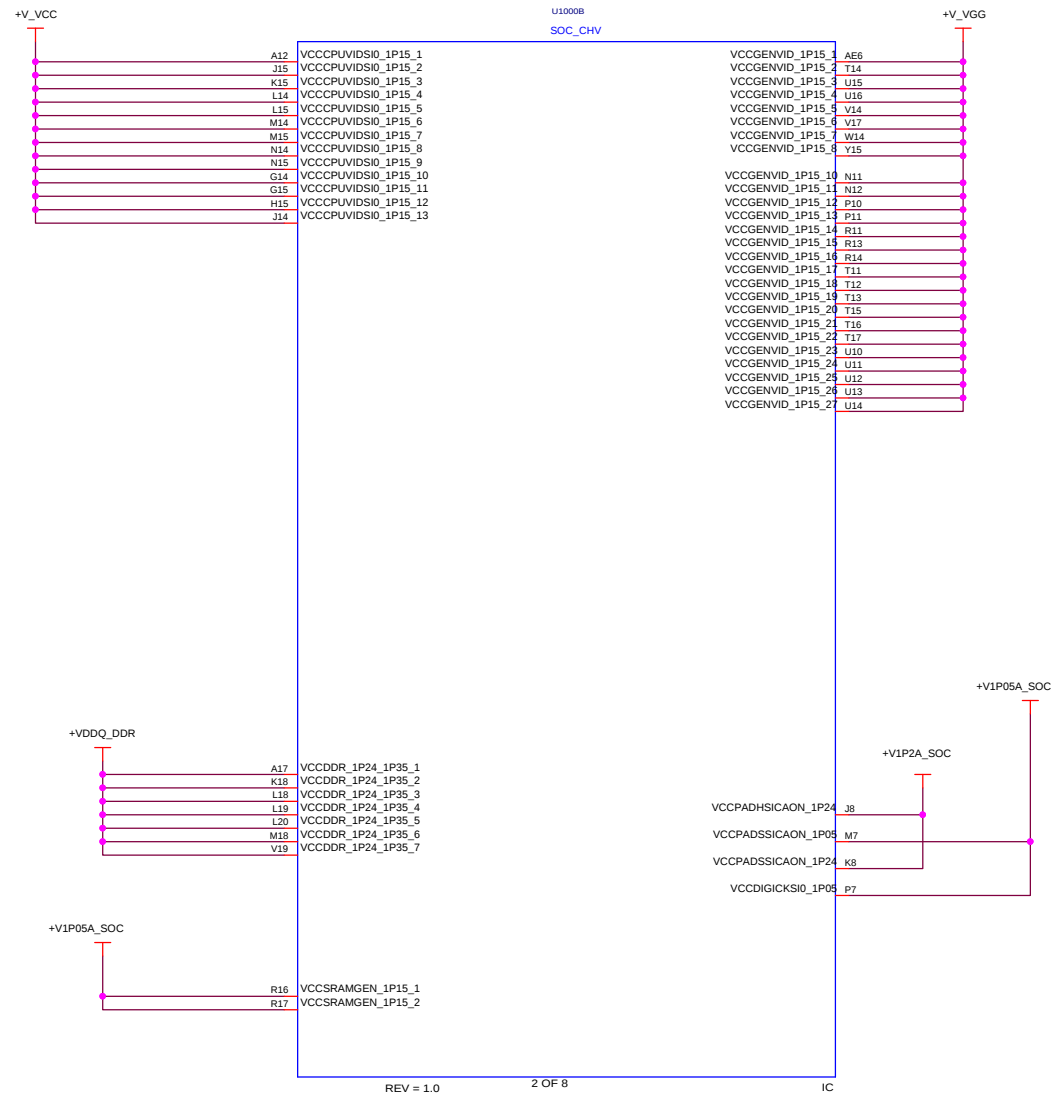
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SOC GND

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SOC: POWER1



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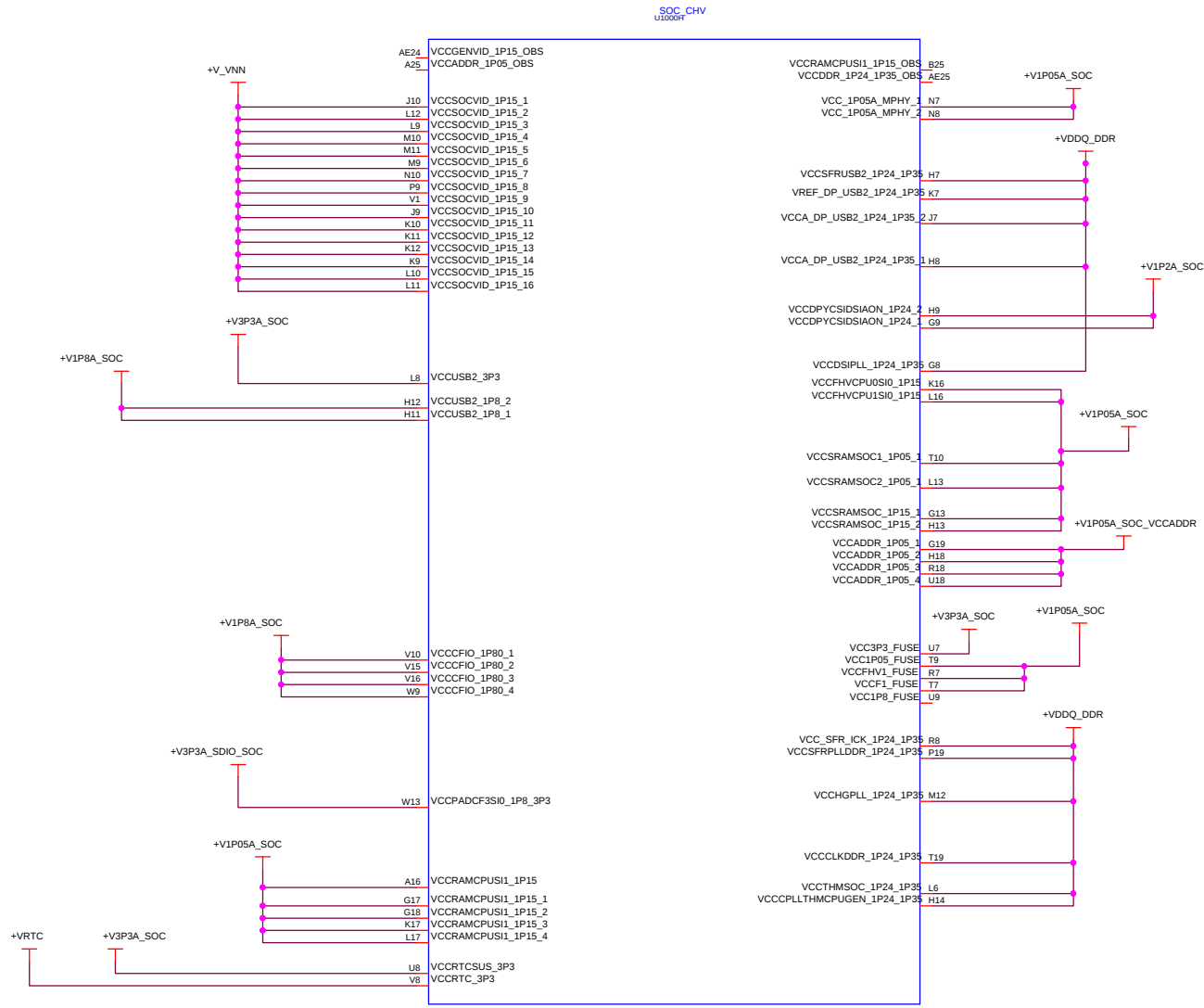
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SOC POWER

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SOC: POWER2



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SOC POWER2

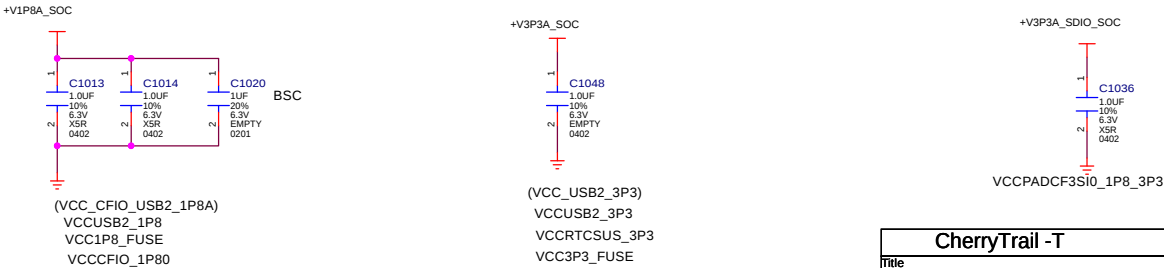
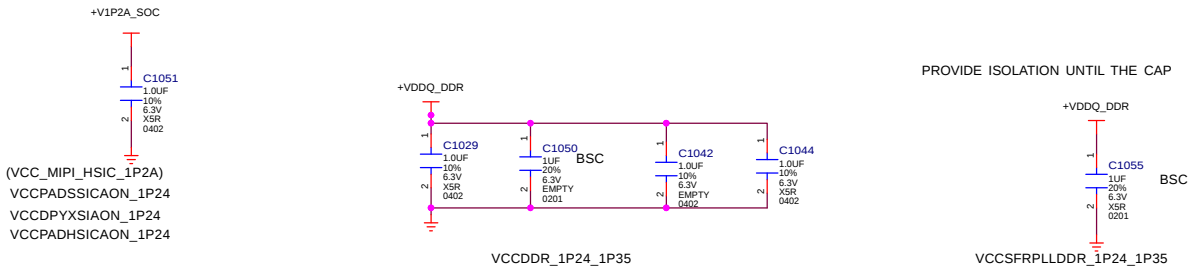
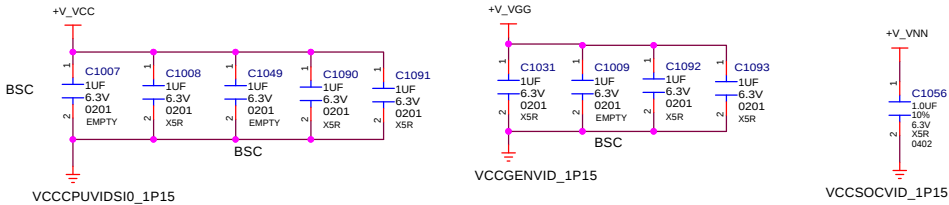
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SOC:DECOUPLING



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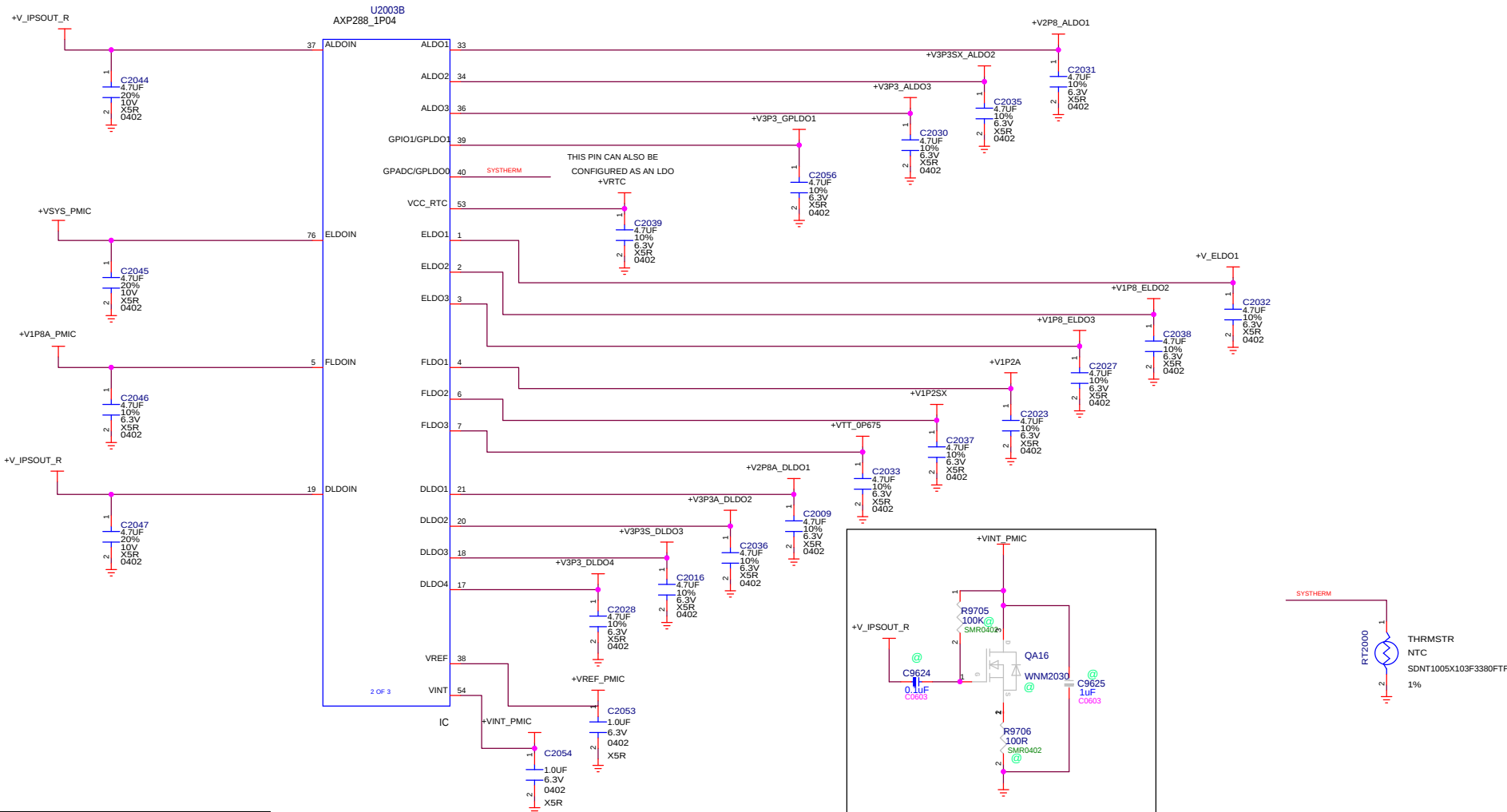
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PMIC: LDOS



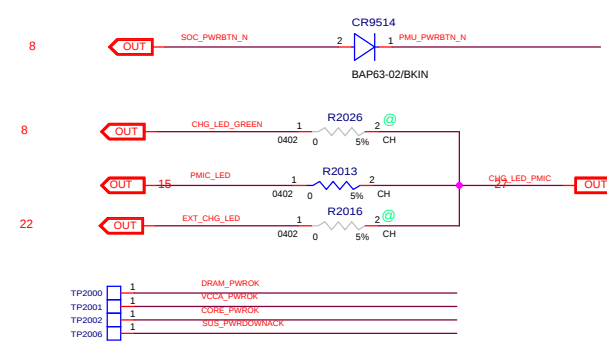
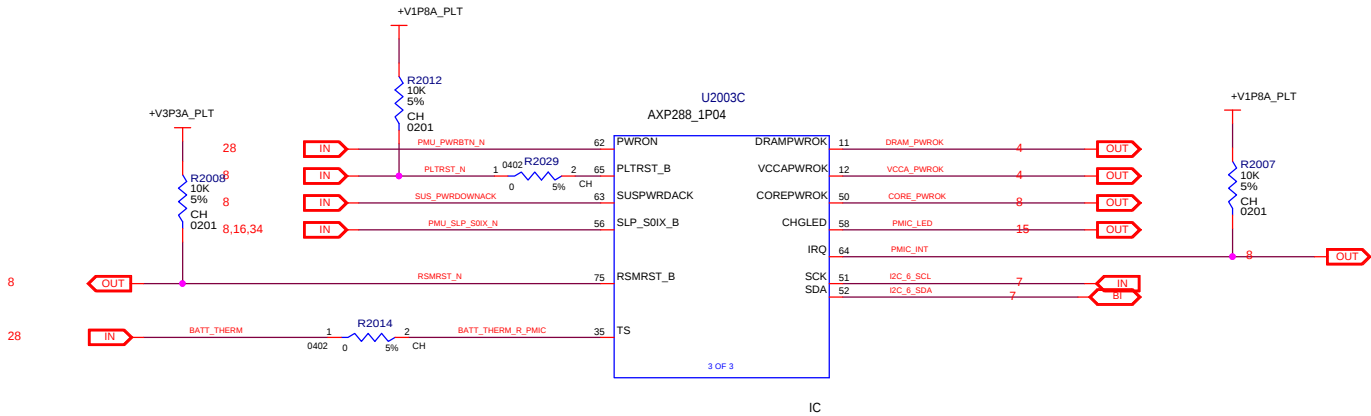
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PMIC: HOST & MISC INTERFACES



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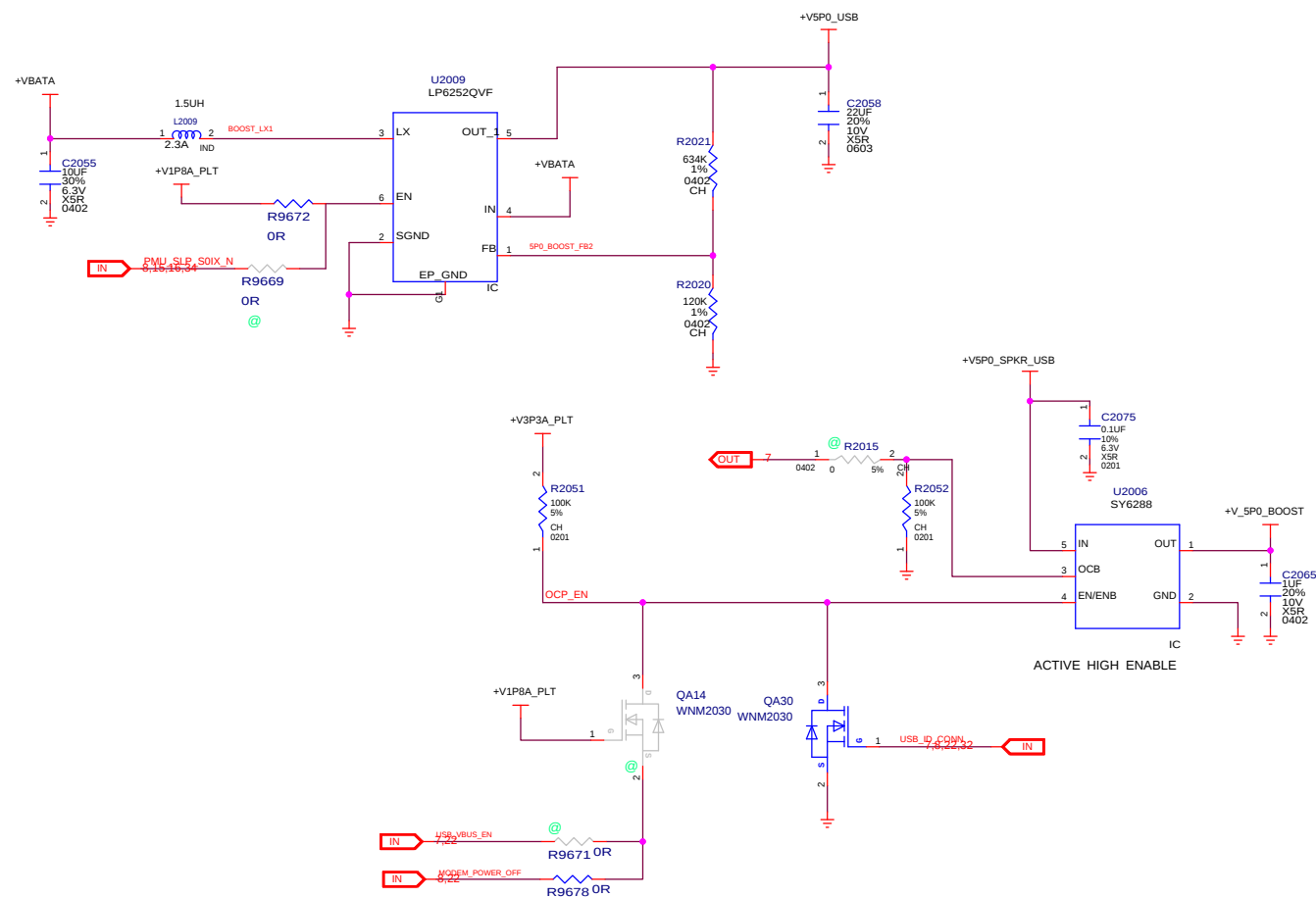
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PMIC: EXTERNAL REGULATORS

5V BOOST USB IN USB HOST MODE

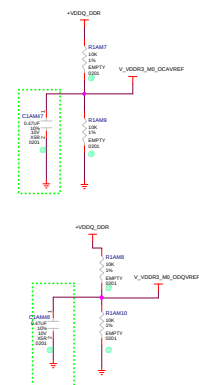
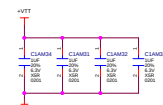
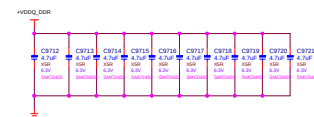
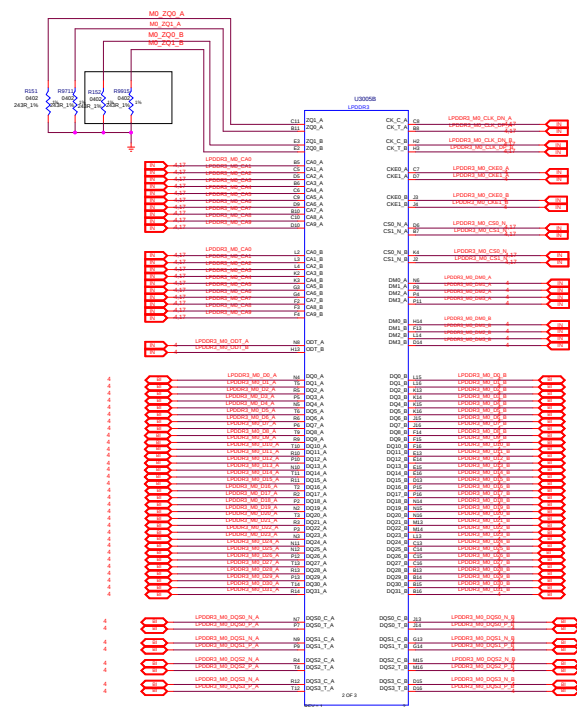


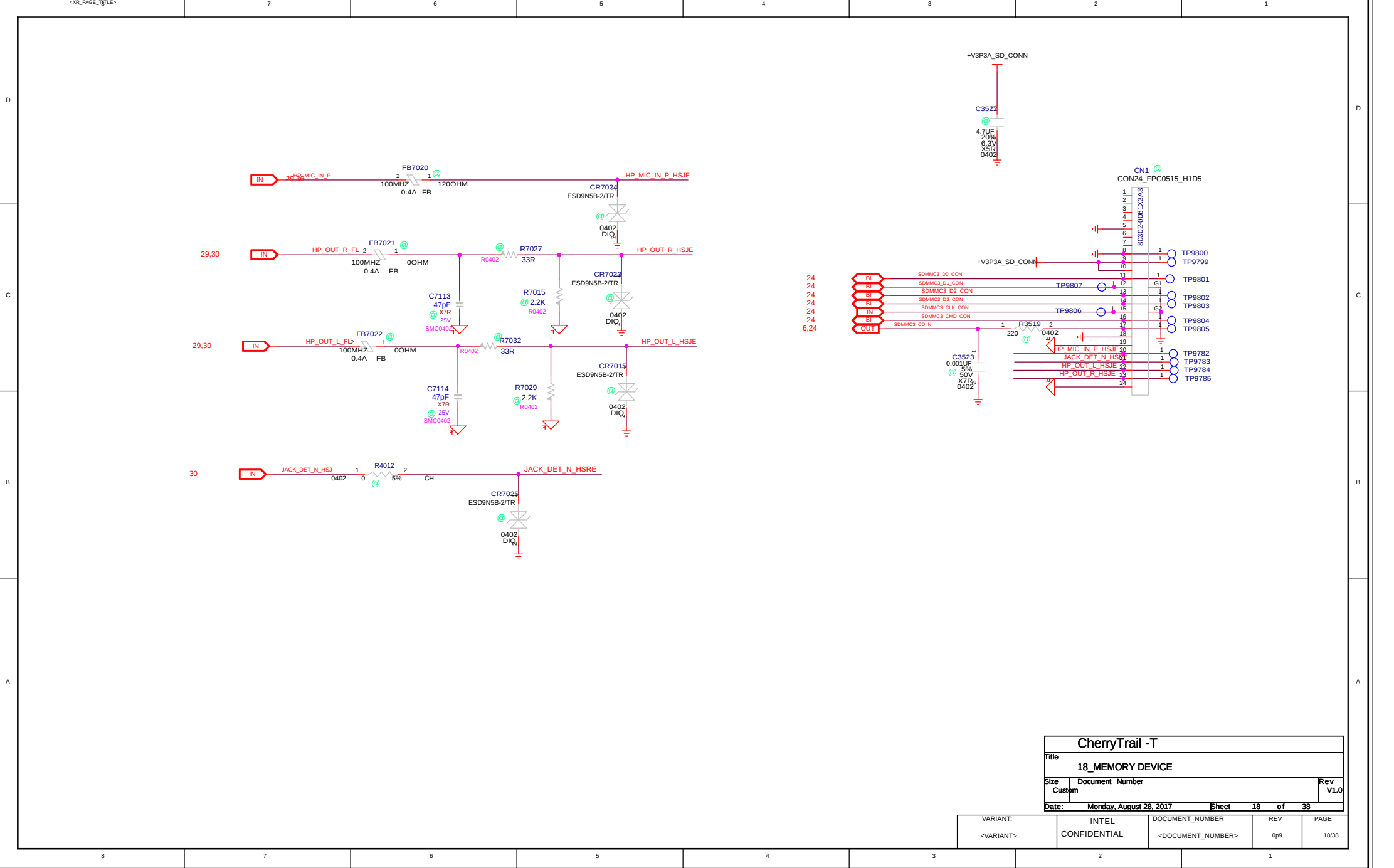
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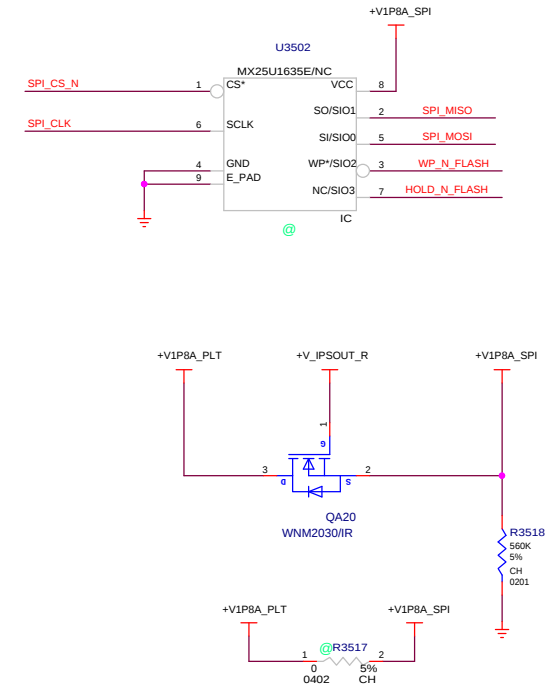
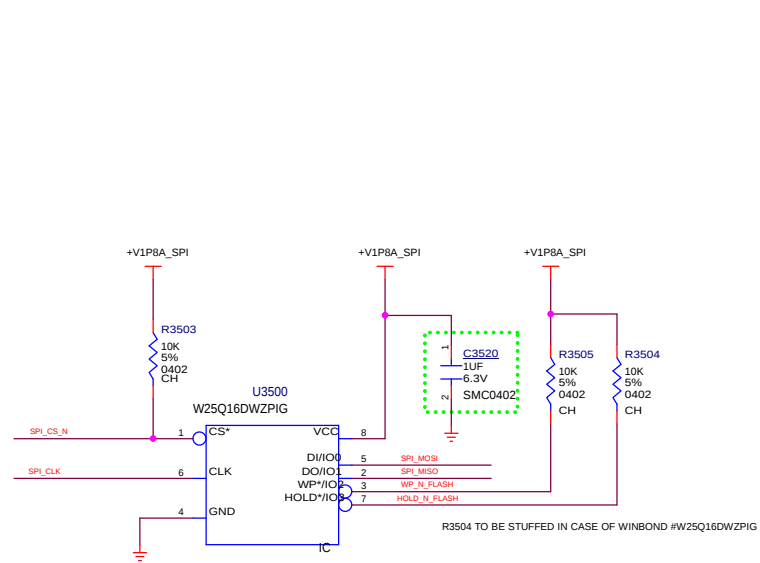




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SPI NOR FLASH



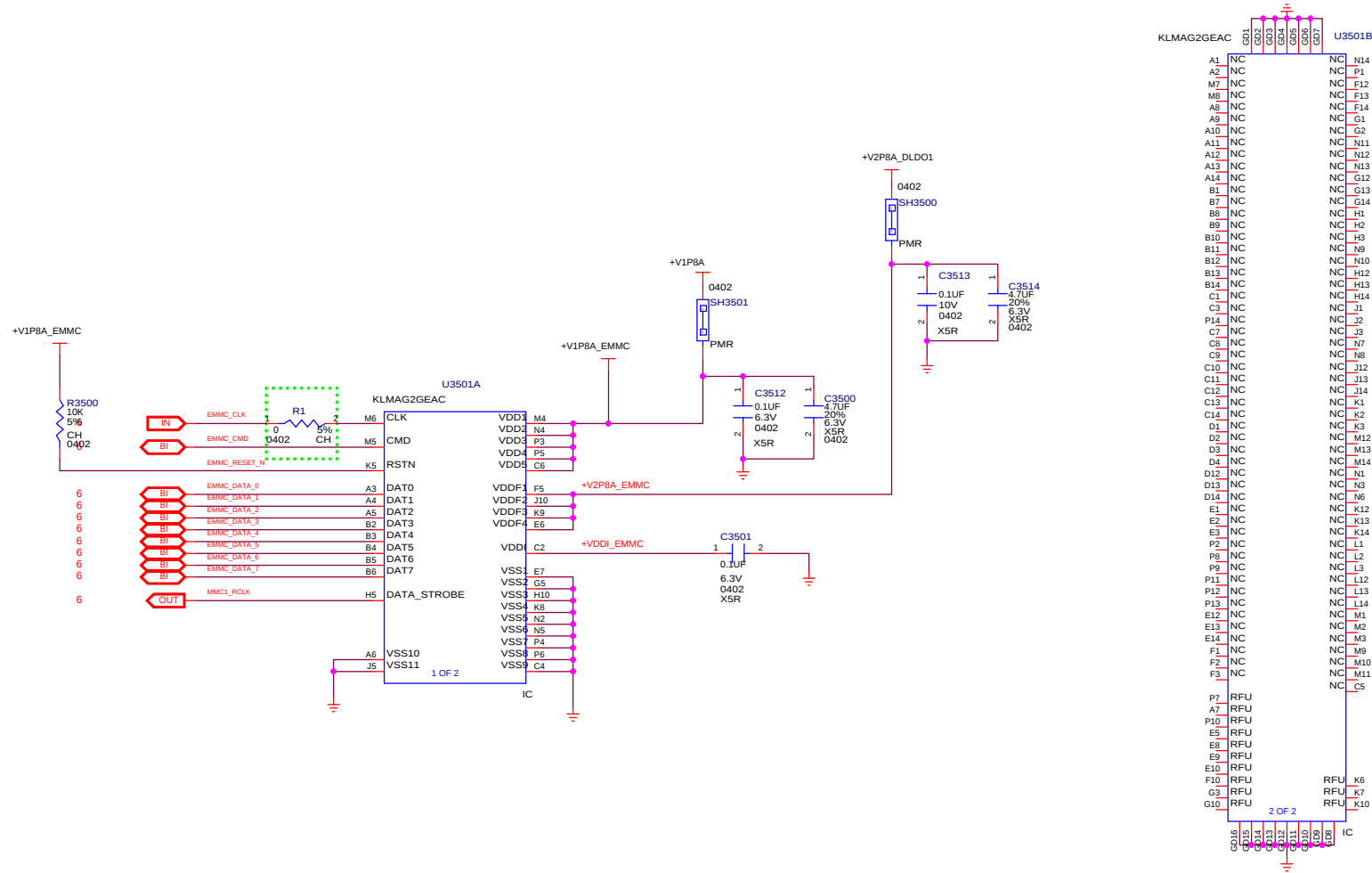
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EMMC



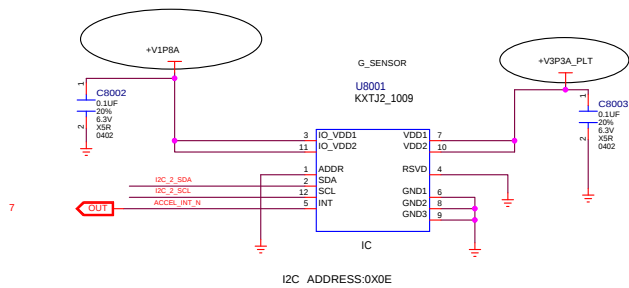
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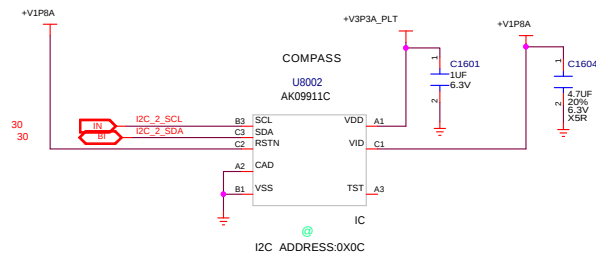
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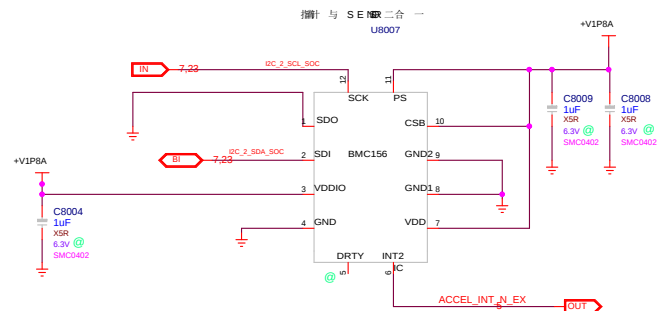
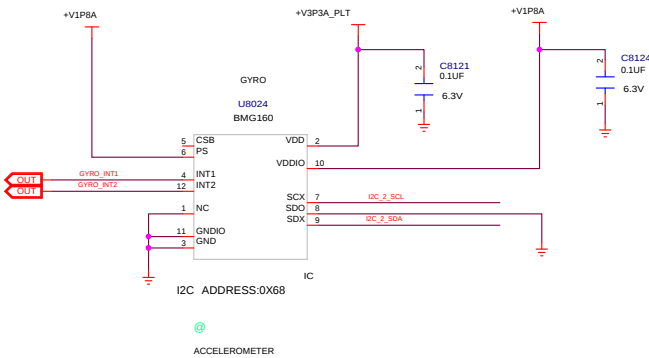
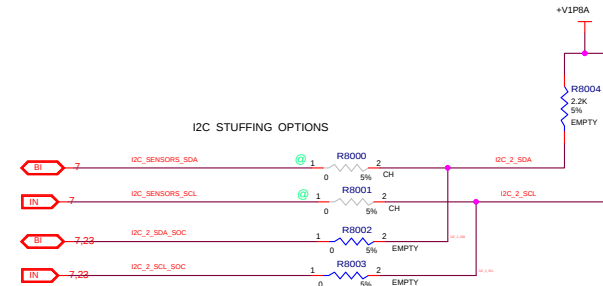
SENSORS



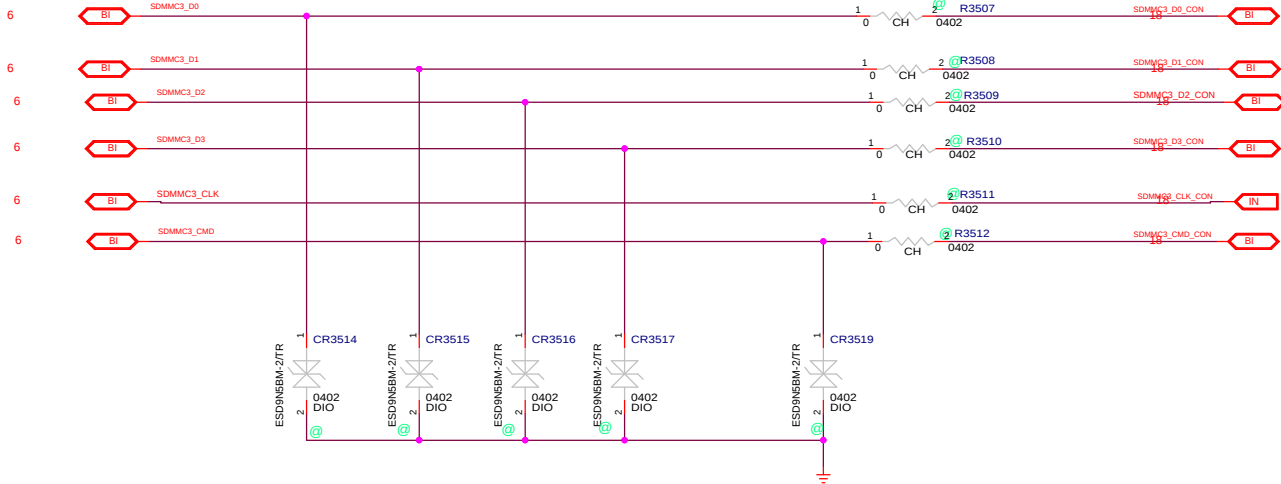
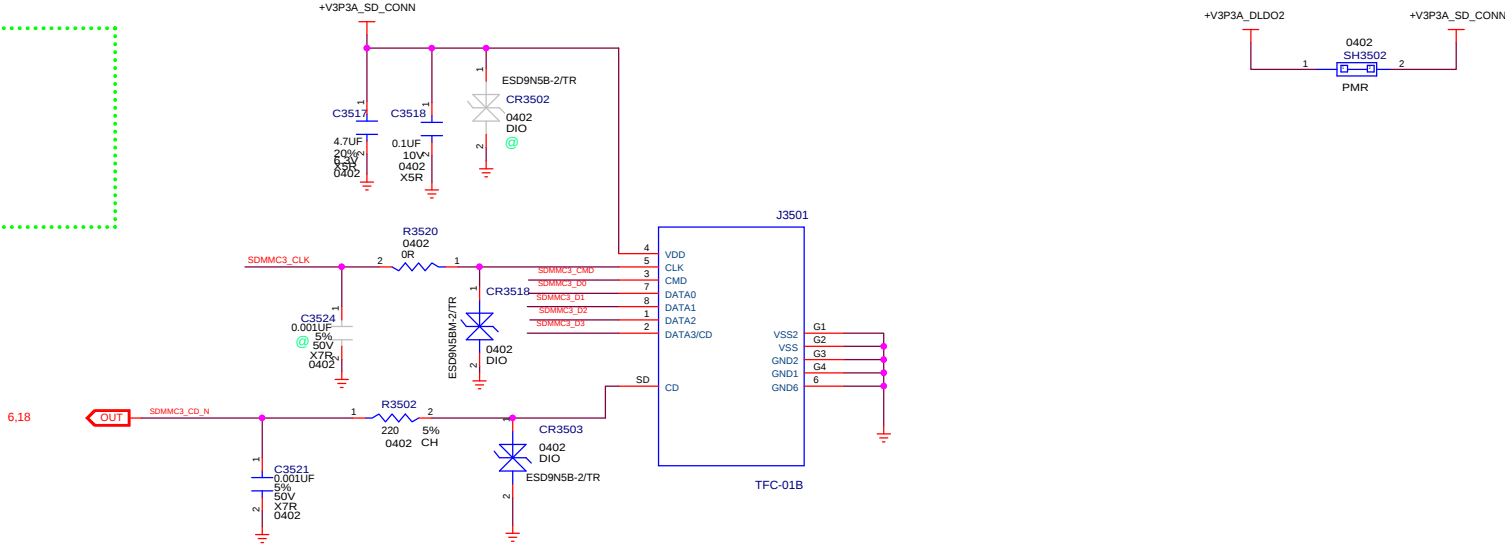
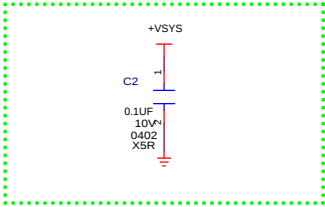
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I2C STUFFING OPTIONS



MICRO-SD CONNECTOR



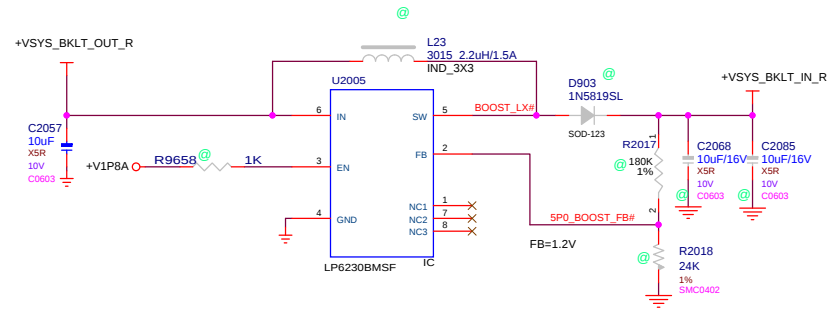
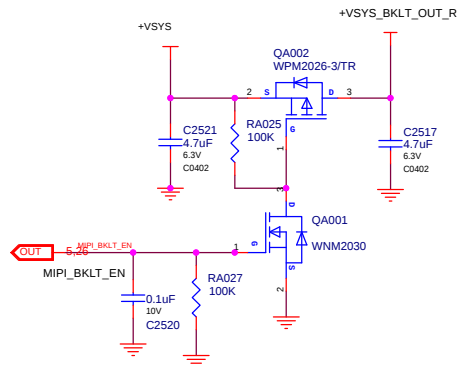
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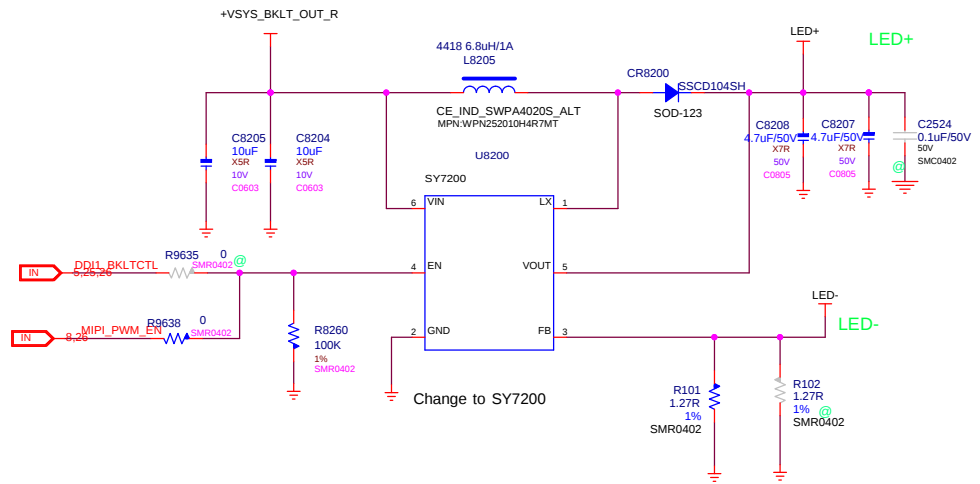
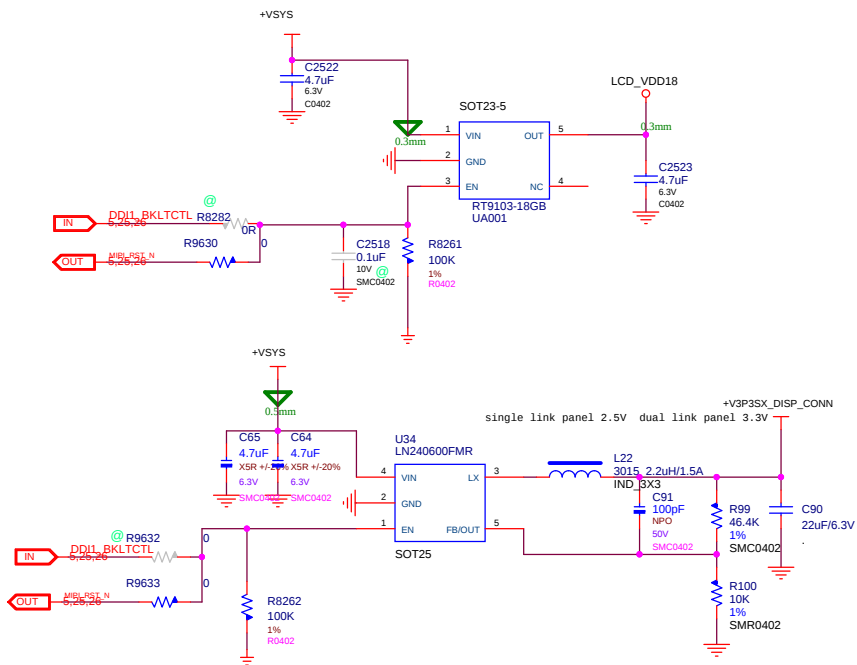
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DISPLAY: BACKLIGHT CONTROLLER



更换背光电路



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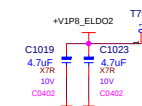
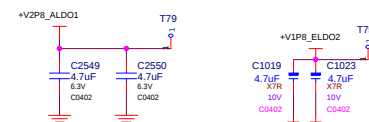
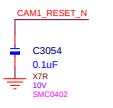
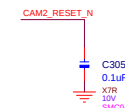
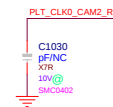
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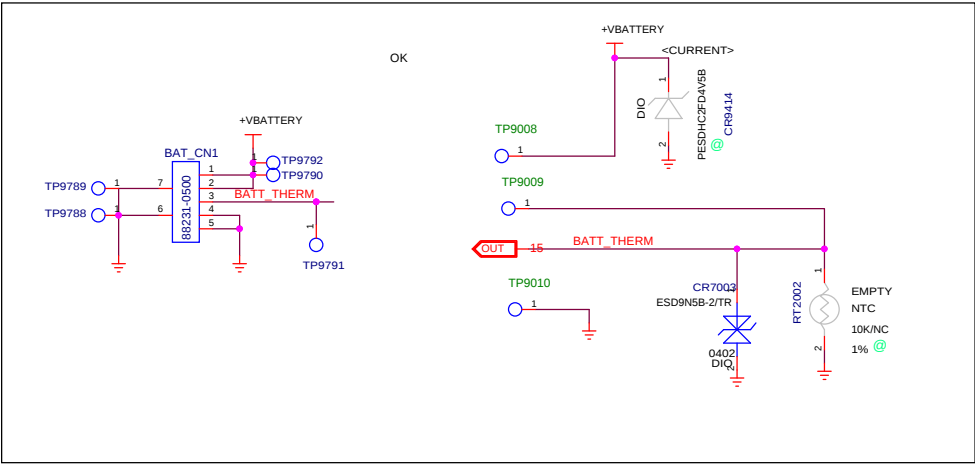
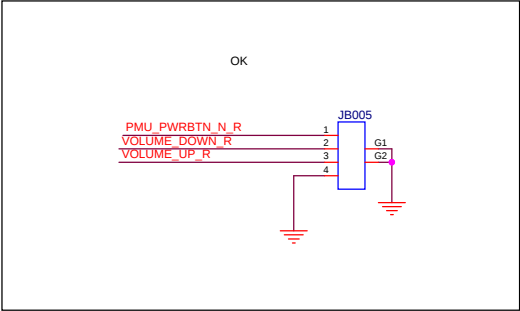
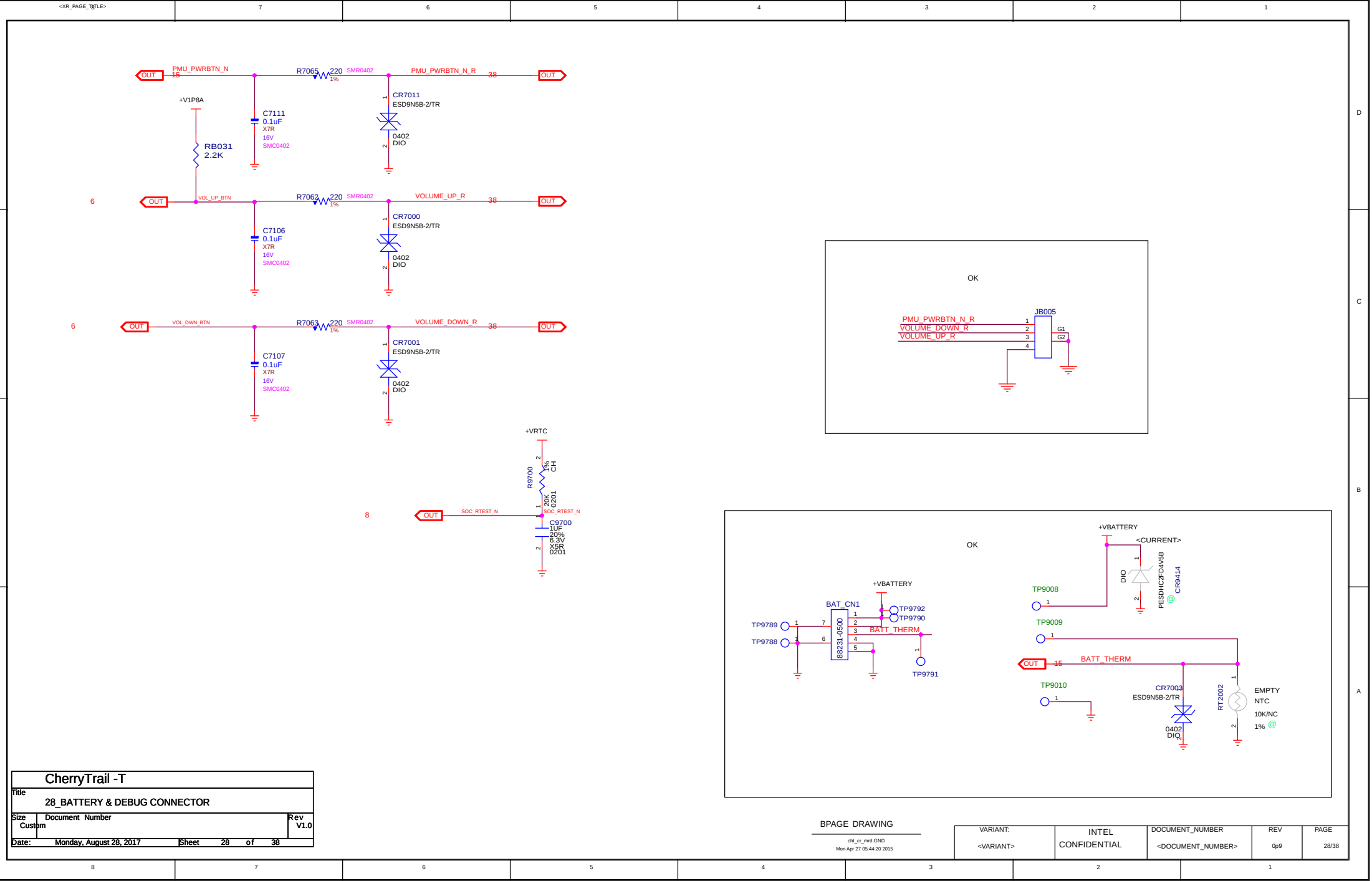
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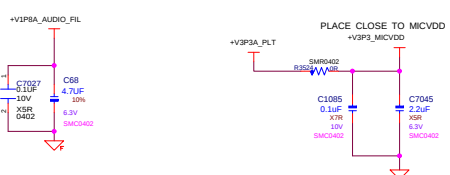
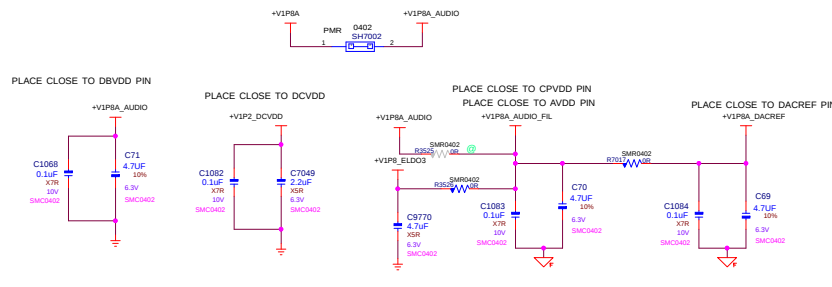
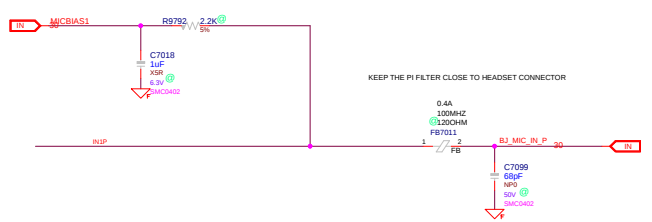
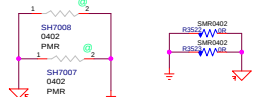
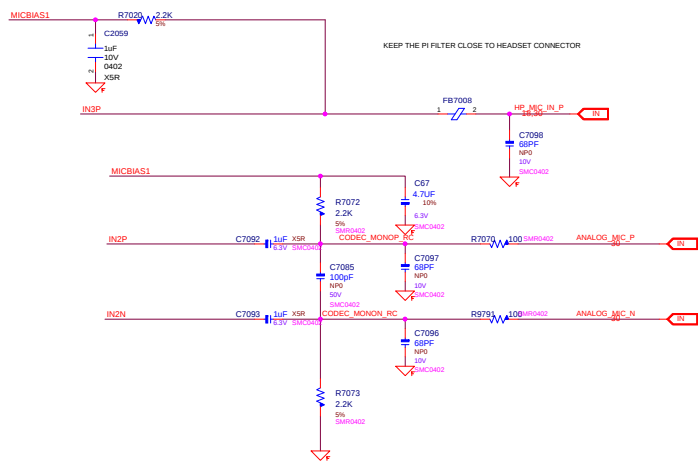
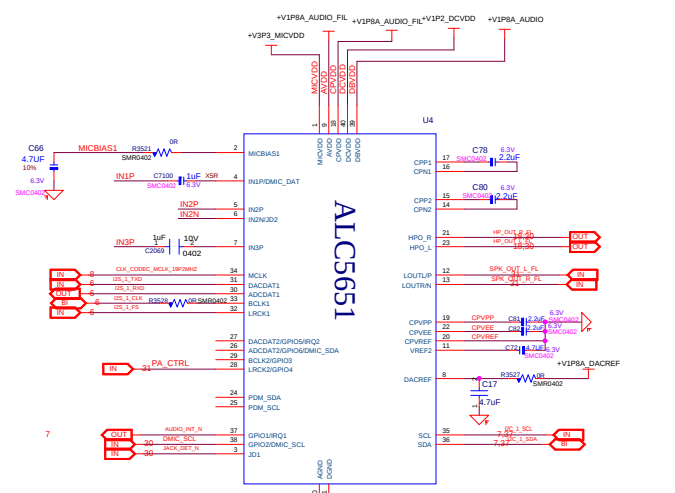
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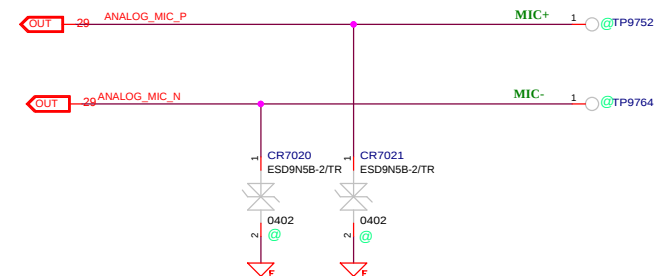
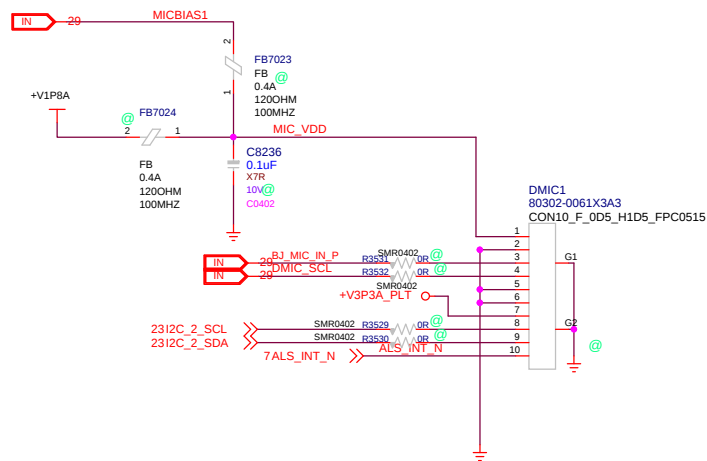
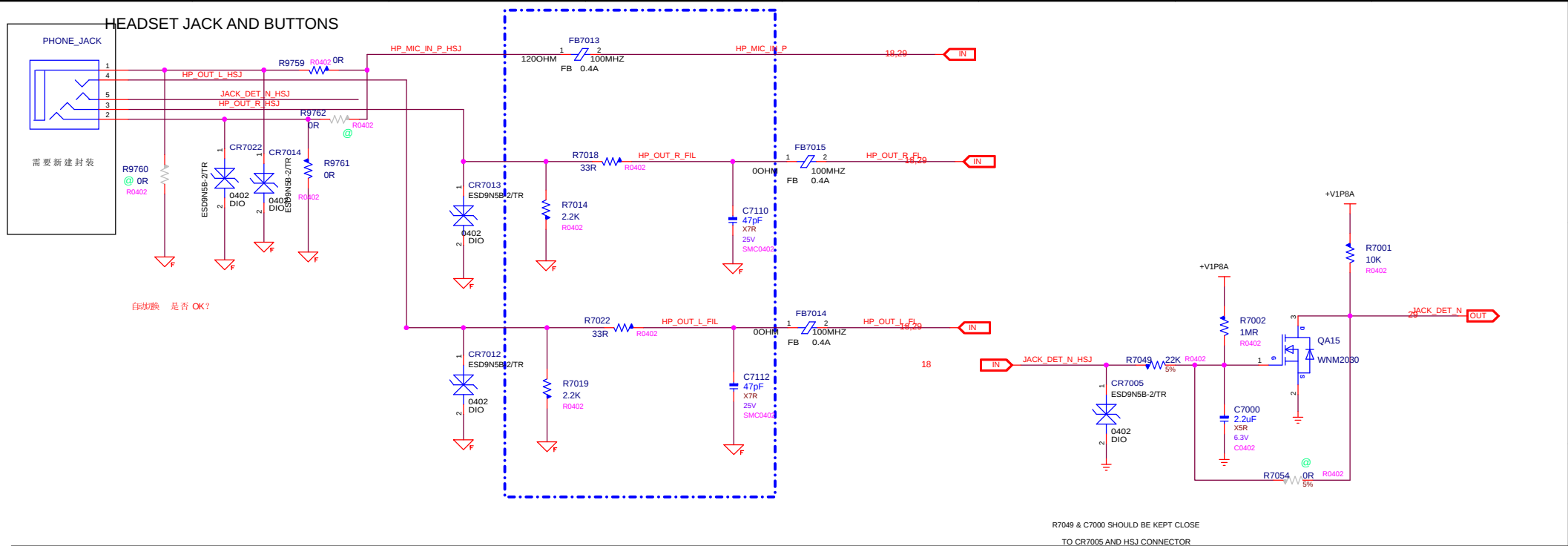
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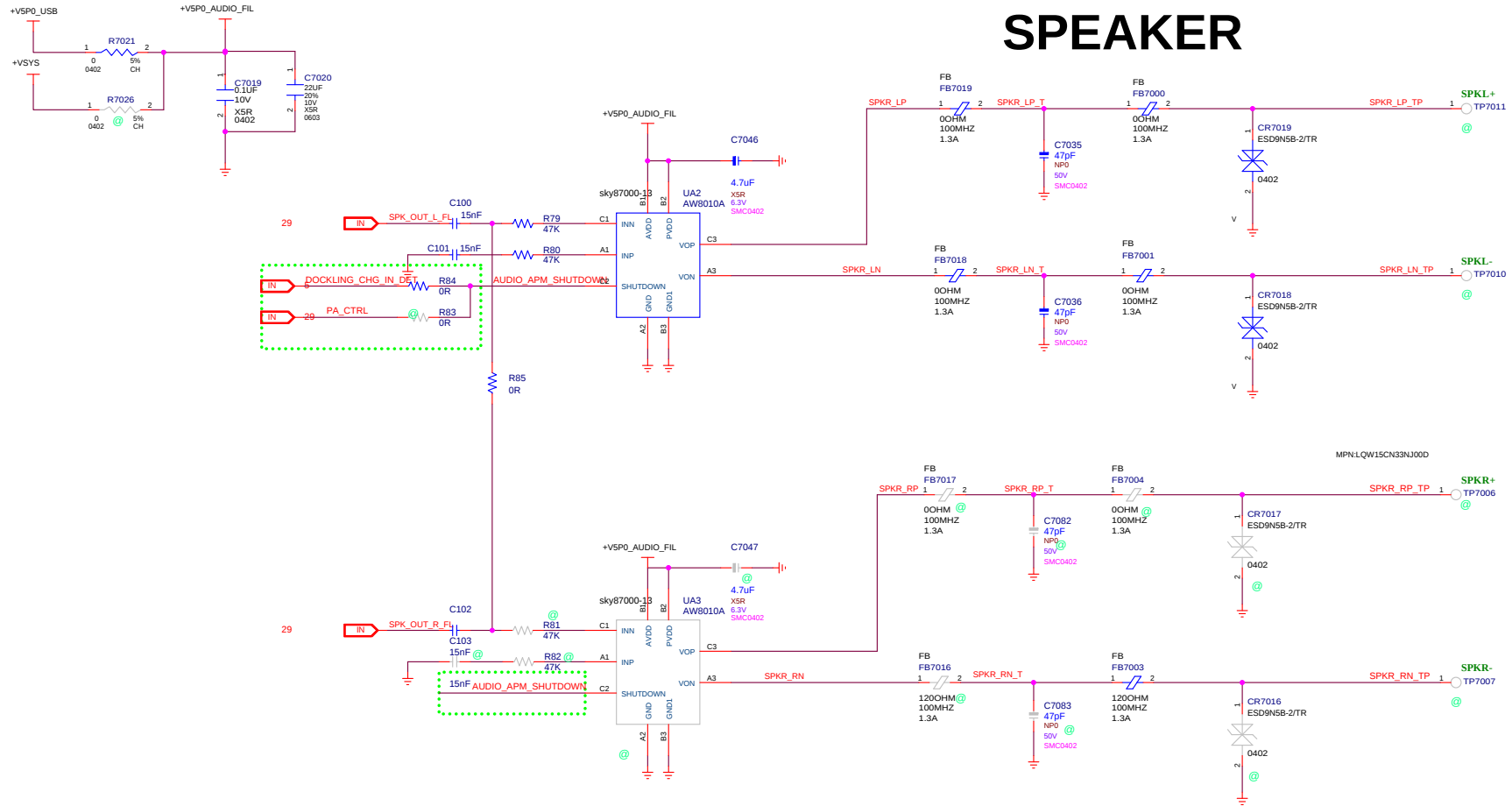
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PLACE CLOSE TO SPKVDDR AND SPKDDL

SPEAKER

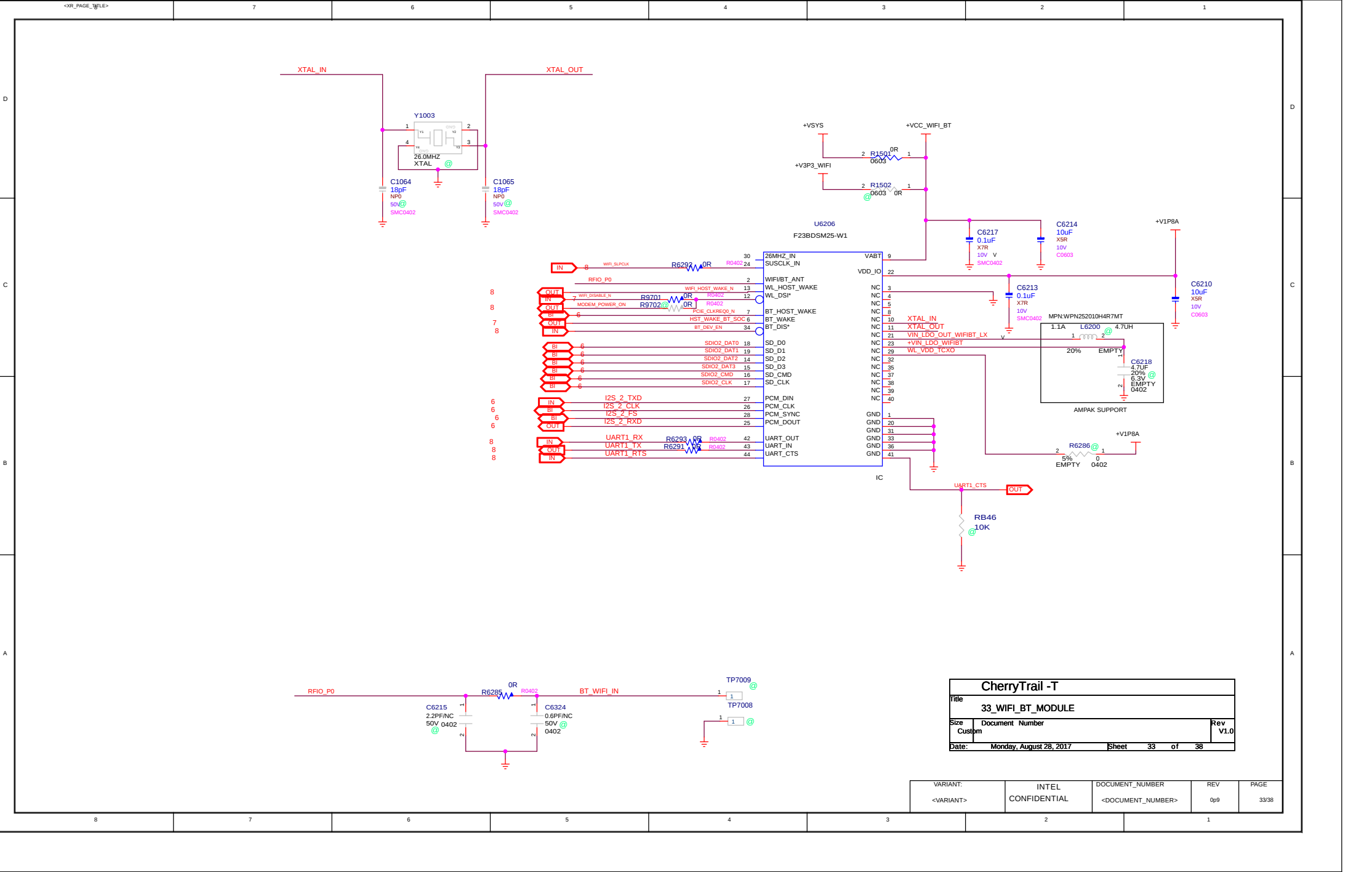


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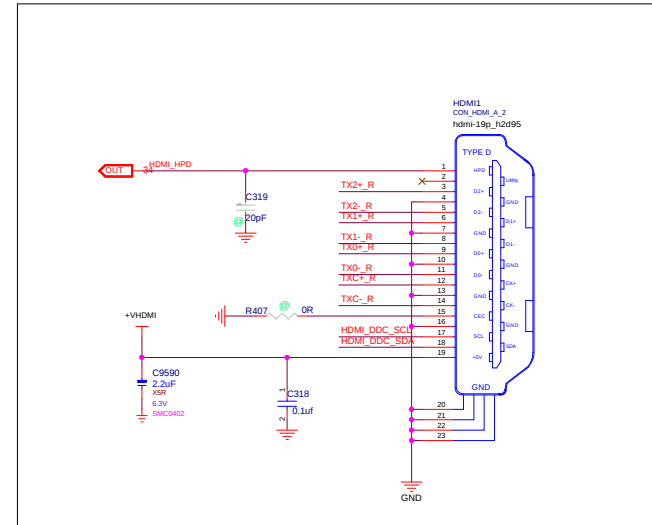
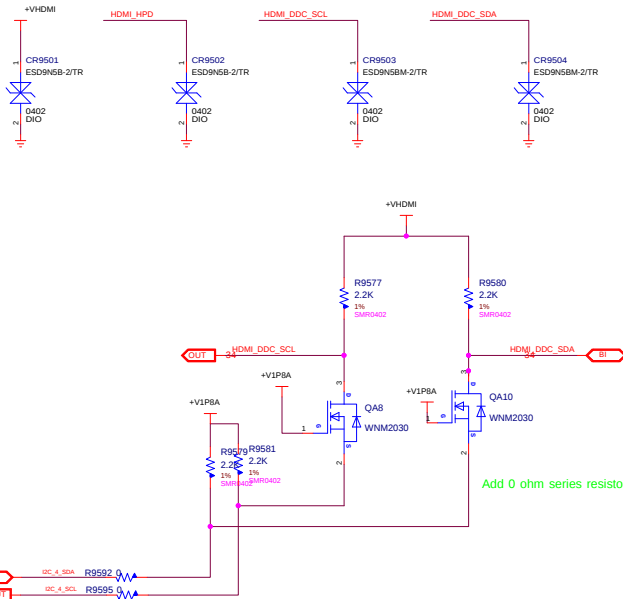
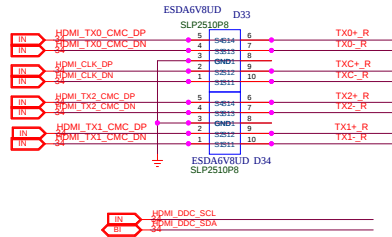
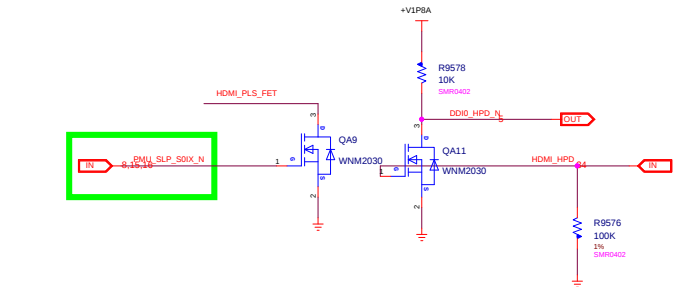
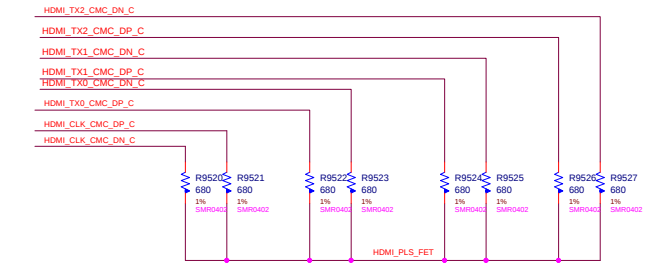
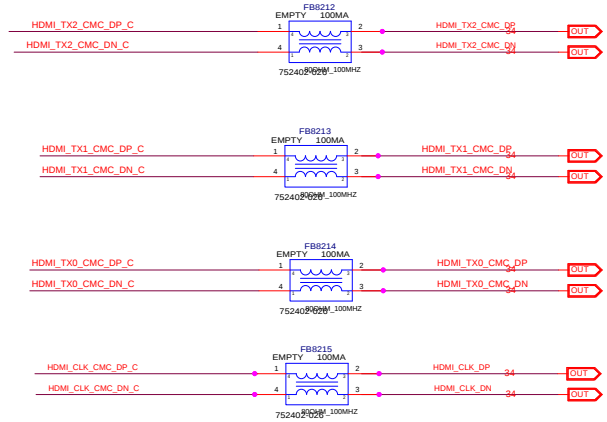
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+VSIM1_VAR POWER FROM MODEM
+VCC_SIM GOING TO SIM CARD CONN

HDMI LEVEL SHIFTERS & CONNECTOR



Add 0 ohm series resistors by Momu in 2014.4.24

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35_3G MODEM				
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CAD NOTE

OVERLAP R1201 PAD 2 AND R1200 PAD 1

CherryTrail -T

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36_GPS

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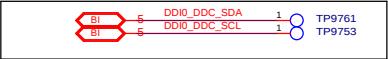
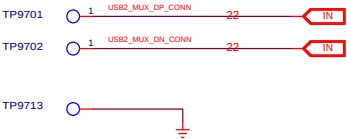
MFG TEST POINTS, SHIELDS & MOUNTING HOLES

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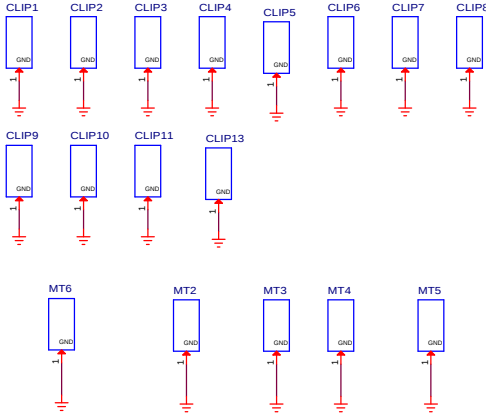
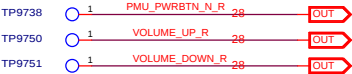
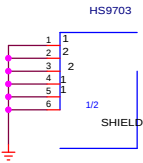
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38_MFG TP'S, SHIELDS & MOUNTING			
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