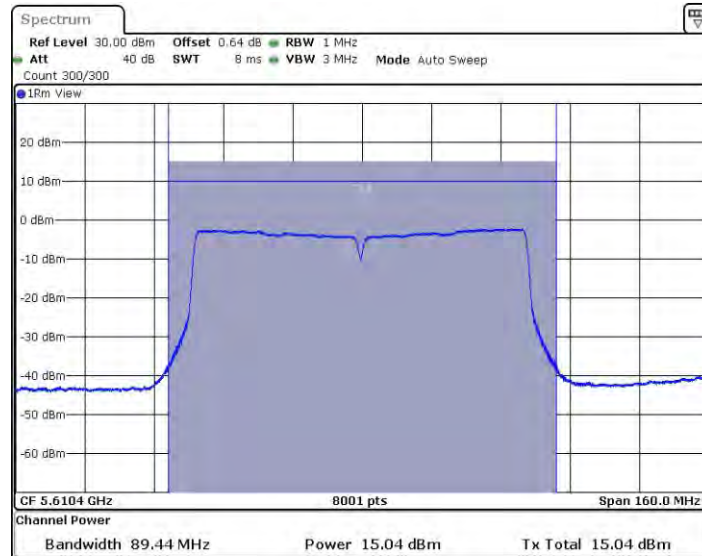
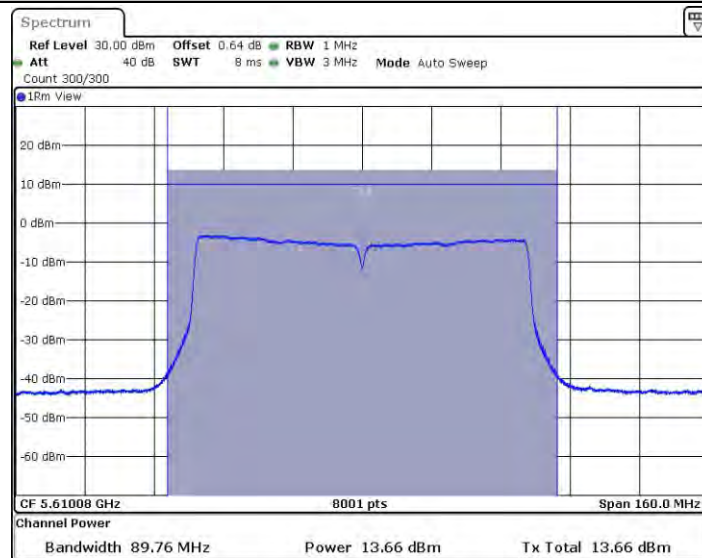
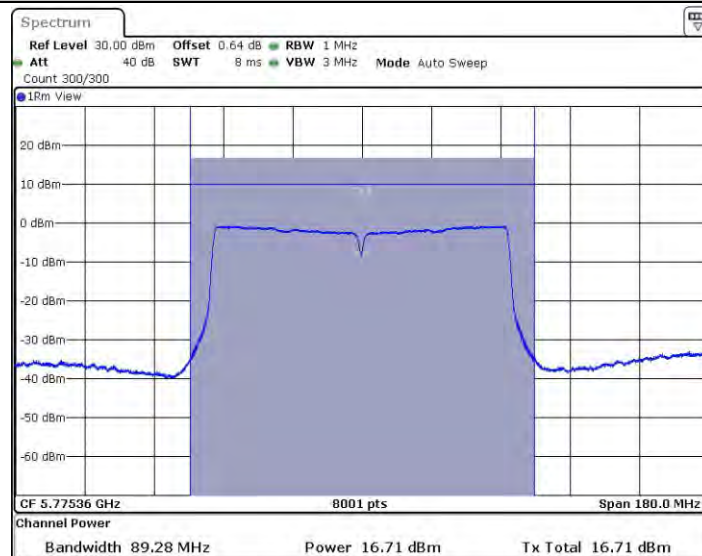




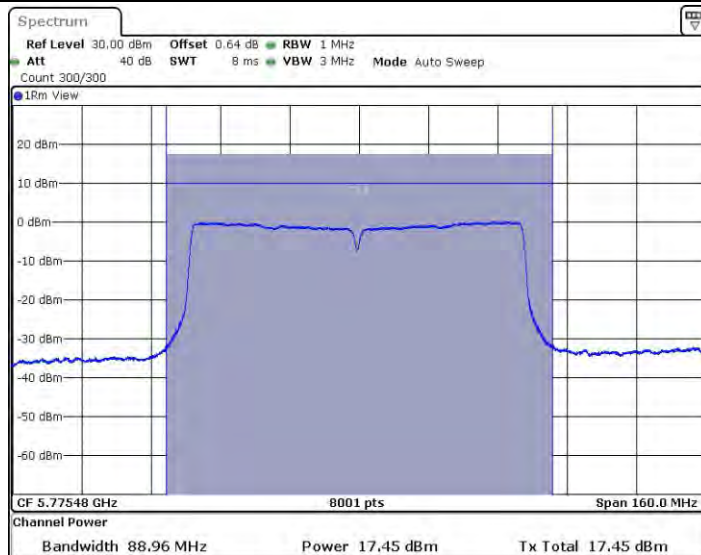
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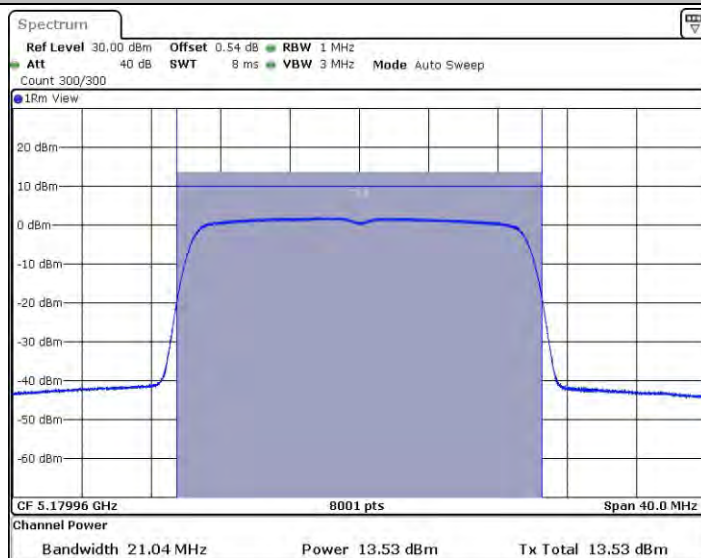
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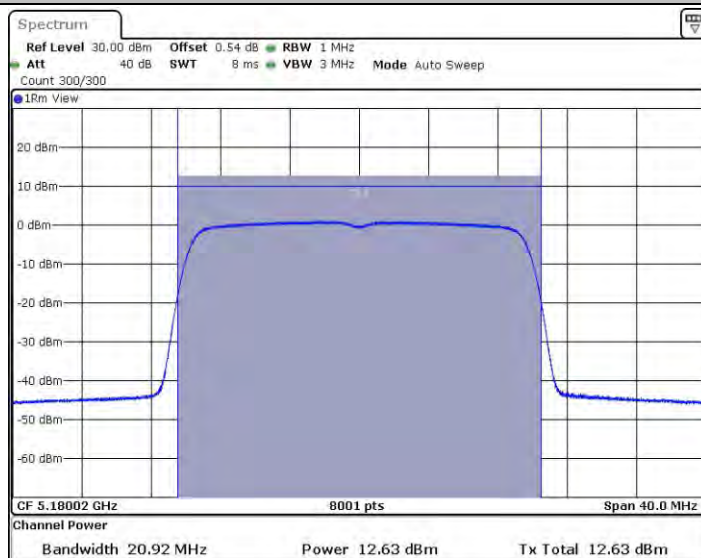
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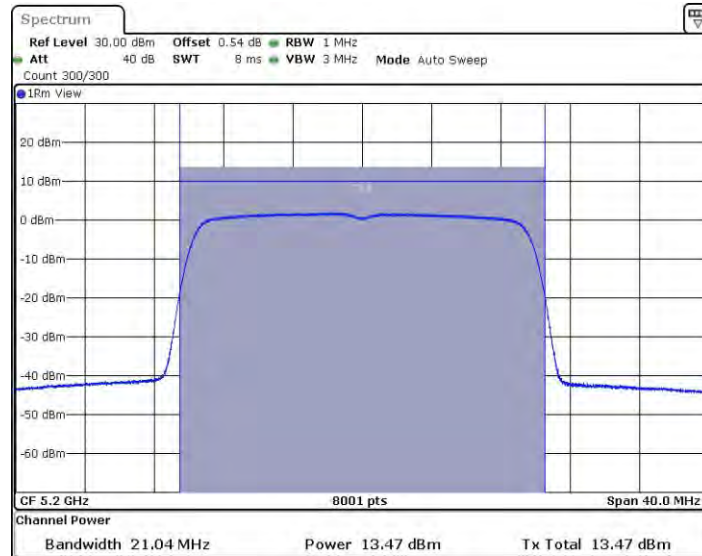
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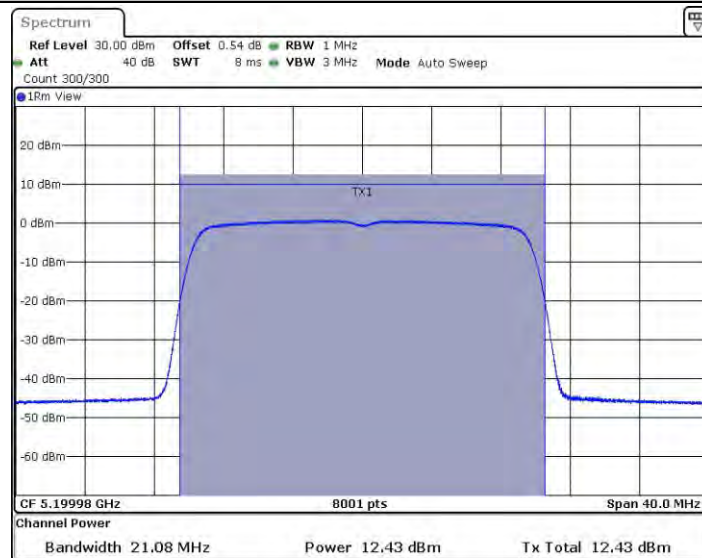
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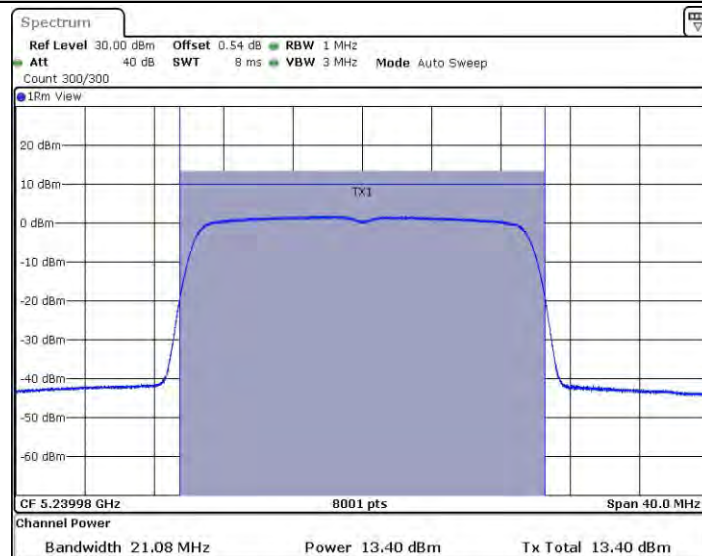
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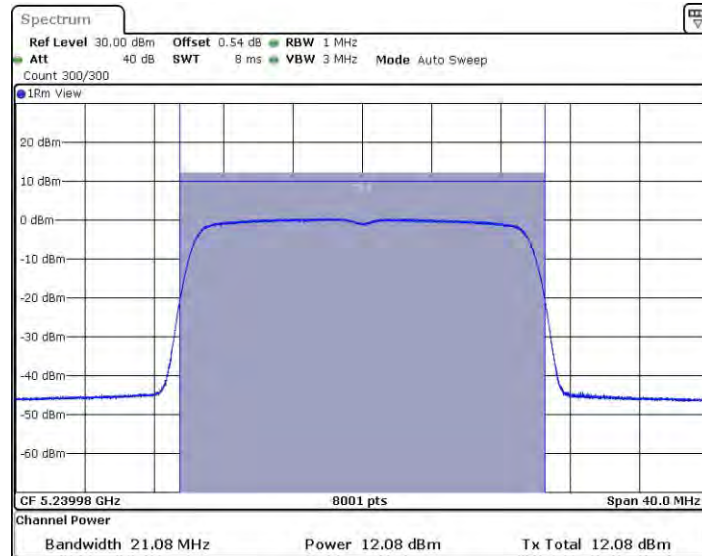
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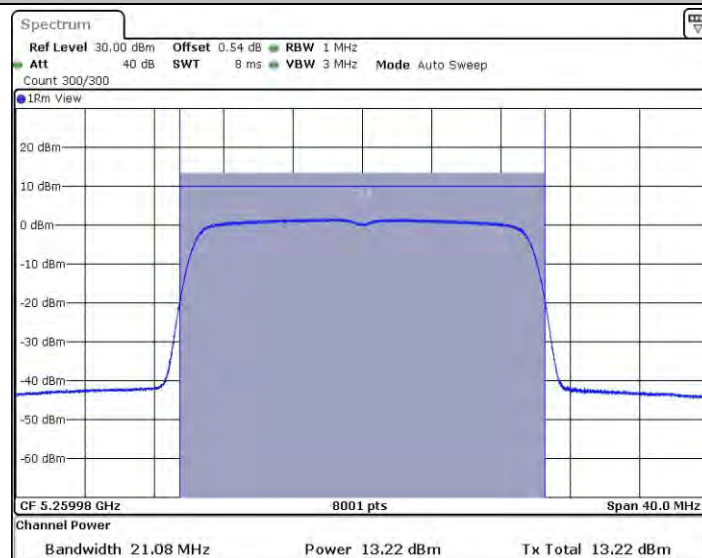
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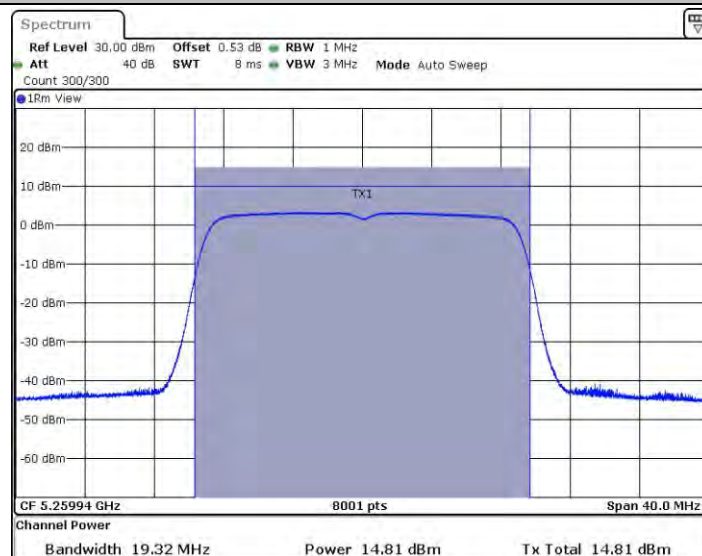
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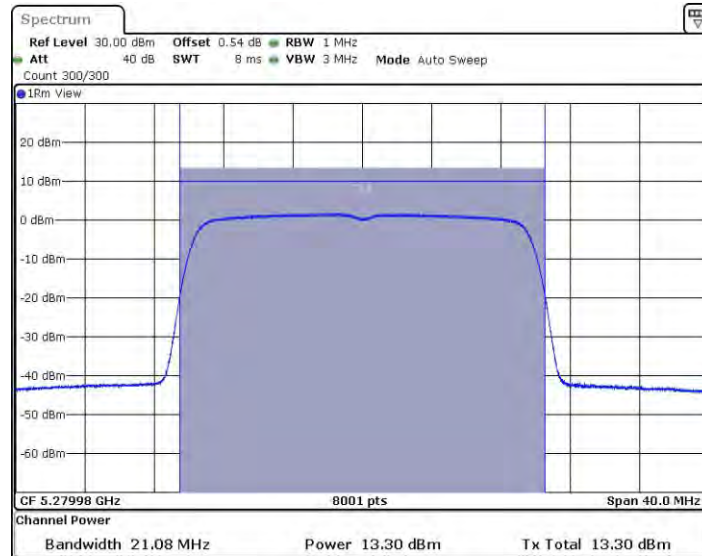
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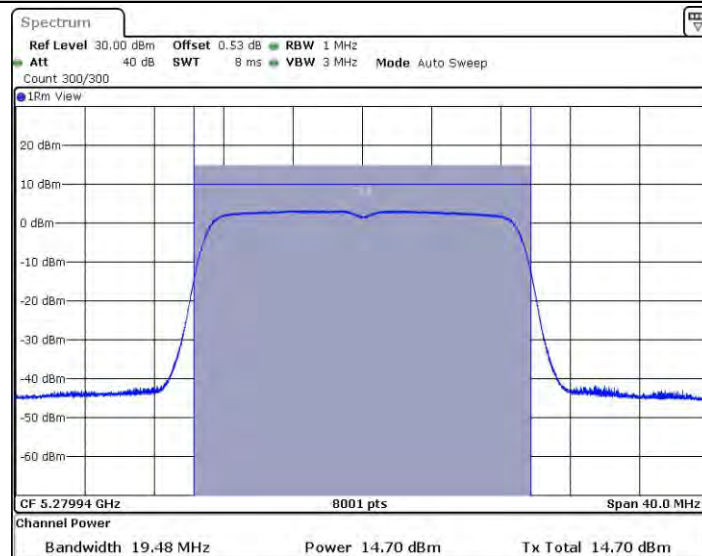
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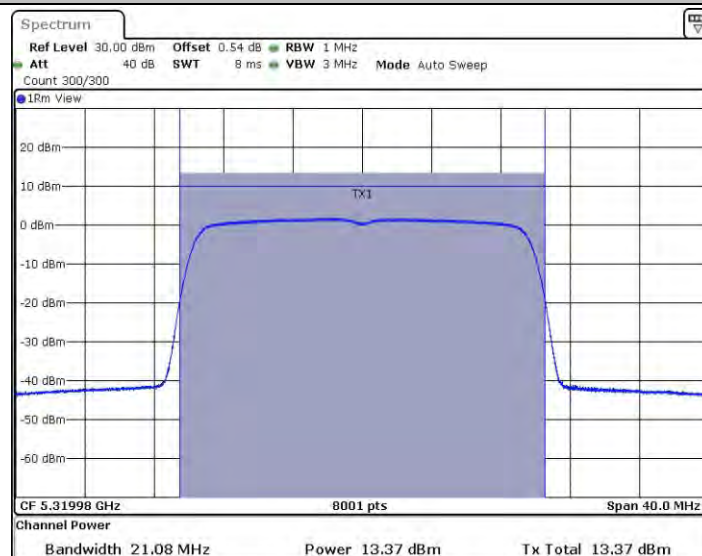
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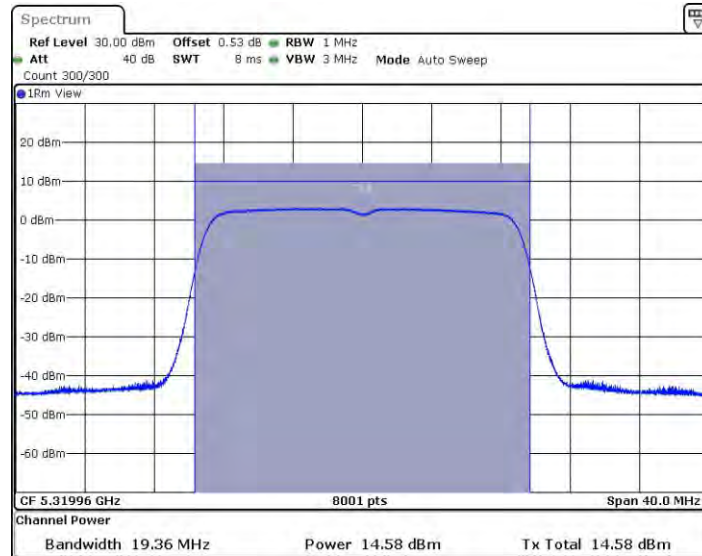
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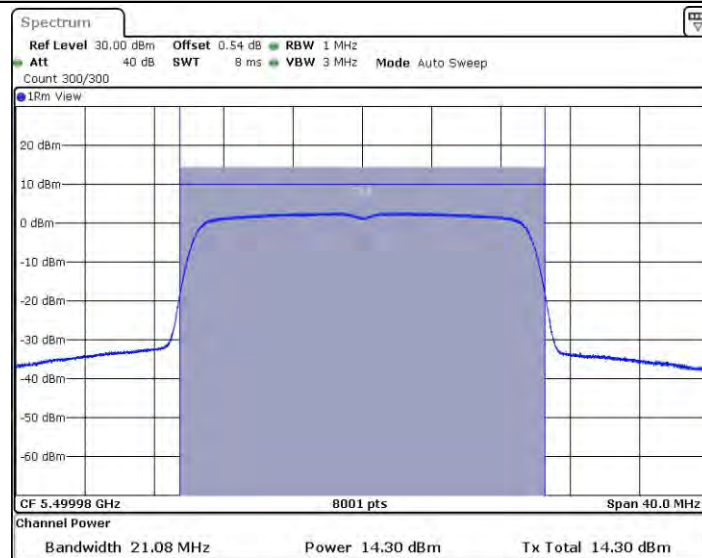
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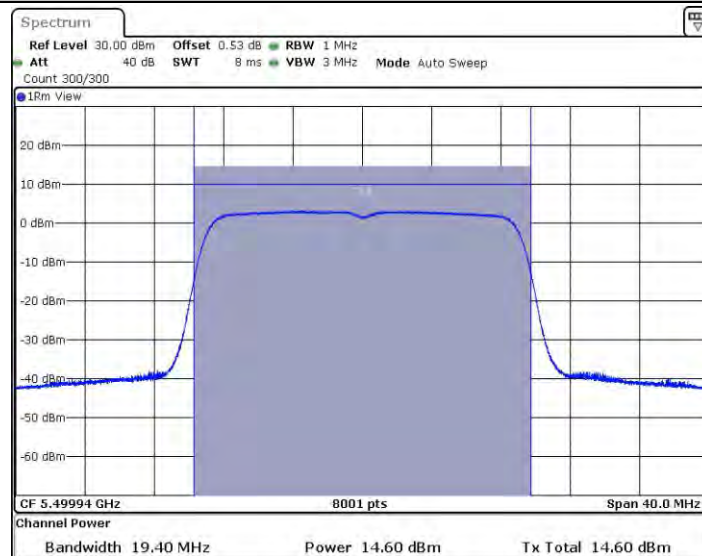
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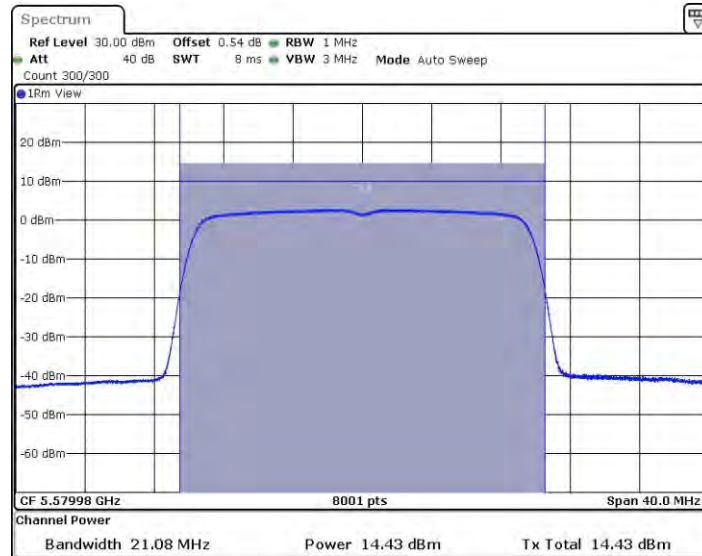
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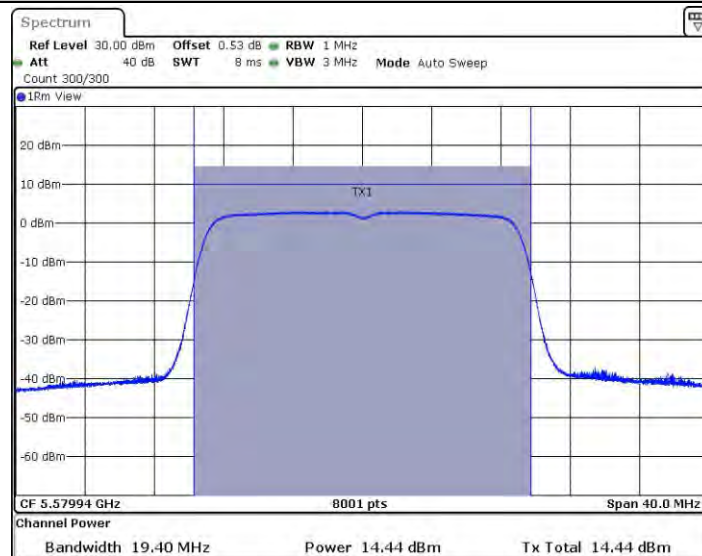
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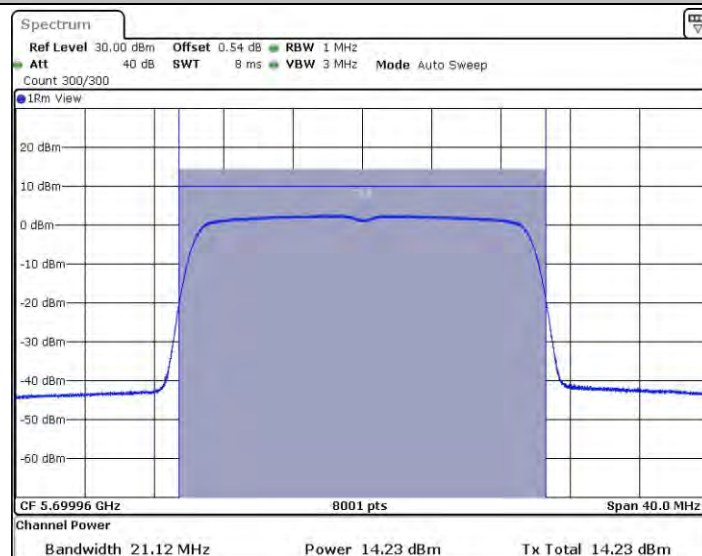
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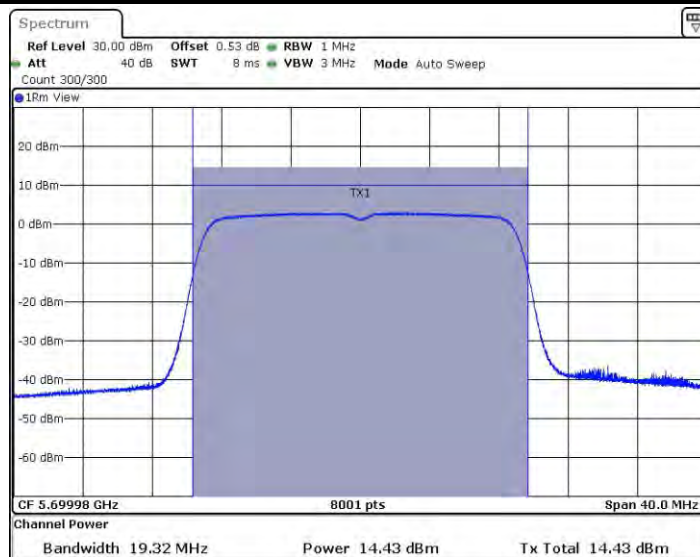
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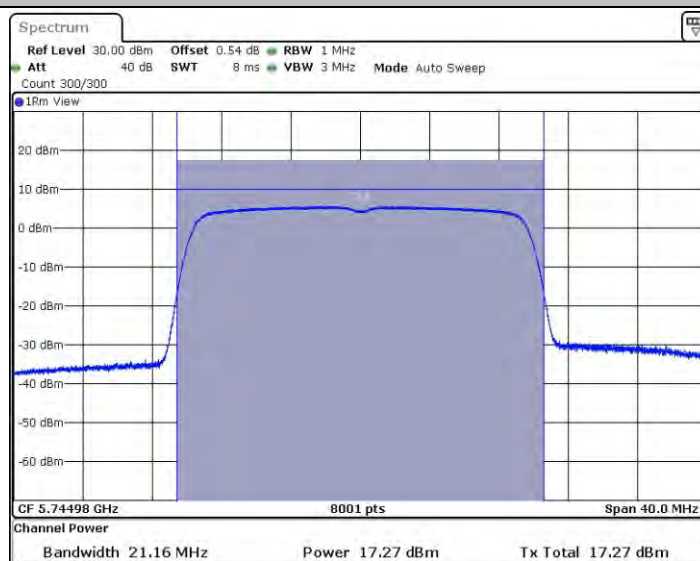
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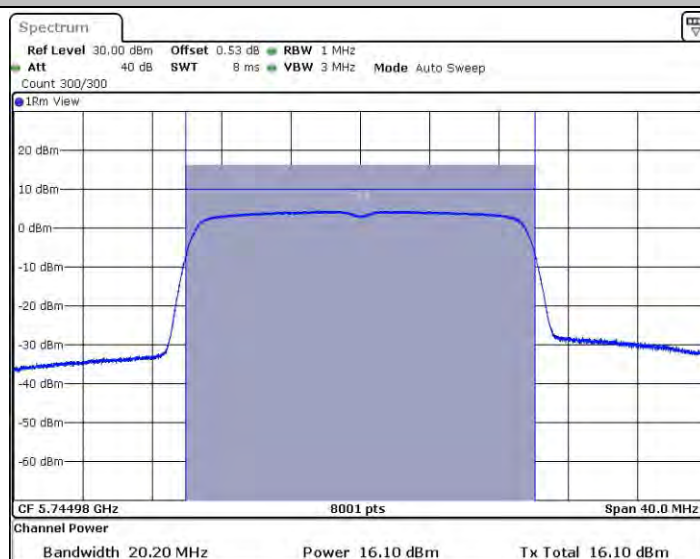
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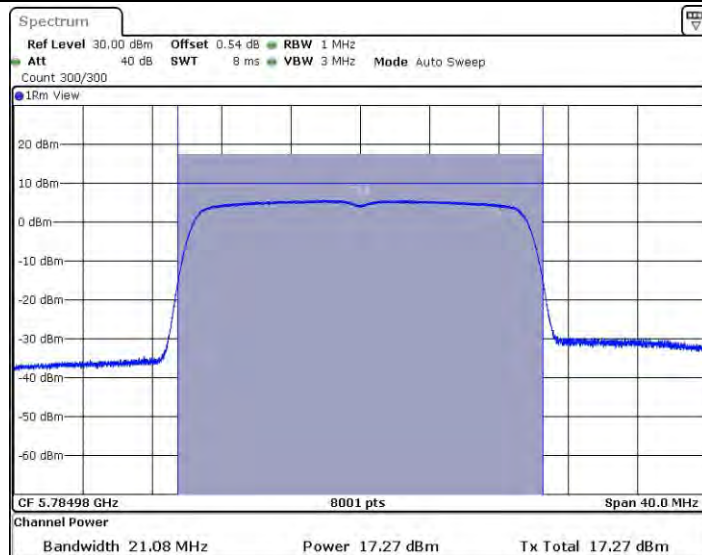
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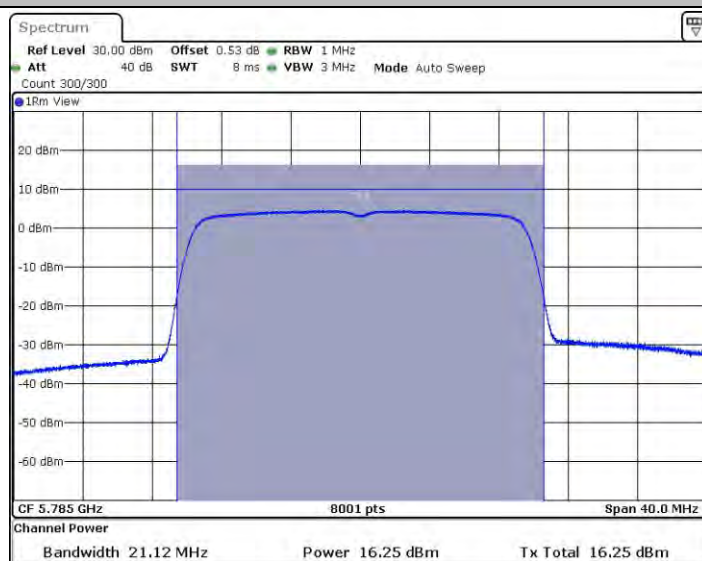
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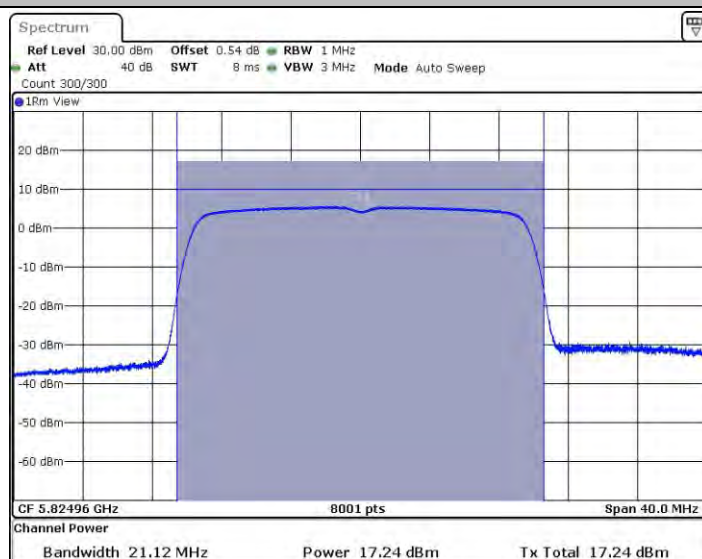
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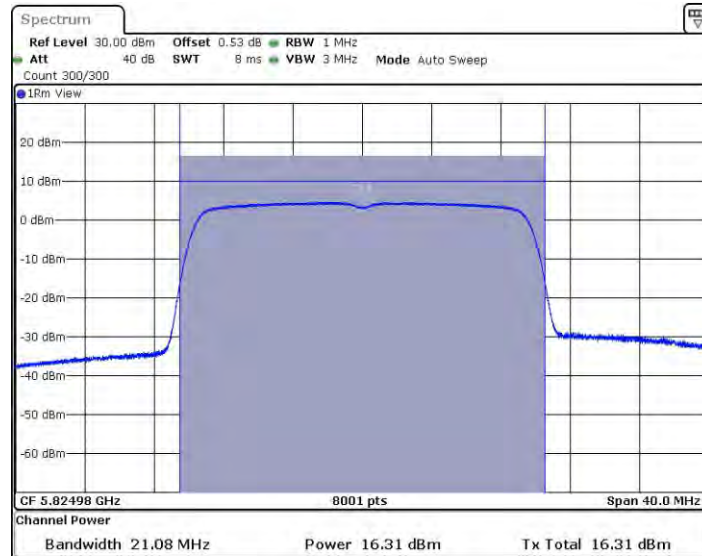
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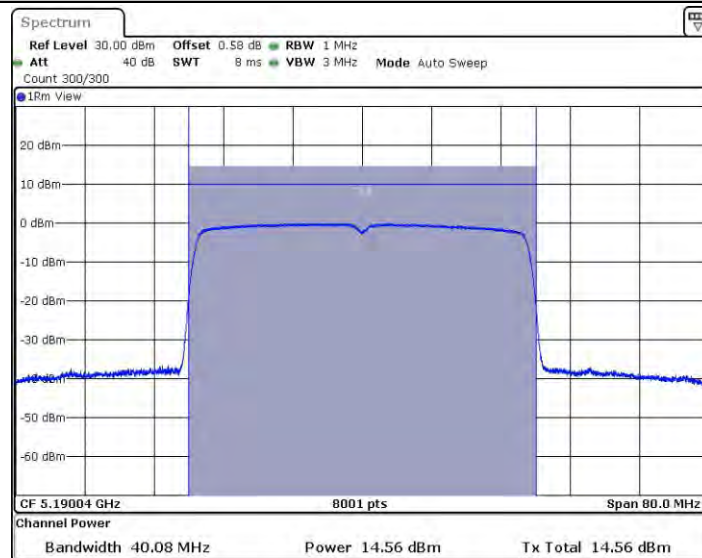
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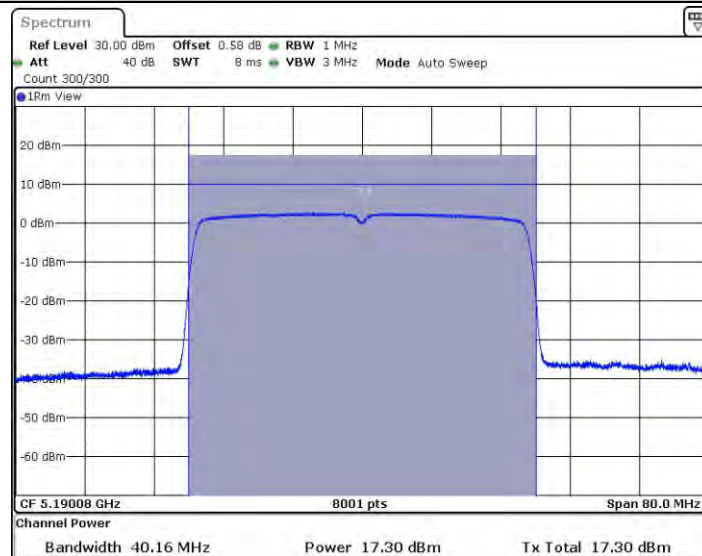
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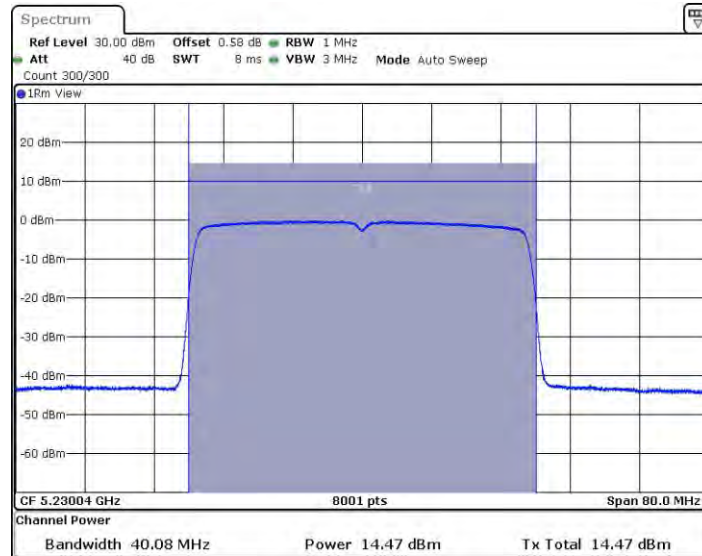
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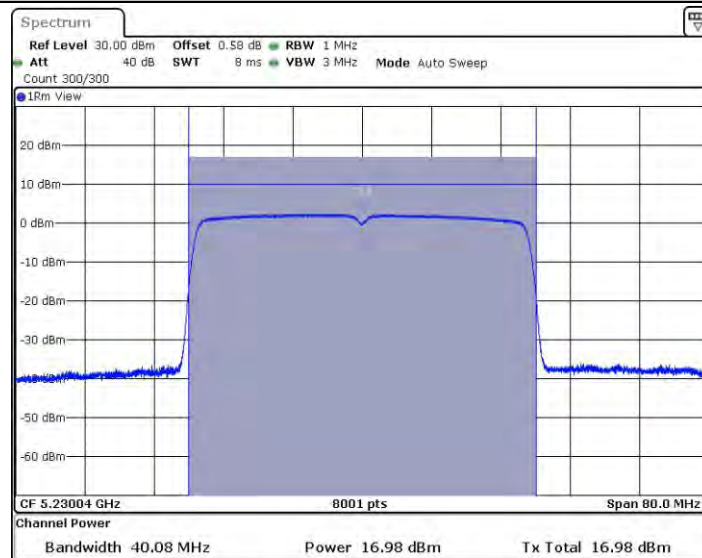
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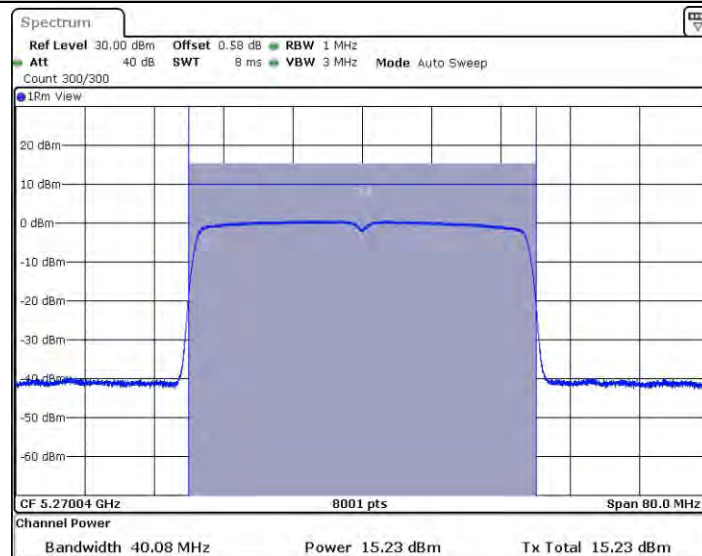
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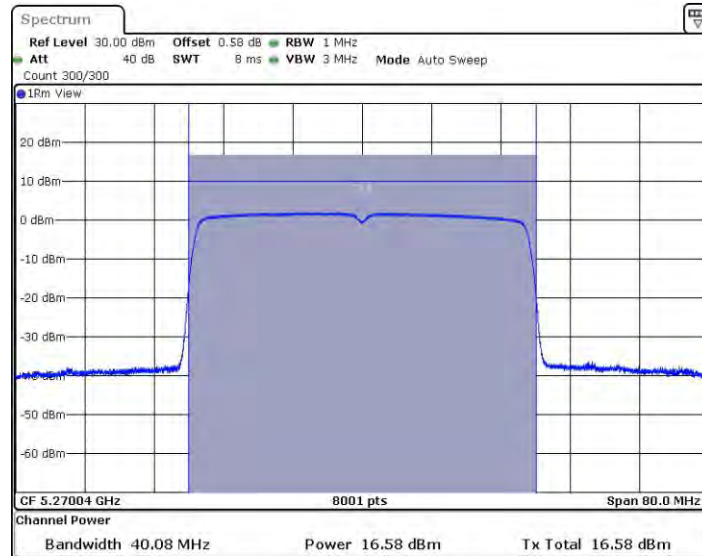
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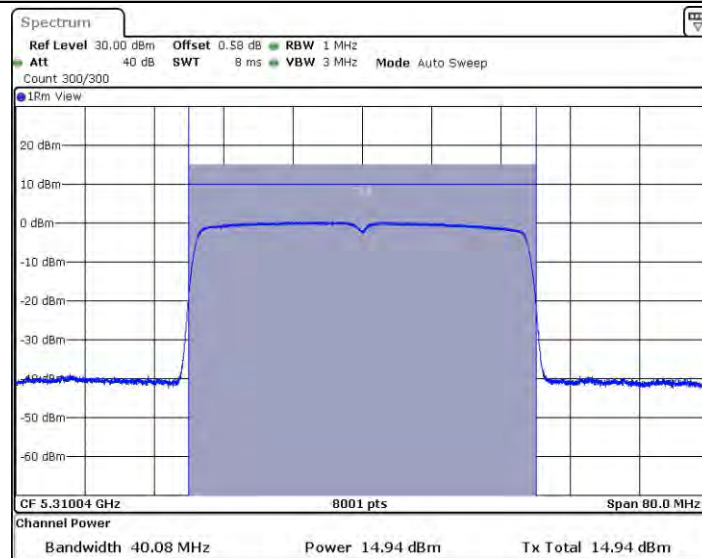
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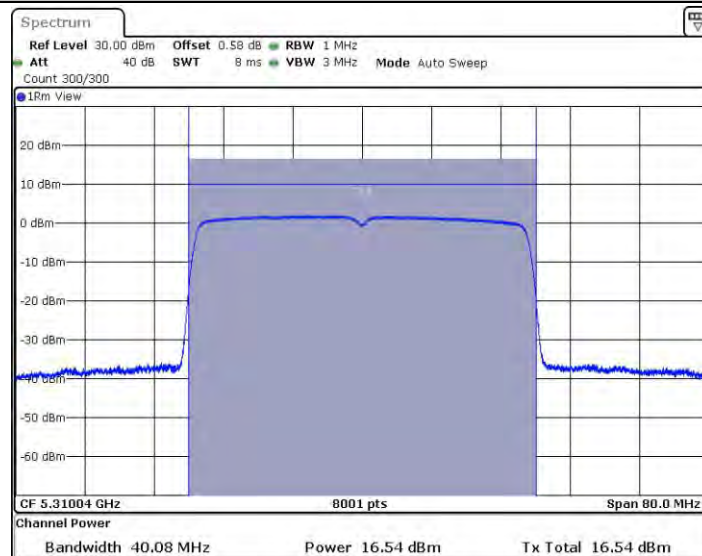
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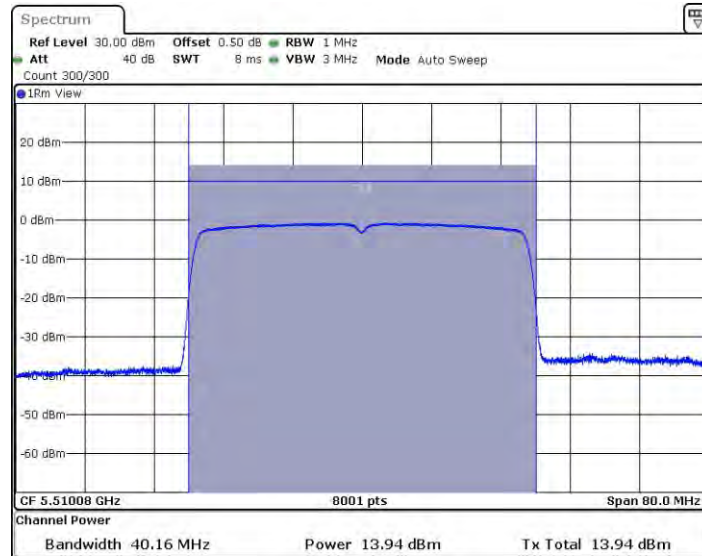
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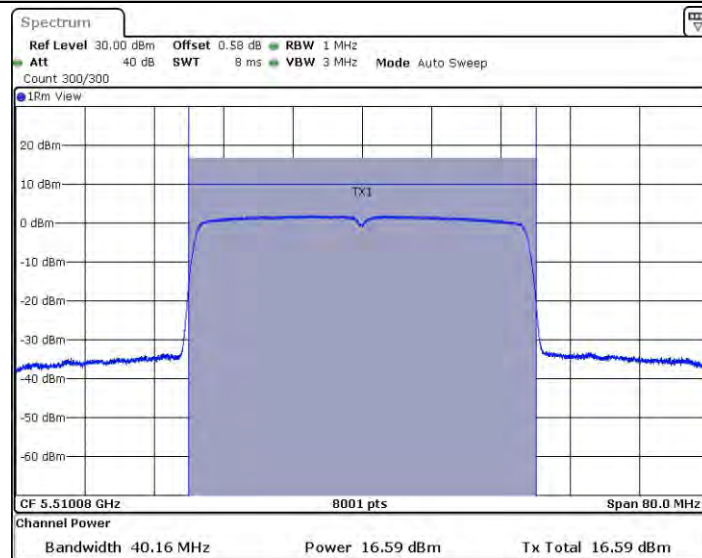
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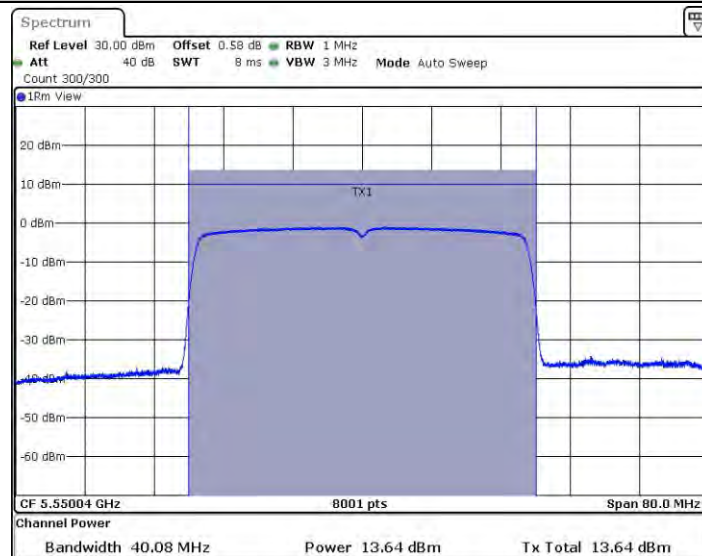
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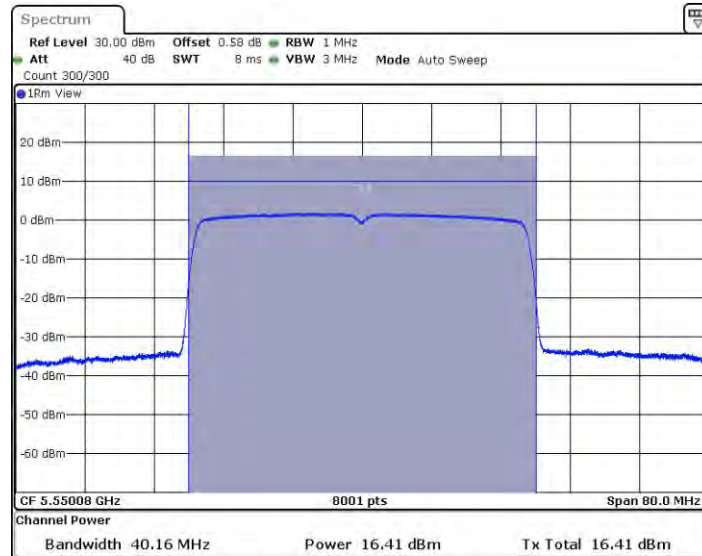
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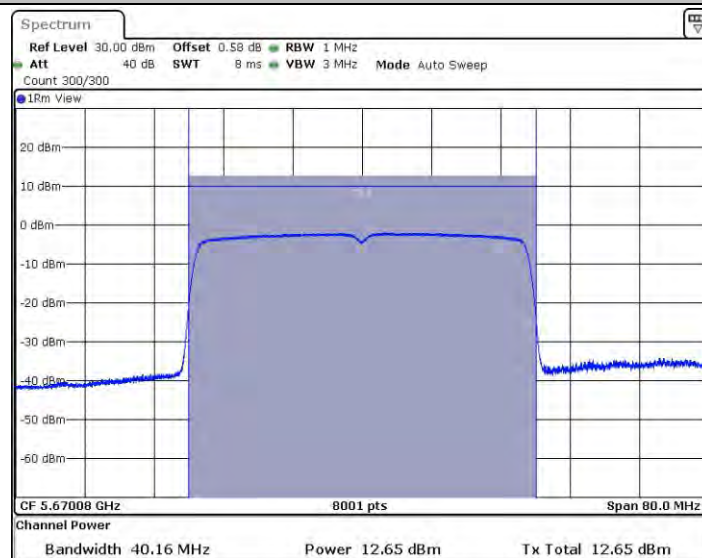
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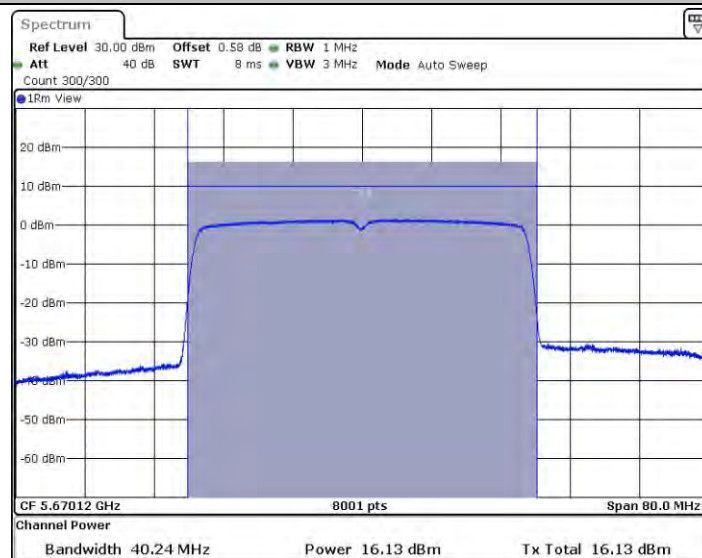
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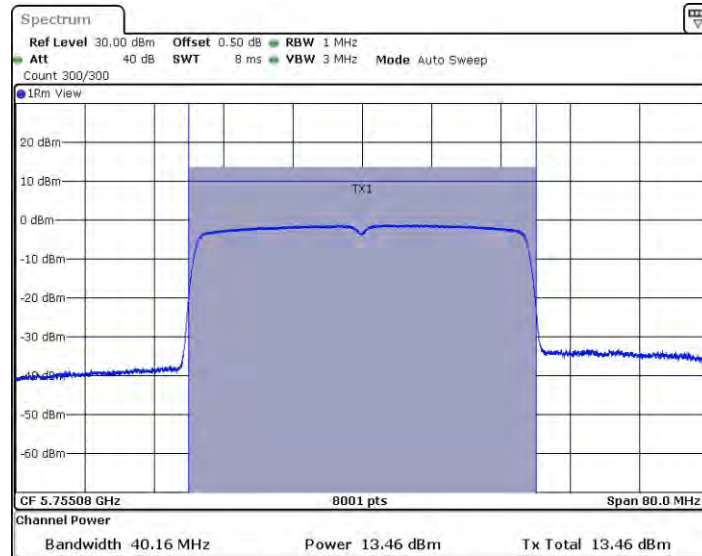
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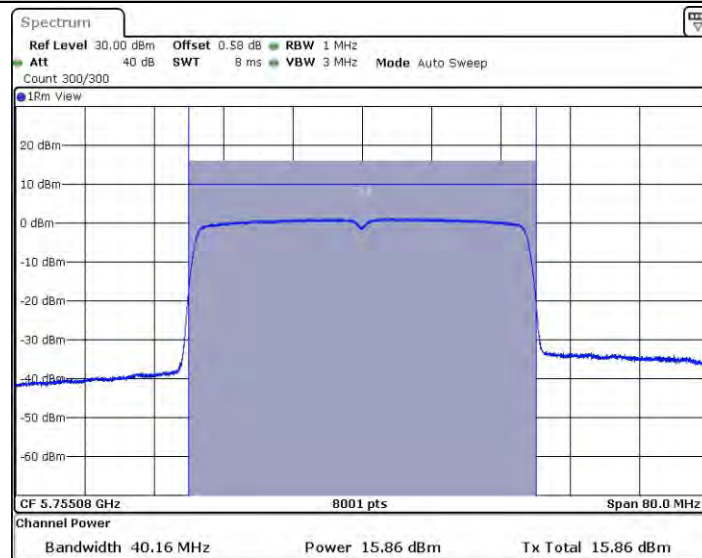
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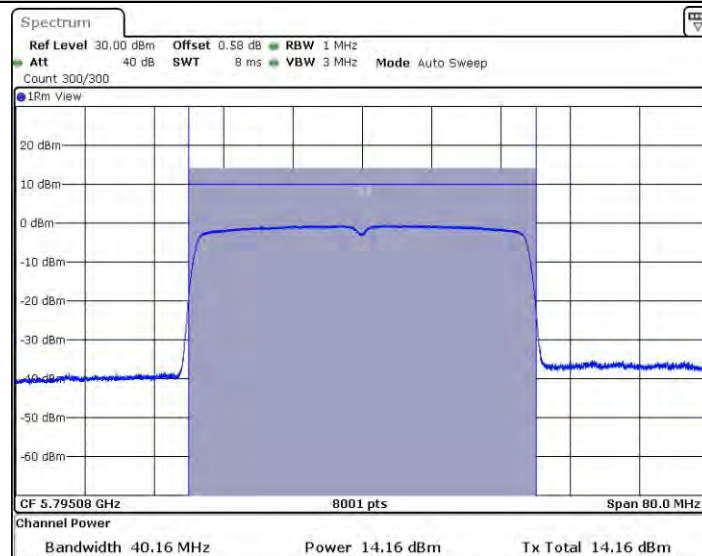
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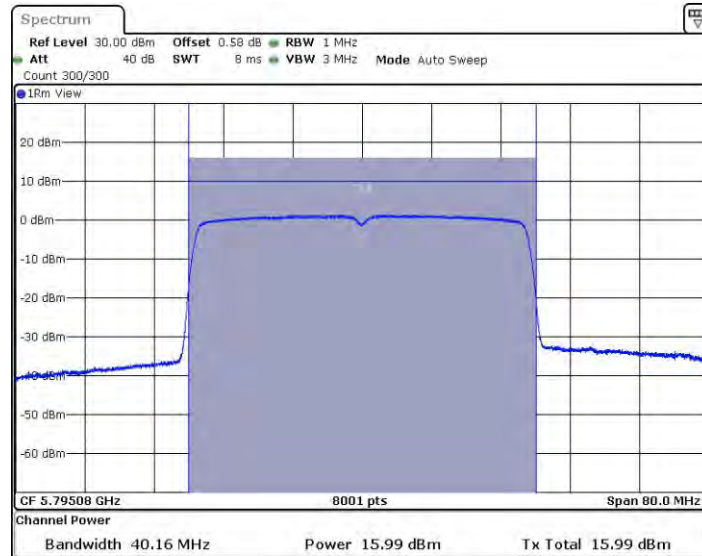
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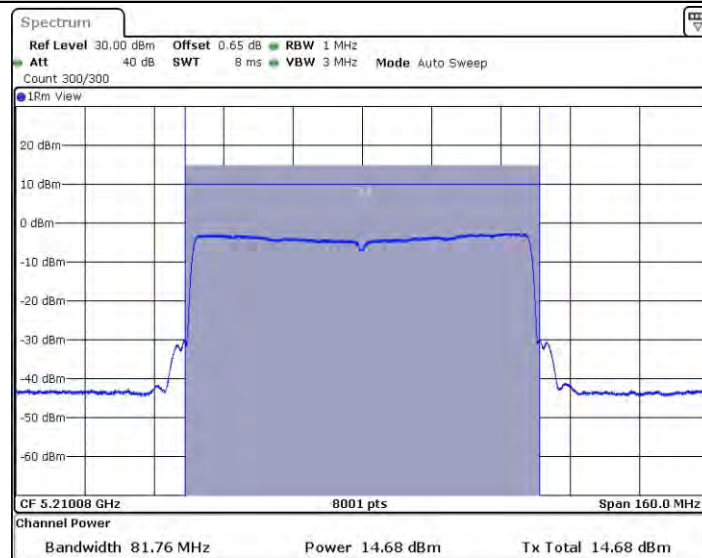
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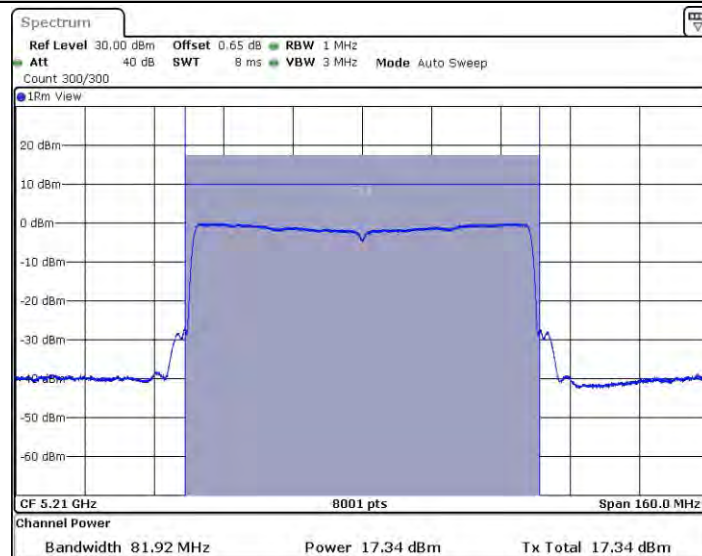
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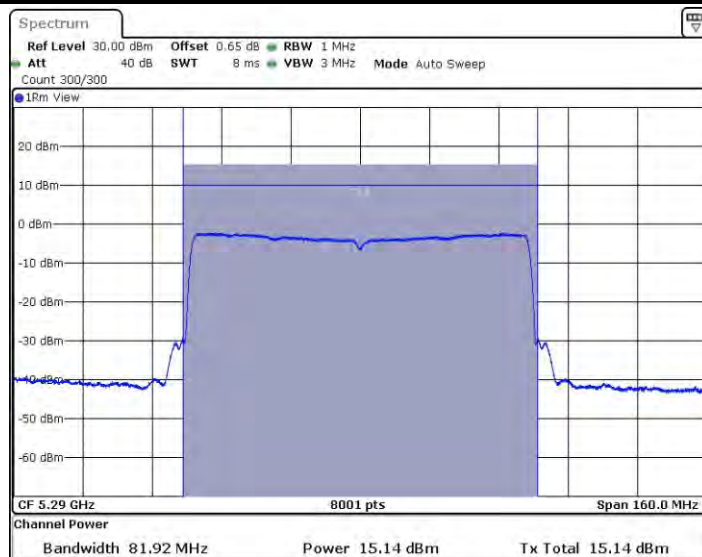
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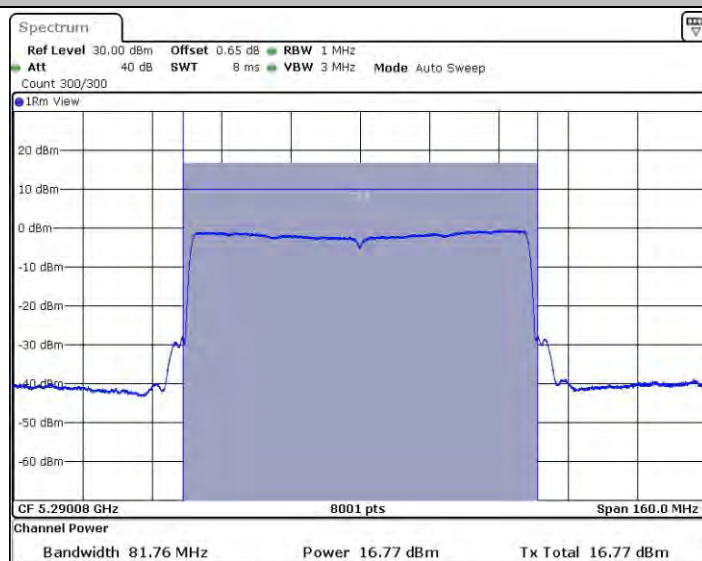
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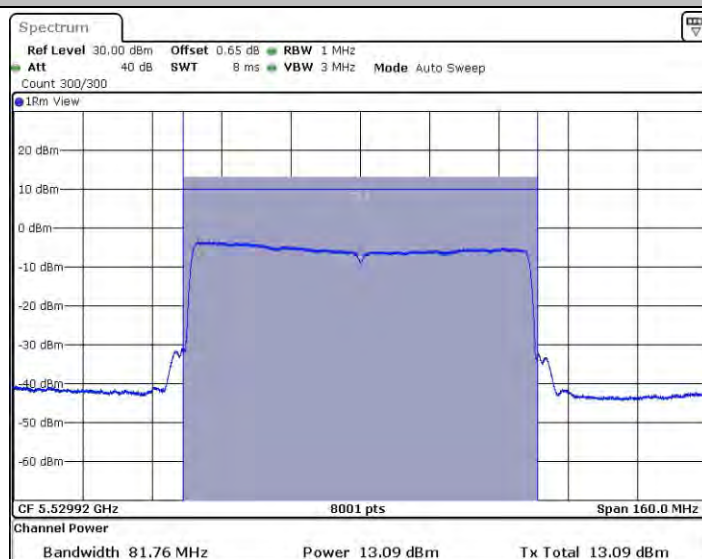
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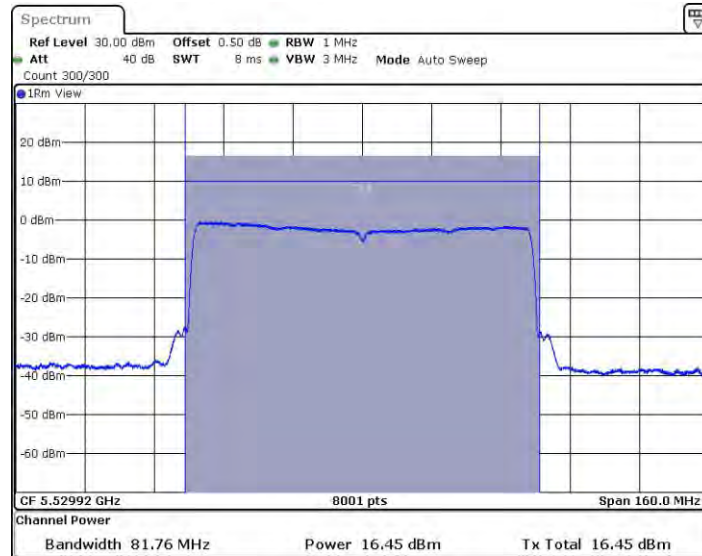
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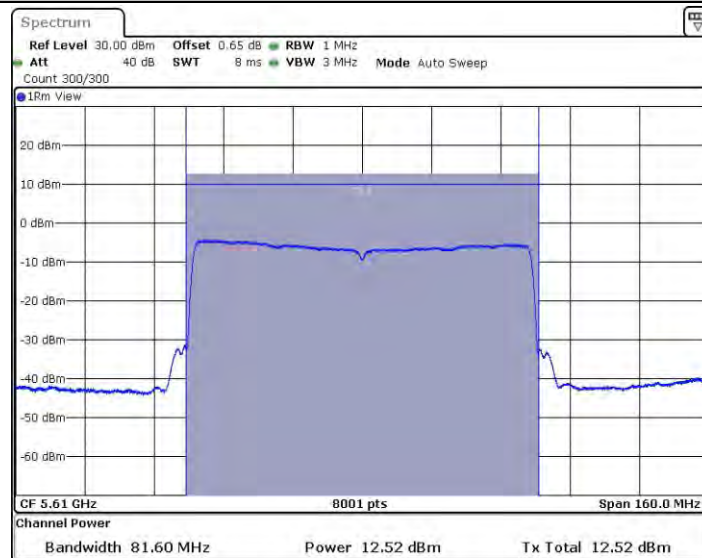
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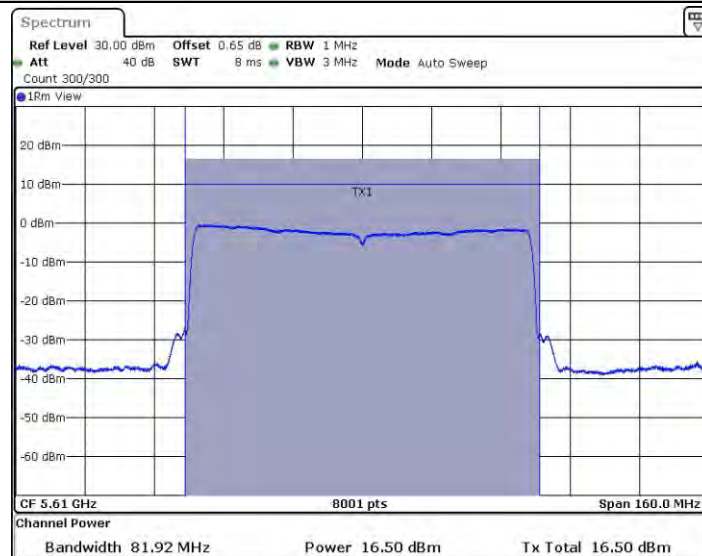
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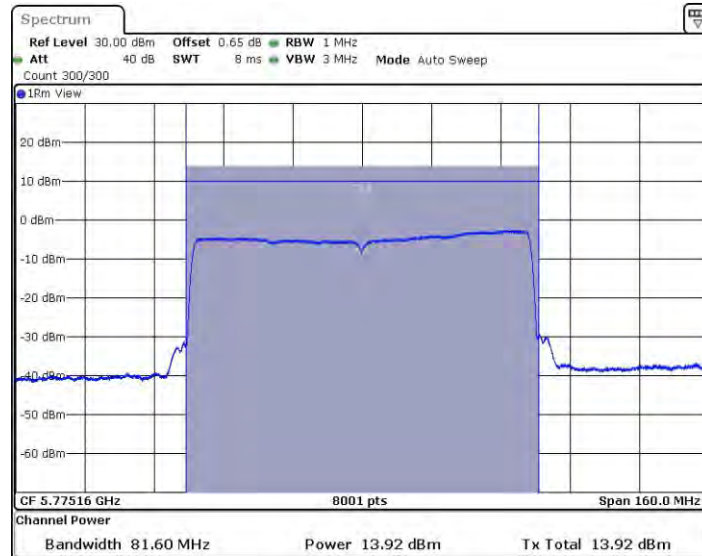
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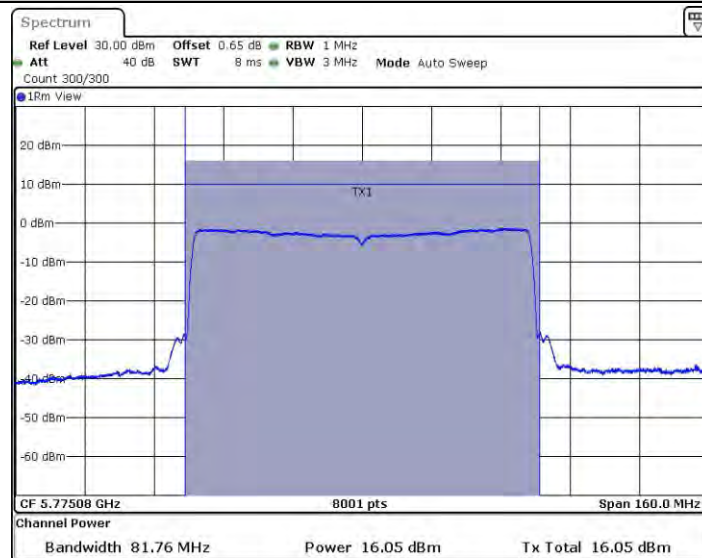
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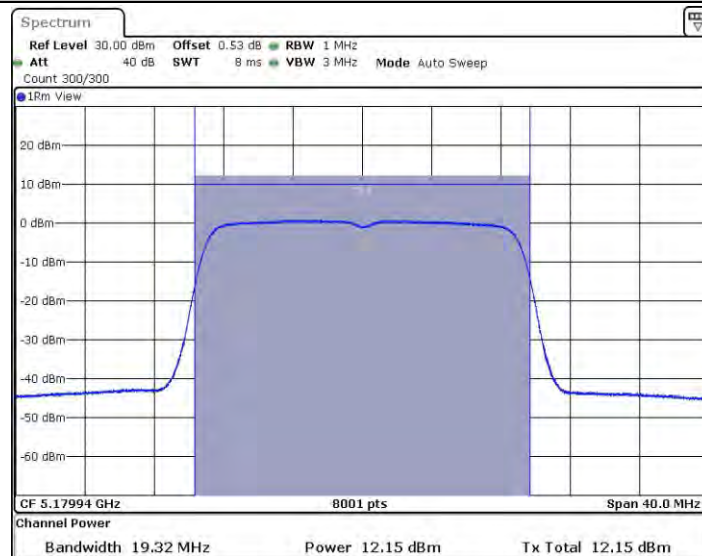
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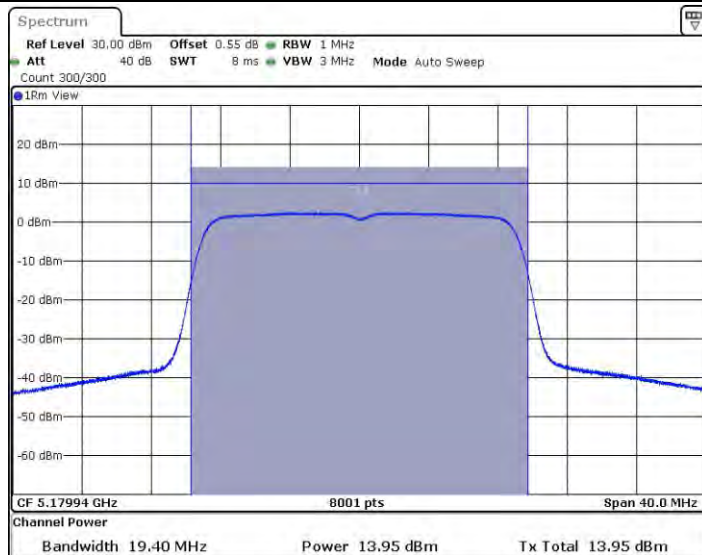
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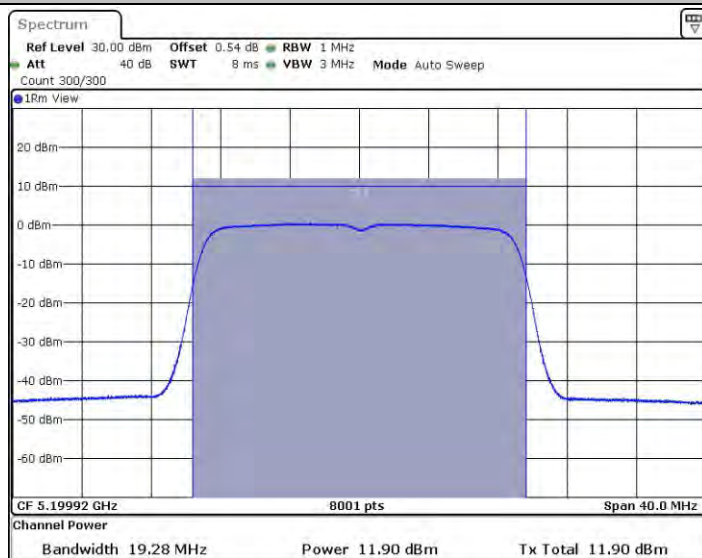
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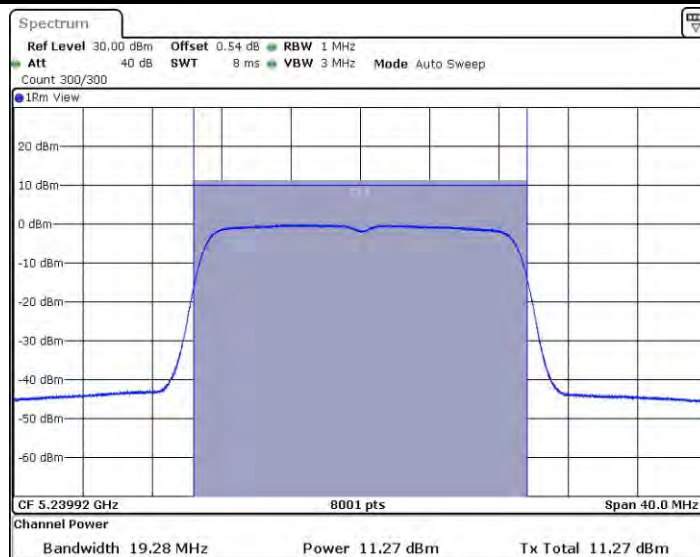
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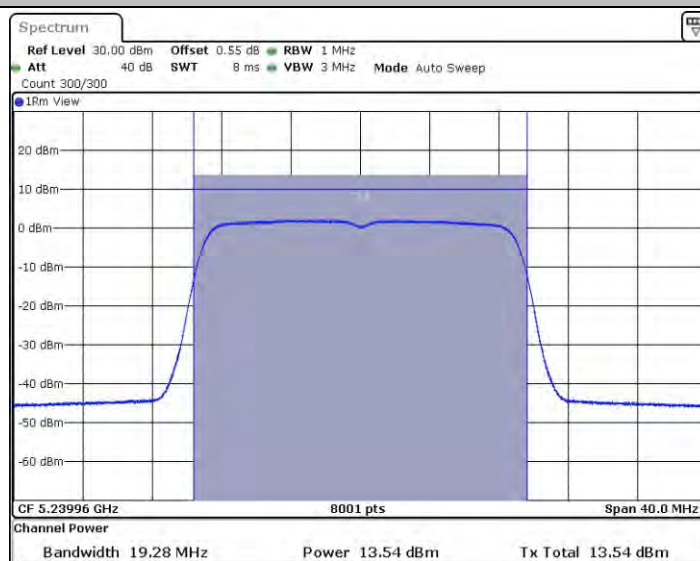
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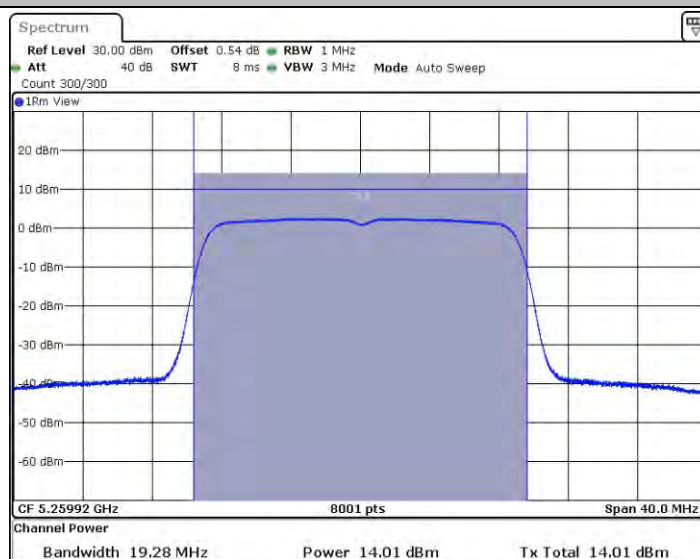
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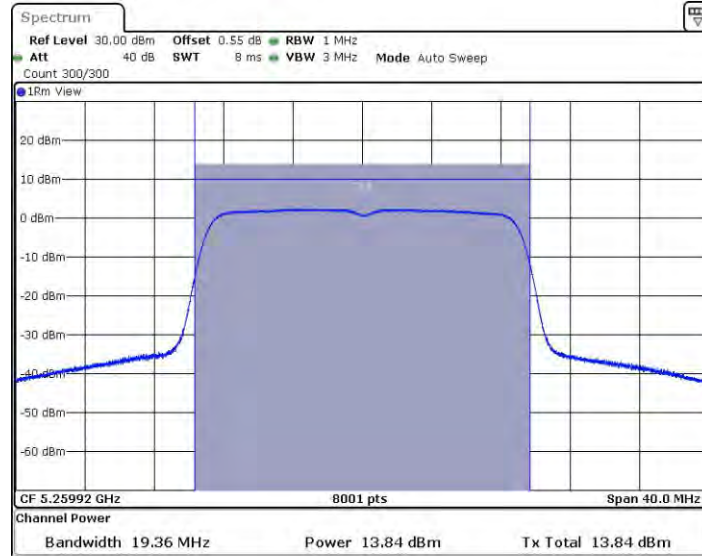
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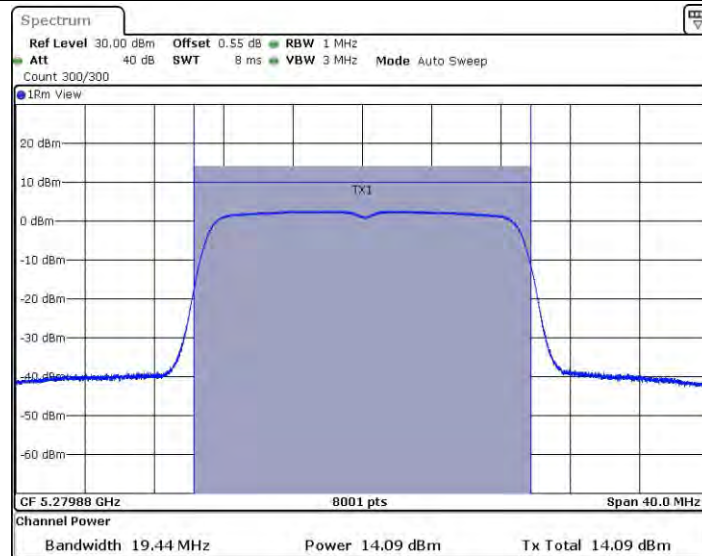
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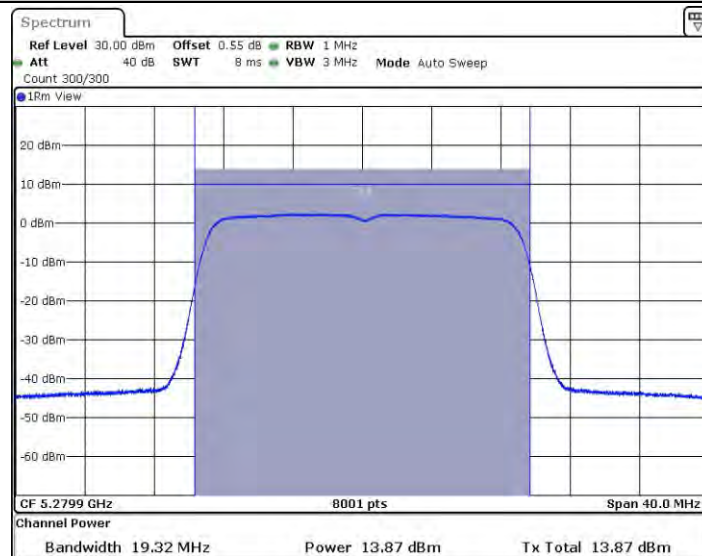
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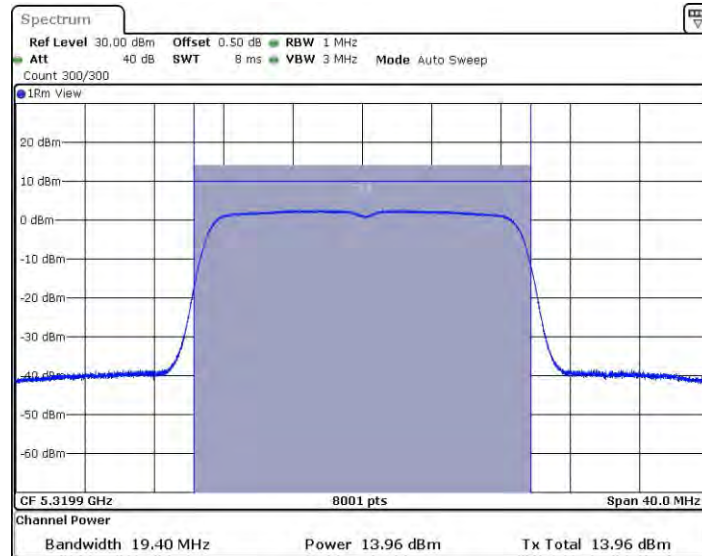
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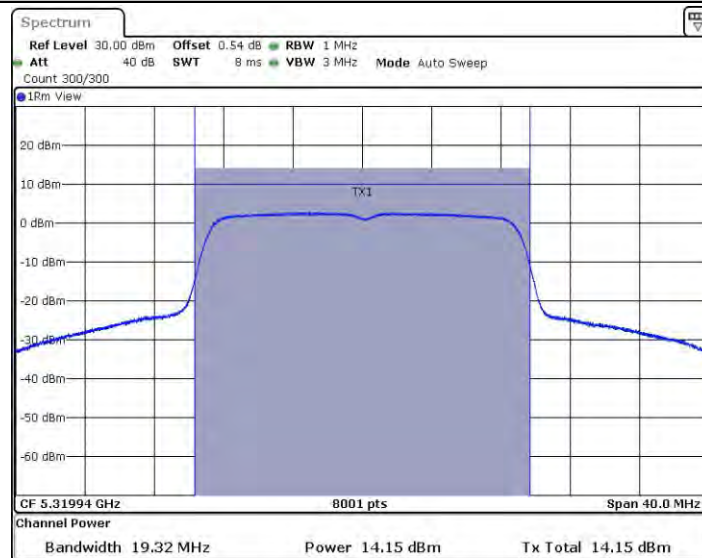
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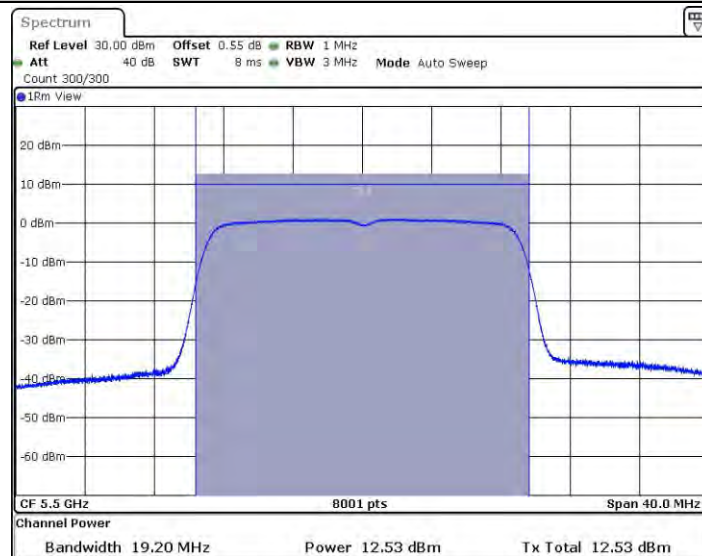
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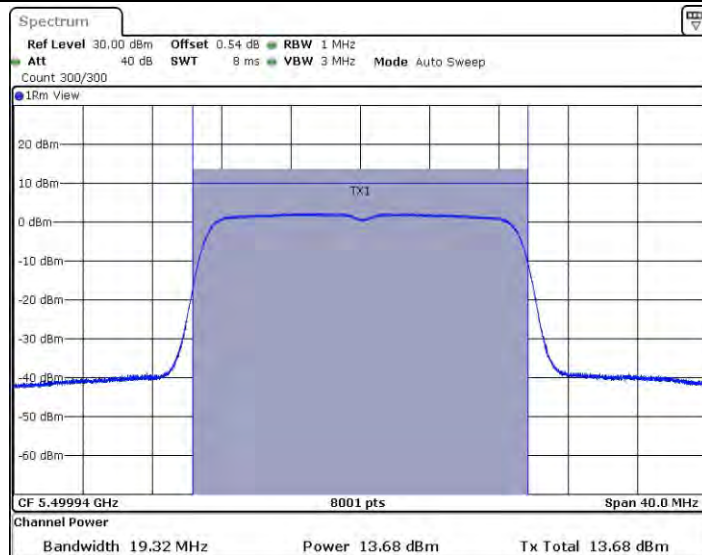
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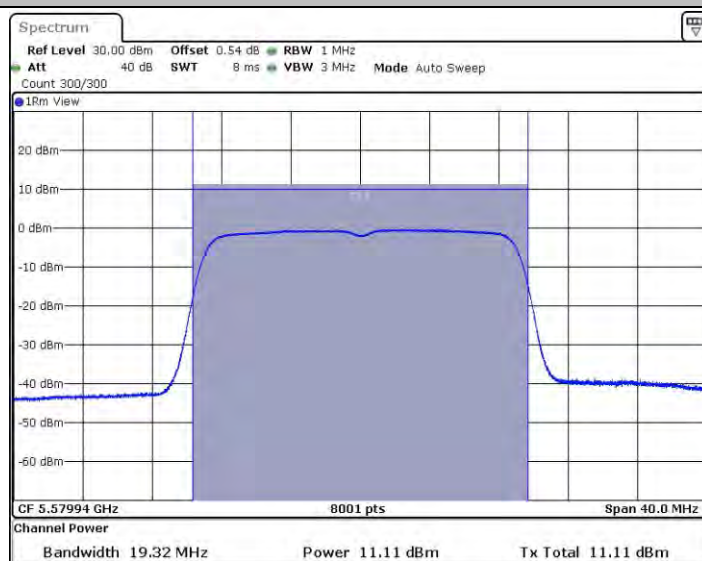
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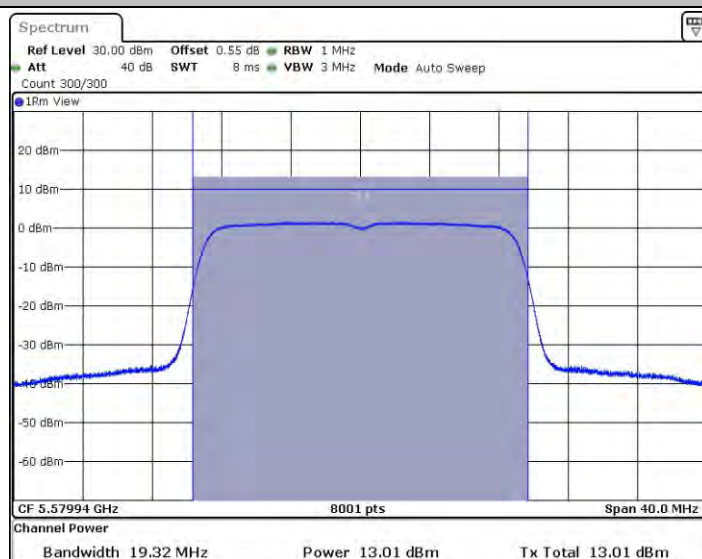
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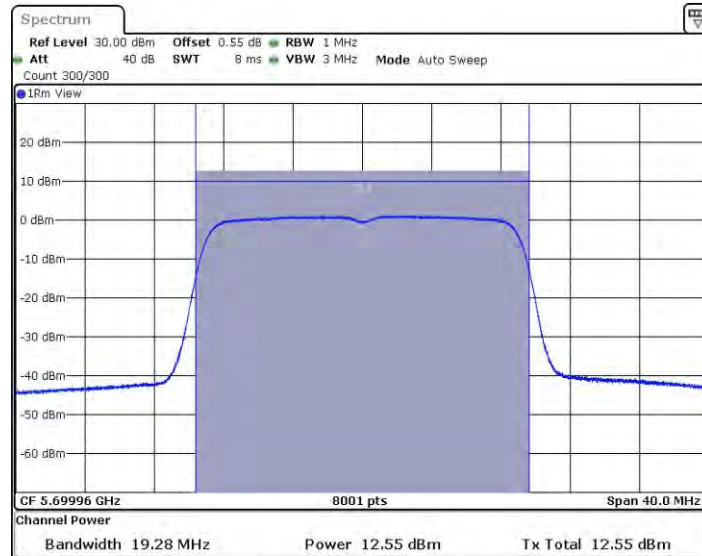
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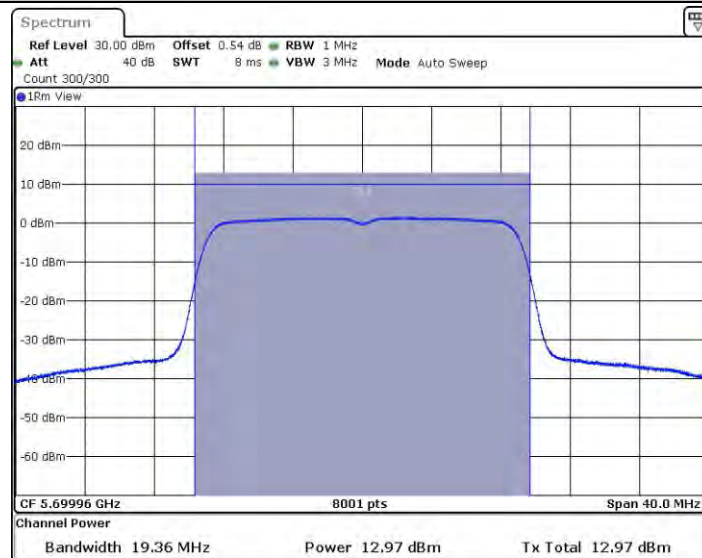
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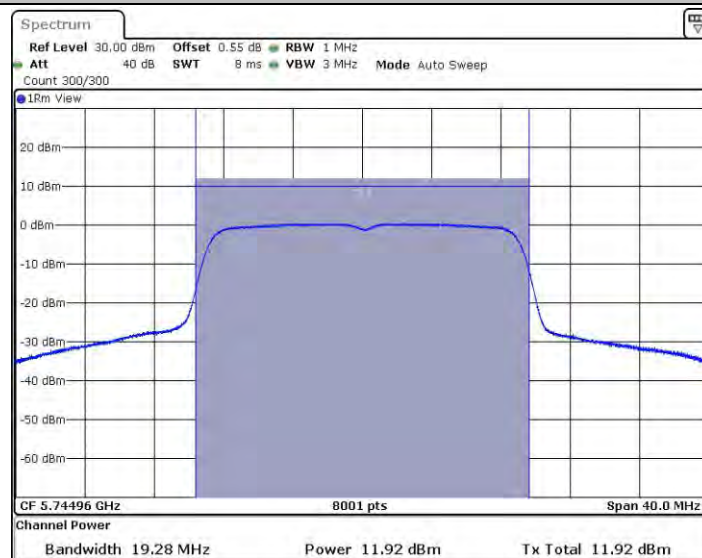
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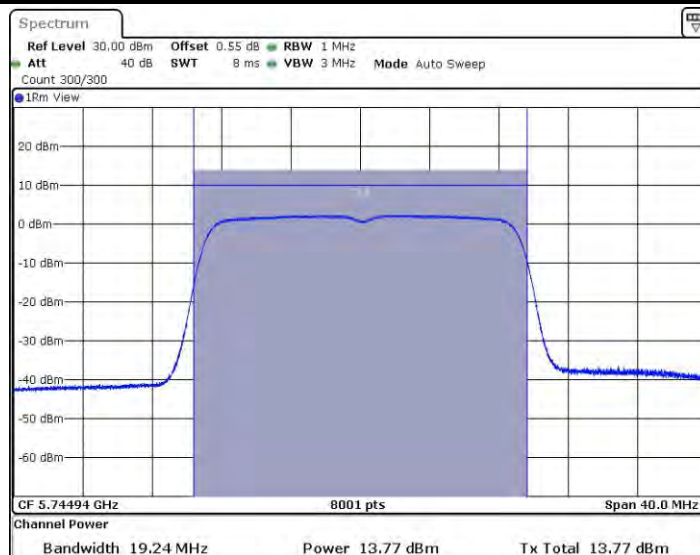
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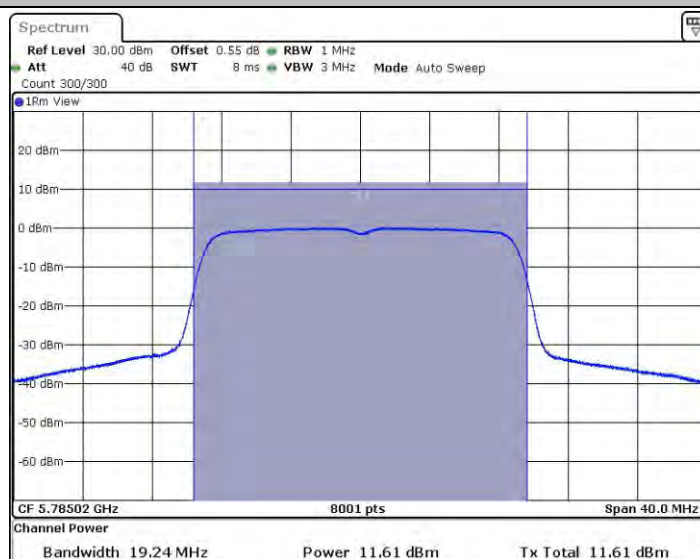
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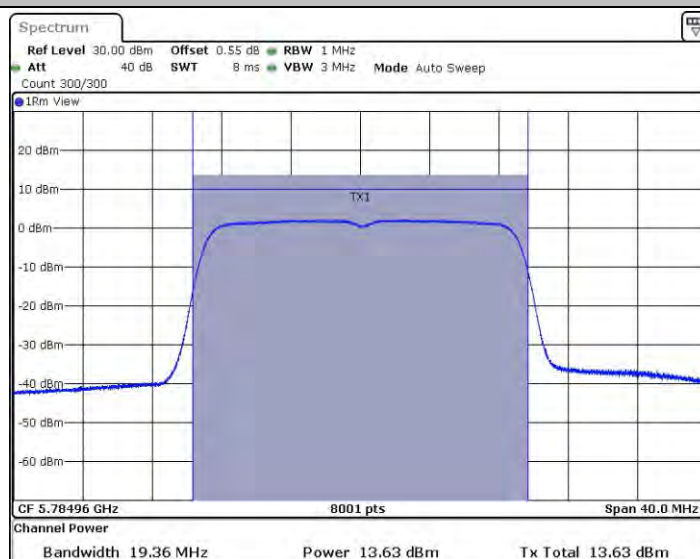
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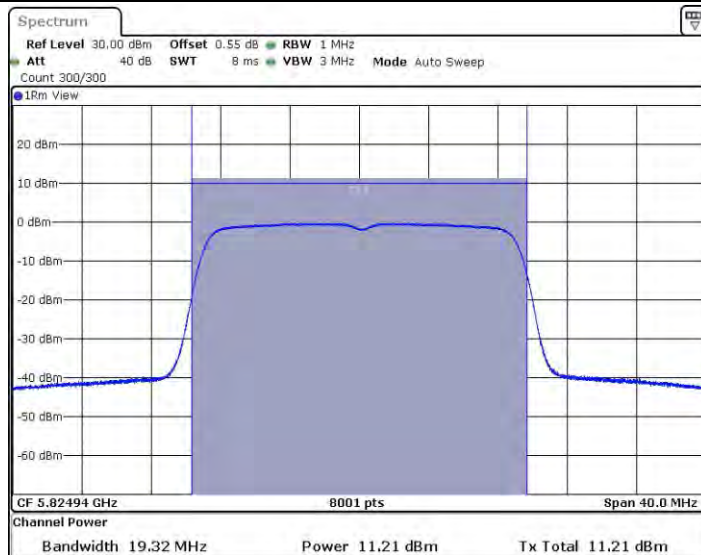
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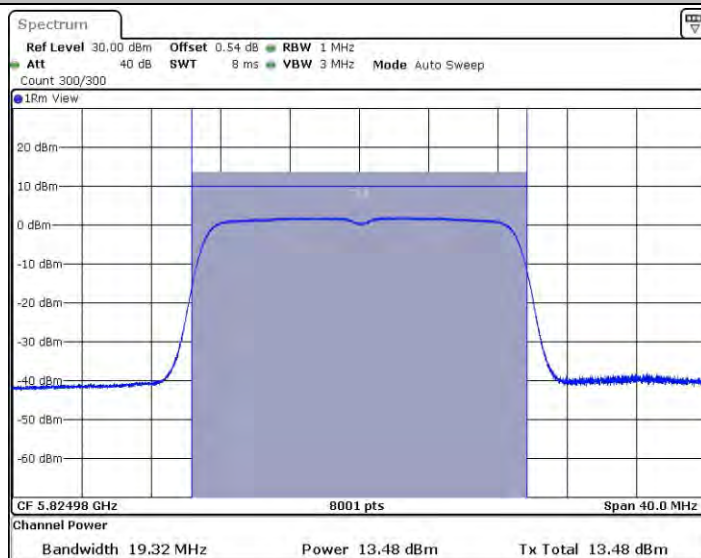
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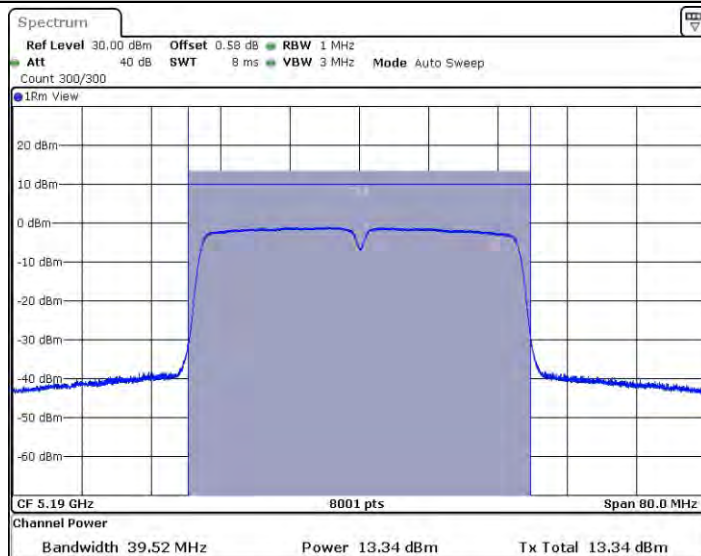
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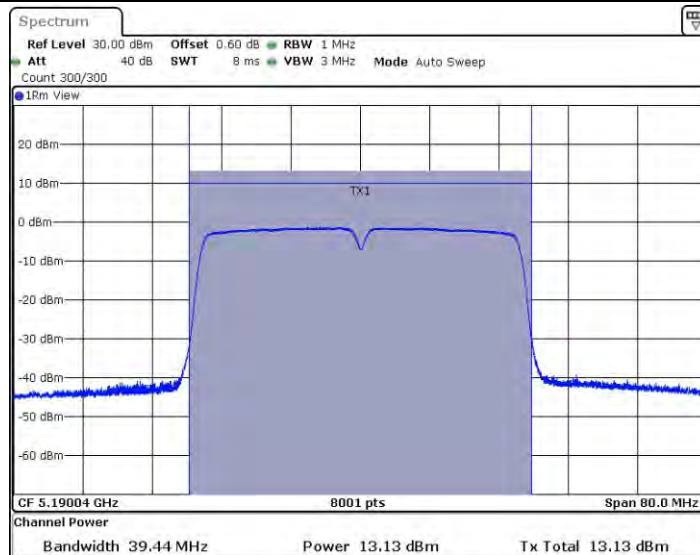
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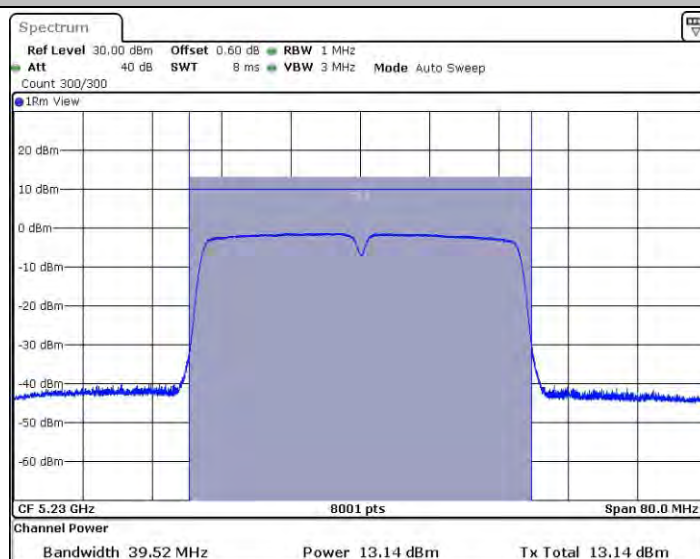
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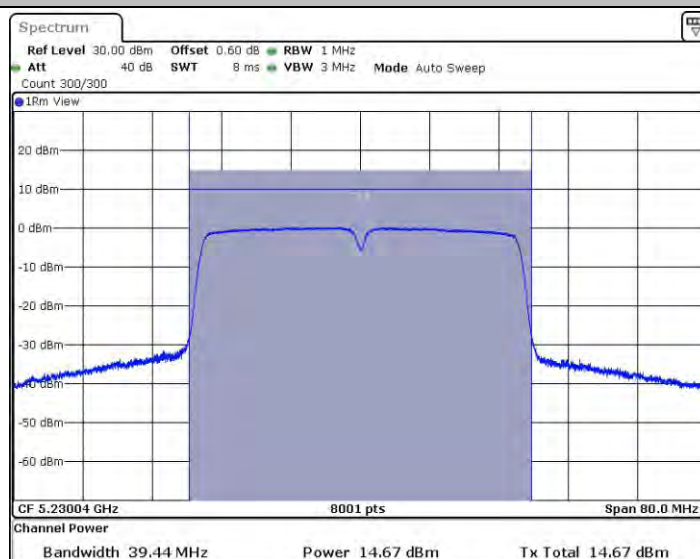
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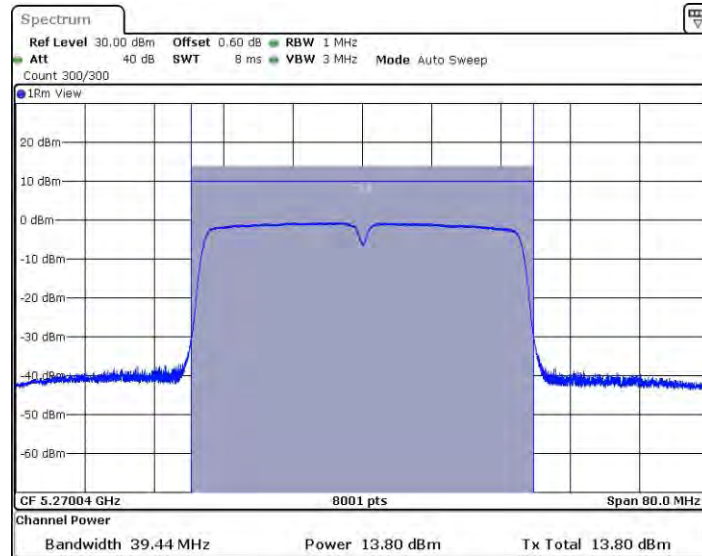
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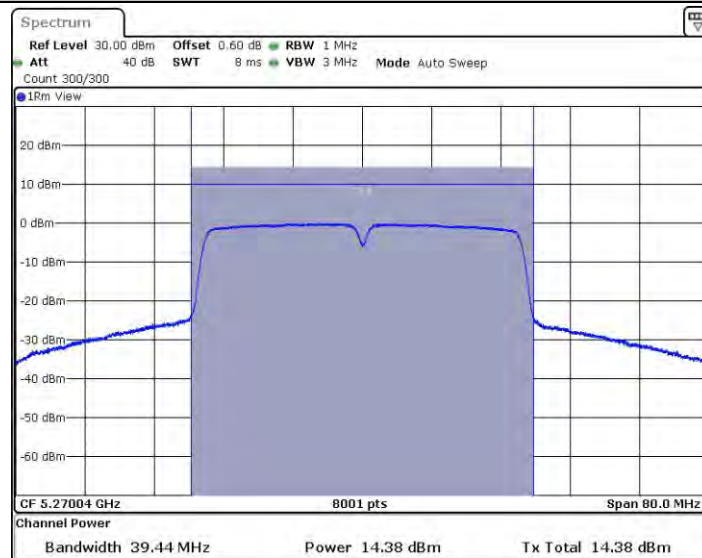
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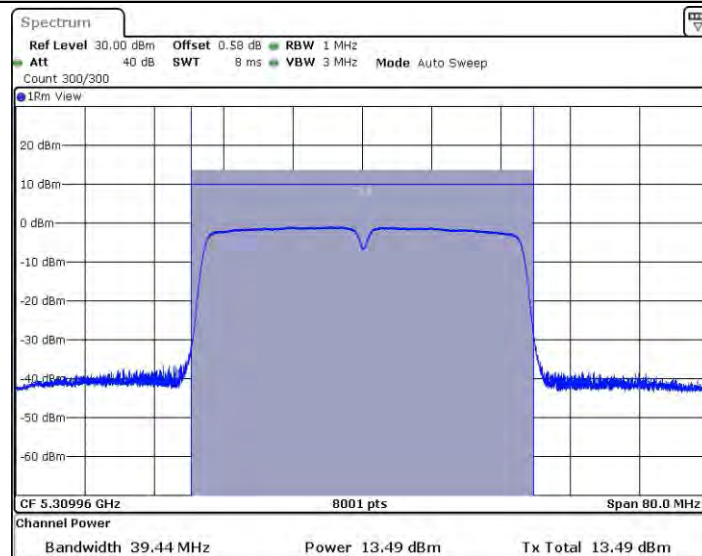
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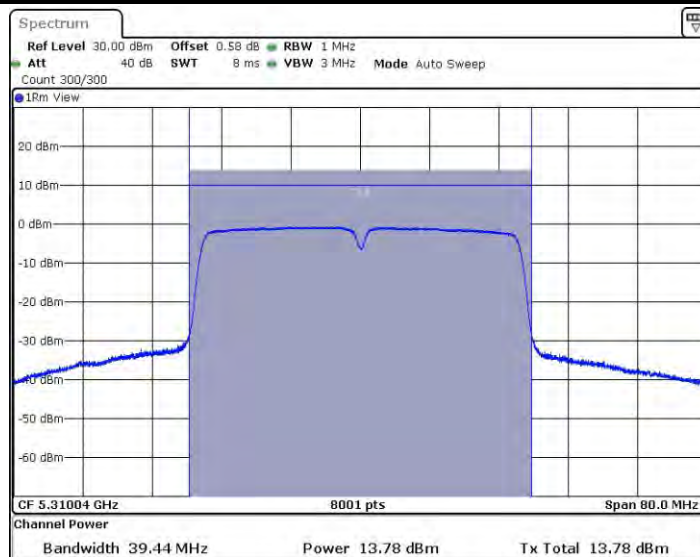
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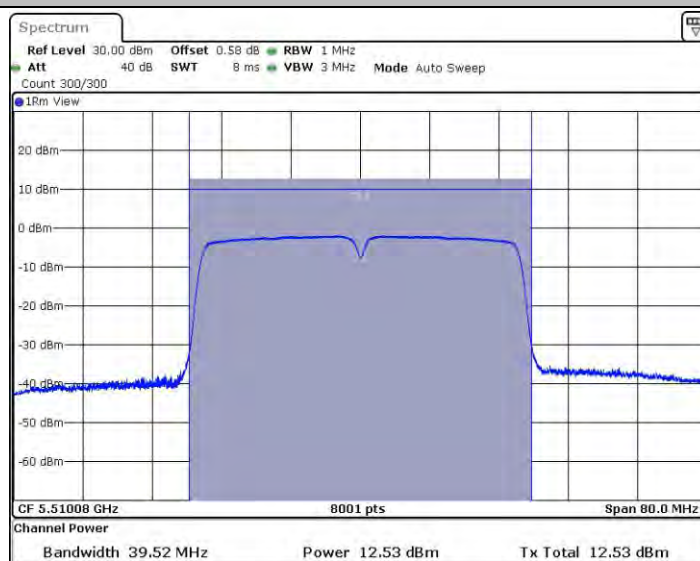
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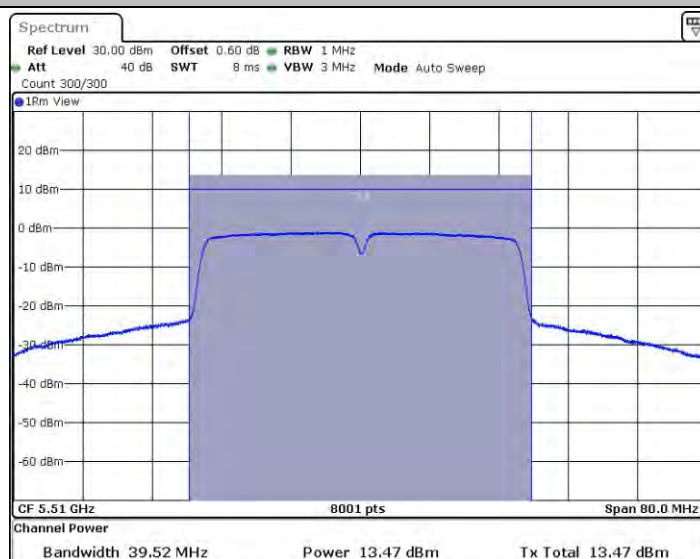
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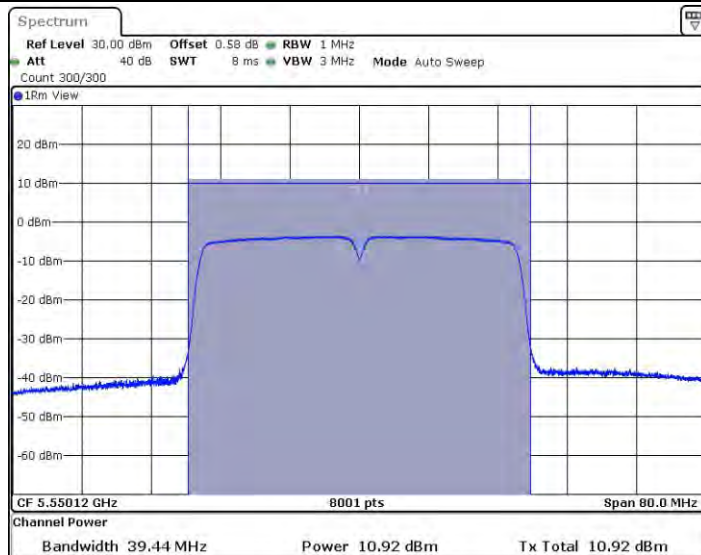
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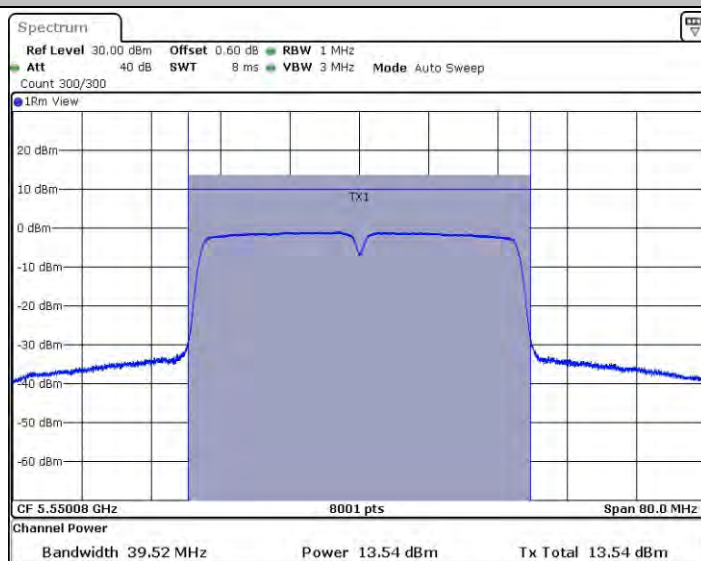
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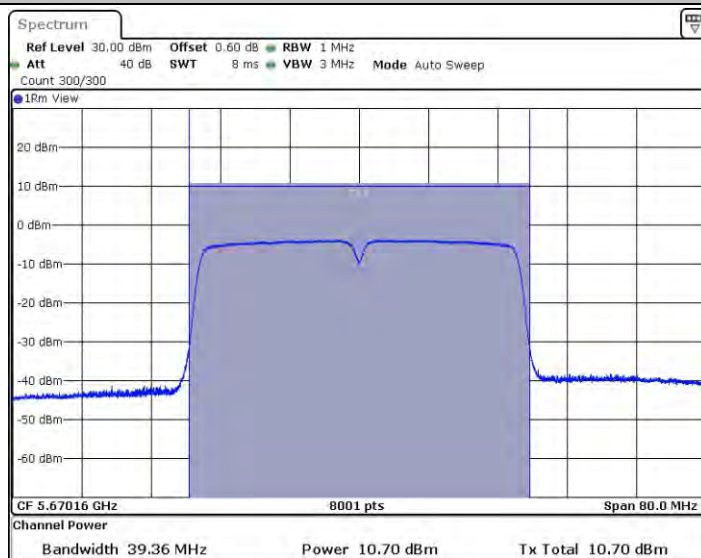
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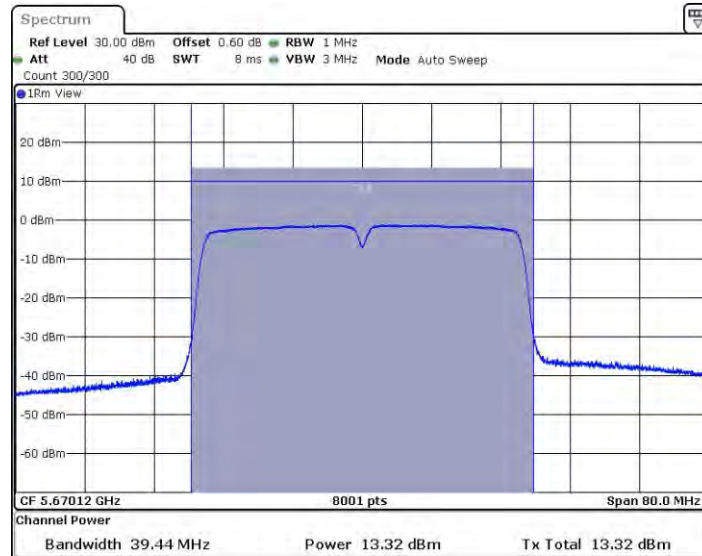
11N40MIMO_Ant0_5550



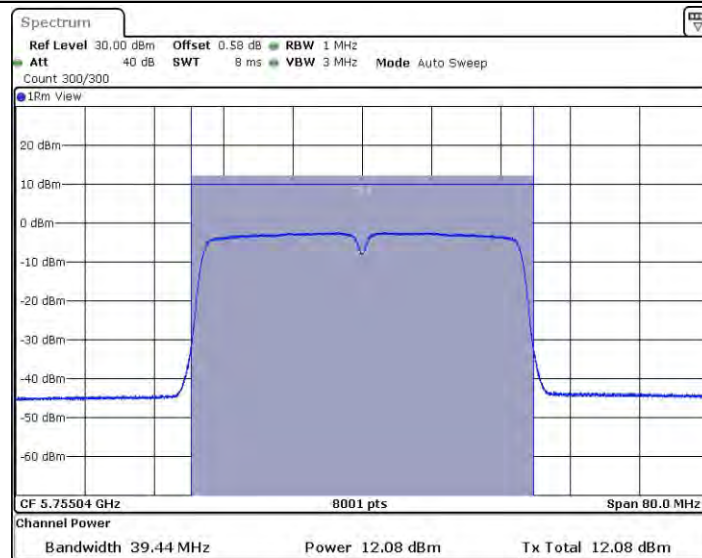
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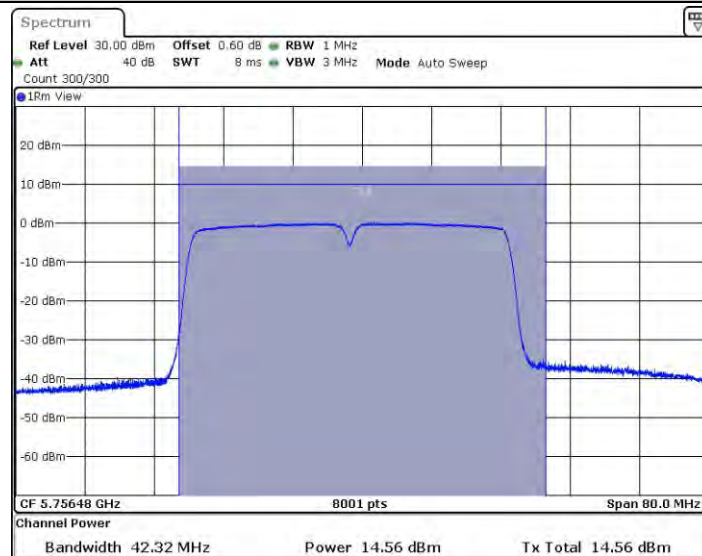
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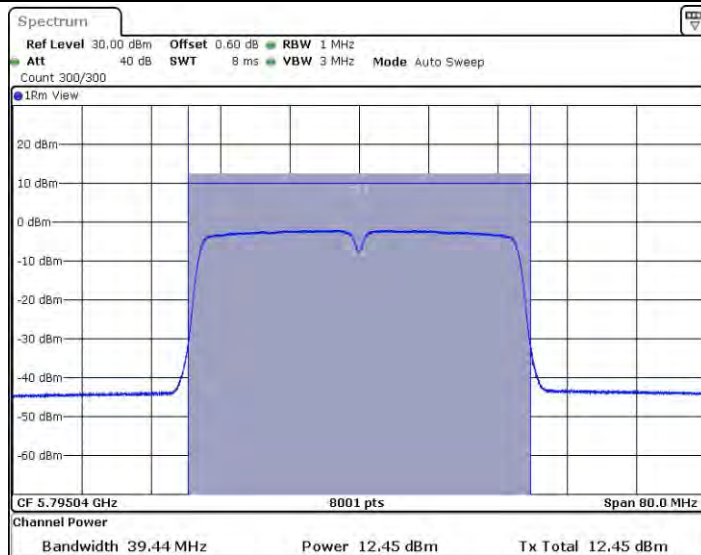
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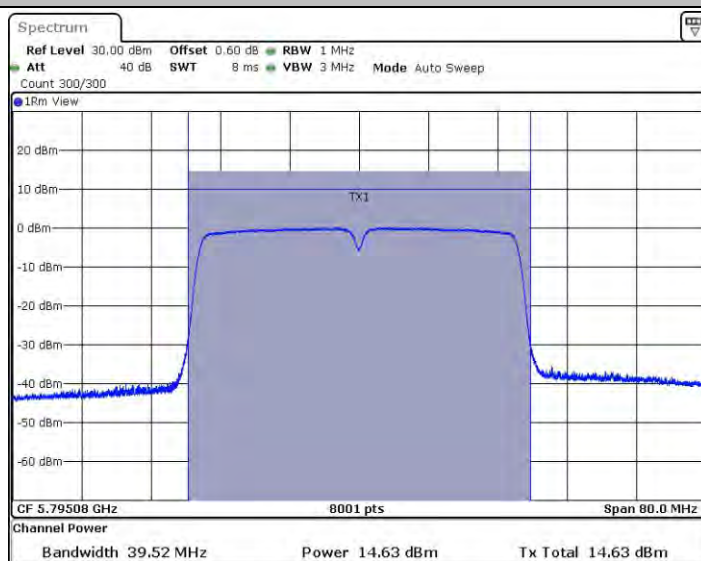
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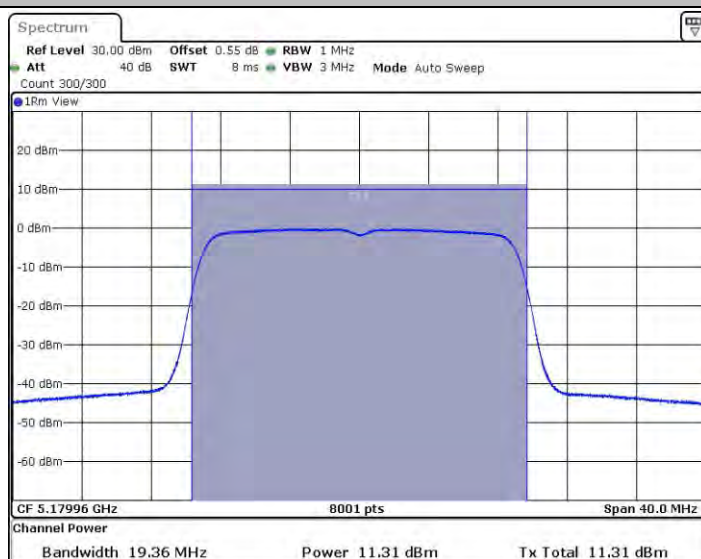
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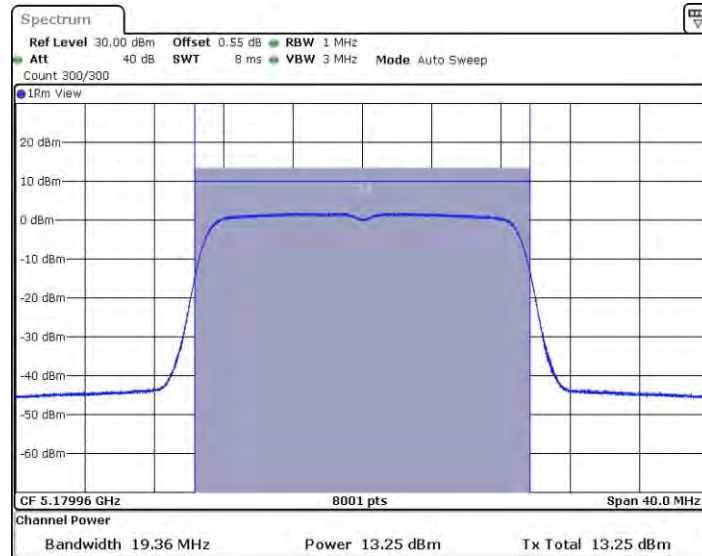
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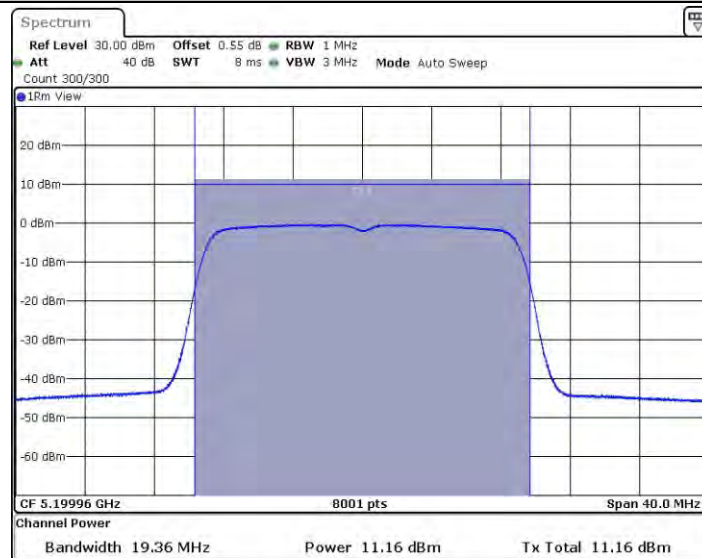
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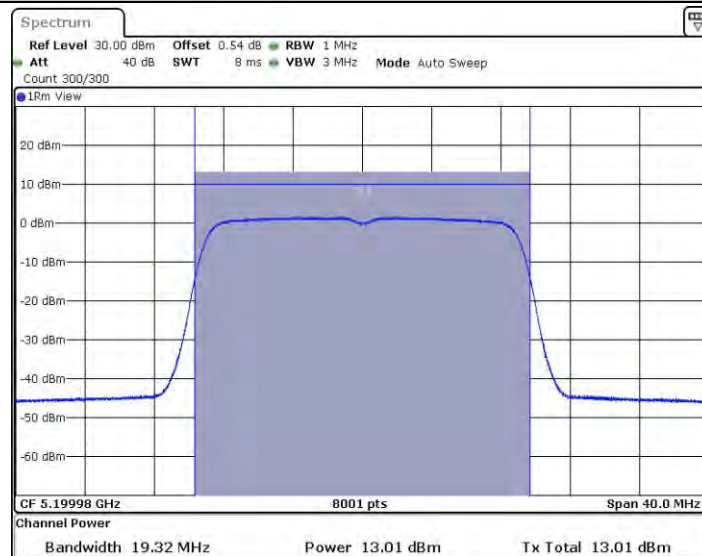
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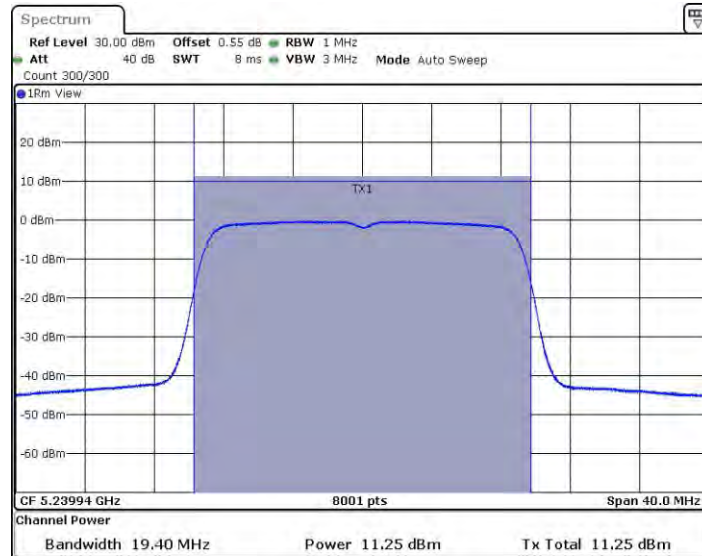
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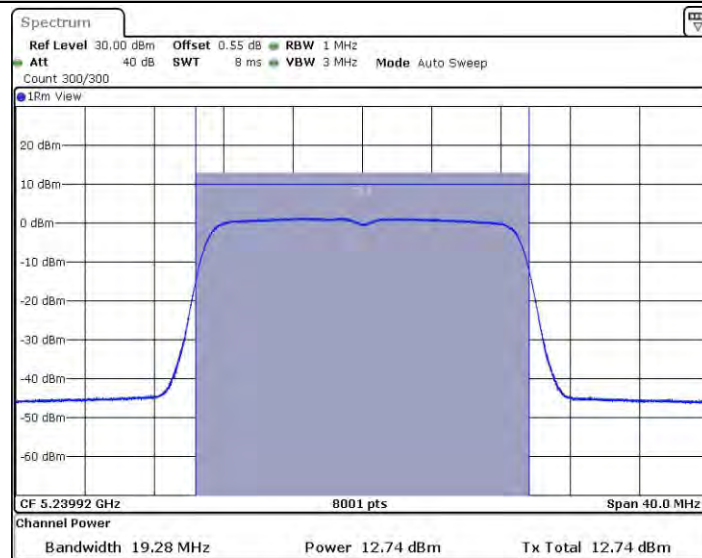
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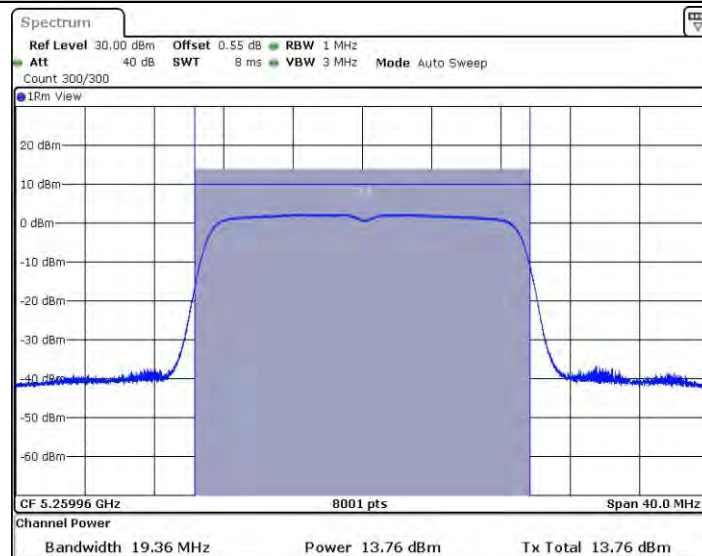
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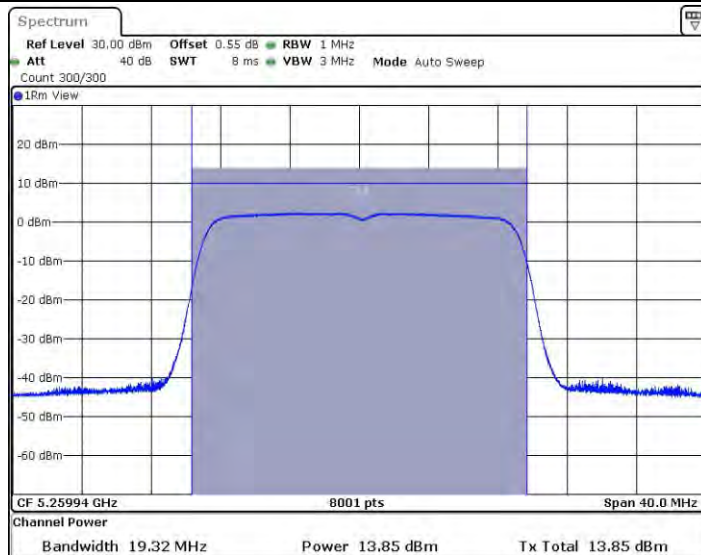
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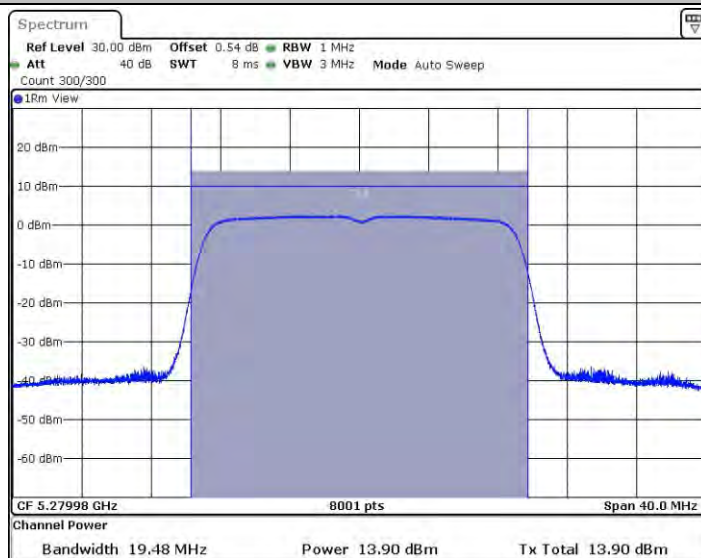
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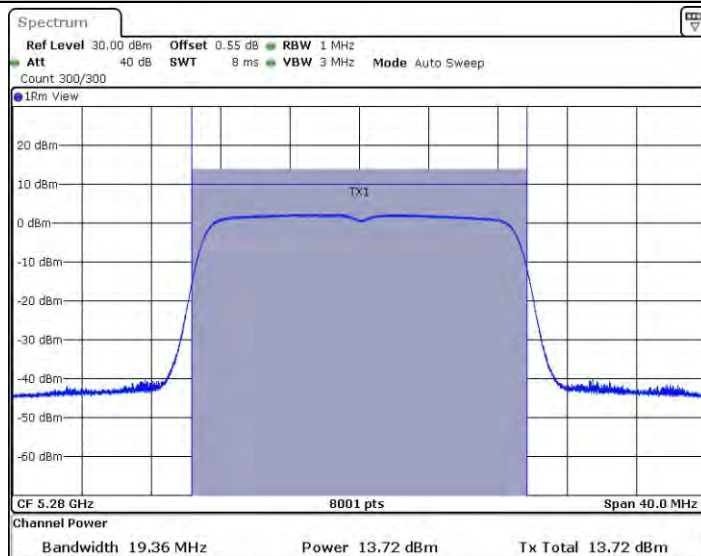
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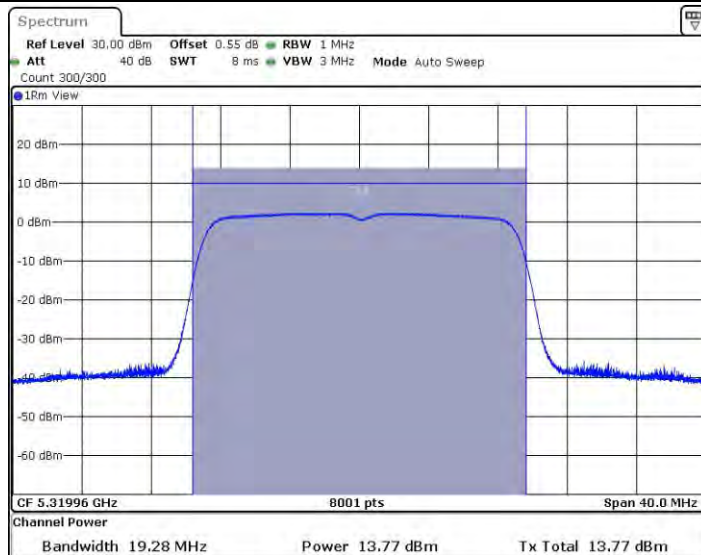
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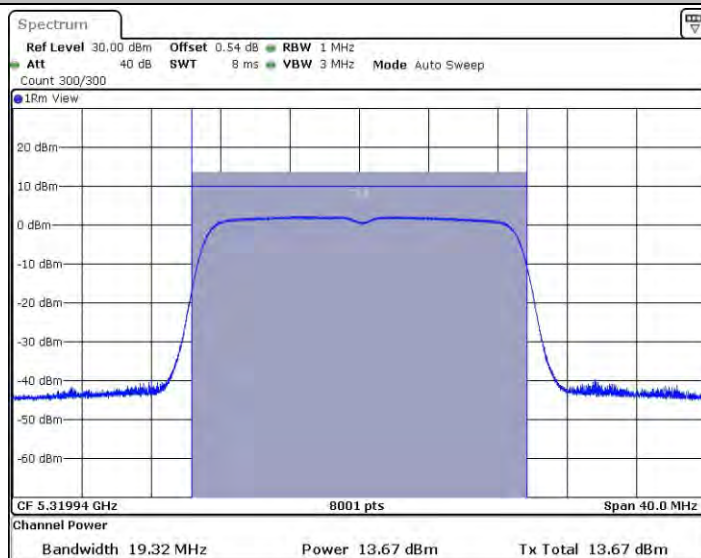
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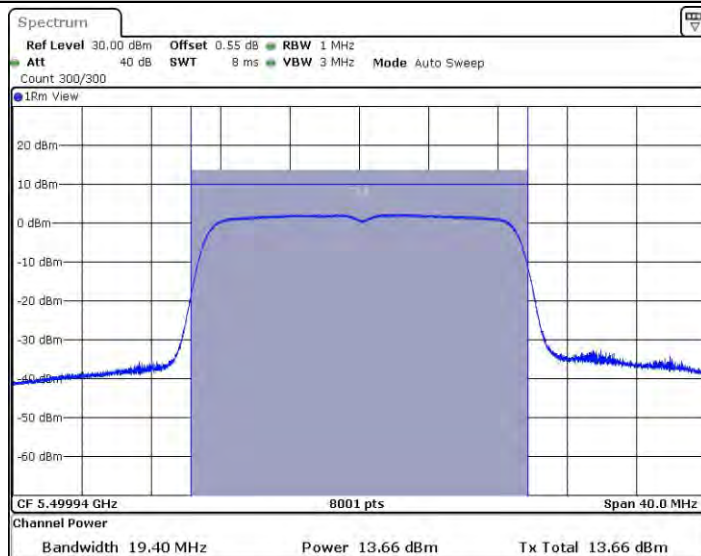
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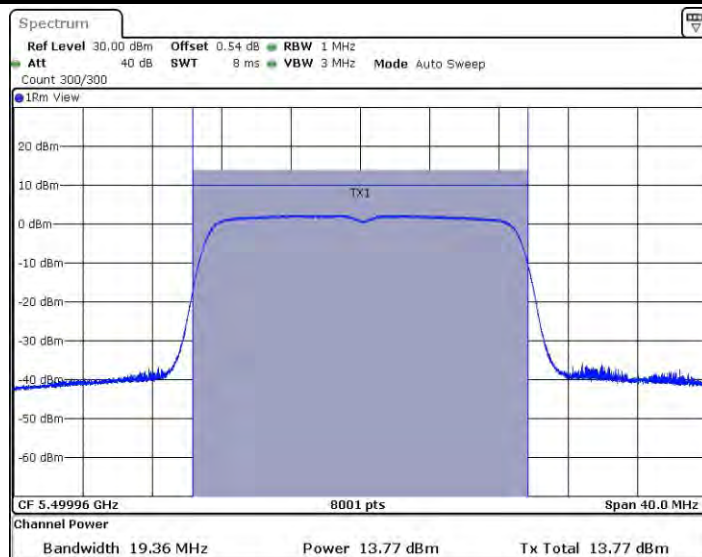
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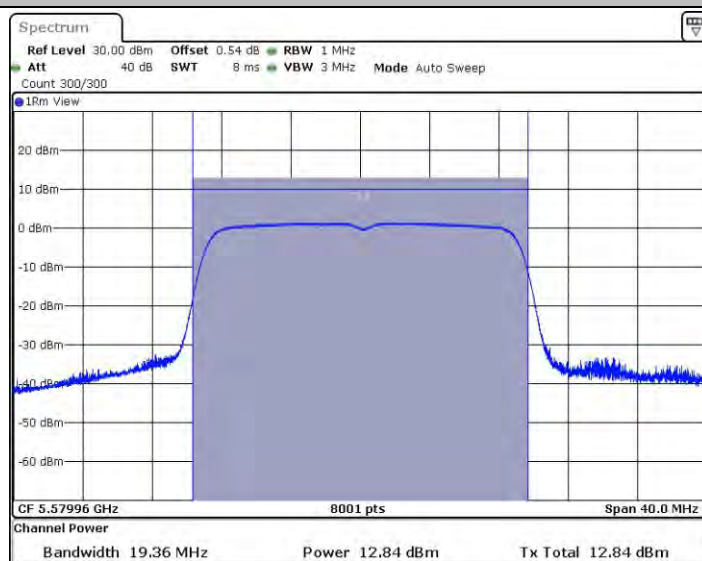
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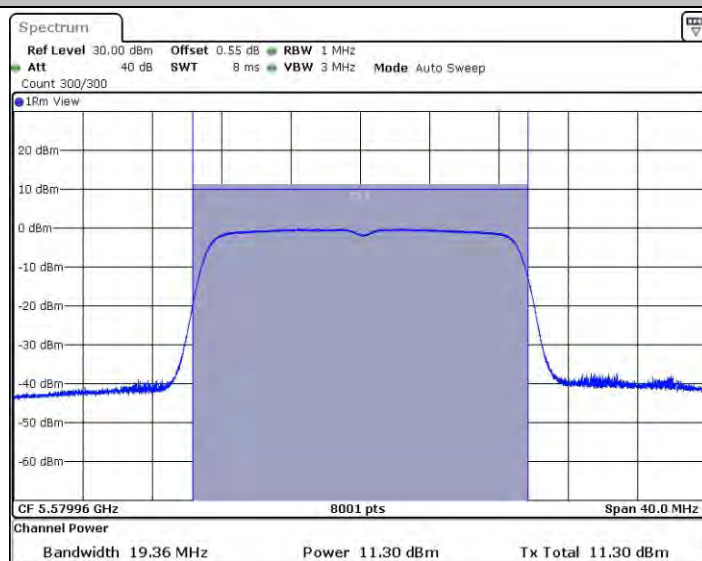
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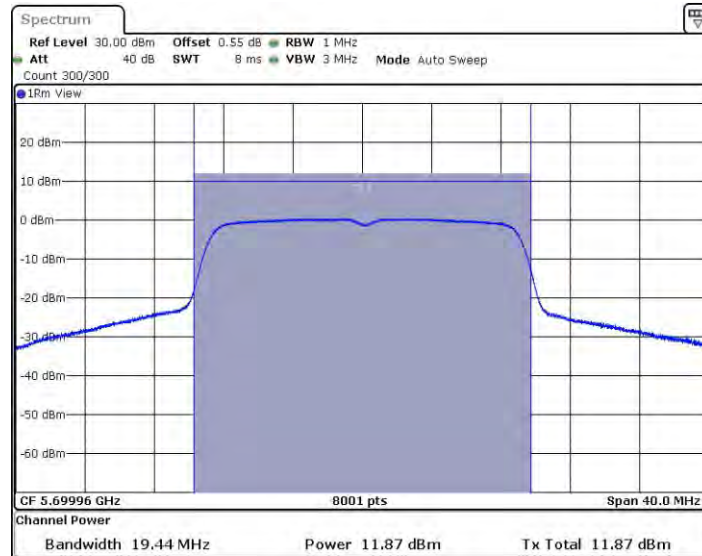
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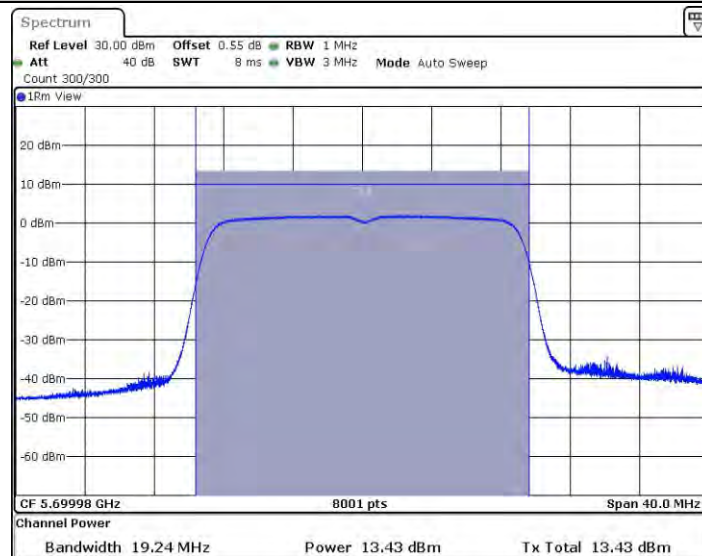
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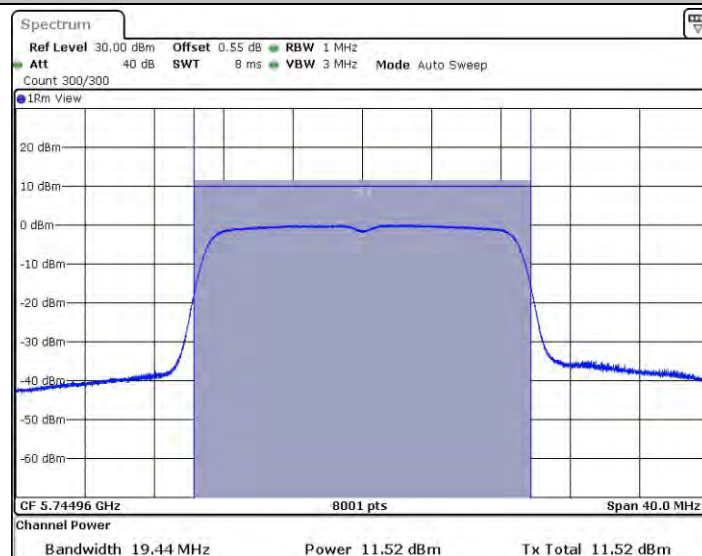
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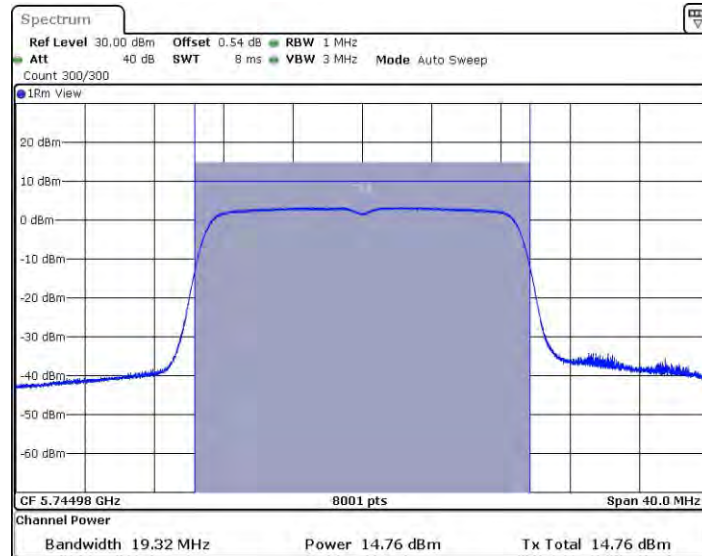
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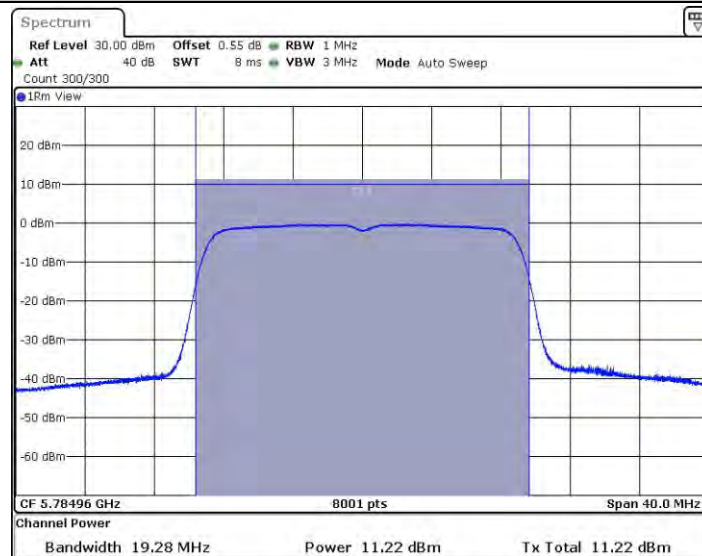
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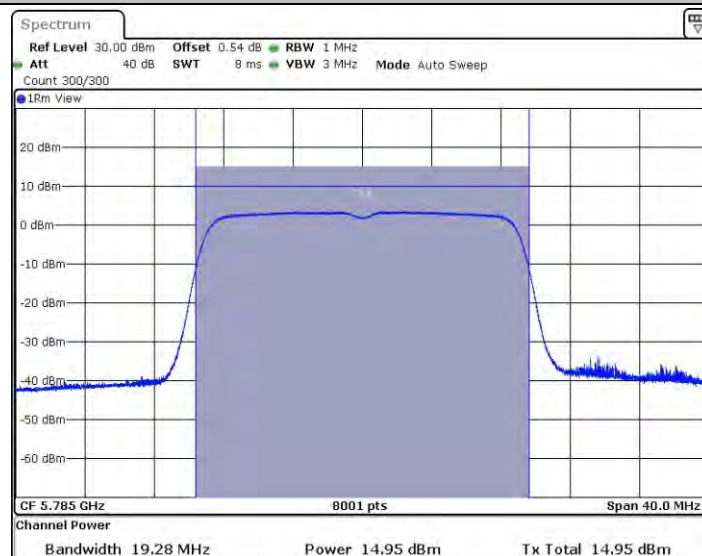
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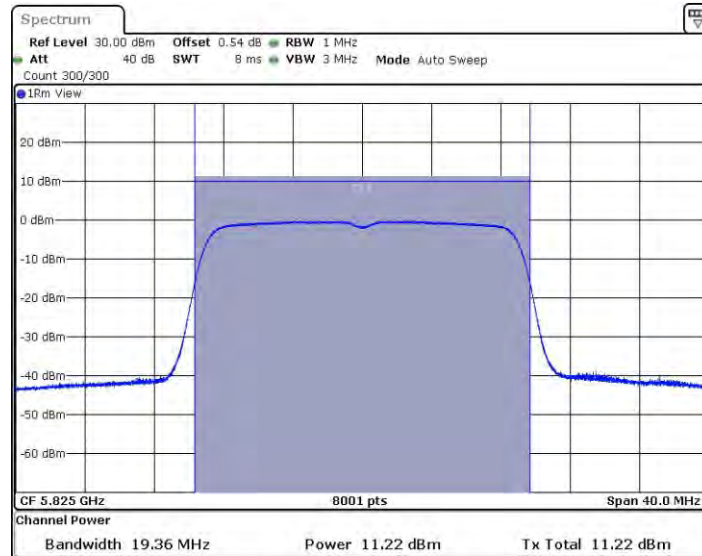
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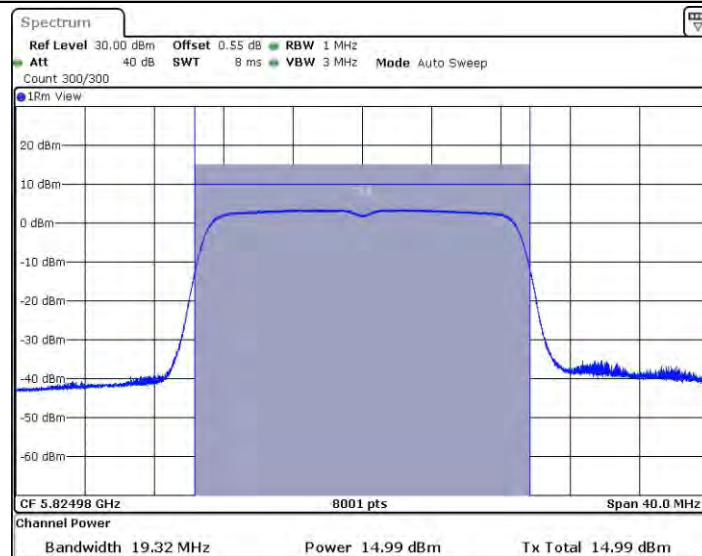
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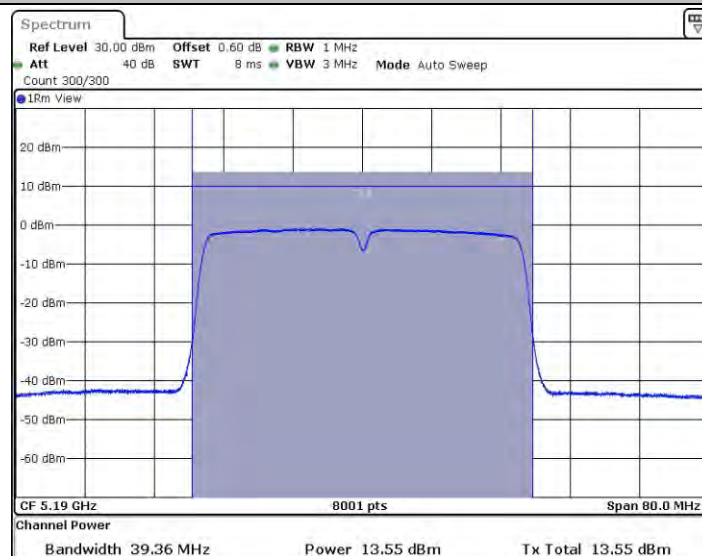
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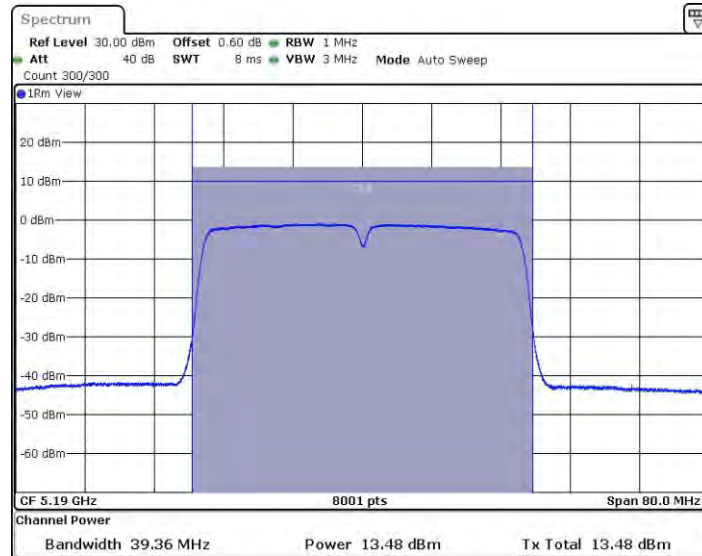
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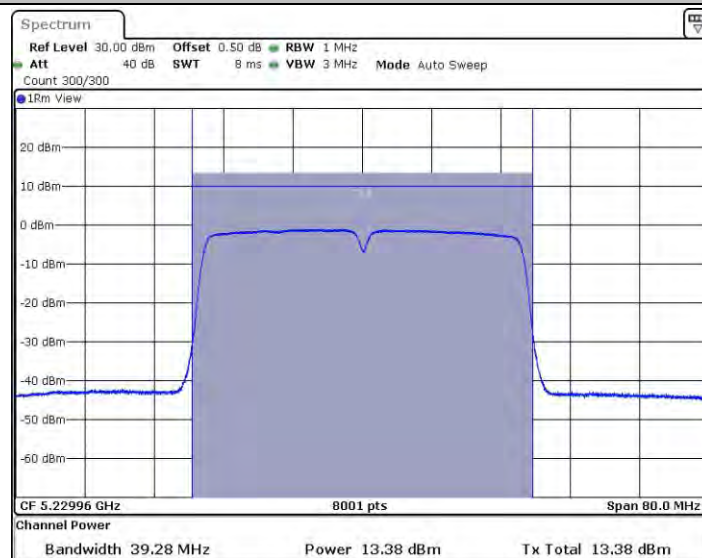
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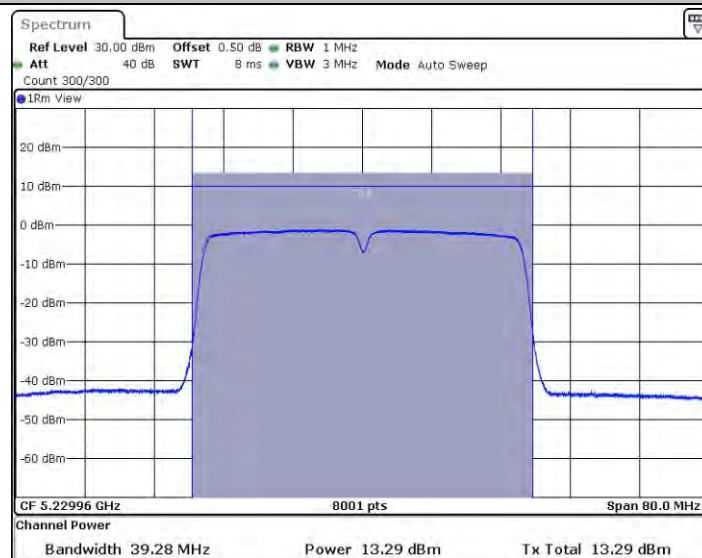
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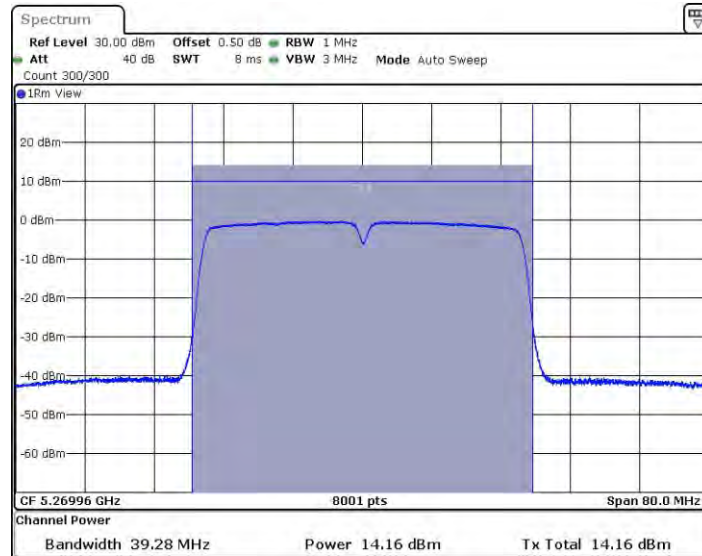
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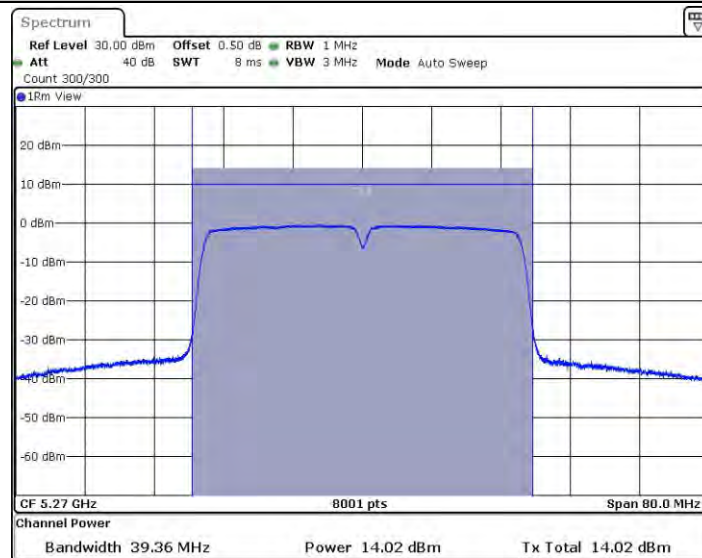
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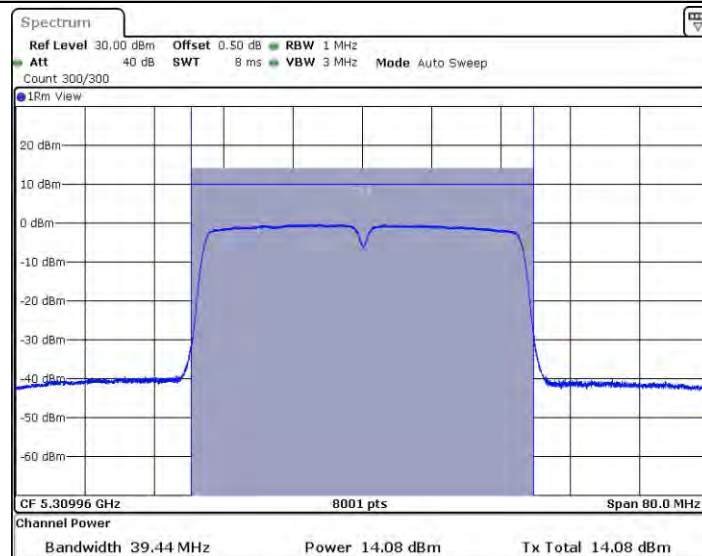
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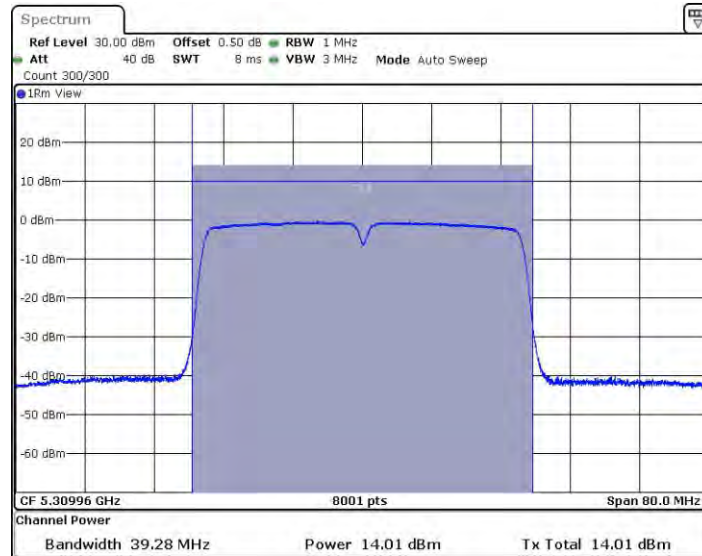
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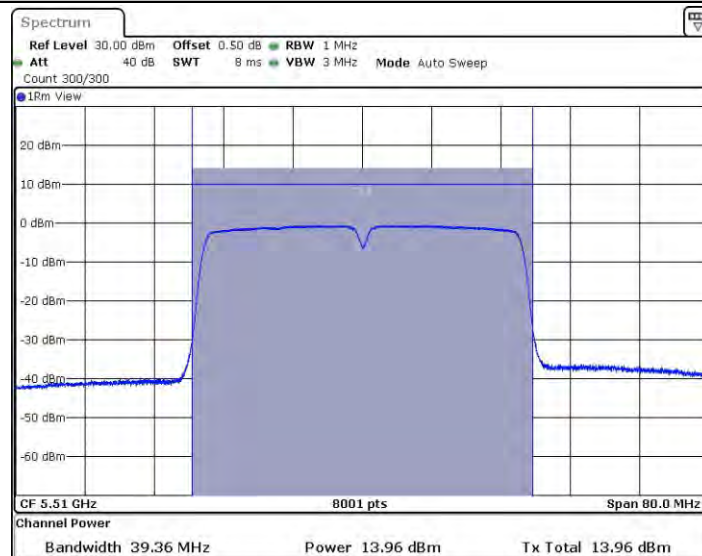
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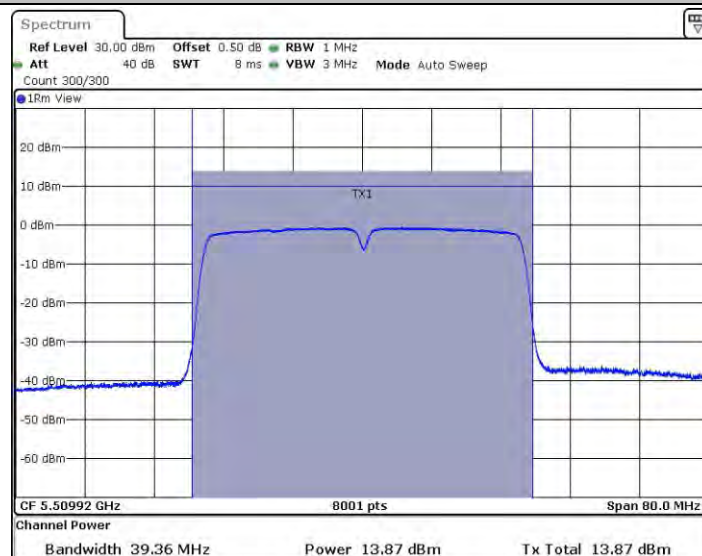
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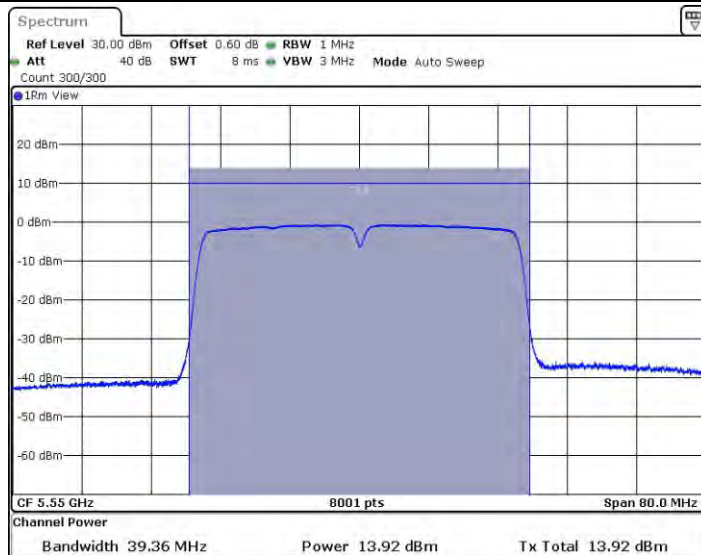
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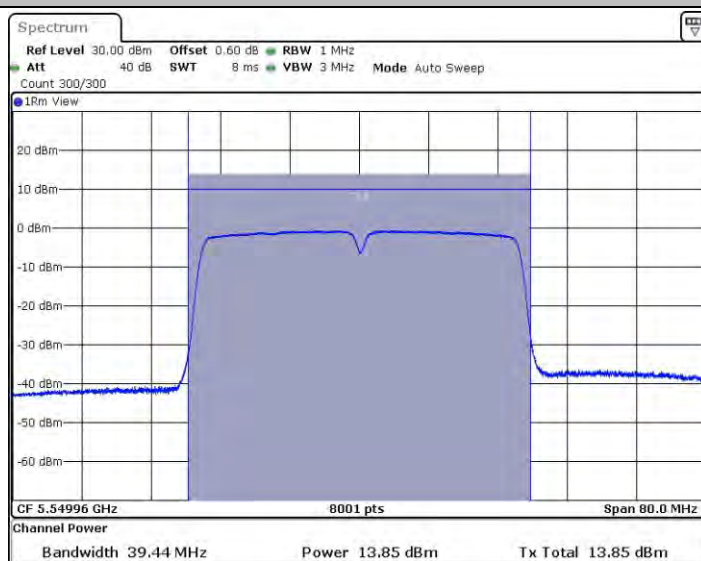
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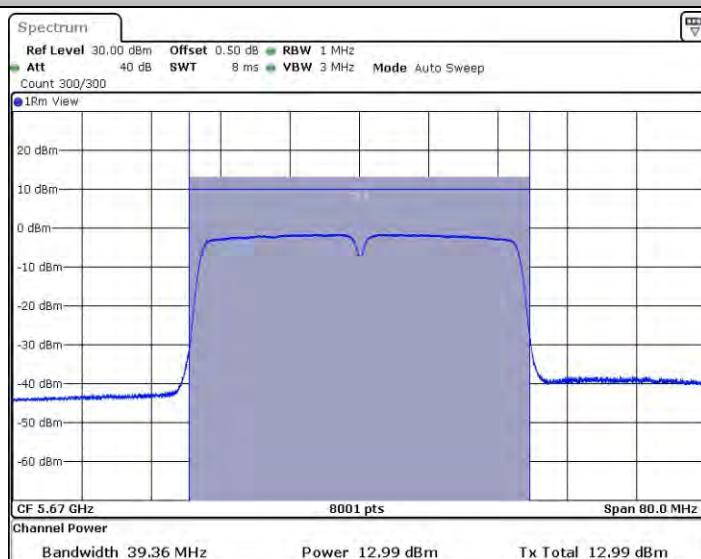
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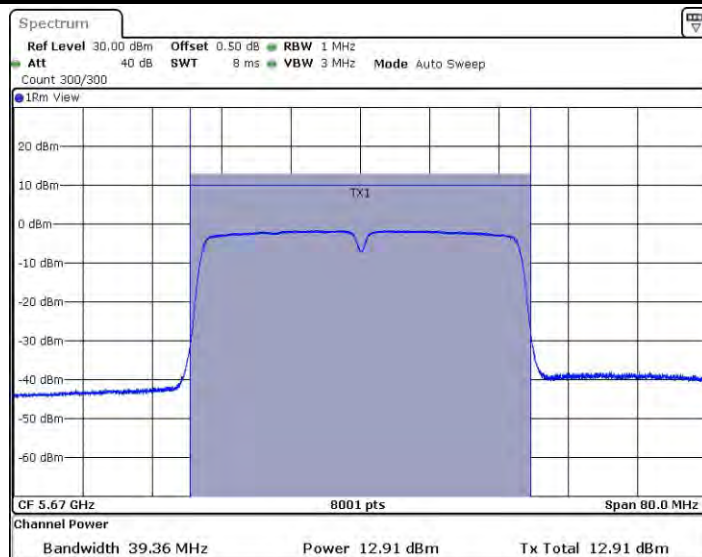
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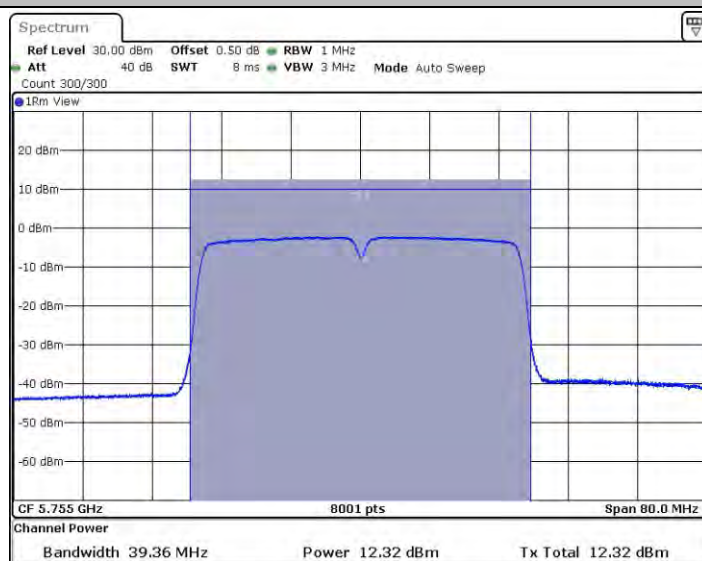
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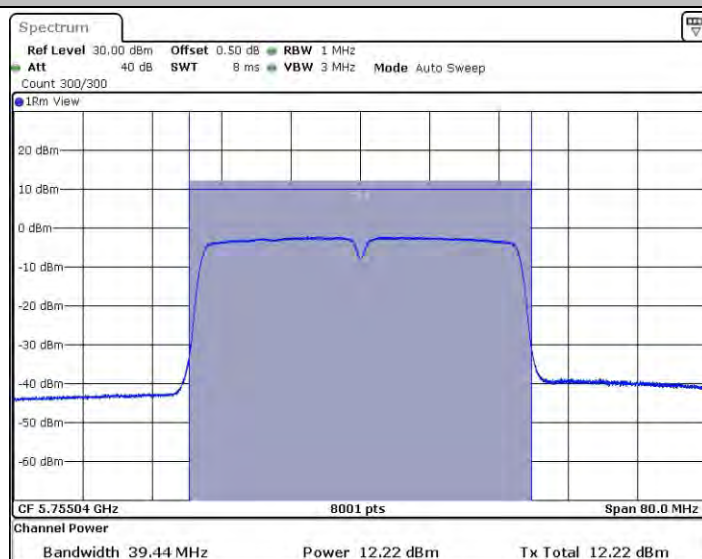
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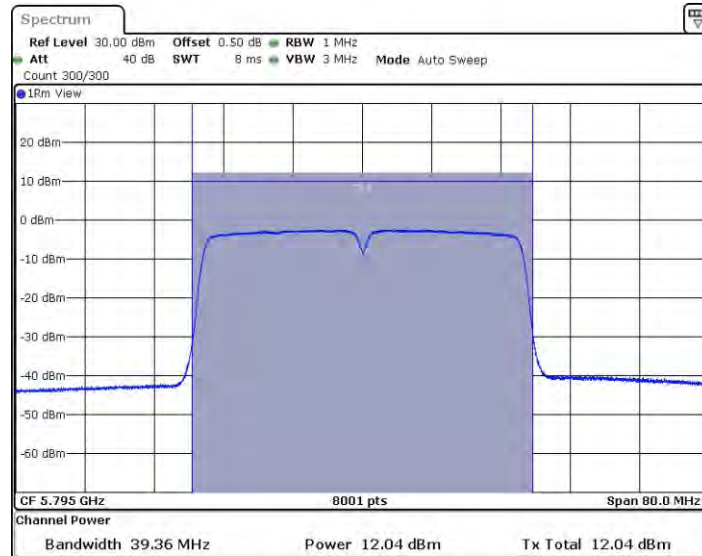
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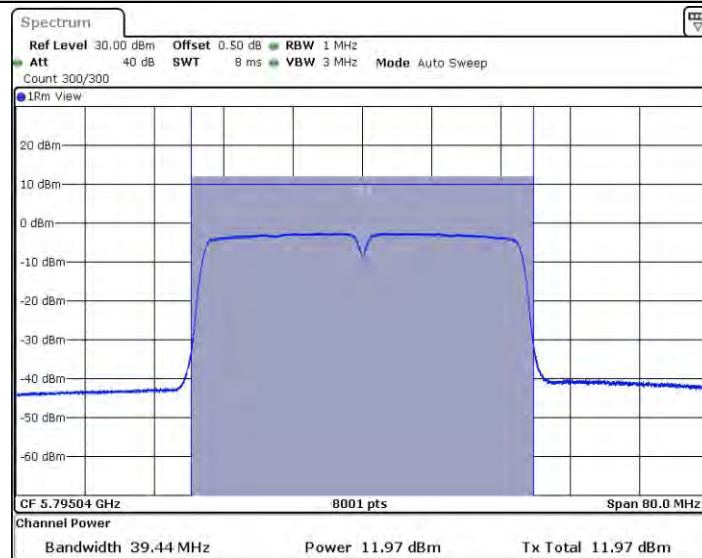
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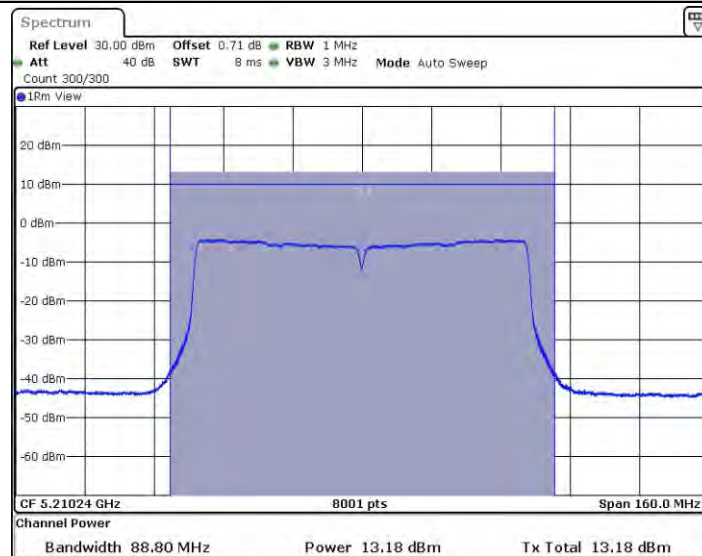
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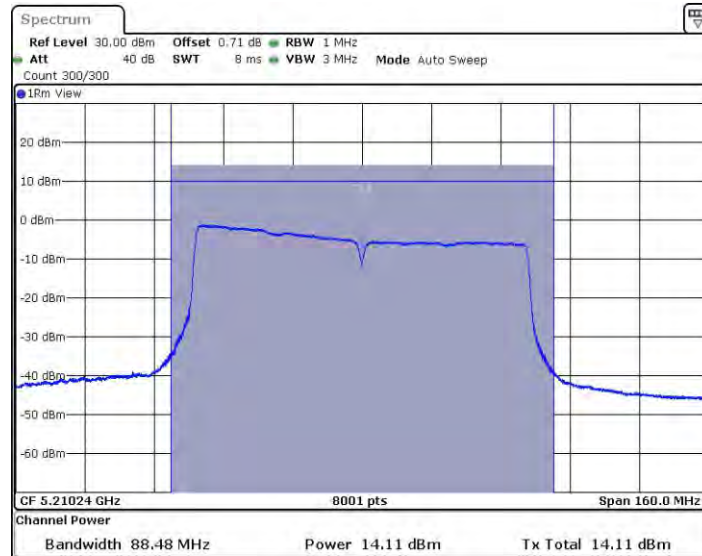
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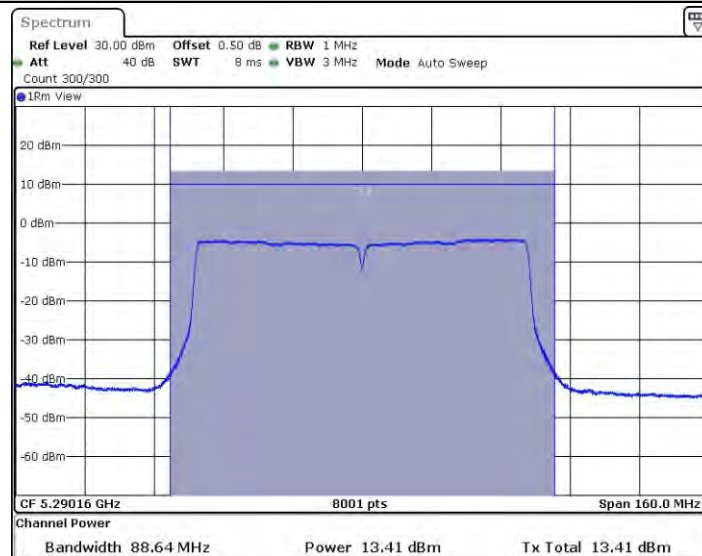
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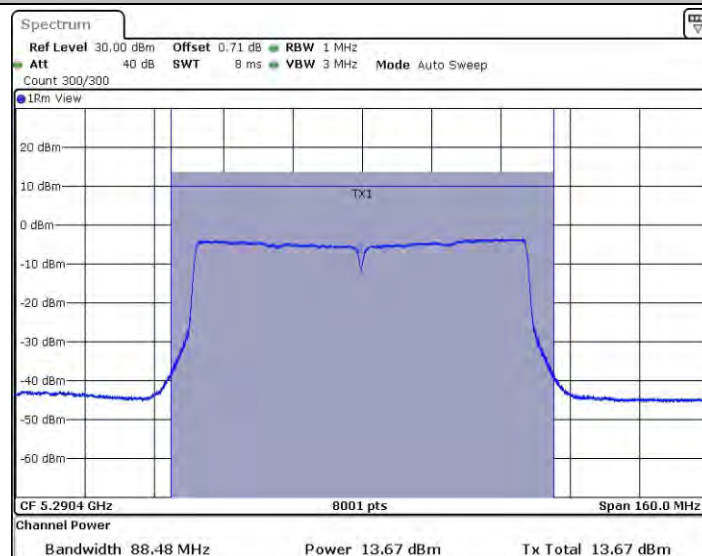
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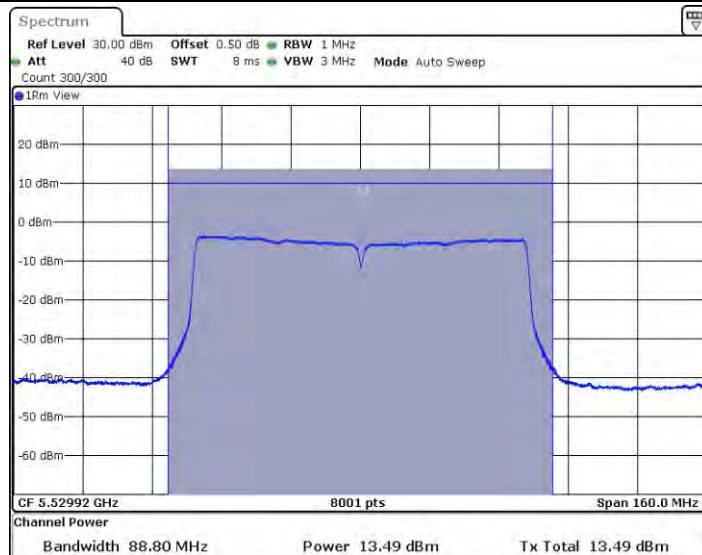
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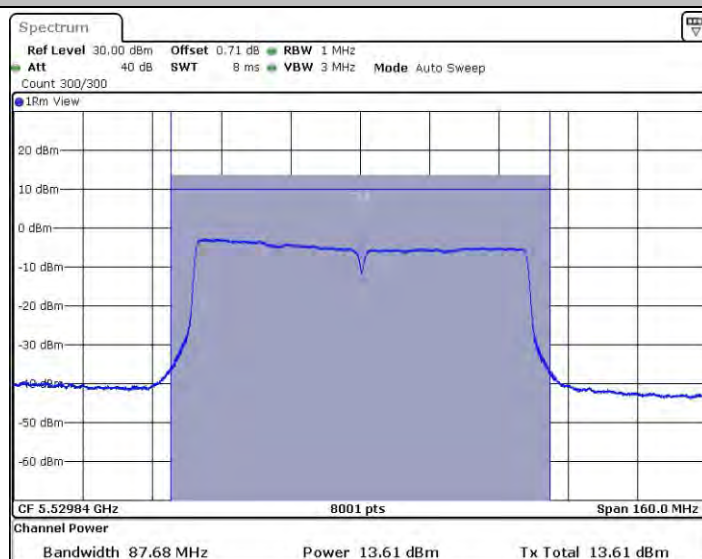
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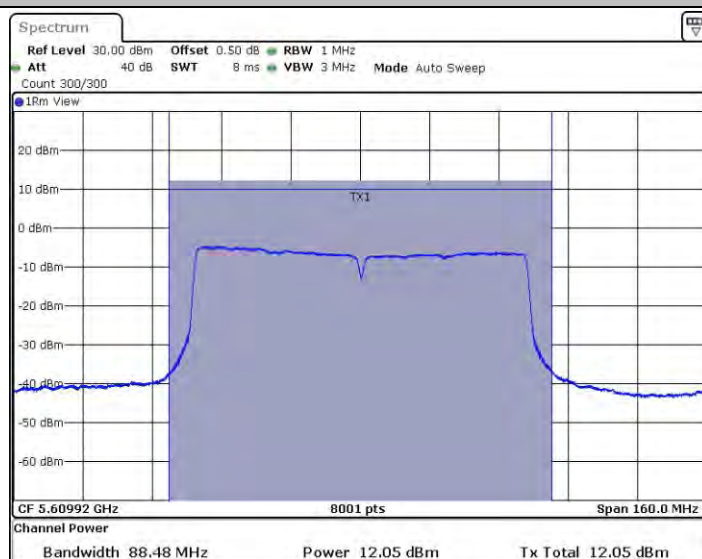
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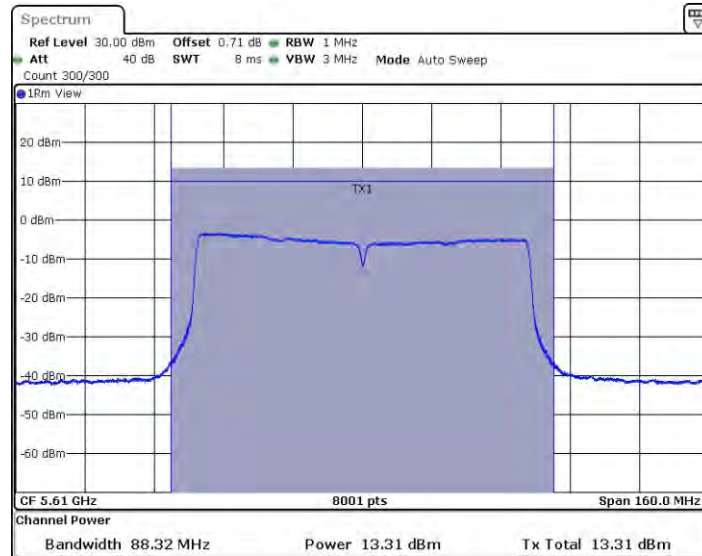
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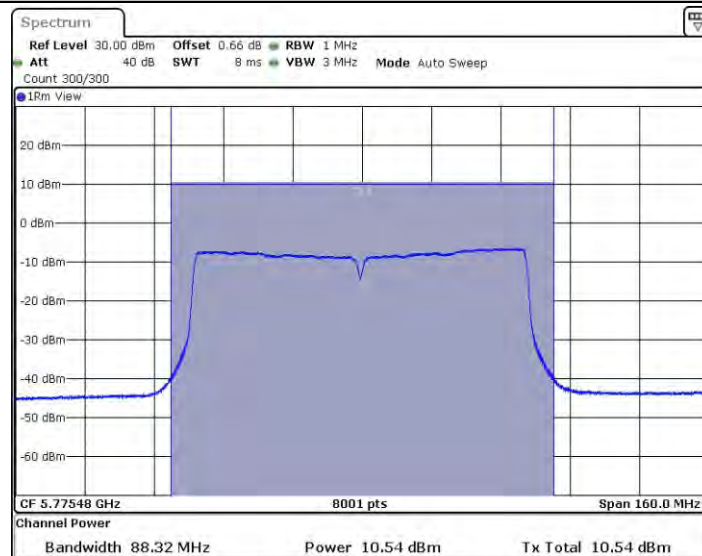
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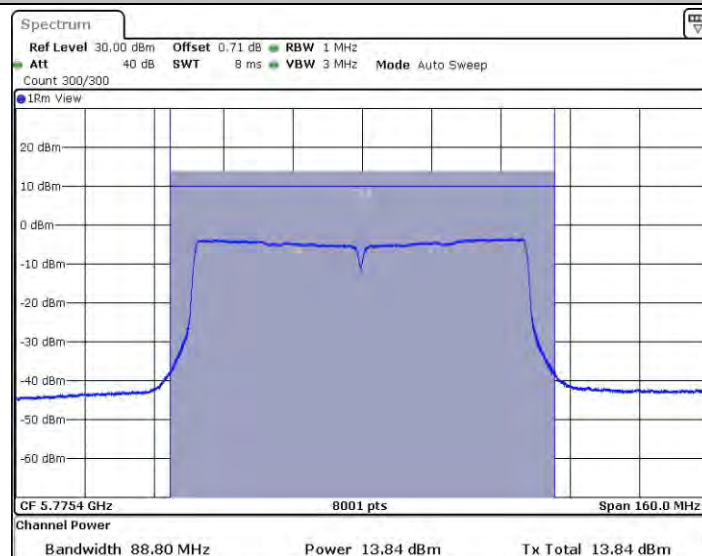
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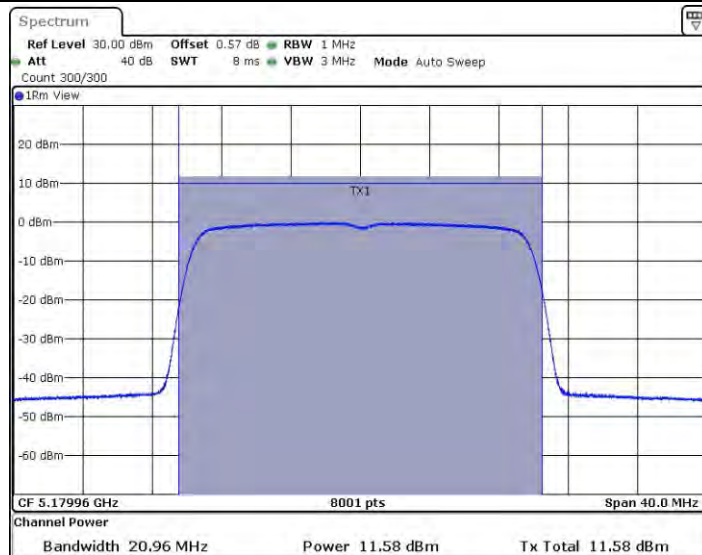
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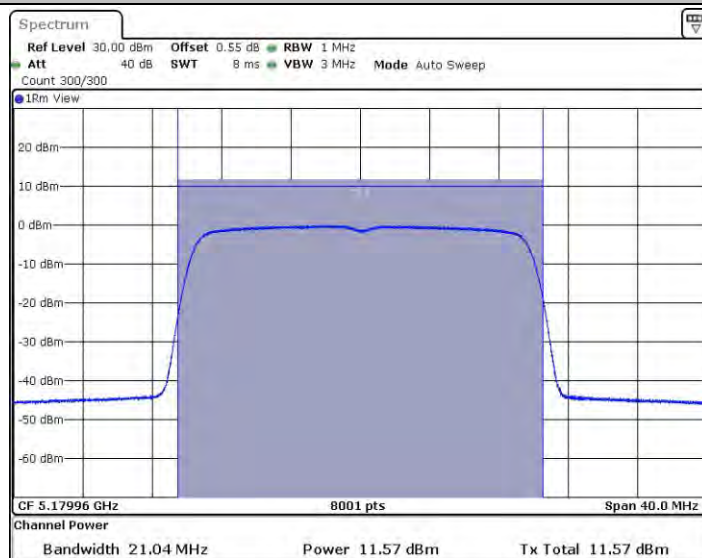
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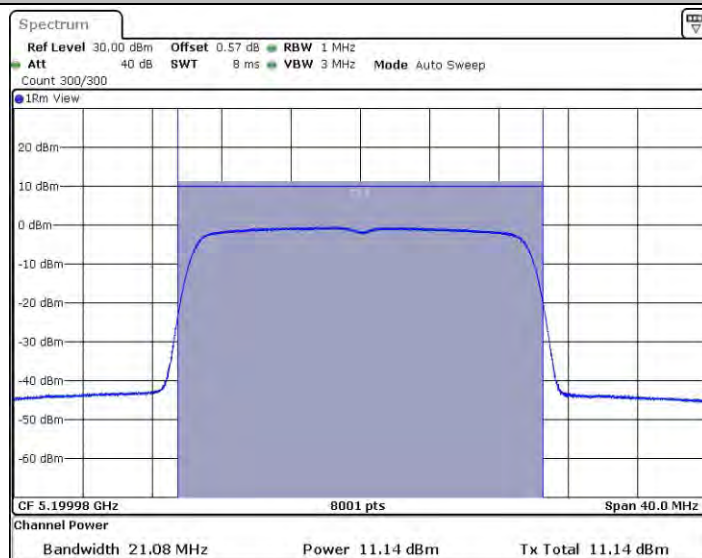
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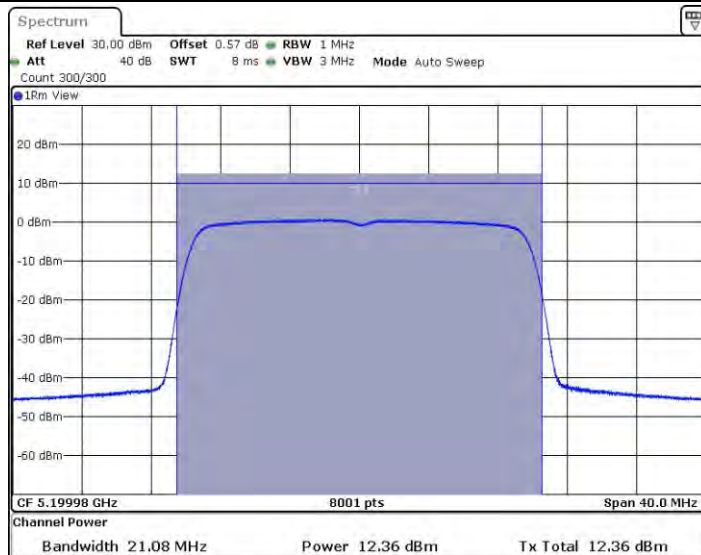
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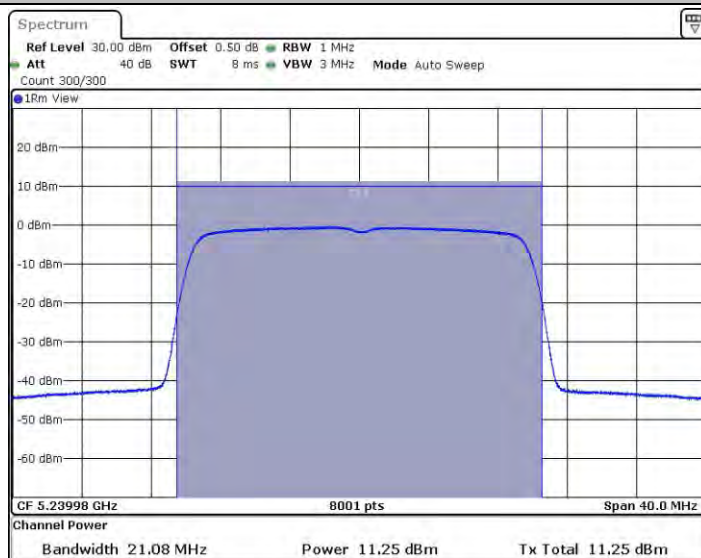
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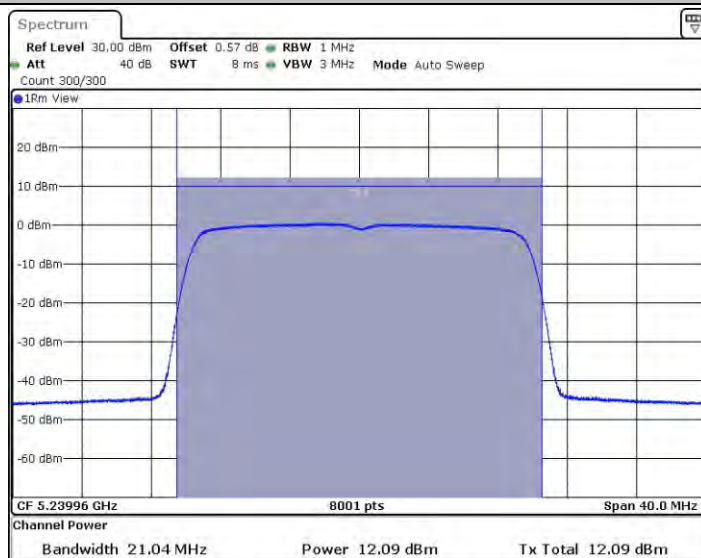
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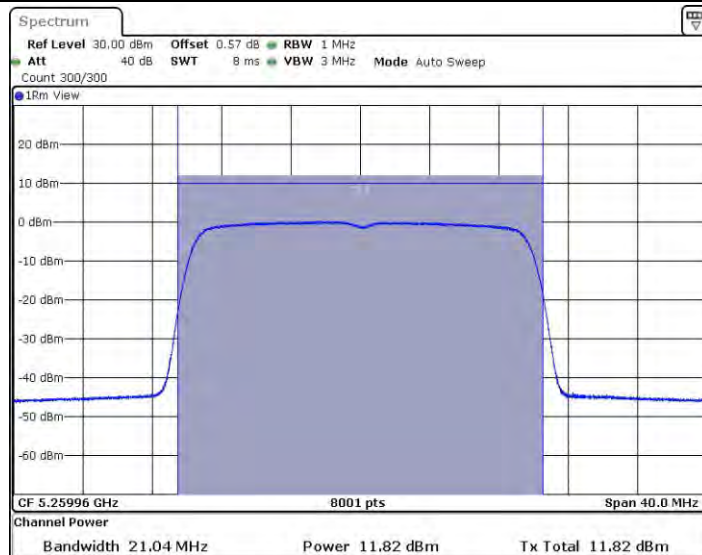
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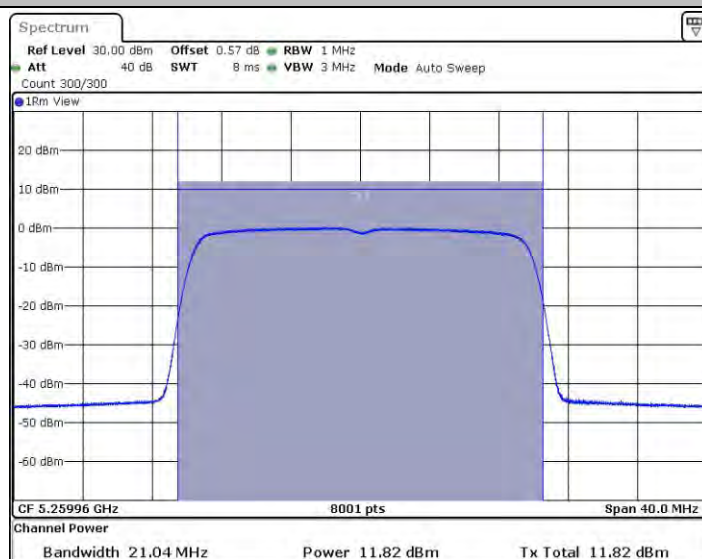
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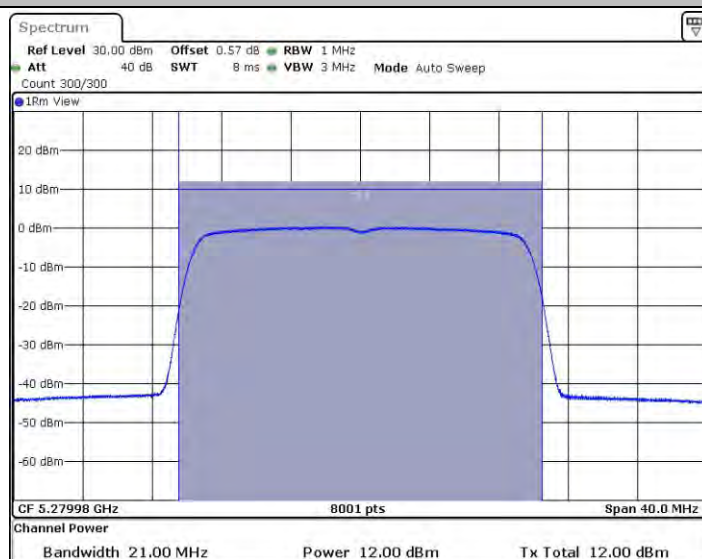
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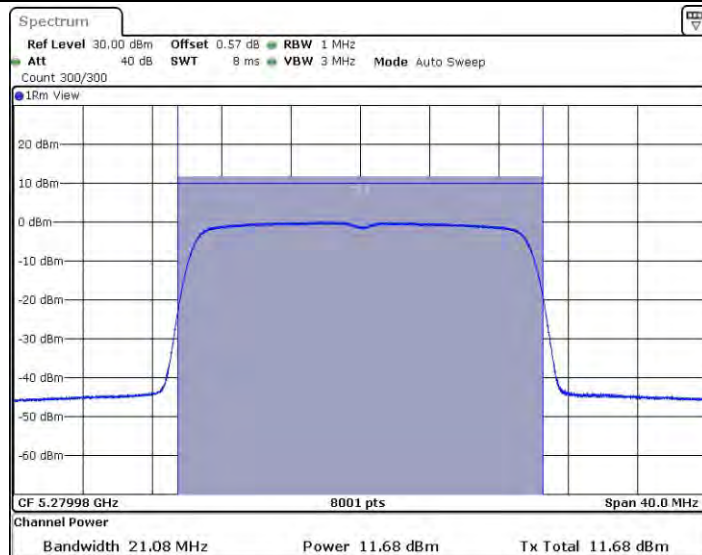
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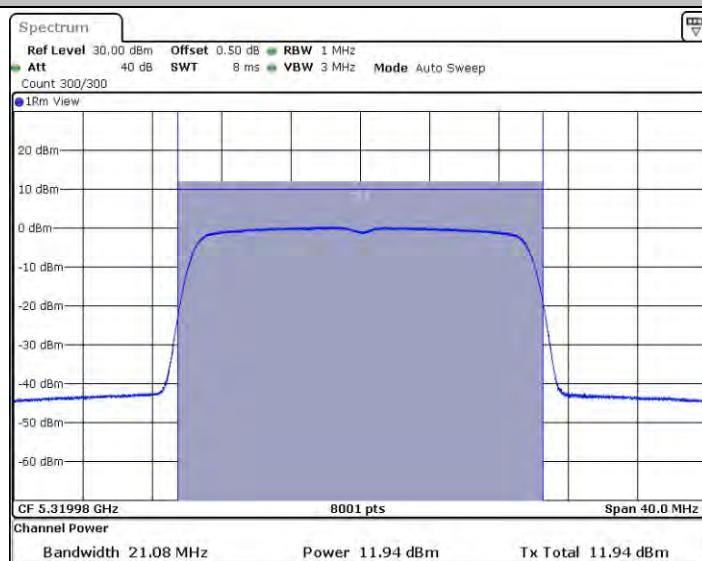
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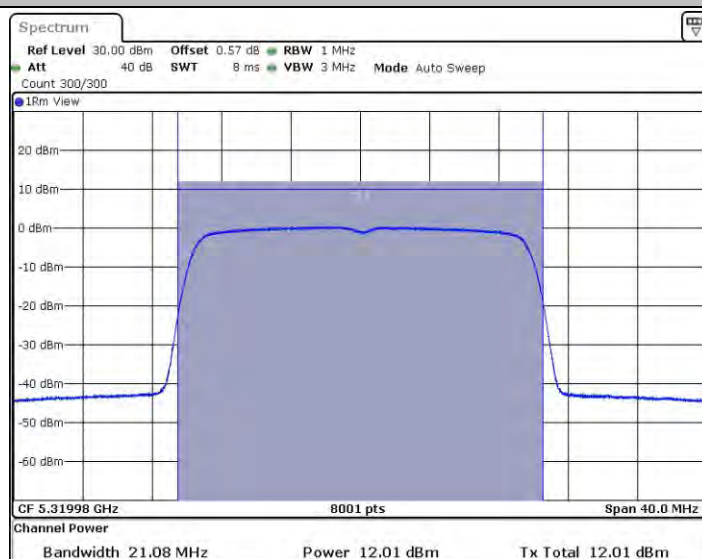
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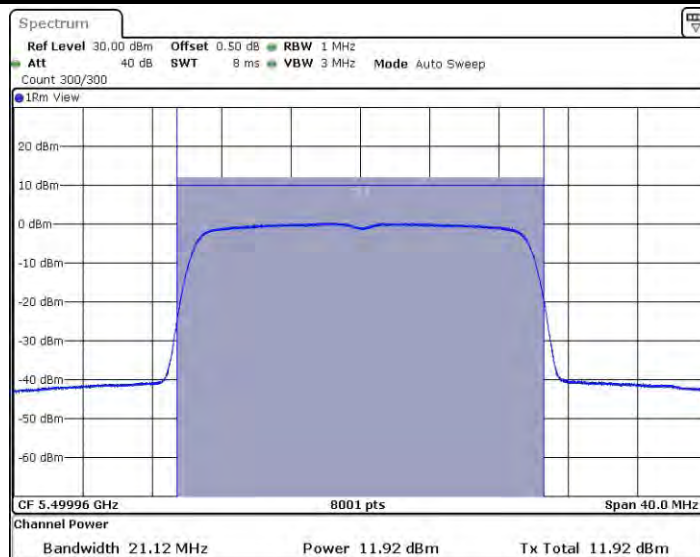
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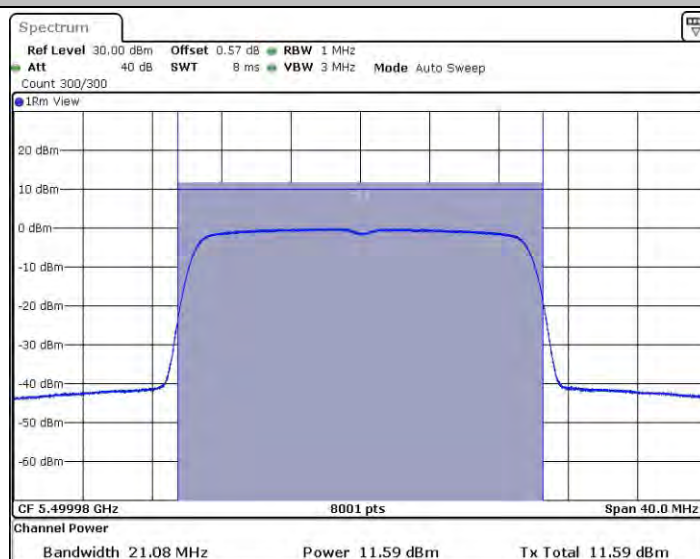
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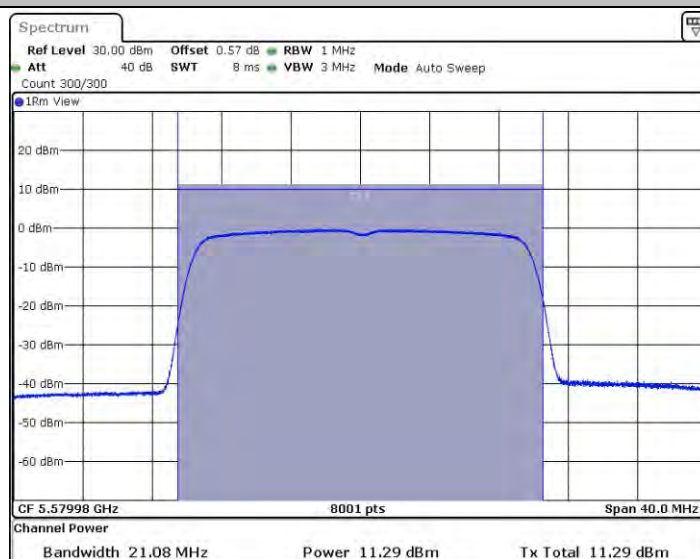
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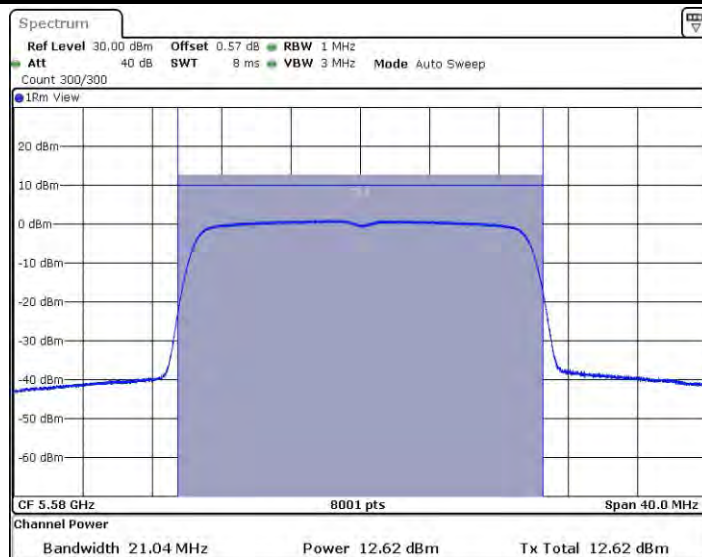
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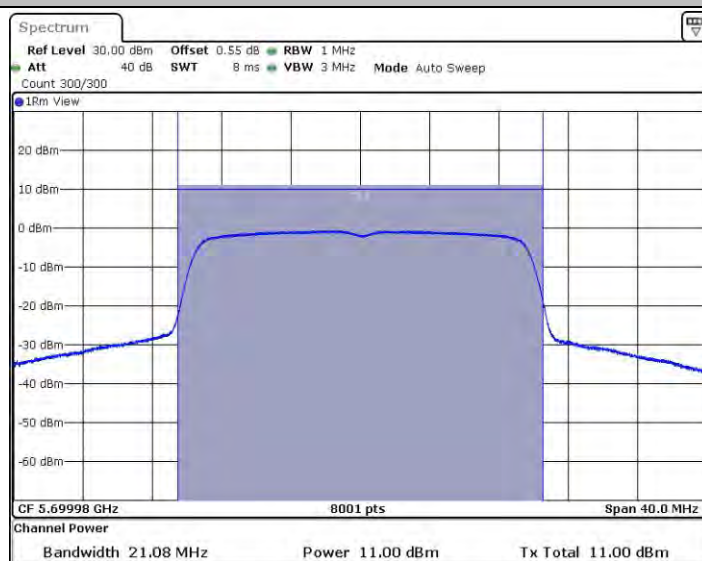
11AX20MIMO_Ant1_5500



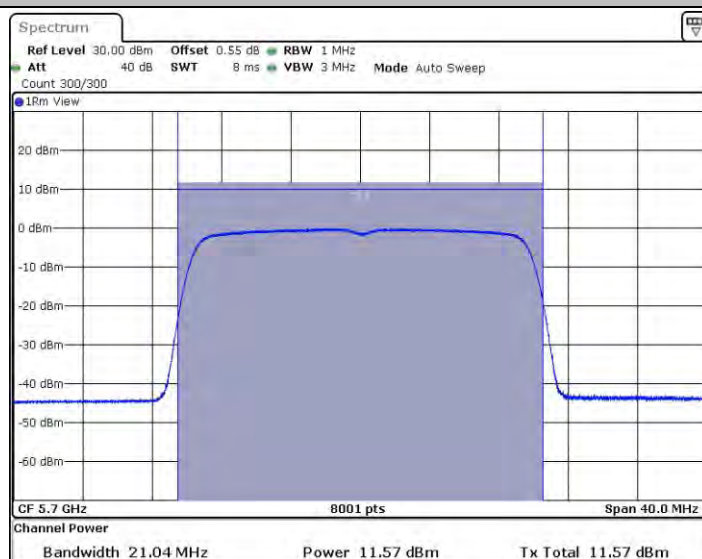
11AX20MIMO_Ant0_5580



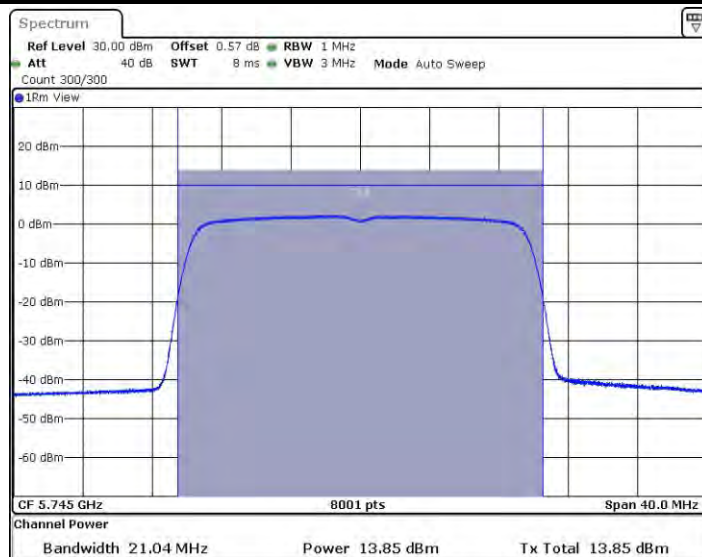
11AX20MIMO_Ant1_5580



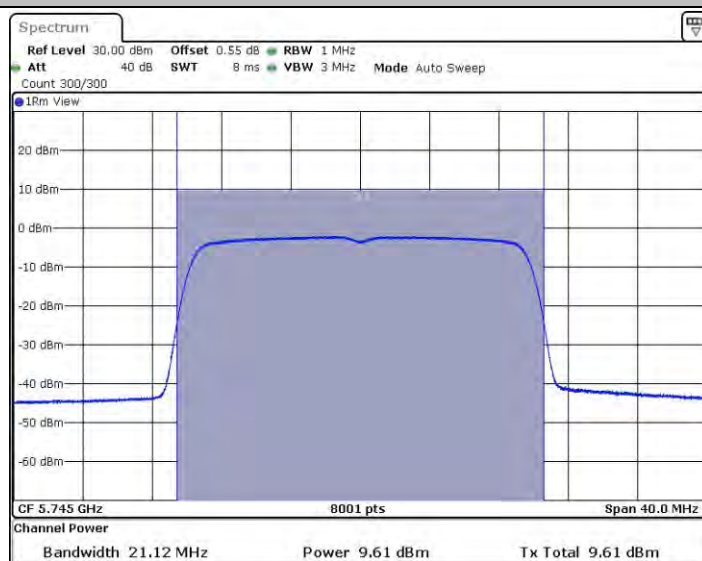
11AX20MIMO_Ant0_5700



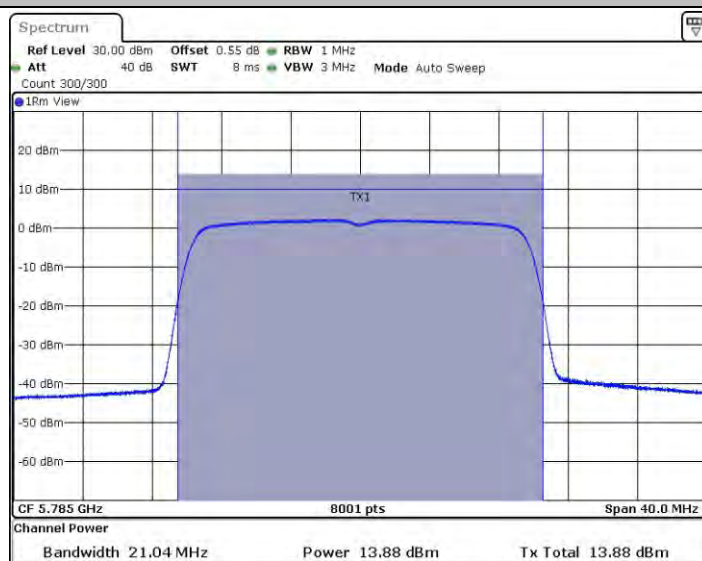
11AX20MIMO_Ant1_5700



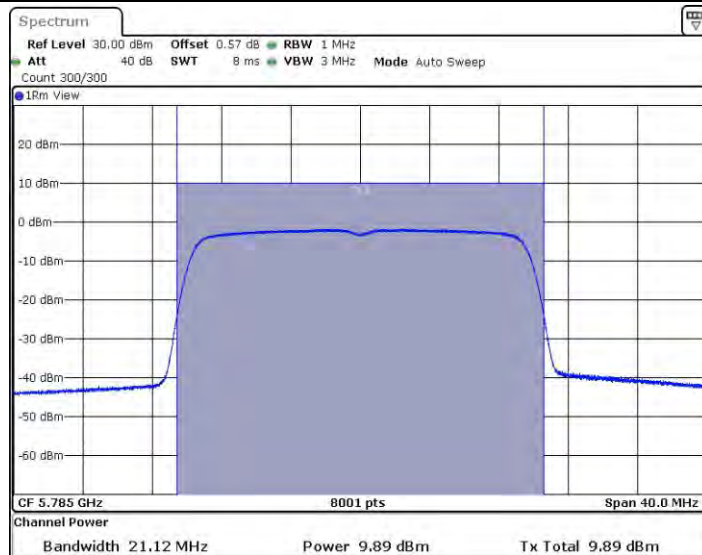
11AX20MIMO_Ant0_5745



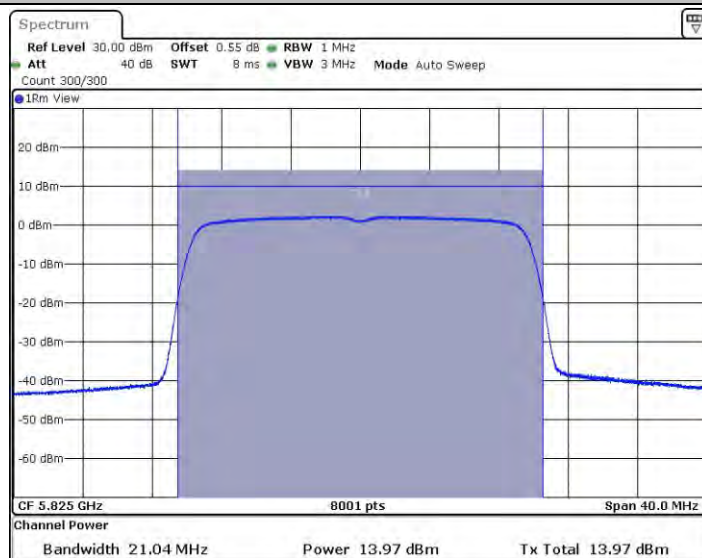
11AX20MIMO_Ant1_5745



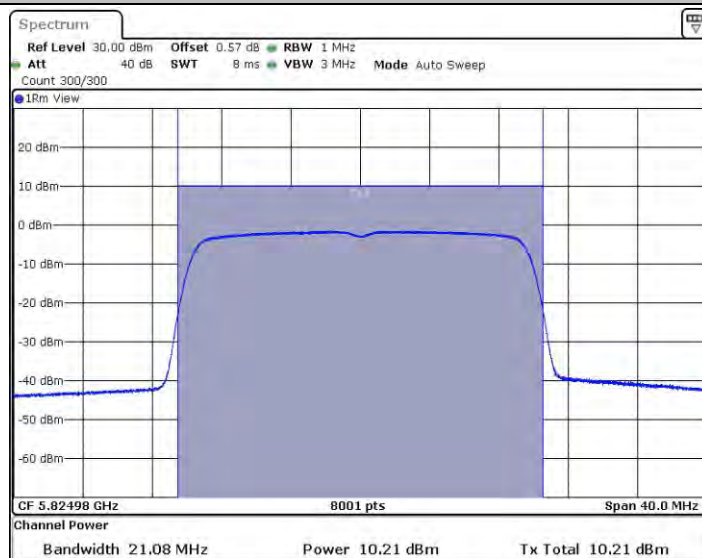
11AX20MIMO_Ant0_5785



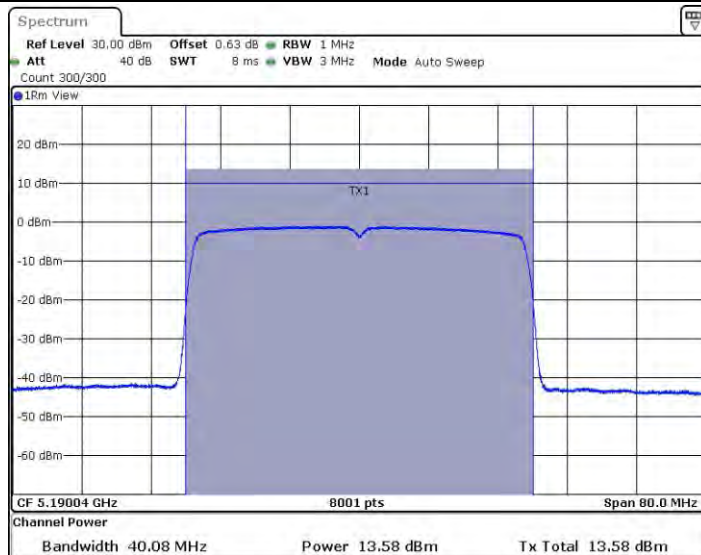
11AX20MIMO_Ant1_5785



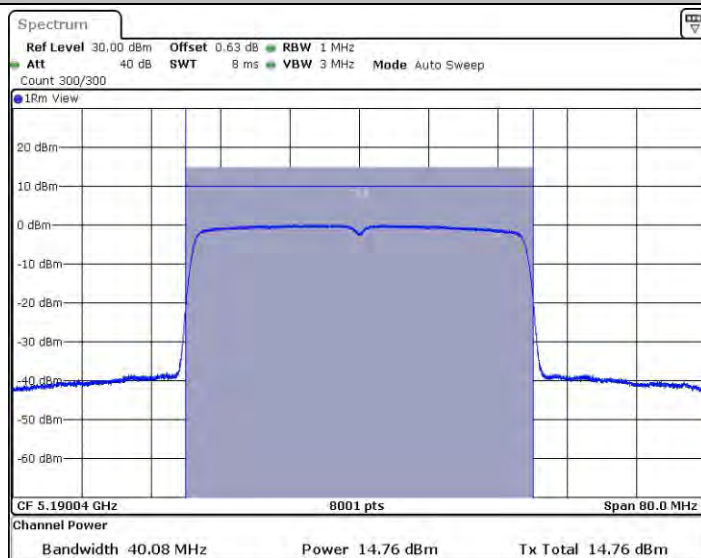
11AX20MIMO_Ant0_5825



11AX20MIMO_Ant1_5825



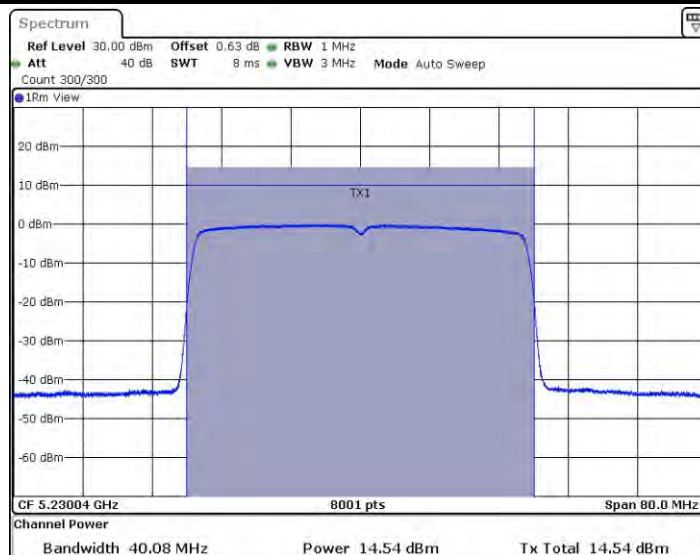
11AX40MIMO_Ant0_5190



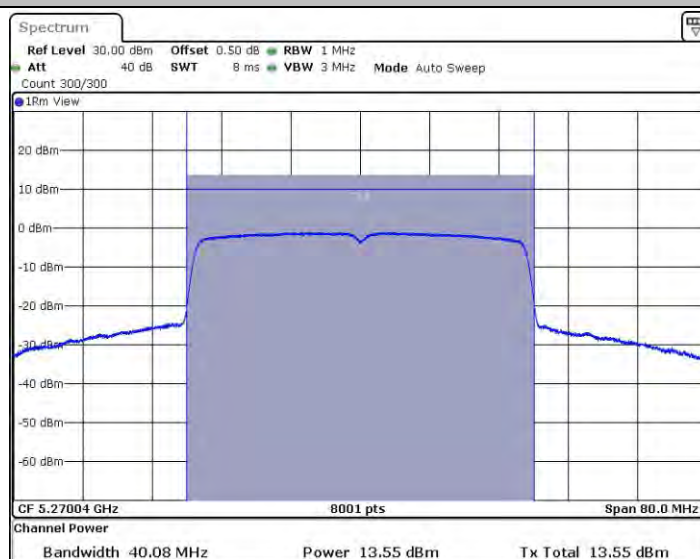
11AX40MIMO_Ant1_5190



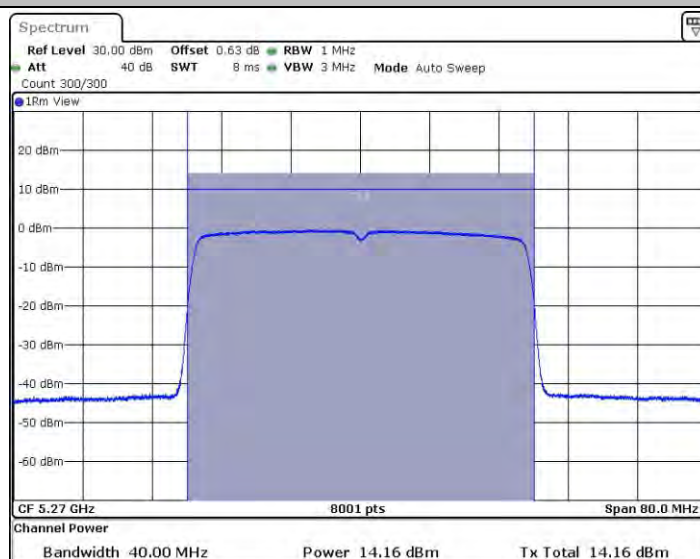
11AX40MIMO_Ant0_5230



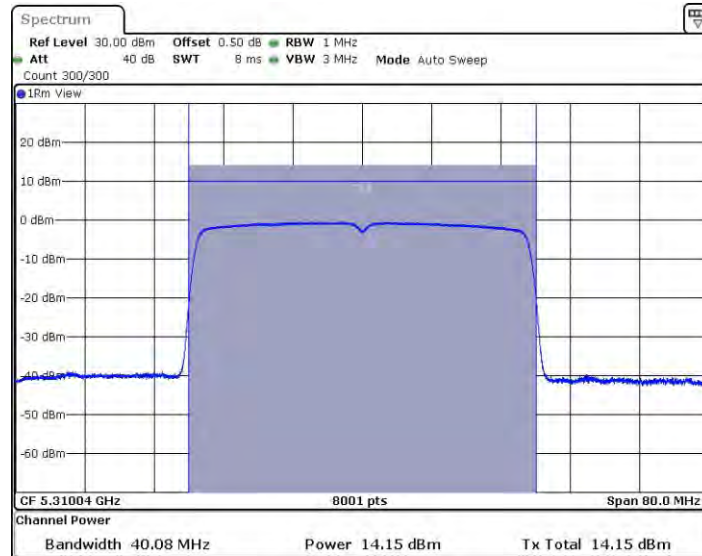
11AX40MIMO_Ant1_5230



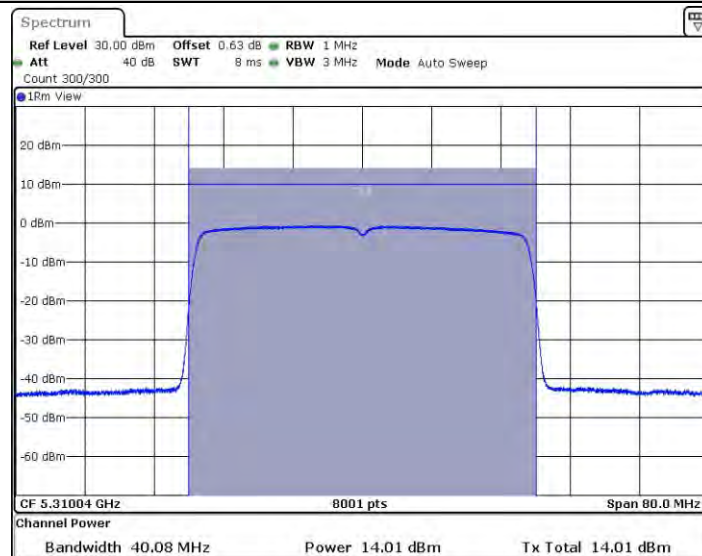
11AX40MIMO_Ant0_5270



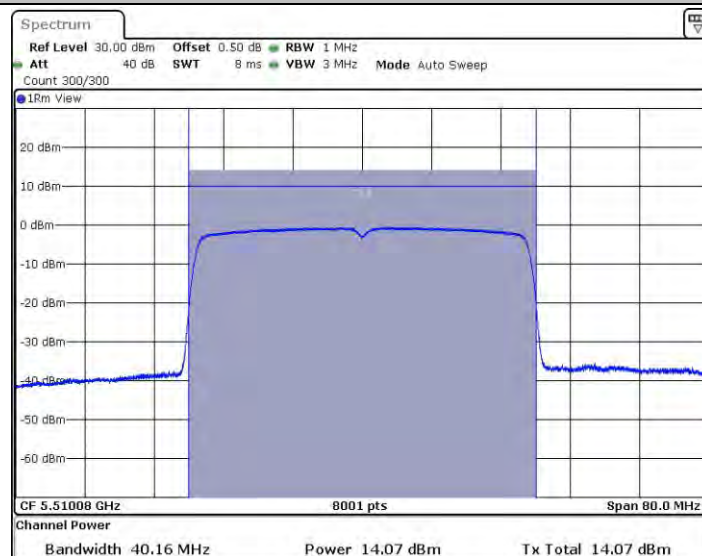
11AX40MIMO_Ant1_5270



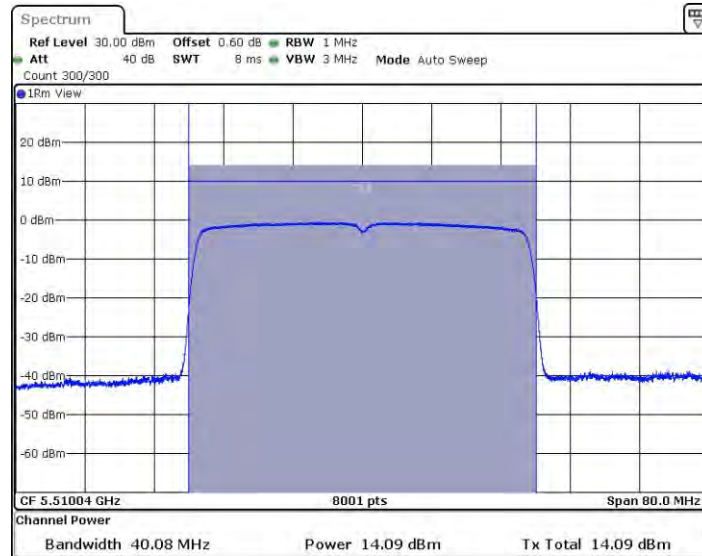
11AX40MIMO_Ant0_5310



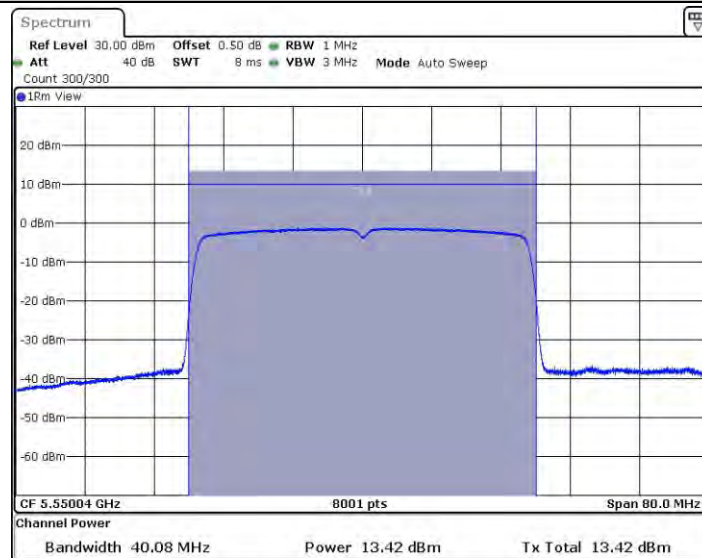
11AX40MIMO_Ant1_5310



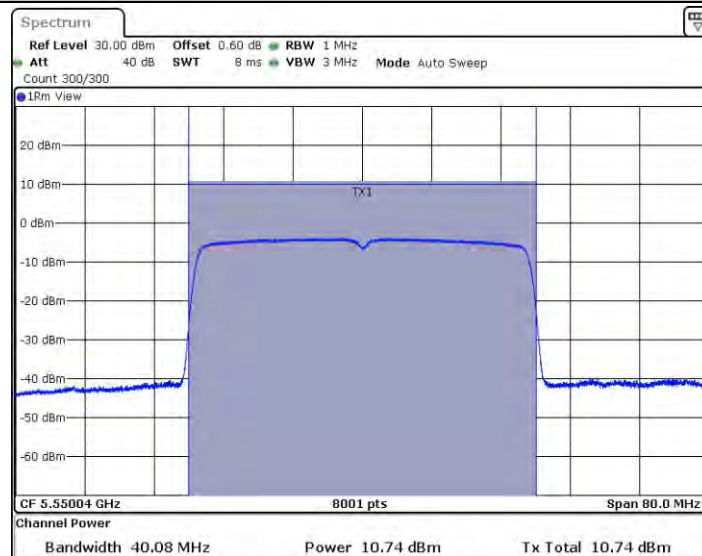
11AX40MIMO_Ant0_5510



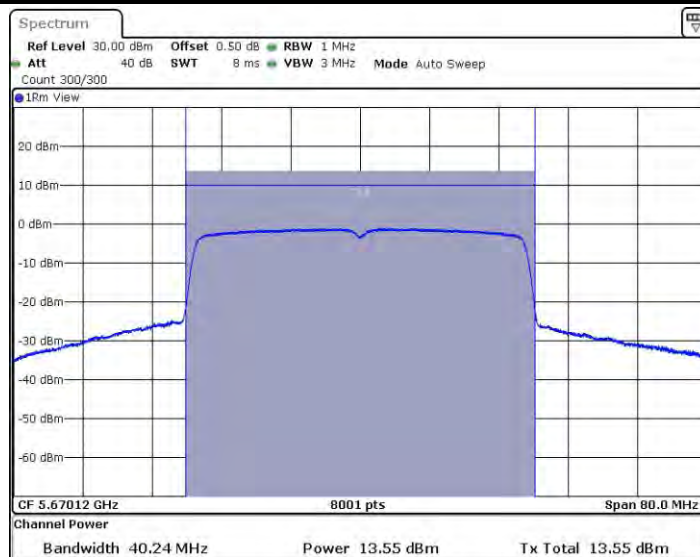
11AX40MIMO_Ant1_5510



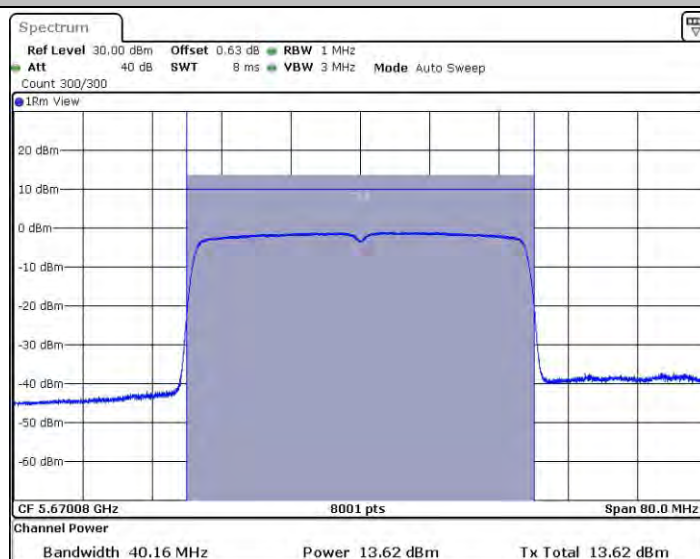
11AX40MIMO_Ant0_5550



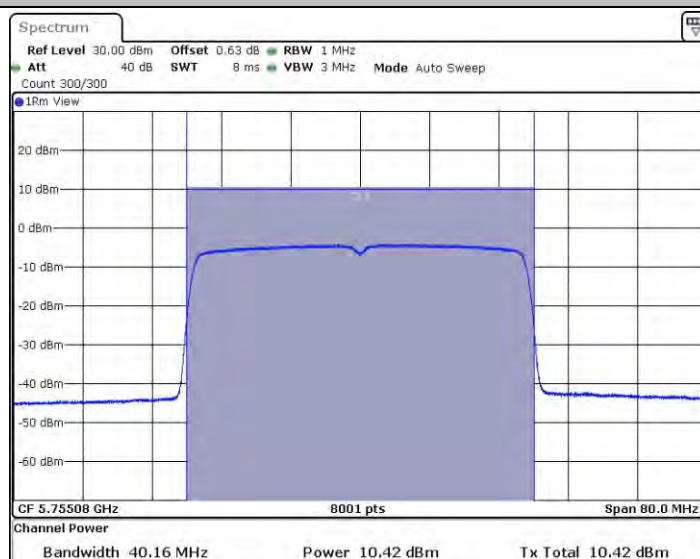
11AX40MIMO_Ant1_5550



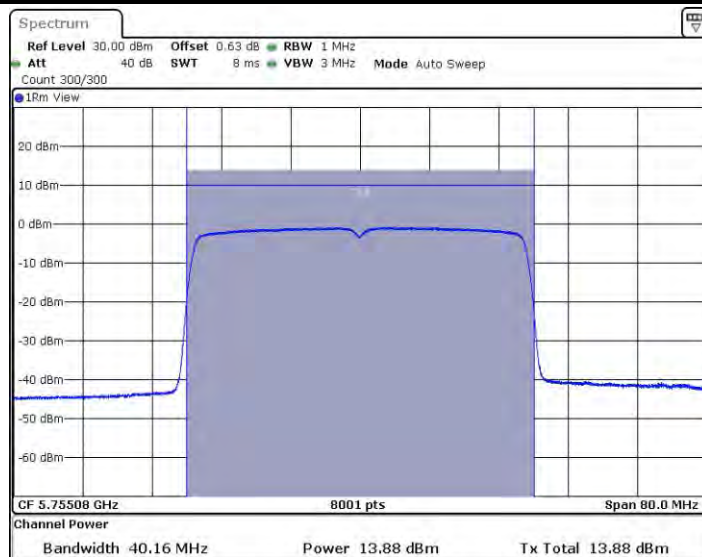
11AX40MIMO_Ant0_5670



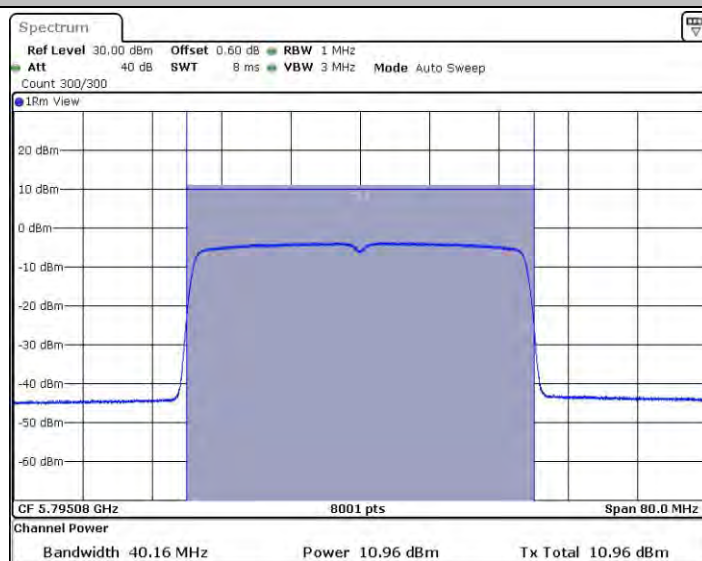
11AX40MIMO_Ant1_5670



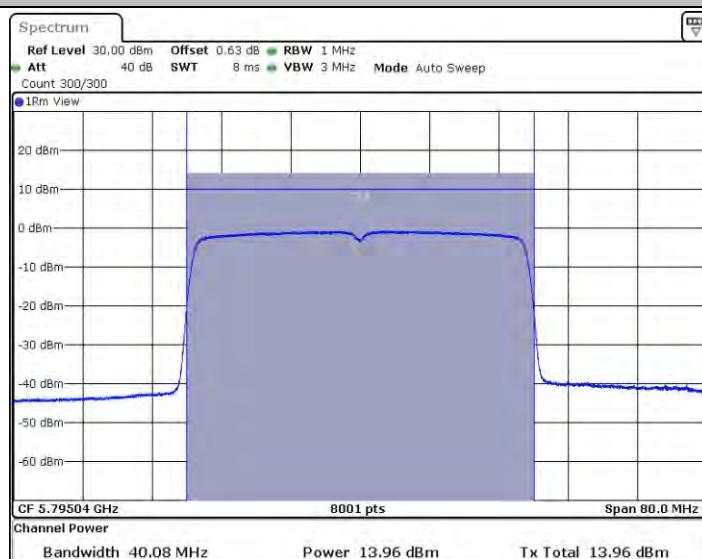
11AX40MIMO_Ant0_5755



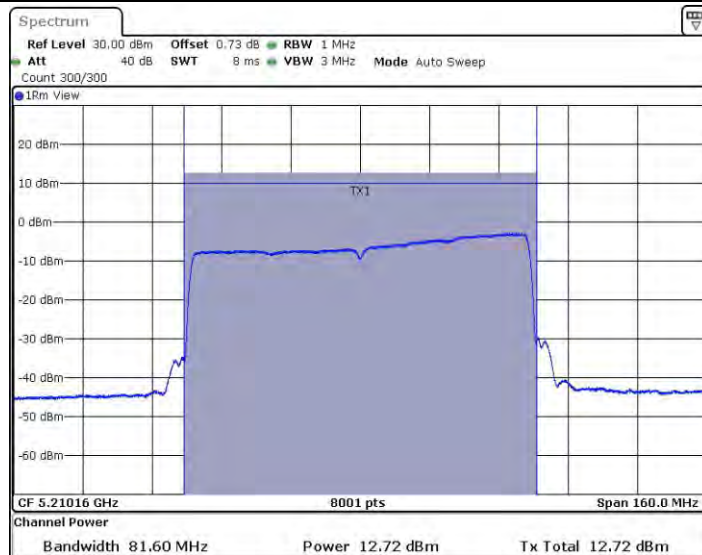
11AX40MIMO_Ant1_5755



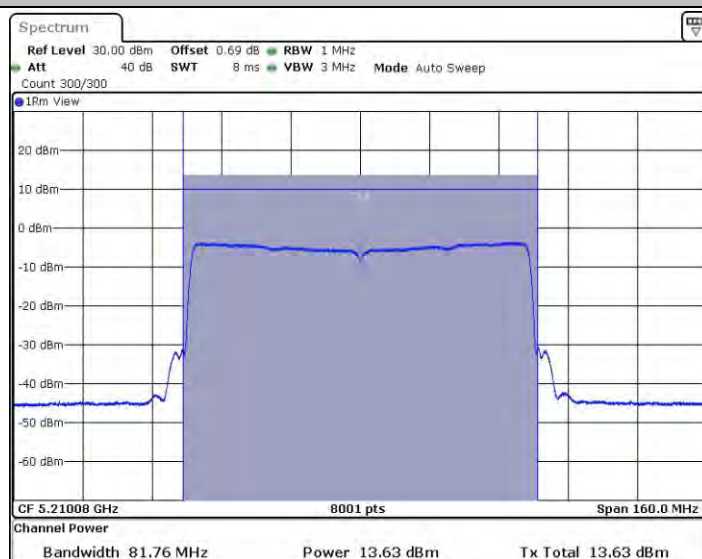
11AX40MIMO_Ant0_5795



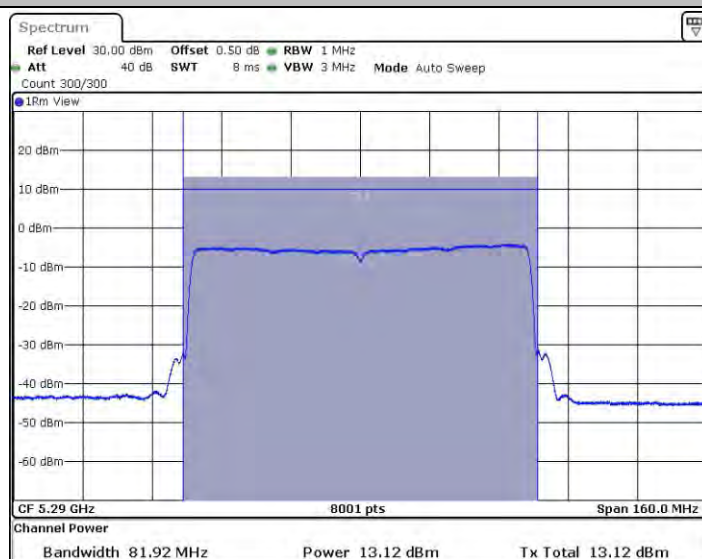
11AX40MIMO_Ant1_5795



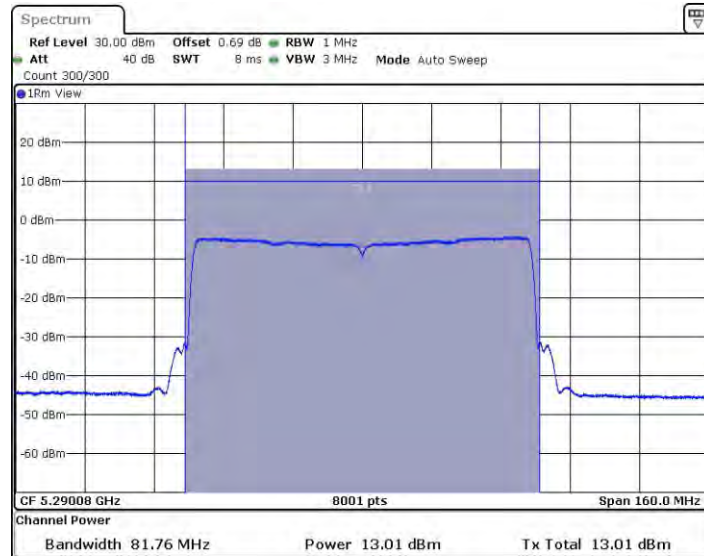
11AX80MIMO_Ant0_5210



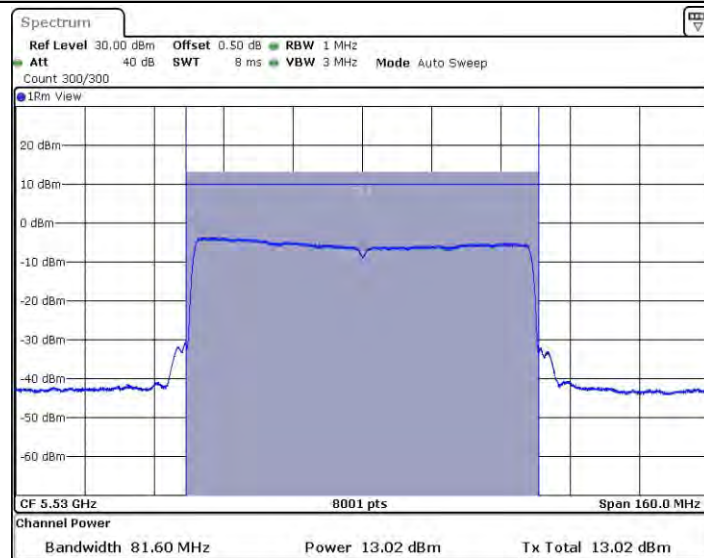
11AX80MIMO_Ant1_5210



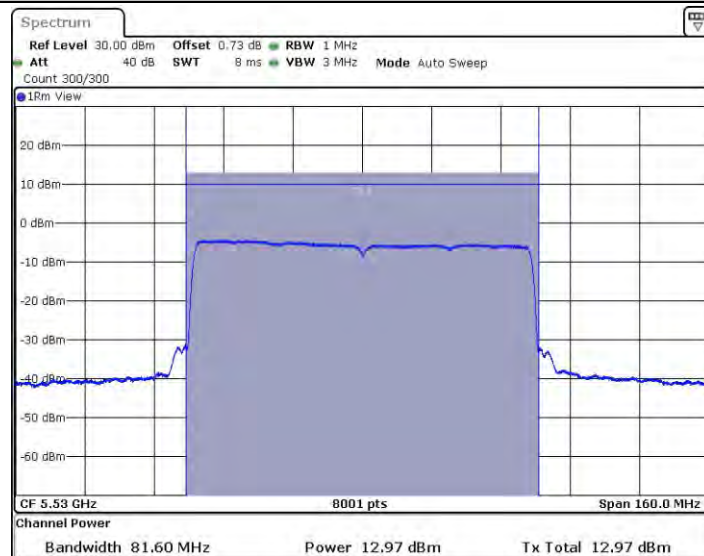
11AX80MIMO_Ant0_5290



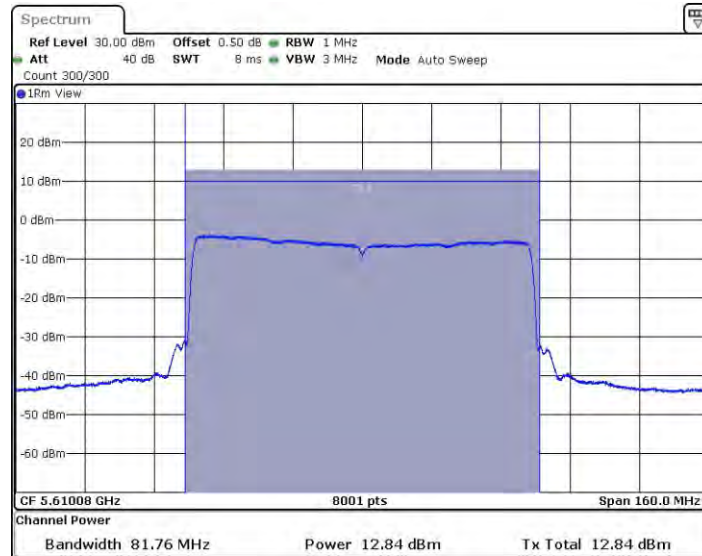
11AX80MIMO_Ant1_5290



11AX80MIMO_Ant0_5530



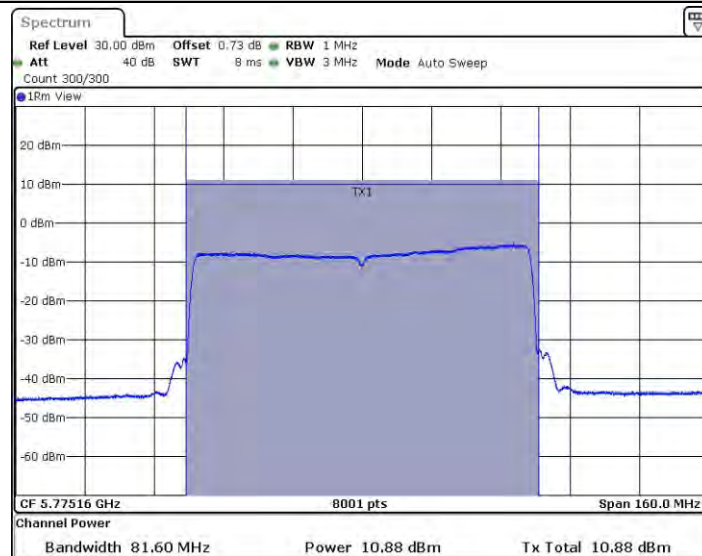
11AX80MIMO_Ant1_5530



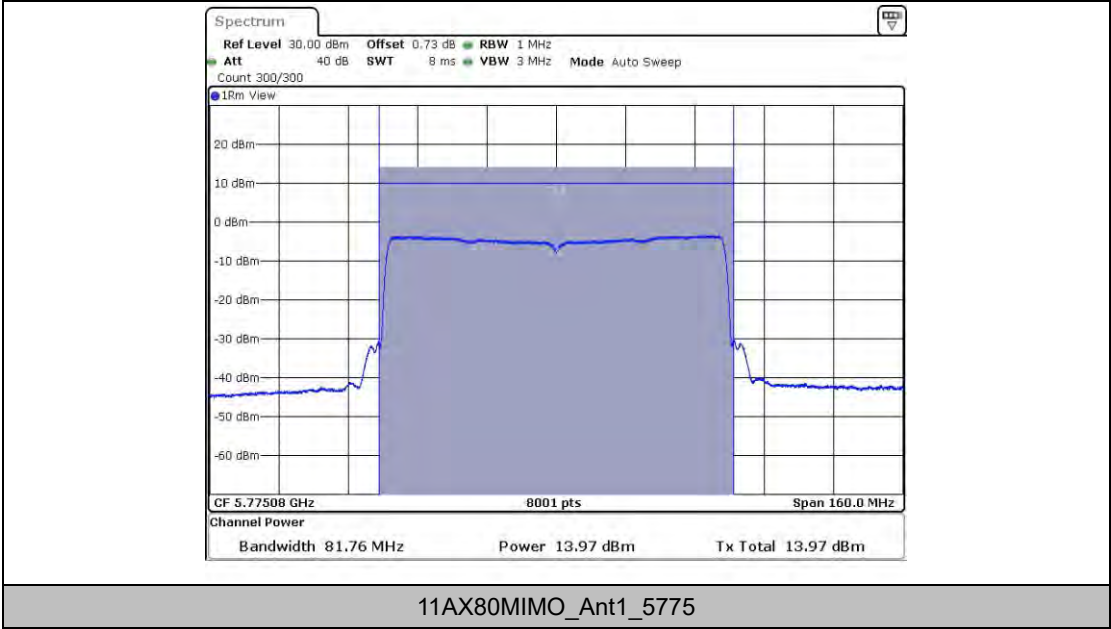
11AX80MIMO_Ant0_5610



11AX80MIMO_Ant1_5610



11AX80MIMO_Ant0_5775



Appendix C: Maximum power spectral density

Test Result

TestMode	Antenna	Channel	Result	Limit	Verdict
11A	Ant0	5180	9.8	<=11	PASS
	Ant1	5180	10.52	<=11	PASS
	Ant0	5200	9.32	<=11	PASS
	Ant1	5200	10.15	<=11	PASS
	Ant0	5240	8.1	<=11	PASS
	Ant1	5240	9.72	<=11	PASS
	Ant0	5260	9.86	<=11	PASS
	Ant1	5260	9.96	<=11	PASS
	Ant0	5280	9.59	<=11	PASS
	Ant1	5280	9.67	<=11	PASS
	Ant0	5320	8.46	<=11	PASS
	Ant1	5320	8.71	<=11	PASS
	Ant0	5500	10.27	<=11	PASS
	Ant1	5500	9.77	<=11	PASS
	Ant0	5580	10.34	<=11	PASS
	Ant1	5580	8.94	<=11	PASS
	Ant0	5700	10.42	<=11	PASS
	Ant1	5700	9.53	<=11	PASS
	Ant0	5745	11.45	<=30	PASS
	Ant1	5745	9.49	<=30	PASS
	Ant0	5785	11.21	<=30	PASS
	Ant1	5785	9.92	<=30	PASS
	Ant0	5825	11.64	<=30	PASS
	Ant1	5825	9.93	<=30	PASS
11N20SISO	Ant0	5180	9.83	<=11	PASS
	Ant1	5180	10.57	<=11	PASS
	Ant0	5200	8.94	<=11	PASS
	Ant1	5200	10.01	<=11	PASS
	Ant0	5240	7.94	<=11	PASS
	Ant1	5240	9.54	<=11	PASS
	Ant0	5260	9.87	<=11	PASS
	Ant1	5260	9.45	<=11	PASS
	Ant0	5280	9.61	<=11	PASS
	Ant1	5280	9.42	<=11	PASS
	Ant0	5320	8.99	<=11	PASS
	Ant1	5320	8.43	<=11	PASS
	Ant0	5500	10.49	<=11	PASS
	Ant1	5500	9.05	<=11	PASS
	Ant0	5580	10	<=11	PASS
	Ant1	5580	9.13	<=11	PASS
	Ant0	5700	10.15	<=11	PASS

	Ant1	5700	8.78	<=11	PASS
	Ant0	5745	10.75	<=30	PASS
	Ant1	5745	8.65	<=30	PASS
	Ant0	5785	10.53	<=30	PASS
	Ant1	5785	9.17	<=30	PASS
	Ant0	5825	10.85	<=30	PASS
	Ant1	5825	9.35	<=30	PASS
11N40SISO	Ant0	5190	4.54	<=11	PASS
	Ant1	5190	6.22	<=11	PASS
	Ant0	5230	4.71	<=11	PASS
	Ant1	5230	6.3	<=11	PASS
	Ant0	5270	5.78	<=11	PASS
	Ant1	5270	5.63	<=11	PASS
	Ant0	5310	5.64	<=11	PASS
	Ant1	5310	5.5	<=11	PASS
	Ant0	5510	6.98	<=11	PASS
	Ant1	5510	5.77	<=11	PASS
	Ant0	5550	7.28	<=11	PASS
	Ant1	5550	6.05	<=11	PASS
	Ant0	5670	7.1	<=11	PASS
	Ant1	5670	5.43	<=11	PASS
	Ant0	5755	7.5	<=30	PASS
	Ant1	5755	6.49	<=30	PASS
	Ant0	5795	6.67	<=30	PASS
	Ant1	5795	6.9	<=30	PASS
11AC20SISO	Ant0	5180	9.08	<=11	PASS
	Ant1	5180	10.3	<=11	PASS
	Ant0	5200	8.65	<=11	PASS
	Ant1	5200	10.02	<=11	PASS
	Ant0	5240	7.78	<=11	PASS
	Ant1	5240	9.63	<=11	PASS
	Ant0	5260	9.72	<=11	PASS
	Ant1	5260	10.33	<=11	PASS
	Ant0	5280	8.7	<=11	PASS
	Ant1	5280	10.1	<=11	PASS
	Ant0	5320	8.21	<=11	PASS
	Ant1	5320	9.28	<=11	PASS
	Ant0	5500	9.98	<=11	PASS
	Ant1	5500	9.92	<=11	PASS
	Ant0	5580	10.79	<=11	PASS
	Ant1	5580	9.88	<=11	PASS
	Ant0	5700	10.53	<=11	PASS
	Ant1	5700	9.57	<=11	PASS
	Ant0	5745	10.98	<=30	PASS
	Ant1	5745	8.64	<=30	PASS
	Ant0	5785	11.09	<=30	PASS

	Ant1	5785	8.95	<=30	PASS
	Ant0	5825	10.13	<=30	PASS
	Ant1	5825	9.26	<=30	PASS
11AC40SISO	Ant0	5190	4.62	<=11	PASS
	Ant1	5190	6.21	<=11	PASS
	Ant0	5230	4.21	<=11	PASS
	Ant1	5230	5.66	<=11	PASS
	Ant0	5270	5.41	<=11	PASS
	Ant1	5270	5.74	<=11	PASS
	Ant0	5310	5.45	<=11	PASS
	Ant1	5310	5.43	<=11	PASS
	Ant0	5510	6.52	<=11	PASS
	Ant1	5510	5.59	<=11	PASS
	Ant0	5550	6.88	<=11	PASS
	Ant1	5550	5.38	<=11	PASS
	Ant0	5670	6.82	<=11	PASS
	Ant1	5670	5.57	<=11	PASS
	Ant0	5755	6.36	<=30	PASS
	Ant1	5755	5.54	<=30	PASS
	Ant0	5795	5.83	<=30	PASS
	Ant1	5795	5.64	<=30	PASS
11AC80SISO	Ant0	5210	1.75	<=11	PASS
	Ant1	5210	2.95	<=11	PASS
	Ant0	5290	2.86	<=11	PASS
	Ant1	5290	2.59	<=11	PASS
	Ant0	5530	2.85	<=11	PASS
	Ant1	5530	2.06	<=11	PASS
	Ant0	5610	2.84	<=11	PASS
	Ant1	5610	1.94	<=11	PASS
	Ant0	5775	3.2	<=30	PASS
	Ant1	5775	3.87	<=30	PASS
11AX20SISO	Ant0	5180	9.6	<=11	PASS
	Ant1	5180	8.58	<=11	PASS
	Ant0	5200	9.42	<=11	PASS
	Ant1	5200	8.33	<=11	PASS
	Ant0	5240	9.43	<=11	PASS
	Ant1	5240	8.25	<=11	PASS
	Ant0	5260	9.33	<=11	PASS
	Ant1	5260	8.19	<=11	PASS
	Ant0	5280	9.36	<=11	PASS
	Ant1	5280	8.01	<=11	PASS
	Ant0	5320	9.41	<=11	PASS
	Ant1	5320	7.77	<=11	PASS
	Ant0	5500	10.65	<=11	PASS
	Ant1	5500	7.85	<=11	PASS
	Ant0	5580	10.57	<=11	PASS

	Ant1	5580	8	<=11	PASS
	Ant0	5700	10.31	<=11	PASS
	Ant1	5700	8.04	<=11	PASS
	Ant0	5745	11.21	<=30	PASS
	Ant1	5745	10.14	<=30	PASS
	Ant0	5785	11.09	<=30	PASS
	Ant1	5785	10.28	<=30	PASS
	Ant0	5825	11.41	<=30	PASS
	Ant1	5825	10.44	<=30	PASS
11AX40SISO	Ant0	5190	8.04	<=11	PASS
	Ant1	5190	10.66	<=11	PASS
	Ant0	5230	8	<=11	PASS
	Ant1	5230	10.54	<=11	PASS
	Ant0	5270	8.74	<=11	PASS
	Ant1	5270	10	<=11	PASS
	Ant0	5310	8.8	<=11	PASS
	Ant1	5310	10.19	<=11	PASS
	Ant0	5510	7.76	<=11	PASS
	Ant1	5510	10.57	<=11	PASS
	Ant0	5550	7.06	<=11	PASS
	Ant1	5550	10.14	<=11	PASS
	Ant0	5670	5.68	<=11	PASS
	Ant1	5670	9.2	<=11	PASS
	Ant0	5755	4.7	<=30	PASS
	Ant1	5755	7.2	<=30	PASS
	Ant0	5795	5.37	<=30	PASS
	Ant1	5795	7.19	<=30	PASS
11AX80SISO	Ant0	5210	4.5	<=11	PASS
	Ant1	5210	7.14	<=11	PASS
	Ant0	5290	4.81	<=11	PASS
	Ant1	5290	6.29	<=11	PASS
	Ant0	5530	3.91	<=11	PASS
	Ant1	5530	7.03	<=11	PASS
	Ant0	5610	2.45	<=11	PASS
	Ant1	5610	7.07	<=11	PASS
	Ant0	5775	2.74	<=30	PASS
	Ant1	5775	4.37	<=30	PASS
11N20MIMO	Ant0	5180	5.7	<=11	PASS
	Ant1	5180	7.27	<=11	PASS
	total	5180	9.57	<=11	PASS
	Ant0	5200	5.36	<=11	PASS
	Ant1	5200	7.08	<=11	PASS
	total	5200	9.31	<=11	PASS
	Ant0	5240	4.84	<=11	PASS
	Ant1	5240	6.86	<=11	PASS
	total	5240	8.98	<=11	PASS

	Ant0	5260	7.45	<=11	PASS
	Ant1	5260	7.17	<=11	PASS
	total	5260	10.32	<=11	PASS
	Ant0	5280	7.58	<=11	PASS
	Ant1	5280	7.45	<=11	PASS
	total	5280	10.53	<=11	PASS
	Ant0	5320	7.32	<=11	PASS
	Ant1	5320	7.58	<=11	PASS
	total	5320	10.46	<=11	PASS
	Ant0	5500	5.49	<=11	PASS
	Ant1	5500	7.19	<=11	PASS
	total	5500	9.43	<=11	PASS
	Ant0	5580	4.6	<=11	PASS
	Ant1	5580	6.61	<=11	PASS
	total	5580	8.73	<=11	PASS
	Ant0	5700	5.95	<=11	PASS
	Ant1	5700	6.61	<=11	PASS
	total	5700	9.30	<=11	PASS
	Ant0	5745	4.2	<=30	PASS
	Ant1	5745	5.88	<=30	PASS
	total	5745	8.13	<=30	PASS
	Ant0	5785	3.77	<=30	PASS
	Ant1	5785	5.69	<=30	PASS
	total	5785	7.85	<=30	PASS
	Ant0	5825	3.16	<=30	PASS
	Ant1	5825	5.44	<=30	PASS
	total	5825	7.46	<=30	PASS
11N40MIMO	Ant0	5190	3.81	<=11	PASS
	Ant1	5190	5.17	<=11	PASS
	total	5190	7.55	<=11	PASS
	Ant0	5230	3.65	<=11	PASS
	Ant1	5230	5.24	<=11	PASS
	total	5230	7.53	<=11	PASS
	Ant0	5270	4.1	<=11	PASS
	Ant1	5270	5.26	<=11	PASS
	total	5270	7.73	<=11	PASS
	Ant0	5310	4.19	<=11	PASS
	Ant1	5310	4.35	<=11	PASS
	total	5310	7.04	<=11	PASS
	Ant0	5510	3.25	<=11	PASS
	Ant1	5510	4.46	<=11	PASS
	total	5510	6.91	<=11	PASS
	Ant0	5550	1.92	<=11	PASS
	Ant1	5550	4.34	<=11	PASS
	total	5550	6.31	<=11	PASS
	Ant0	5670	2.05	<=11	PASS

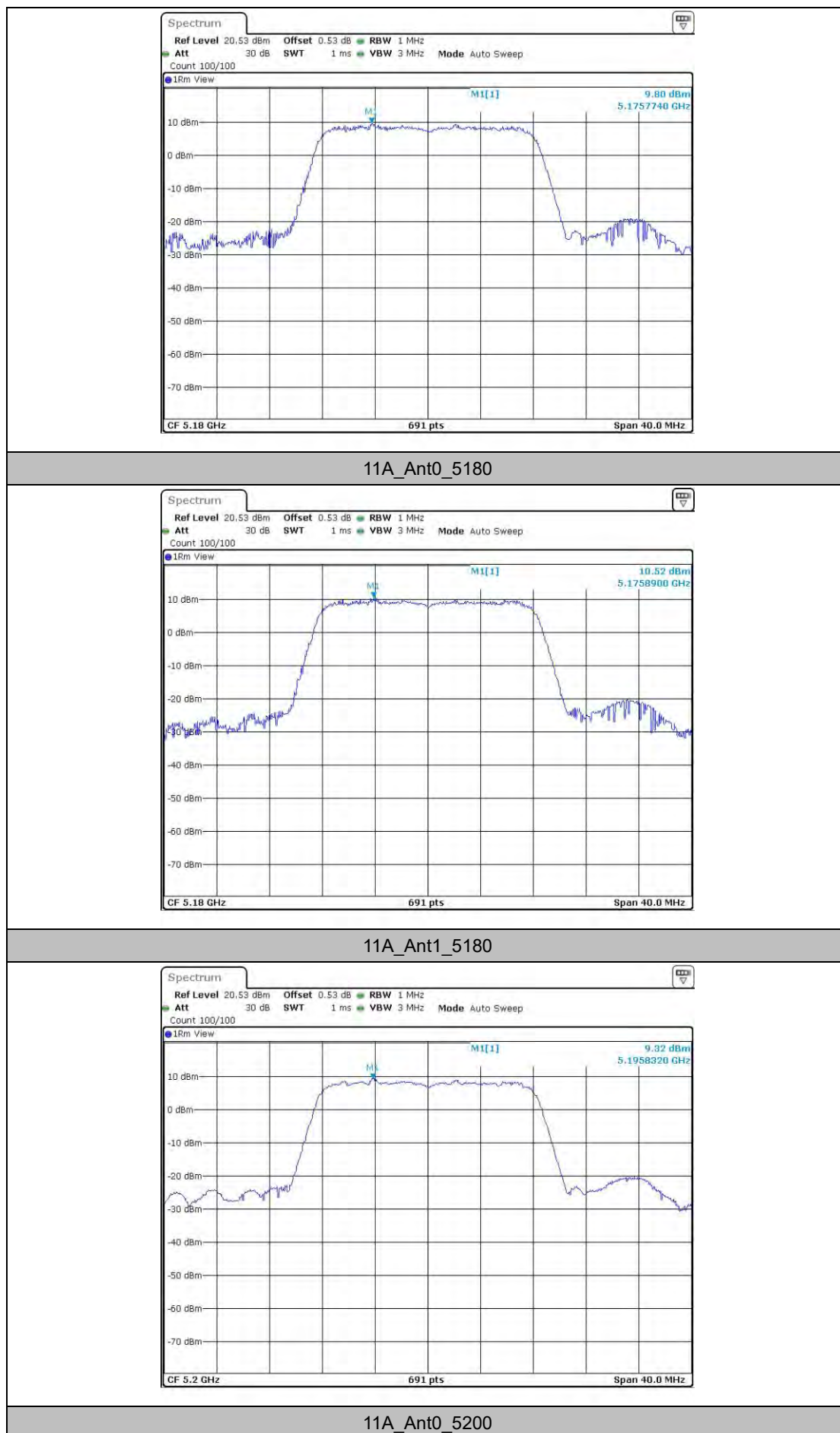
	Ant1	5670	4.45	<=11	PASS
	total	5670	6.42	<=11	PASS
	Ant0	5755	1.36	<=30	PASS
	Ant1	5755	3.78	<=30	PASS
	total	5755	5.75	<=30	PASS
	Ant0	5795	2.23	<=30	PASS
	Ant1	5795	4.07	<=30	PASS
	total	5795	6.26	<=30	PASS
11AC20MIMO	Ant0	5180	4.69	<=11	PASS
	Ant1	5180	7.25	<=11	PASS
	total	5180	9.17	<=11	PASS
	Ant0	5200	4.42	<=11	PASS
	Ant1	5200	7.44	<=11	PASS
	total	5200	9.20	<=11	PASS
	Ant0	5240	4.65	<=11	PASS
	Ant1	5240	7.21	<=11	PASS
	total	5240	9.13	<=11	PASS
	Ant0	5260	7.17	<=11	PASS
	Ant1	5260	8.59	<=11	PASS
	total	5260	10.95	<=11	PASS
	Ant0	5280	7.22	<=11	PASS
	Ant1	5280	8.39	<=11	PASS
	total	5280	10.85	<=11	PASS
	Ant0	5320	7.04	<=11	PASS
	Ant1	5320	8.32	<=11	PASS
	total	5320	10.74	<=11	PASS
	Ant0	5500	7.49	<=11	PASS
	Ant1	5500	8.27	<=11	PASS
	total	5500	10.91	<=11	PASS
	Ant0	5580	6.89	<=11	PASS
	Ant1	5580	5.3	<=11	PASS
	total	5580	9.18	<=11	PASS
	Ant0	5700	6.06	<=11	PASS
	Ant1	5700	7.48	<=11	PASS
	total	5700	9.84	<=11	PASS
	Ant0	5745	3.72	<=30	PASS
	Ant1	5745	7.72	<=30	PASS
	total	5745	9.18	<=30	PASS
	Ant0	5785	3.24	<=30	PASS
	Ant1	5785	7.62	<=30	PASS
	total	5785	8.97	<=30	PASS
	Ant0	5825	3.4	<=30	PASS
	Ant1	5825	7.46	<=30	PASS
	total	5825	8.90	<=30	PASS
11AC40MIMO	Ant0	5190	3.99	<=11	PASS
	Ant1	5190	4.2	<=11	PASS

	total	5190	7.11	<=11	PASS
	Ant0	5230	3.69	<=11	PASS
	Ant1	5230	3.58	<=11	PASS
	total	5230	6.65	<=11	PASS
	Ant0	5270	4.71	<=11	PASS
	Ant1	5270	4.38	<=11	PASS
	total	5270	7.56	<=11	PASS
	Ant0	5310	4.72	<=11	PASS
	Ant1	5310	4.29	<=11	PASS
	total	5310	7.52	<=11	PASS
	Ant0	5510	4.49	<=11	PASS
	Ant1	5510	4.57	<=11	PASS
	total	5510	7.54	<=11	PASS
	Ant0	5550	4.6	<=11	PASS
	Ant1	5550	4.49	<=11	PASS
	total	5550	7.56	<=11	PASS
	Ant0	5670	4.1	<=11	PASS
	Ant1	5670	3.88	<=11	PASS
	total	5670	7.00	<=11	PASS
	Ant0	5755	1.86	<=30	PASS
	Ant1	5755	1.85	<=30	PASS
	total	5755	4.87	<=30	PASS
	Ant0	5795	1.57	<=30	PASS
	Ant1	5795	1.4	<=30	PASS
	total	5795	4.50	<=30	PASS
11AC80MIMO	Ant0	5210	0.98	<=11	PASS
	Ant1	5210	4.24	<=11	PASS
	total	5210	5.92	<=11	PASS
	Ant0	5290	1.33	<=11	PASS
	Ant1	5290	2.11	<=11	PASS
	total	5290	4.75	<=11	PASS
	Ant0	5530	1.85	<=11	PASS
	Ant1	5530	2.32	<=11	PASS
	total	5530	5.10	<=11	PASS
	Ant0	5610	0.79	<=11	PASS
	Ant1	5610	1.91	<=11	PASS
	total	5610	4.40	<=11	PASS
	Ant0	5775	-2.84	<=30	PASS
	Ant1	5775	0.96	<=30	PASS
	total	5775	2.47	<=30	PASS
11AX20MIMO	Ant0	5180	7.39	<=11	PASS
	Ant1	5180	7.91	<=11	PASS
	total	5180	10.67	<=11	PASS
	Ant0	5200	6.82	<=11	PASS
	Ant1	5200	8.15	<=11	PASS
	total	5200	10.55	<=11	PASS

	Ant0	5240	7.05	<=11	PASS
	Ant1	5240	8.06	<=11	PASS
	total	5240	10.59	<=11	PASS
	Ant0	5260	7.64	<=11	PASS
	Ant1	5260	7.68	<=11	PASS
	total	5260	10.67	<=11	PASS
	Ant0	5280	7.83	<=11	PASS
	Ant1	5280	7.85	<=11	PASS
	total	5280	10.85	<=11	PASS
	Ant0	5320	7.97	<=11	PASS
	Ant1	5320	7.99	<=11	PASS
	total	5320	10.99	<=11	PASS
	Ant0	5500	7.99	<=11	PASS
	Ant1	5500	7.85	<=11	PASS
	total	5500	10.93	<=11	PASS
	Ant0	5580	7.42	<=11	PASS
	Ant1	5580	8.5	<=11	PASS
	total	5580	11.00	<=11	PASS
	Ant0	5700	7.24	<=11	PASS
	Ant1	5700	7.87	<=11	PASS
	total	5700	10.58	<=11	PASS
	Ant0	5745	8.04	<=30	PASS
	Ant1	5745	3.61	<=30	PASS
	total	5745	9.38	<=30	PASS
	Ant0	5785	7.78	<=30	PASS
	Ant1	5785	3.65	<=30	PASS
	total	5785	9.20	<=30	PASS
	Ant0	5825	7.87	<=30	PASS
	Ant1	5825	4.04	<=30	PASS
	total	5825	9.37	<=30	PASS
11AX40MIMO	Ant0	5190	7.18	<=11	PASS
	Ant1	5190	8.25	<=11	PASS
	total	5190	10.76	<=11	PASS
	Ant0	5230	6.83	<=11	PASS
	Ant1	5230	7.94	<=11	PASS
	total	5230	10.43	<=11	PASS
	Ant0	5270	7.05	<=11	PASS
	Ant1	5270	7.74	<=11	PASS
	total	5270	10.42	<=11	PASS
	Ant0	5310	7.55	<=11	PASS
	Ant1	5310	7.44	<=11	PASS
	total	5310	10.51	<=11	PASS
	Ant0	5510	7.42	<=11	PASS
	Ant1	5510	7.92	<=11	PASS
	total	5510	10.69	<=11	PASS
	Ant0	5550	6.88	<=11	PASS

	Ant1	5550	7.61	<=11	PASS
	total	5550	10.27	<=11	PASS
	Ant0	5670	7.25	<=11	PASS
	Ant1	5670	6.91	<=11	PASS
	total	5670	10.09	<=11	PASS
	Ant0	5755	1.45	<=30	PASS
	Ant1	5755	5.15	<=30	PASS
	total	5755	6.69	<=30	PASS
	Ant0	5795	2.13	<=30	PASS
	Ant1	5795	5.19	<=30	PASS
	total	5795	6.93	<=30	PASS
11AX80MIMO	Ant0	5210	3.8	<=11	PASS
	Ant1	5210	3.8	<=11	PASS
	total	5210	6.81	<=11	PASS
	Ant0	5290	2.81	<=11	PASS
	Ant1	5290	2.75	<=11	PASS
	total	5290	5.79	<=11	PASS
	Ant0	5530	3.5	<=11	PASS
	Ant1	5530	2.81	<=11	PASS
	total	5530	6.18	<=11	PASS
	Ant0	5610	3.19	<=11	PASS
	Ant1	5610	2.44	<=11	PASS
	total	5610	5.84	<=11	PASS
	Ant0	5775	-0.13	<=30	PASS
	Ant1	5775	2.23	<=30	PASS
	total	5775	4.22	<=30	PASS

Test Graphs





11A_Ant1_5200



11A_Ant0_5240



11A_Ant1_5240



11A_Ant0_5260



11A_Ant1_5260



11A_Ant0_5280



11A_Ant1_5280



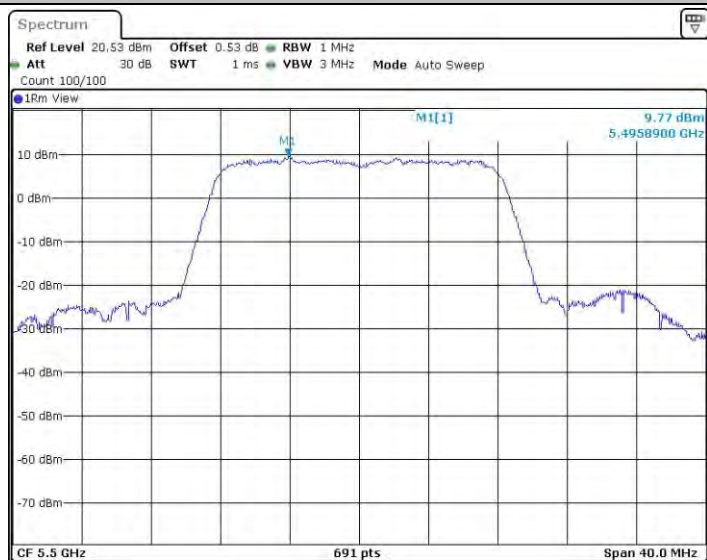
11A_Ant0_5320



11A_Ant1_5320



11A_Ant0_5500



11A_Ant1_5500



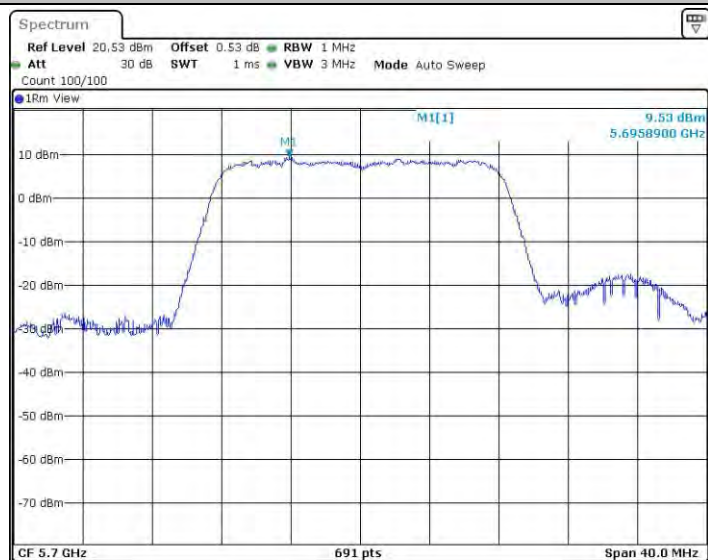
11A_Ant0_5580



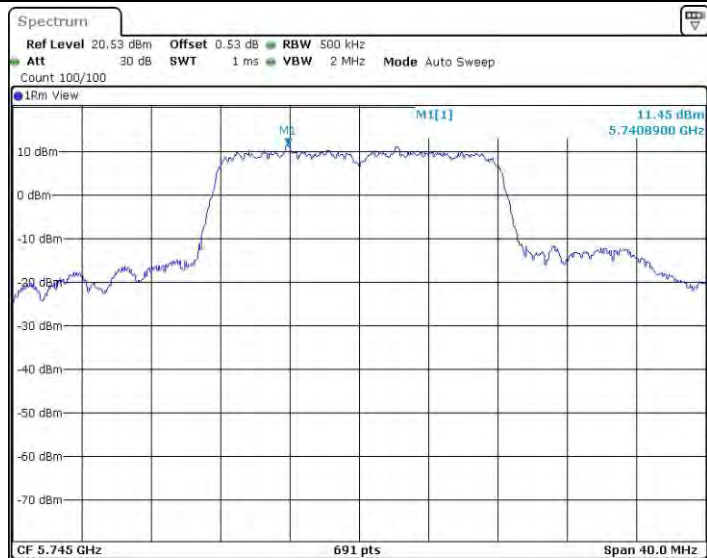
11A_Ant1_5580



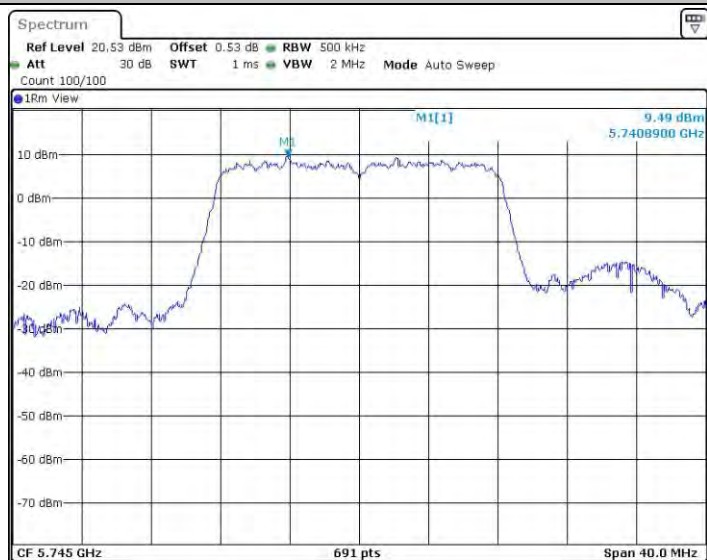
11A_Ant0_5700



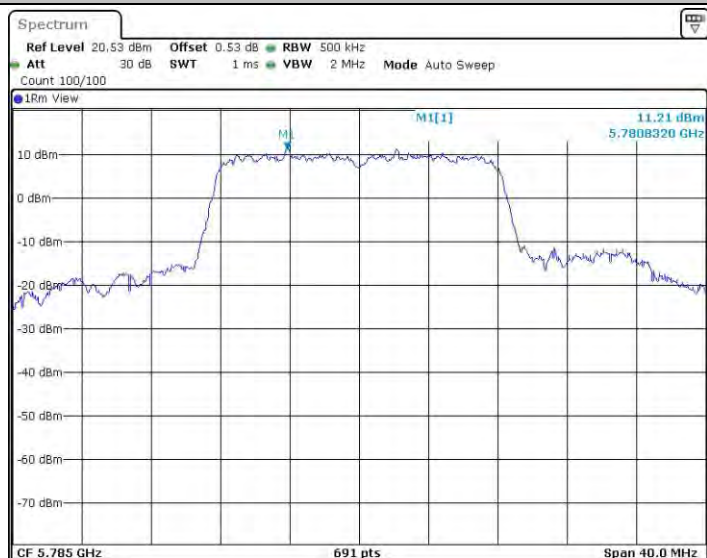
11A_Ant1_5700



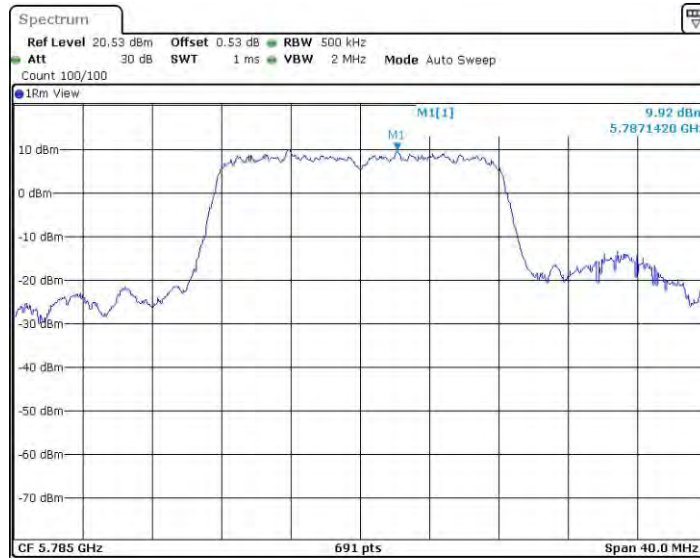
11A_Ant0_5745



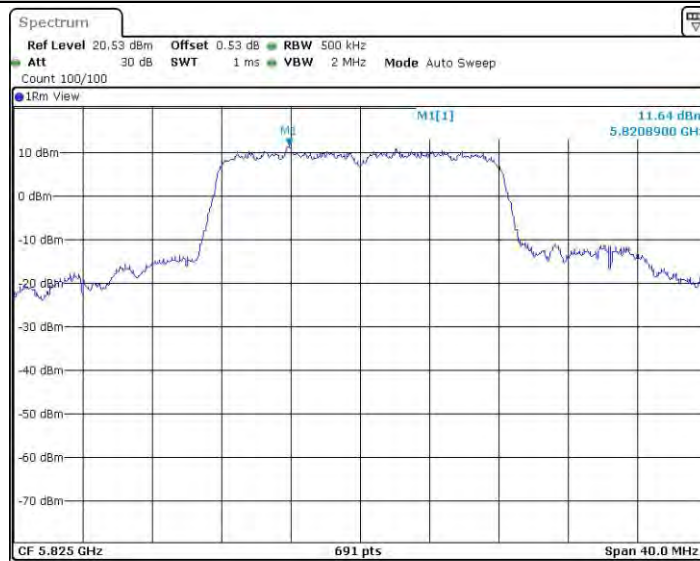
11A_Ant1_5745



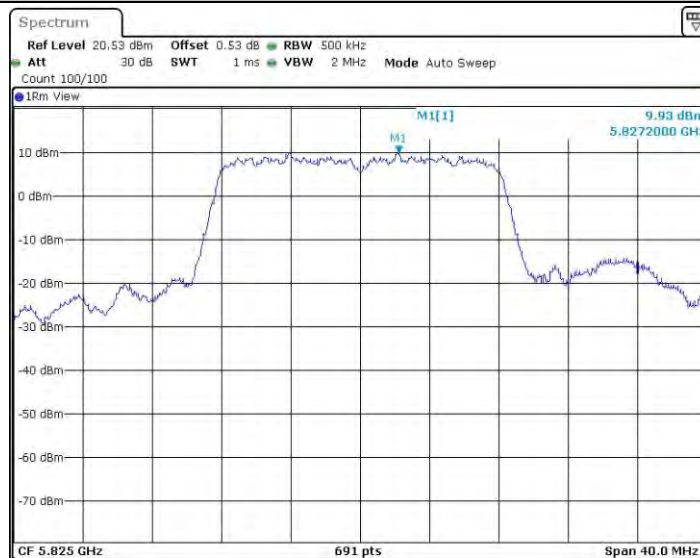
11A_Ant0_5785



11A_Ant1_5785



11A_Ant0_5825



11A_Ant1_5825



11N20SISO_Ant0_5180



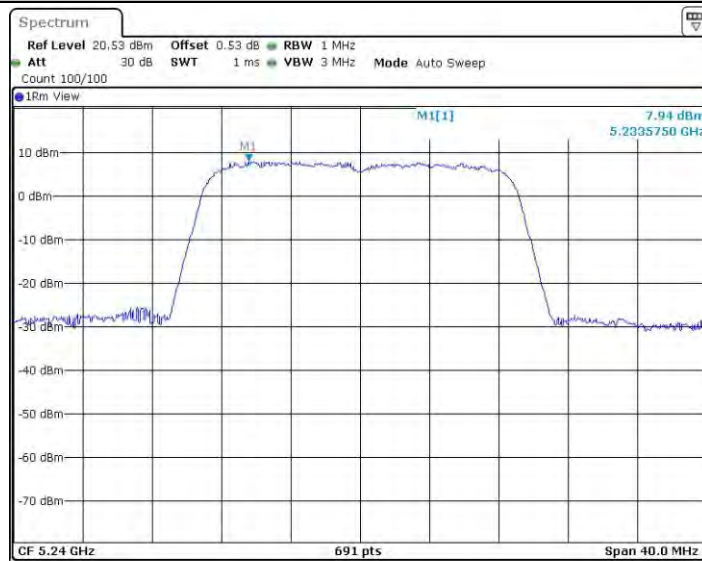
11N20SISO_Ant1_5180



11N20SISO_Ant0_5200



11N20SISO_Ant1_5200



11N20SISO_Ant0_5240



11N20SISO_Ant1_5240



11N20SISO_Ant0_5260



11N20SISO_Ant1_5260



11N20SISO_Ant0_5280



11N20SISO_Ant1_5280



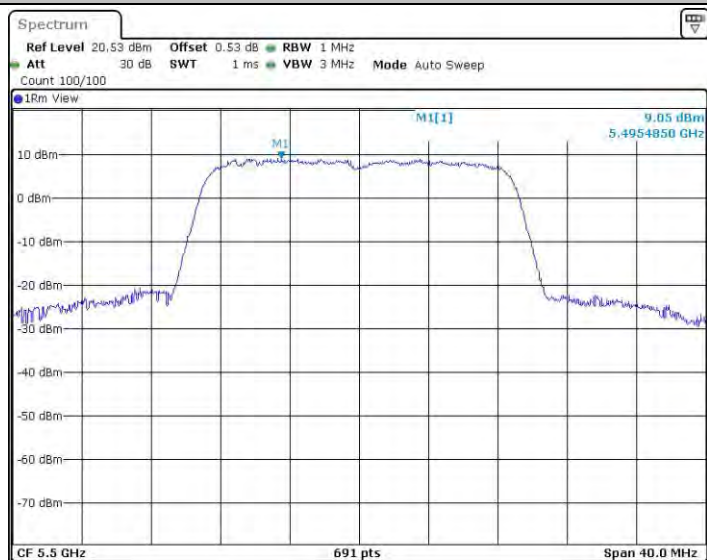
11N20SISO_Ant0_5320



11N20SISO_Ant1_5320



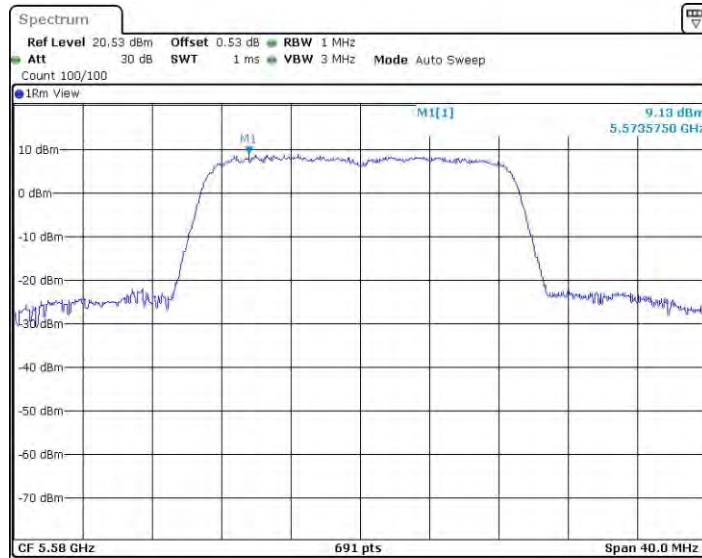
11N20SISO_Ant0_5500



11N20SISO_Ant1_5500



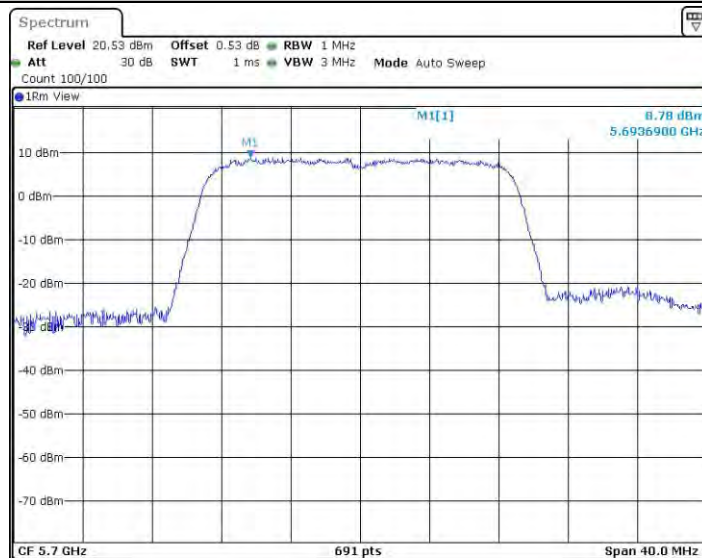
11N20SISO_Ant0_5580



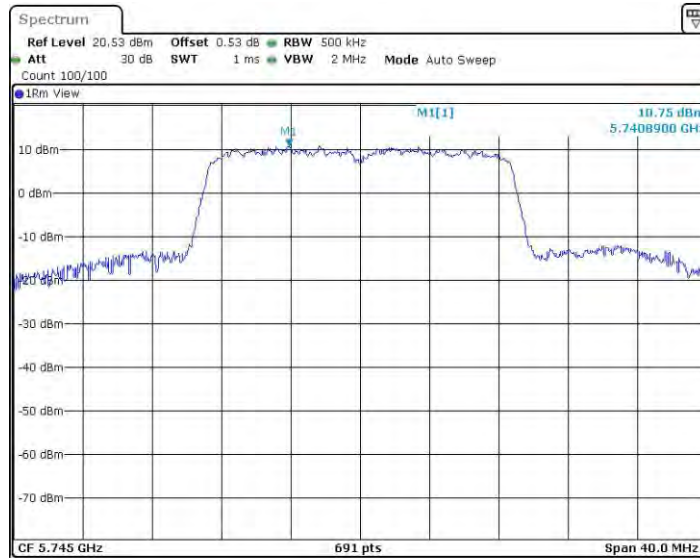
11N20SISO_Ant1_5580



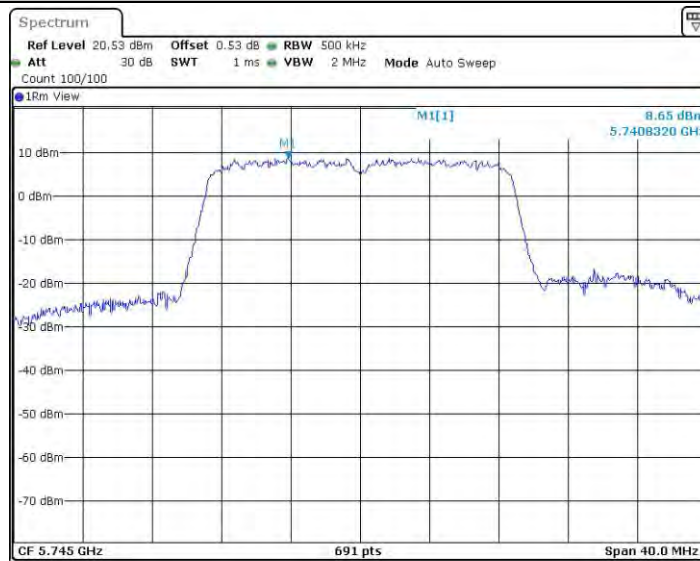
11N20SISO_Ant0_5700



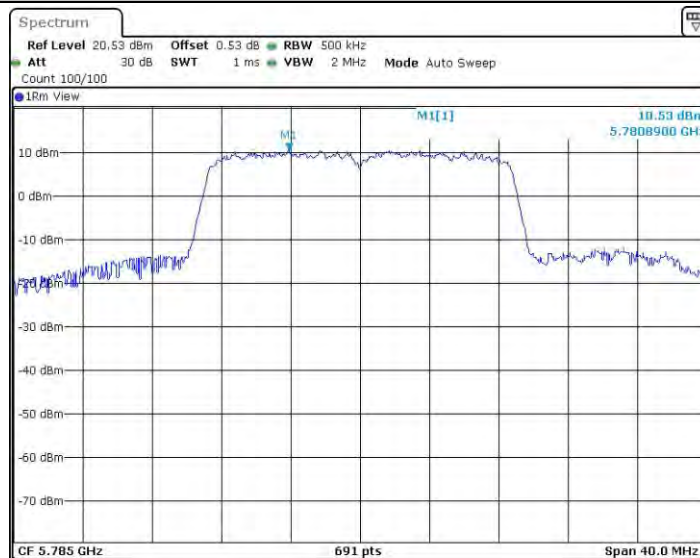
11N20SISO_Ant1_5700



11N20SISO_Ant0_5745



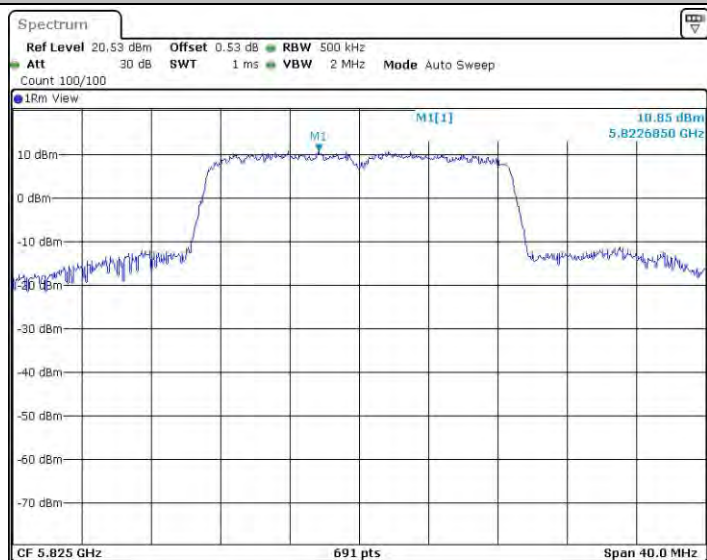
11N20SISO_Ant1_5745



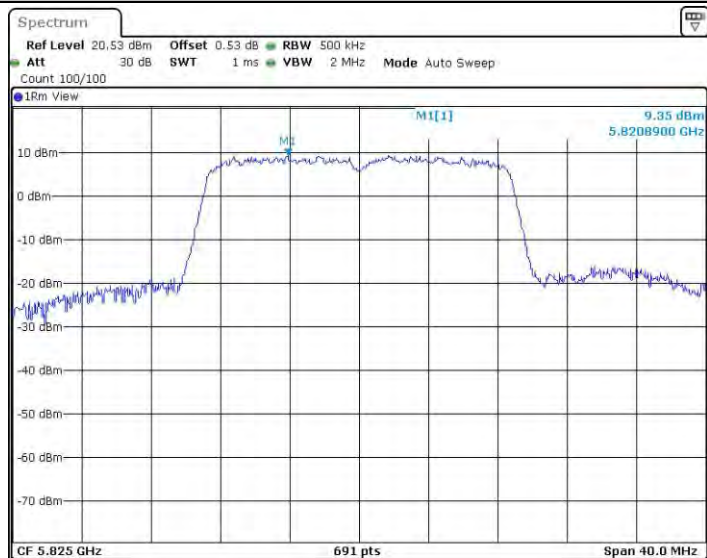
11N20SISO_Ant0_5785



11N20SISO_Ant1_5785



11N20SISO_Ant0_5825



11N20SISO_Ant1_5825