

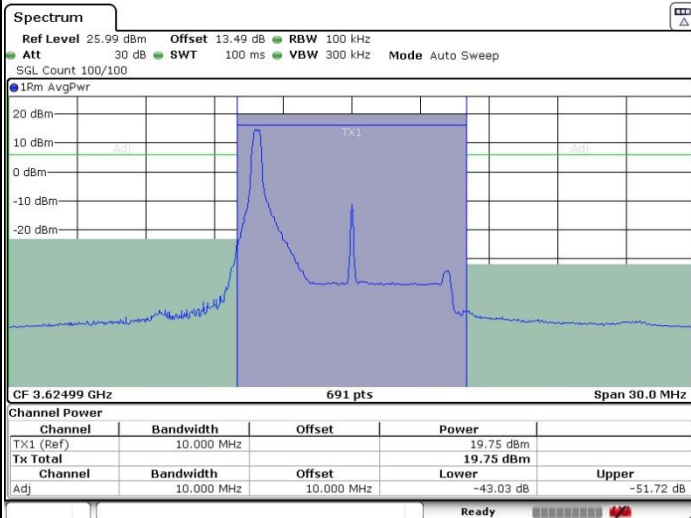


FR1 n48 / 10MHz

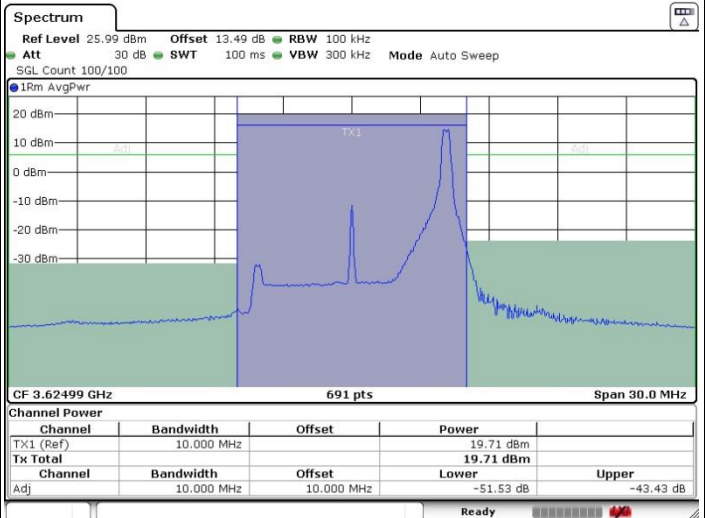
16QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

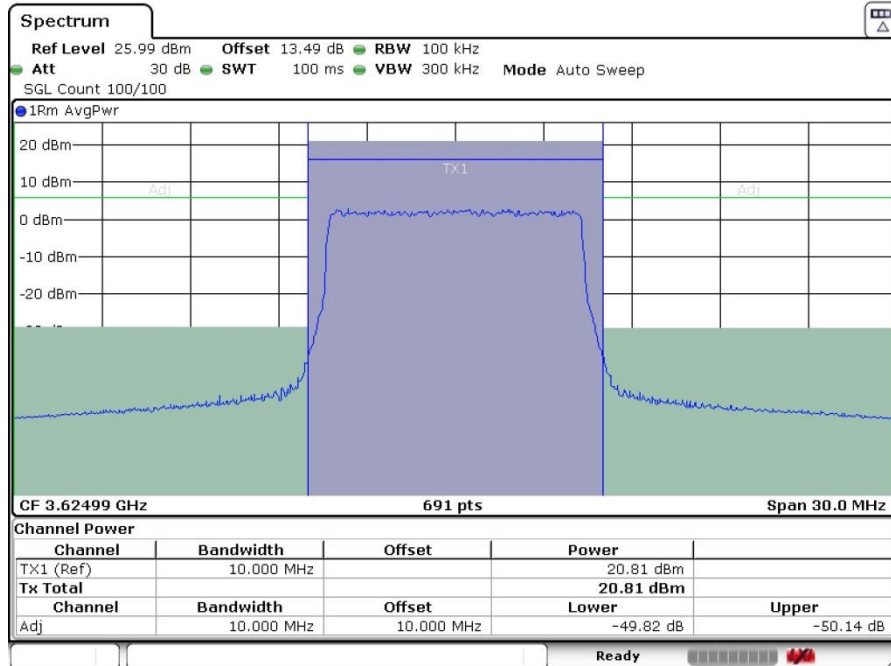


Date: 14.MAR.2023 12:03:50

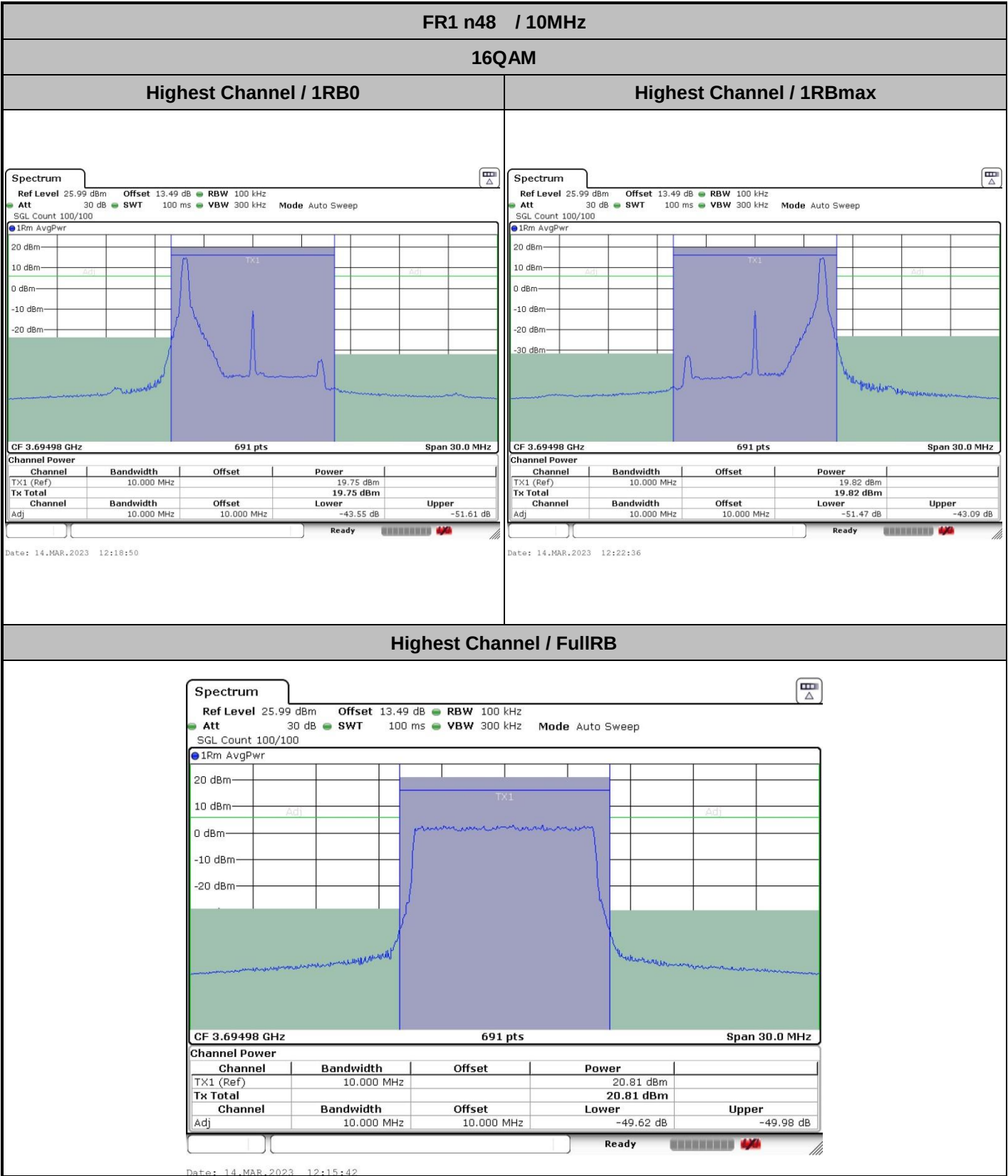


Date: 14.MAR.2023 12:06:09

Middle Channel / FullRB



Date: 14.MAR.2023 12:01:36



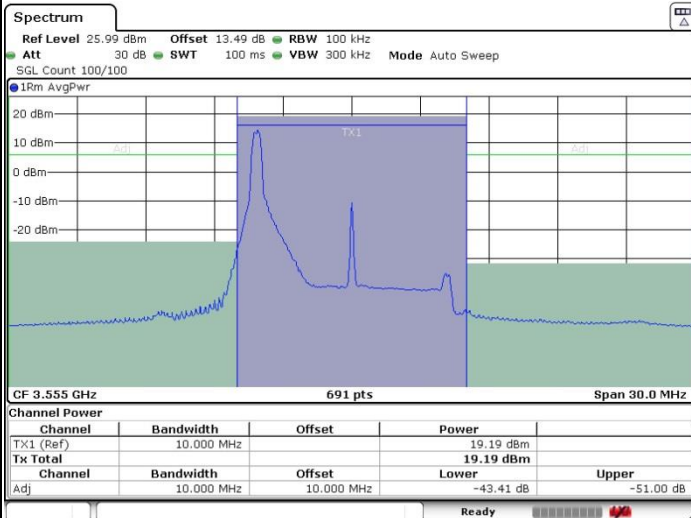


FR1 n48 / 10MHz

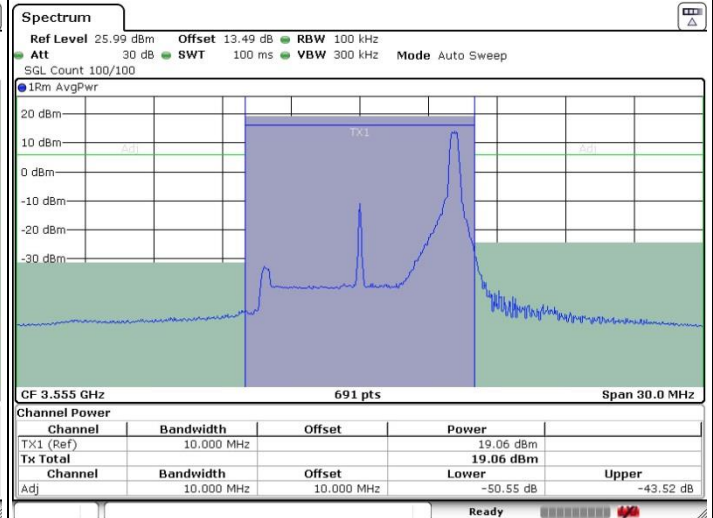
64QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

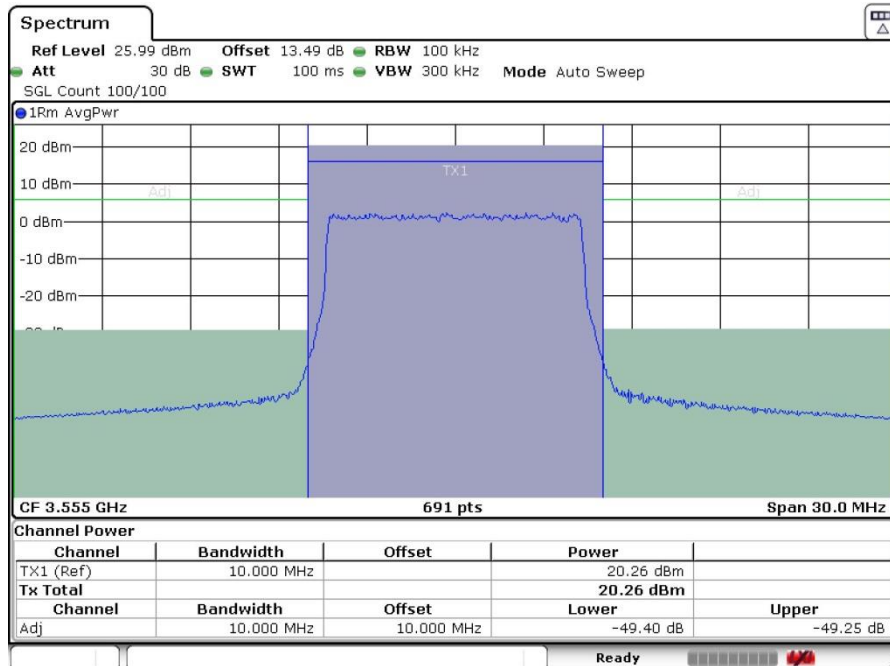


Date: 14.MAR.2023 12:11:11



Date: 14.MAR.2023 12:08:14

Lowest Channel / FullRB



Date: 14.MAR.2023 12:12:35

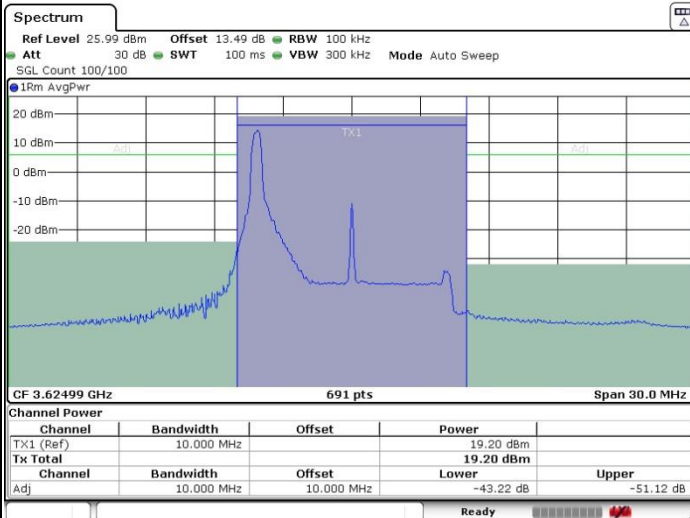


FR1 n48 / 10MHz

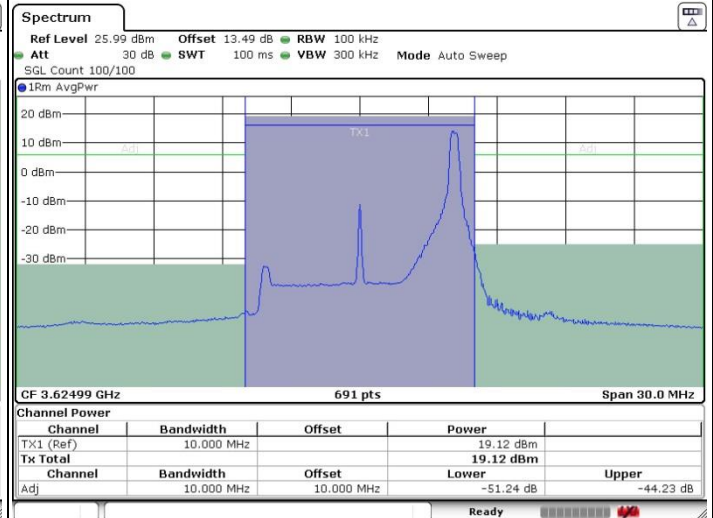
64QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

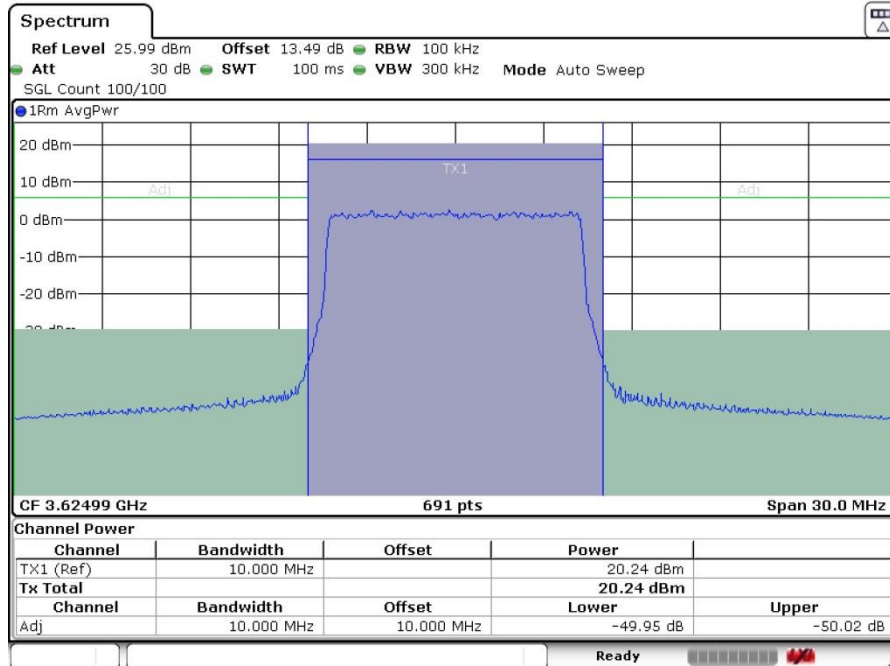


Date: 14.MAR.2023 12:03:25



Date: 14.MAR.2023 12:06:34

Middle Channel / FullRB



Date: 14.MAR.2023 12:01:57

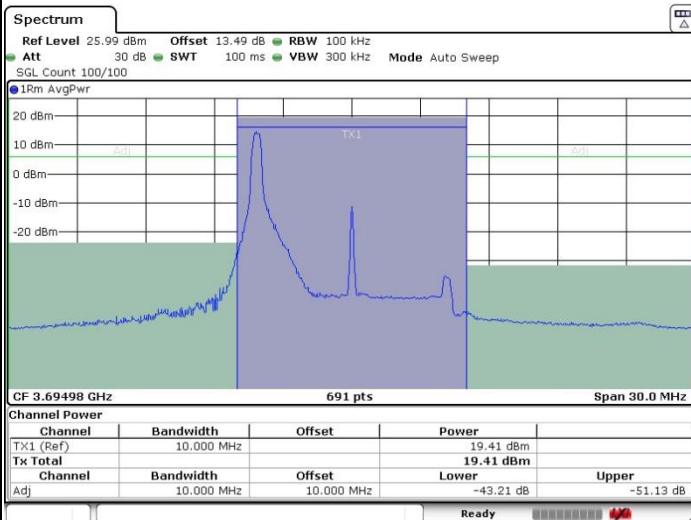


FR1 n48 / 10MHz

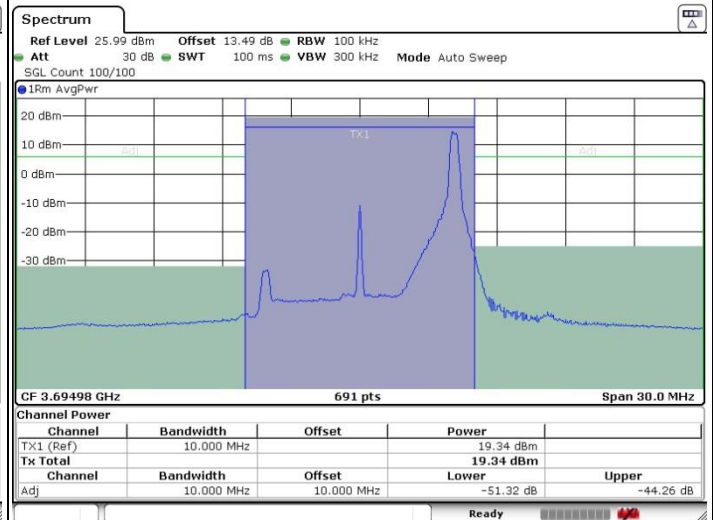
64QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax

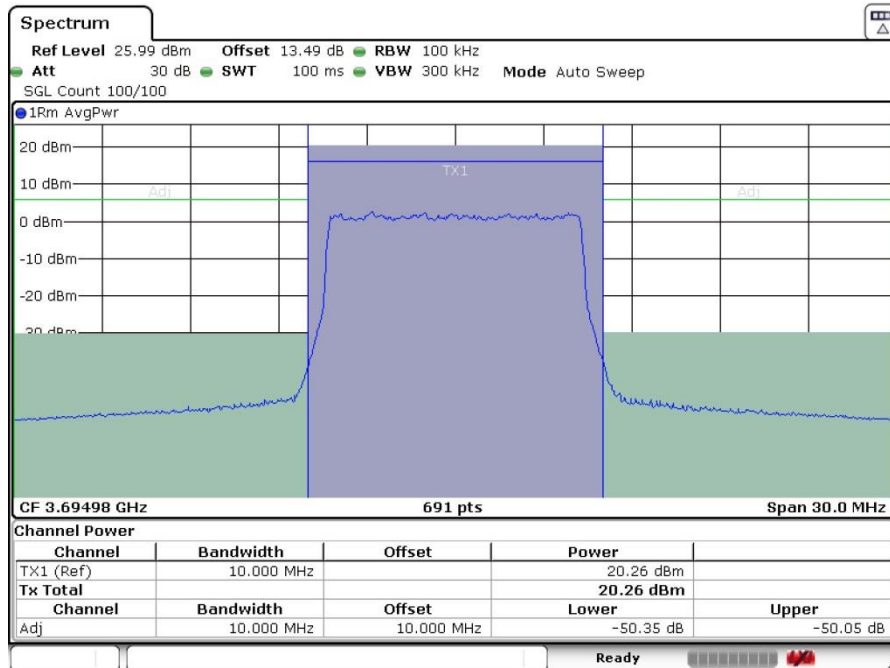


Date: 14.MAR.2023 12:18:22

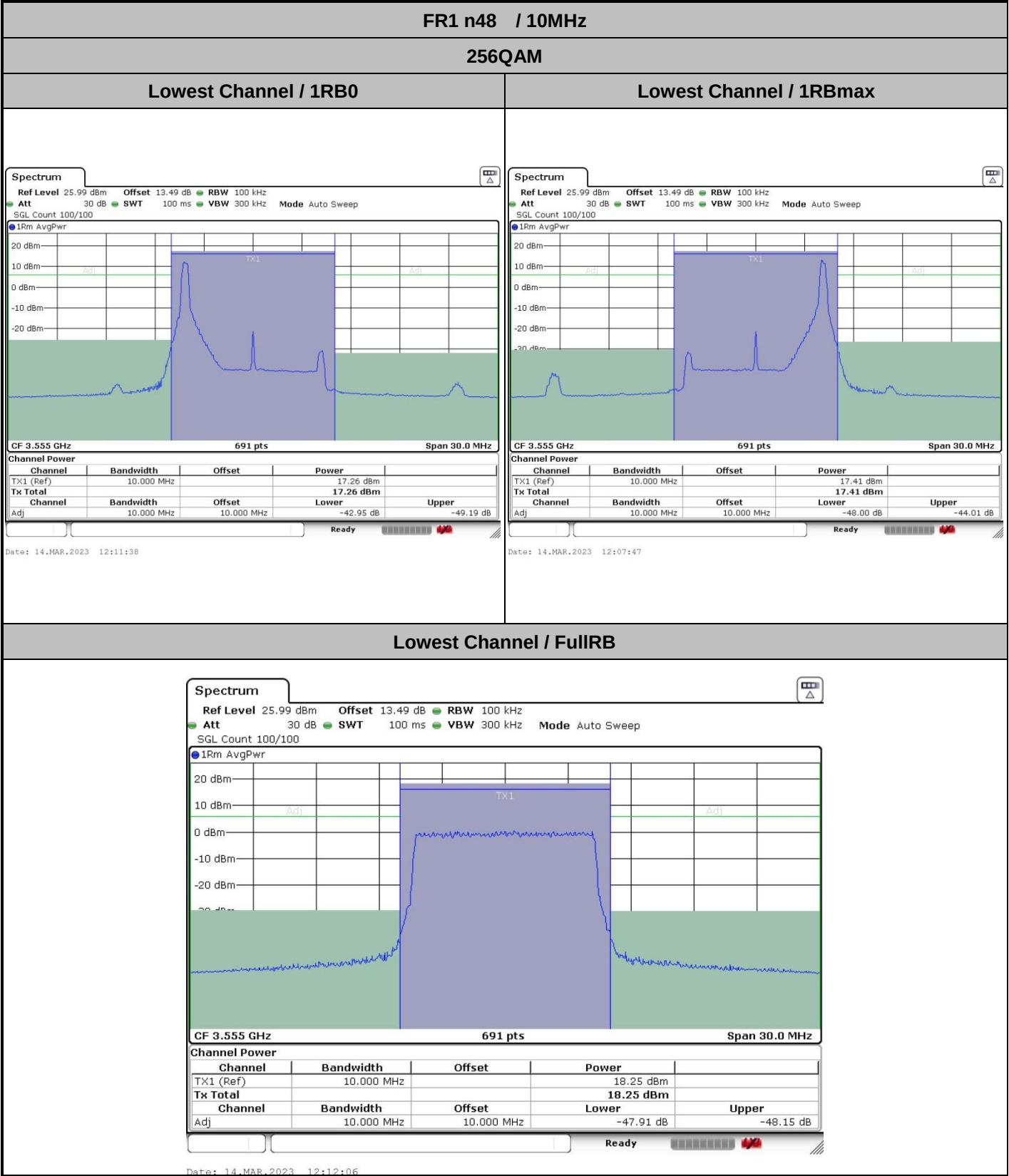


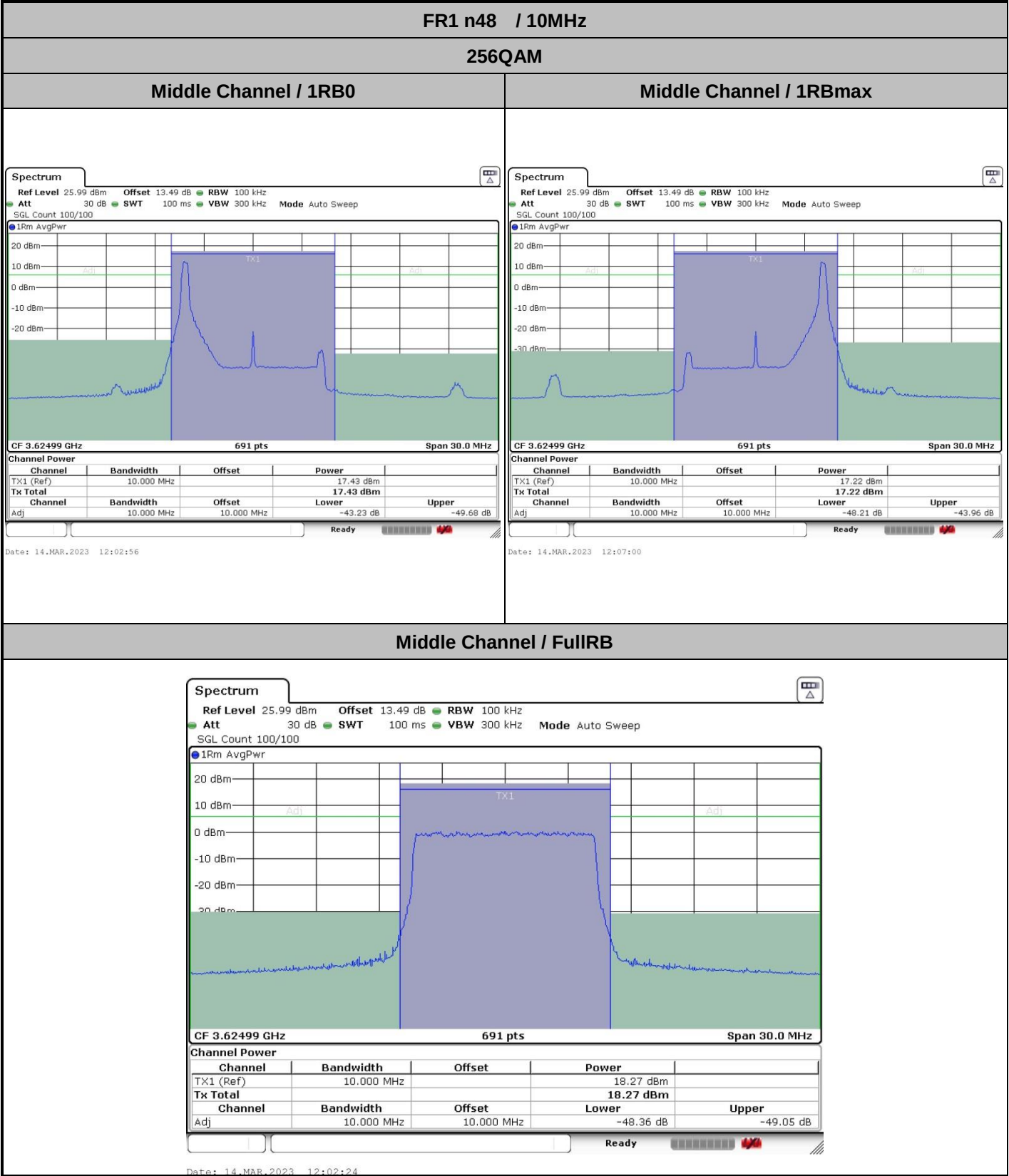
Date: 14.MAR.2023 12:23:05

Highest Channel / FullRB



Date: 14.MAR.2023 12:16:08







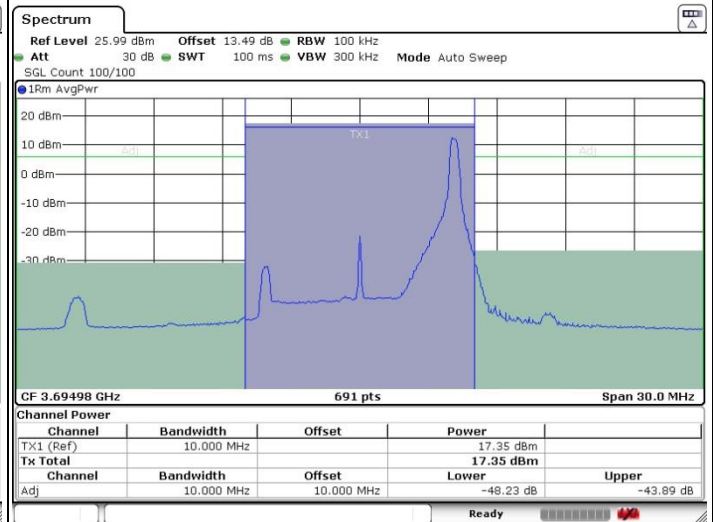
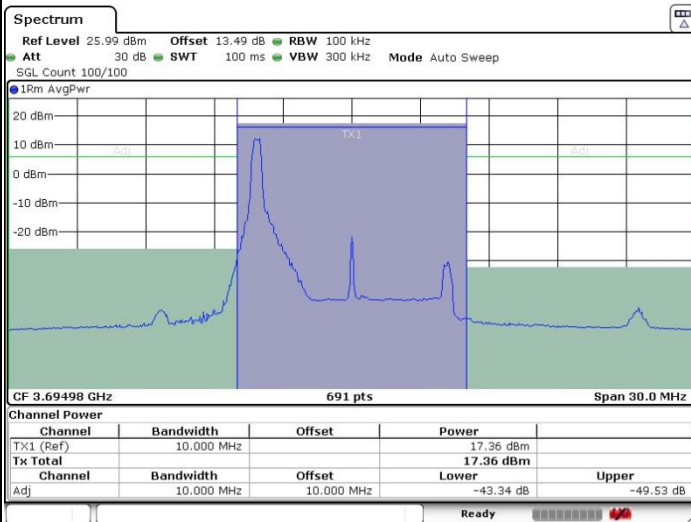


FR1 n48 / 10MHz

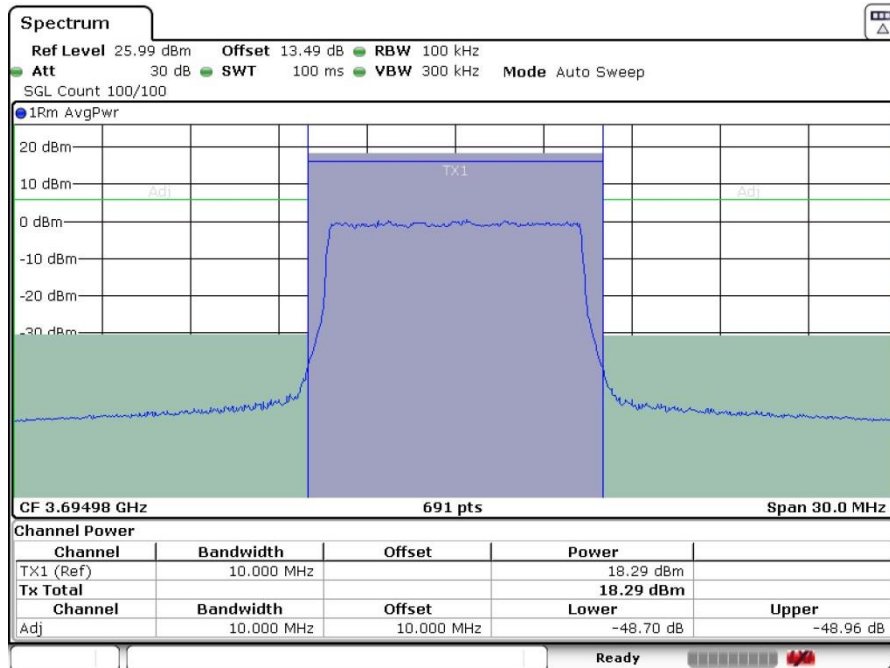
256QAM

Highest Channel / 1RB0

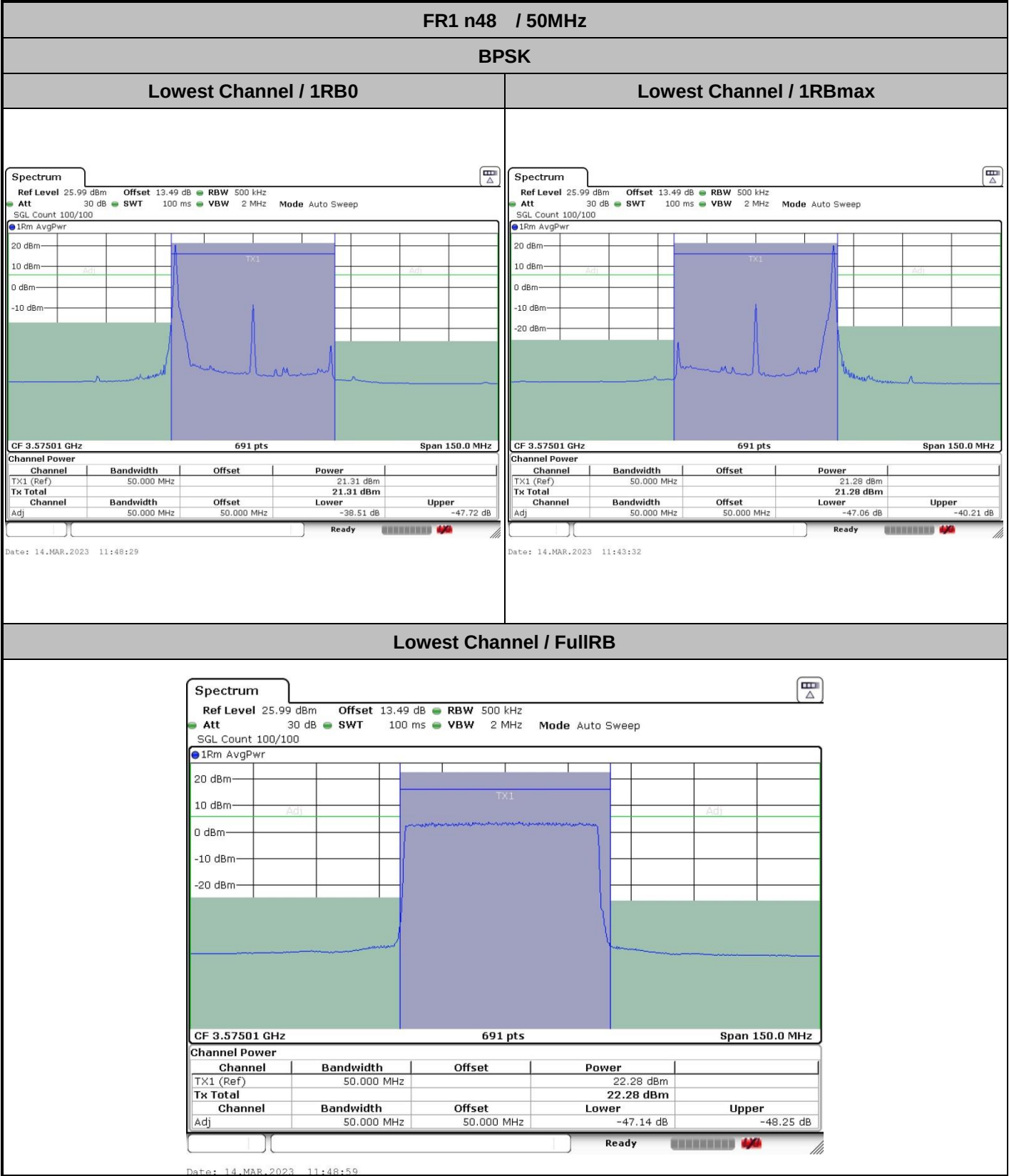
Highest Channel / 1RBmax



Highest Channel / FullRB





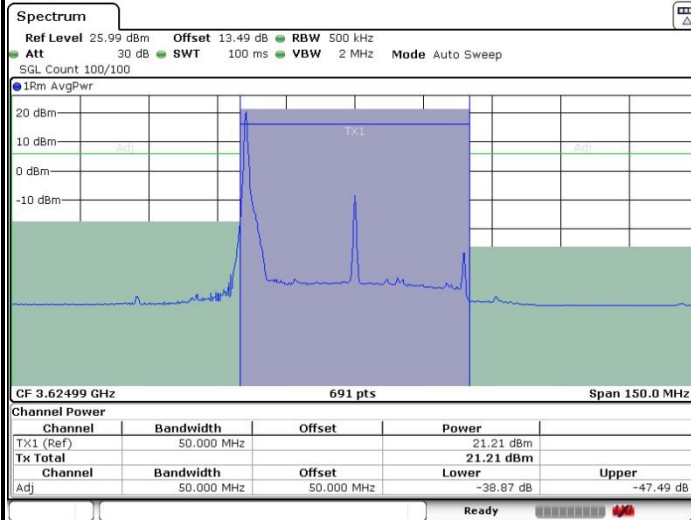




FR1 n48 / 50MHz

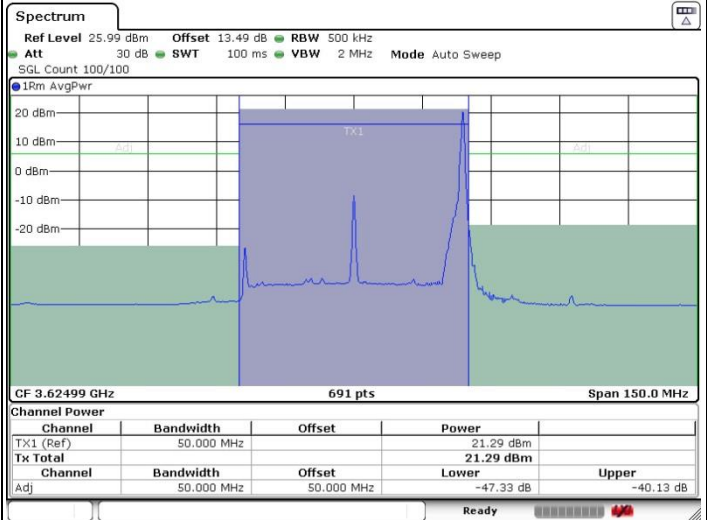
BPSK

Middle Channel / 1RB0



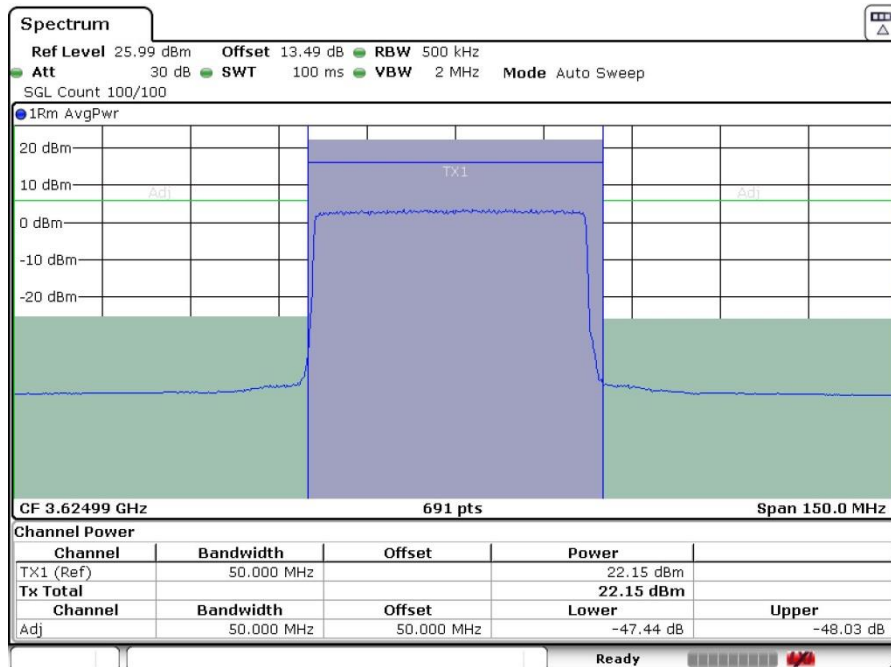
Date: 14.MAR.2023 11:36:23

Middle Channel / 1RBmax



Date: 14.MAR.2023 11:41:47

Middle Channel / FullRB



Date: 14.MAR.2023 11:35:55

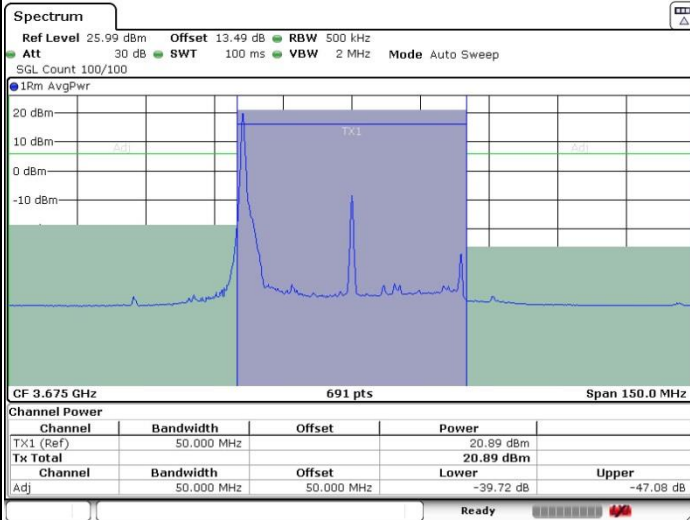


FR1 n48 / 50MHz

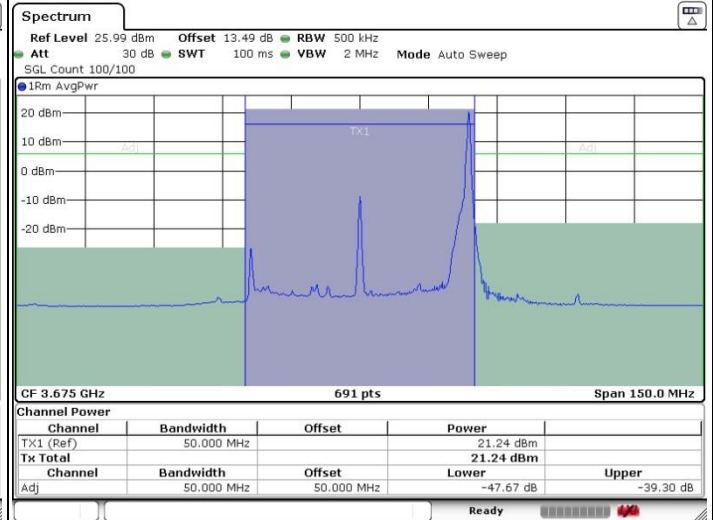
BPSK

Highest Channel / 1RB0

Highest Channel / 1RBmax

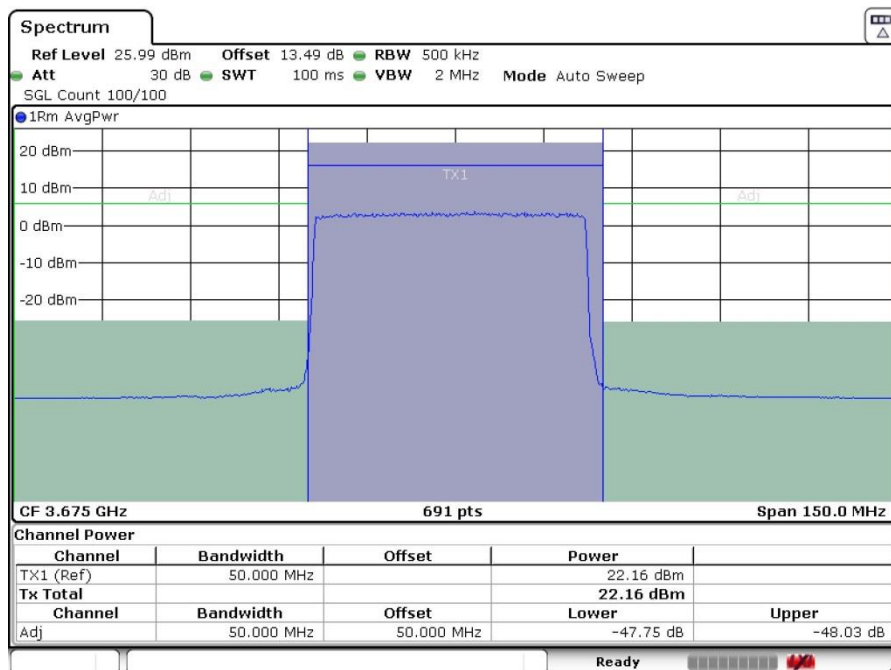


Date: 14.MAR.2023 11:54:25



Date: 14.MAR.2023 11:58:08

Highest Channel / FullRB



Date: 14.MAR.2023 11:53:59

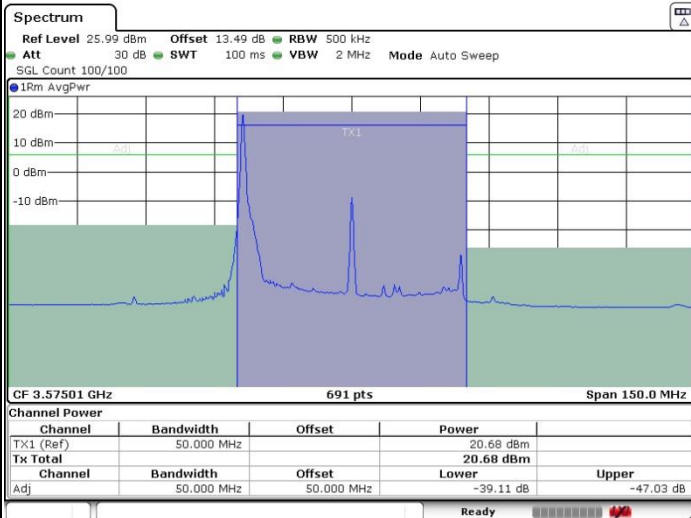


FR1 n48 / 50MHz

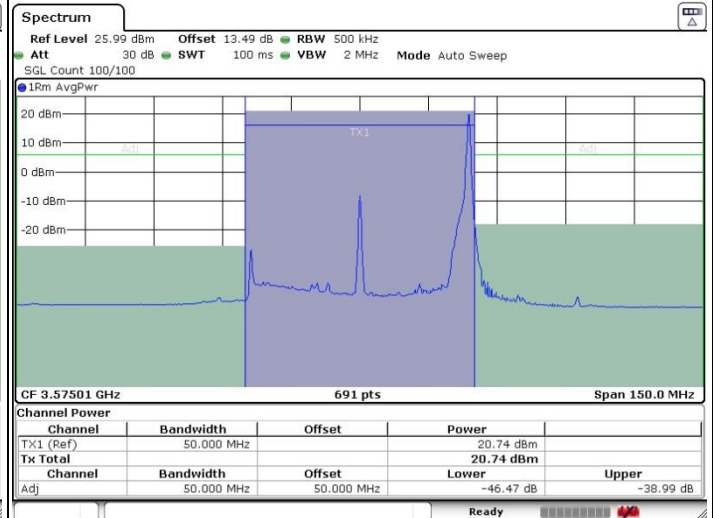
QPSK

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

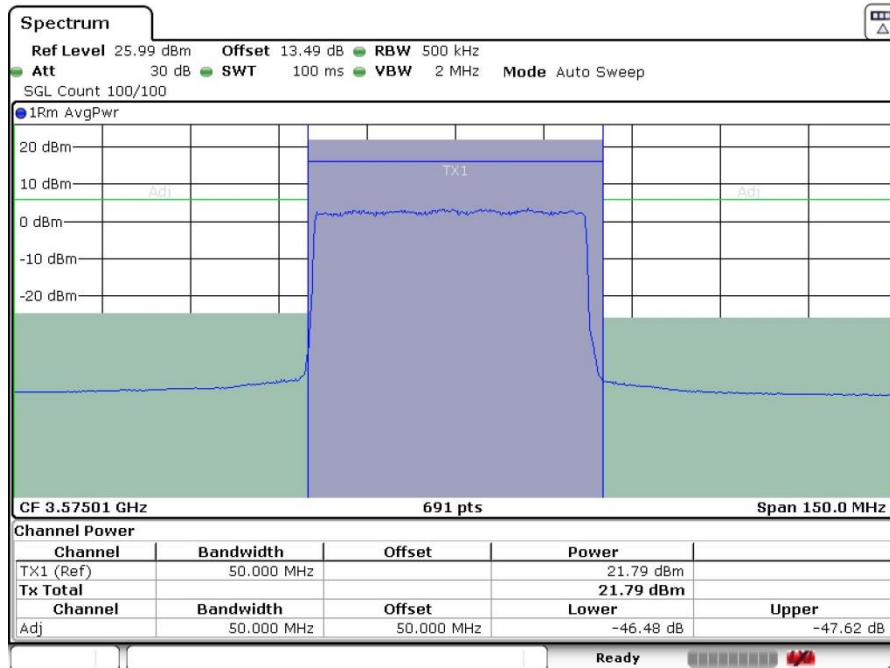


Date: 14.MAR.2023 11:48:06



Date: 14.MAR.2023 11:44:00

Lowest Channel / FullRB



Date: 14.MAR.2023 11:49:32

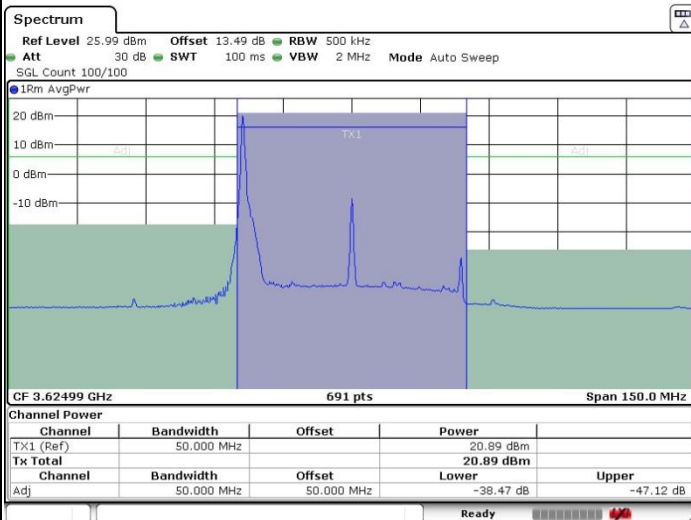


FR1 n48 / 50MHz

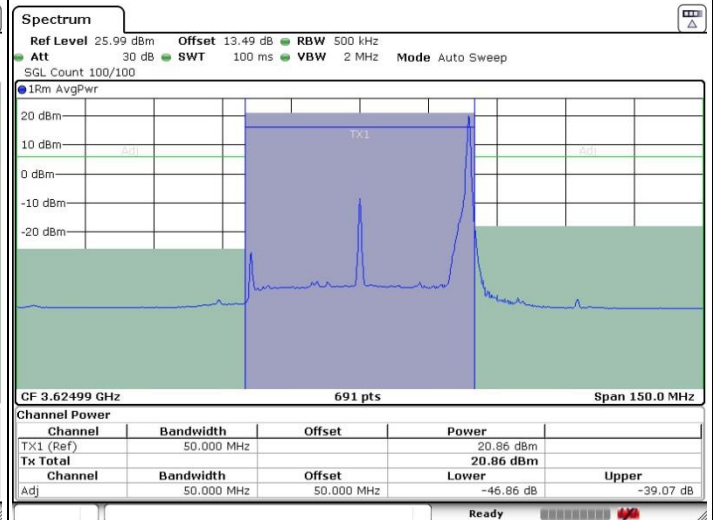
QPSK

Middle Channel / 1RB0

Middle Channel / 1RBmax

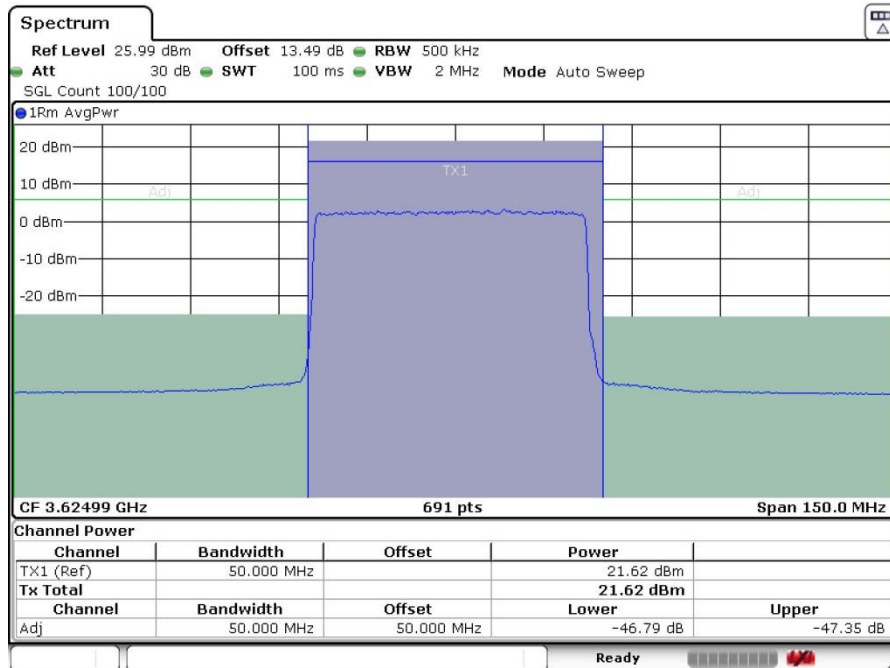


Date: 14.MAR.2023 11:36:50



Date: 14.MAR.2023 11:41:18

Middle Channel / FullRB



Date: 14.MAR.2023 11:35:30

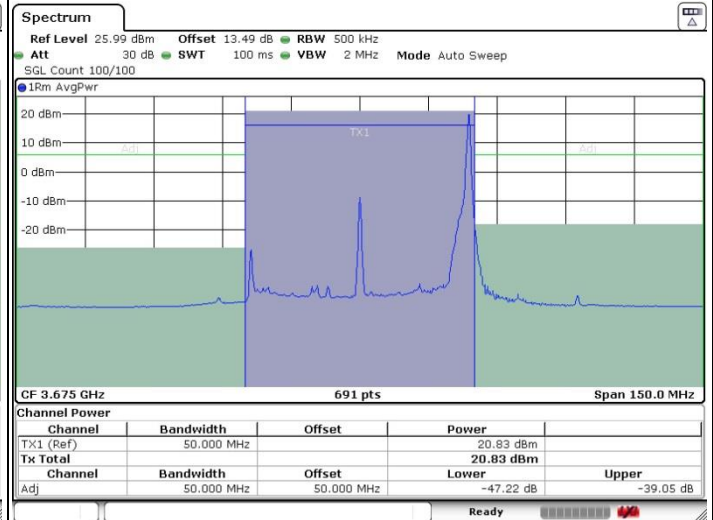
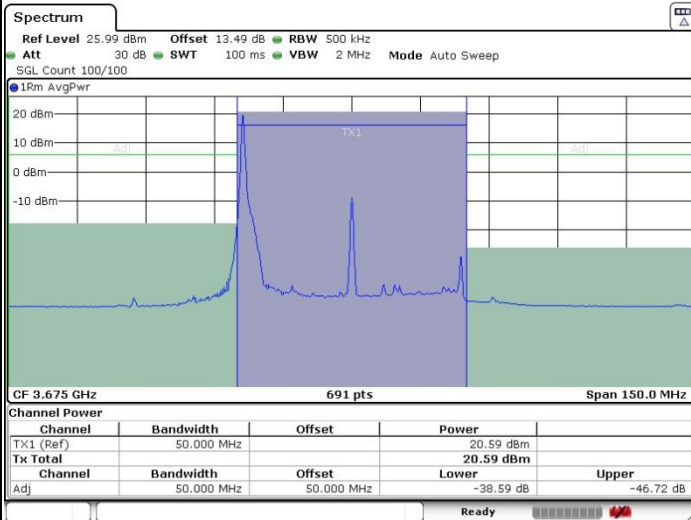


FR1 n48 / 50MHz

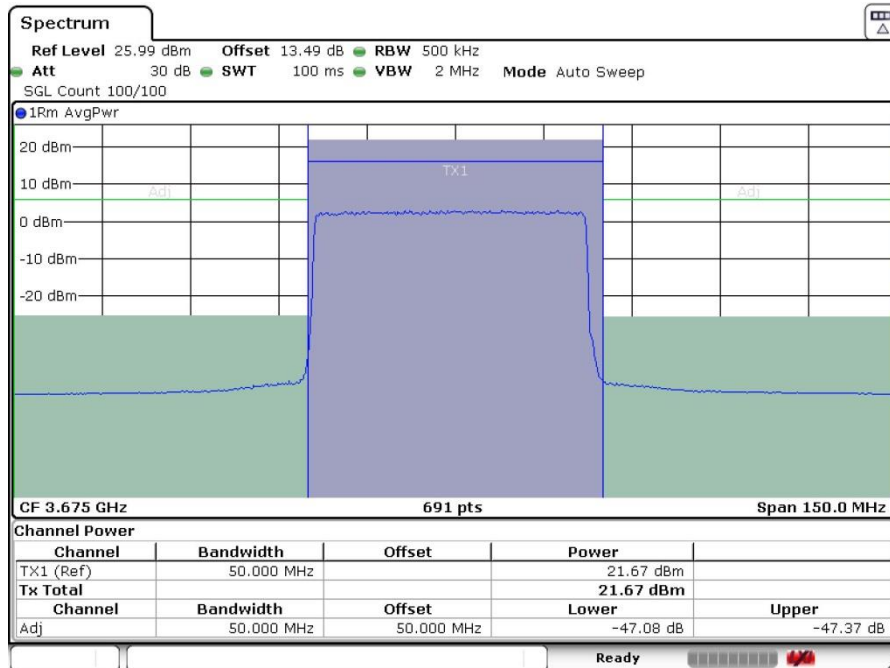
QPSK

Highest Channel / 1RB0

Highest Channel / 1RBmax



Highest Channel / FullRB



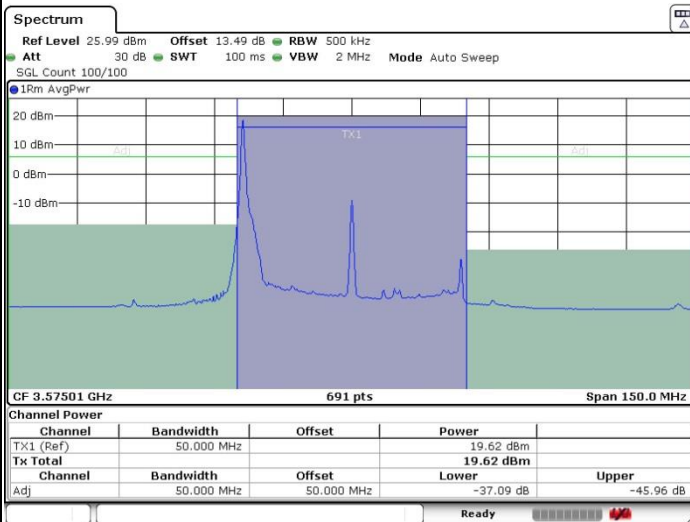


FR1 n48 / 50MHz

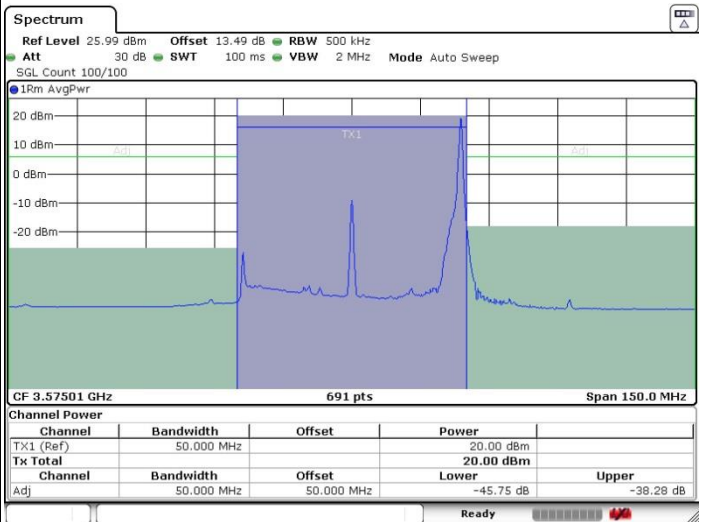
16QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

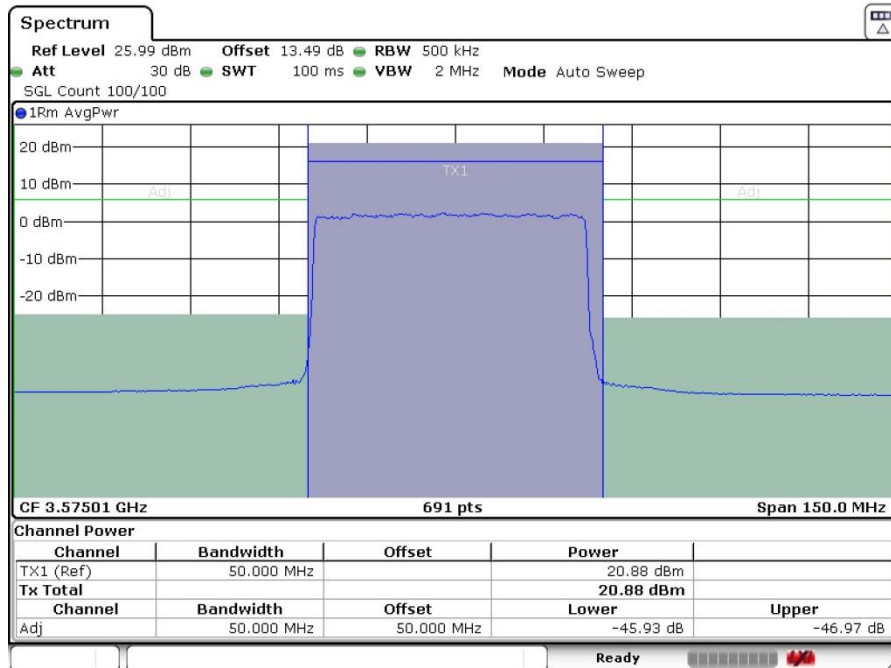


Date: 14.MAR.2023 11:47:40



Date: 14.MAR.2023 11:44:56

Lowest Channel / FullRB



Date: 14.MAR.2023 11:50:01



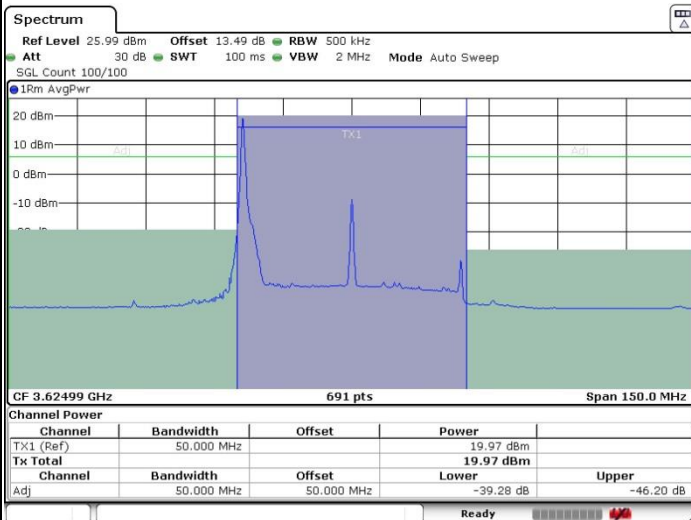


FR1 n48 / 50MHz

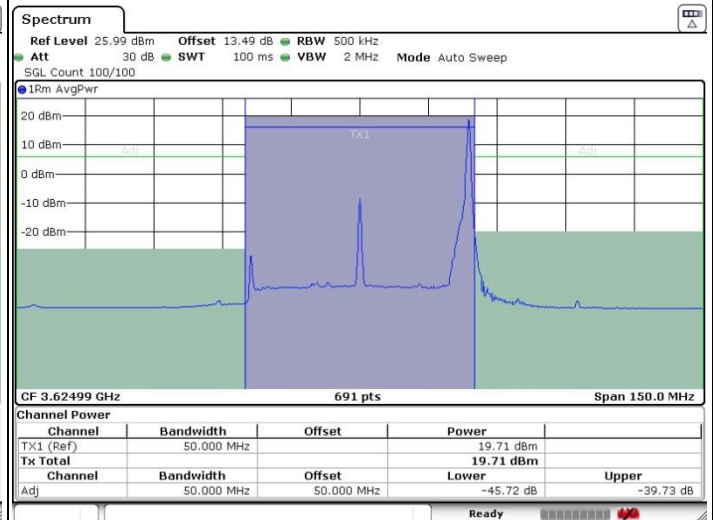
16QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

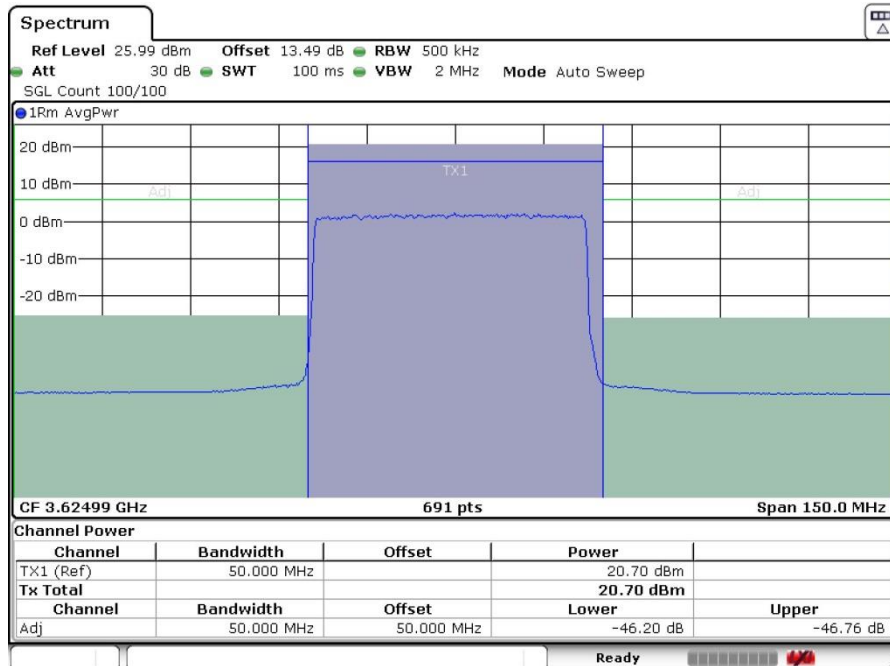


Date: 14.MAR.2023 11:37:41



Date: 14.MAR.2023 11:40:24

Middle Channel / FullRB



Date: 14.MAR.2023 11:35:05

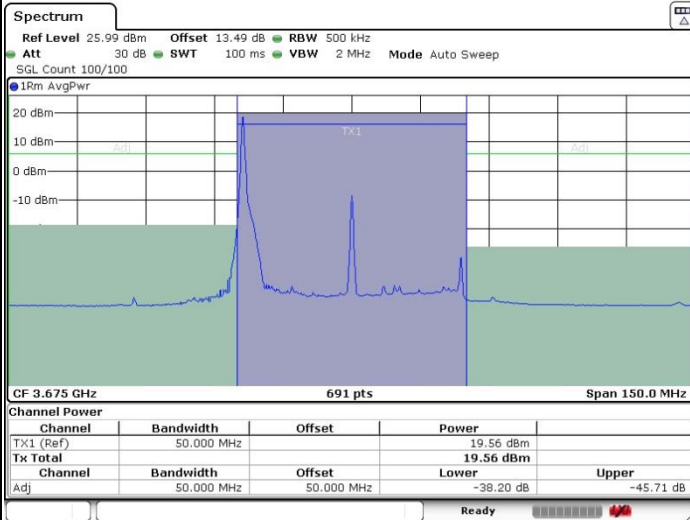


FR1 n48 / 50MHz

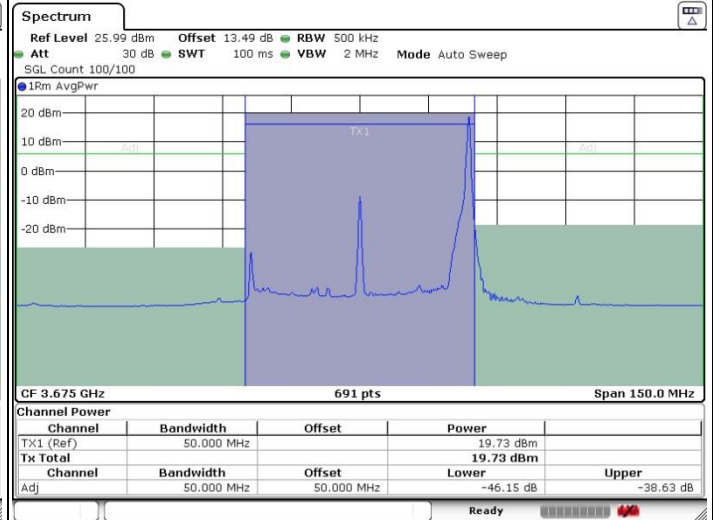
16QAM

Highest Channel / 1RB0

Highest Channel / 1RBmax

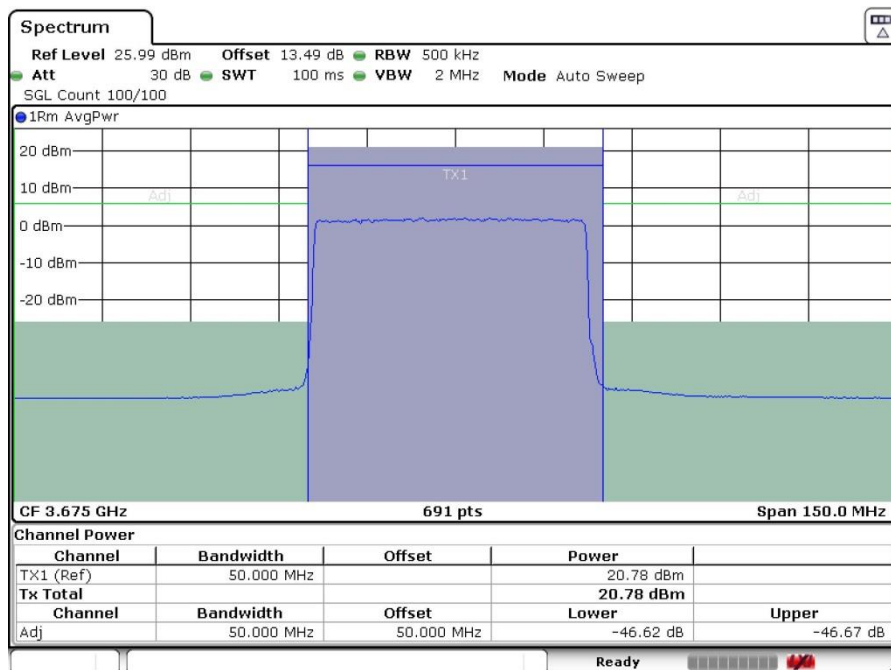


Date: 14.MAR.2023 11:55:16



Date: 14.MAR.2023 11:57:22

Highest Channel / FullRB



Date: 14.MAR.2023 11:53:11

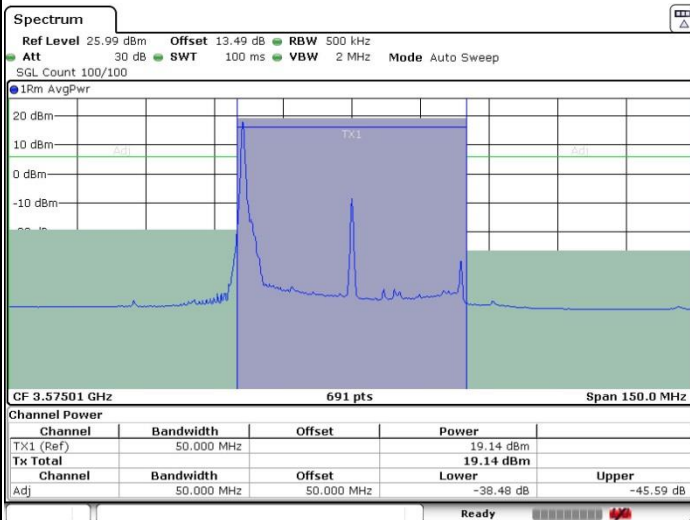


FR1 n48 / 50MHz

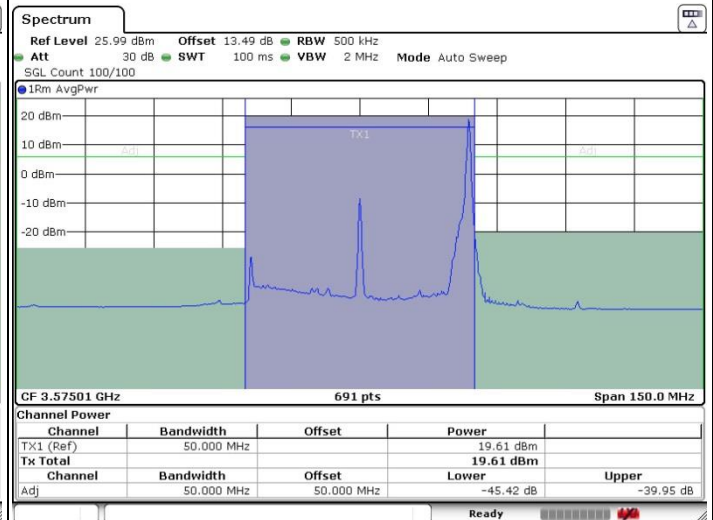
64QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax

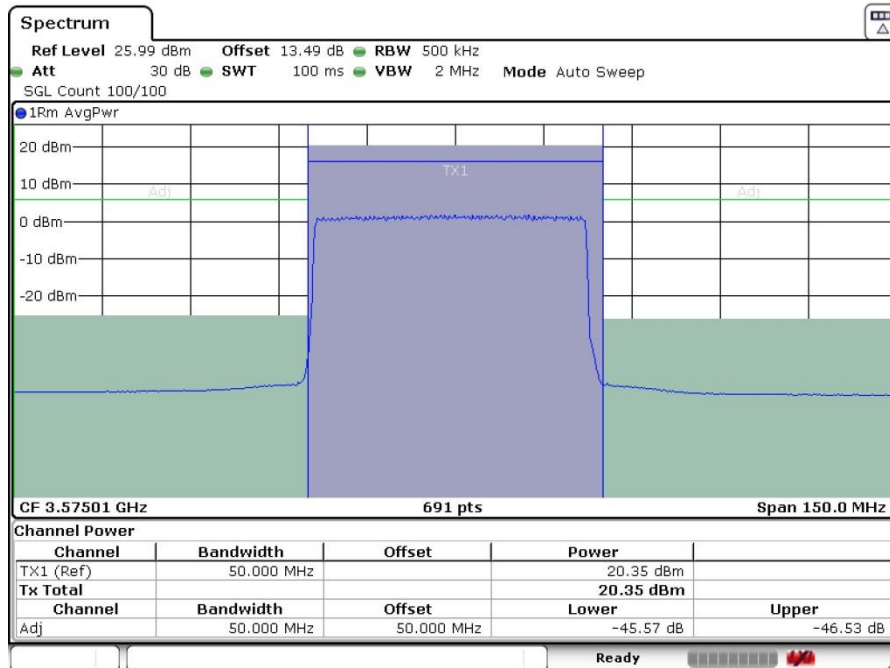


Date: 14.MAR.2023 11:46:50



Date: 14.MAR.2023 11:45:22

Lowest Channel / FullRB



Date: 14.MAR.2023 11:50:26

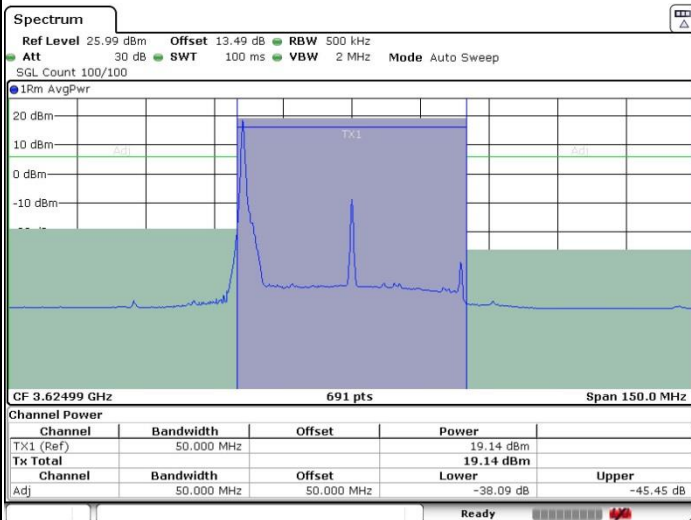


FR1 n48 / 50MHz

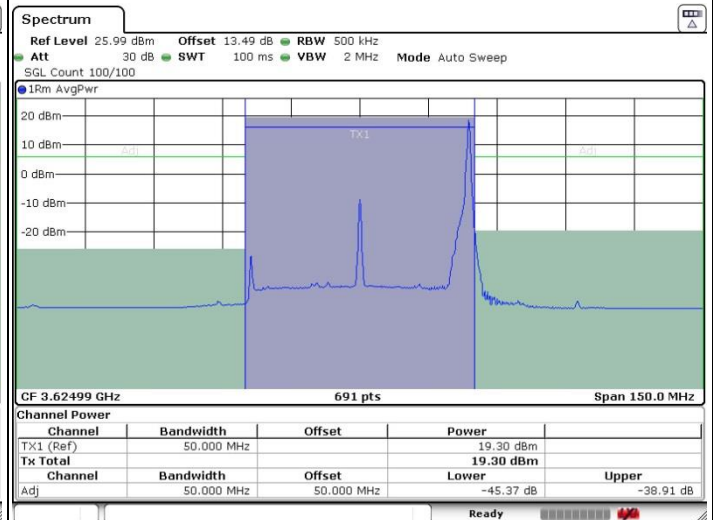
64QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

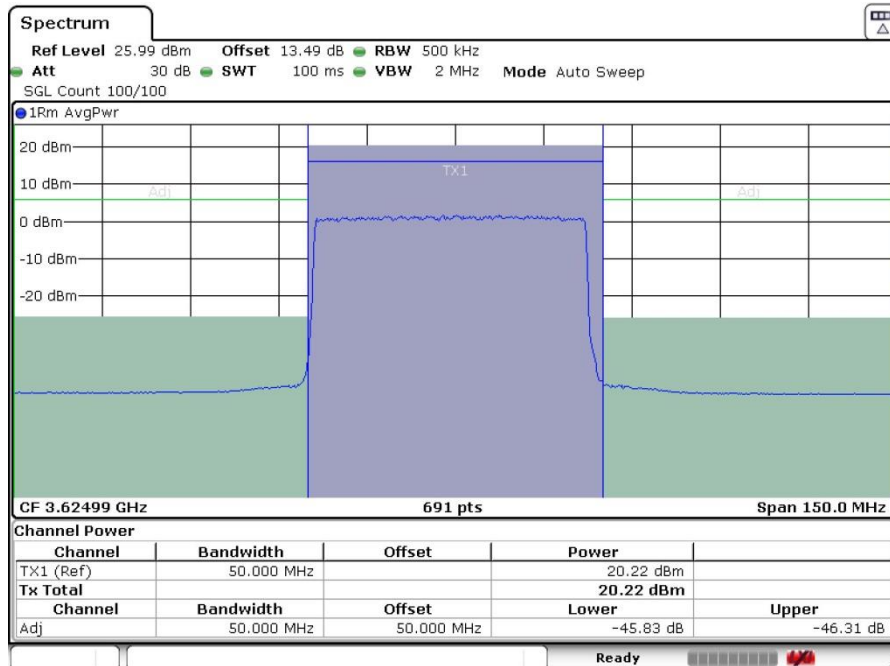


Date: 14.MAR.2023 11:38:18

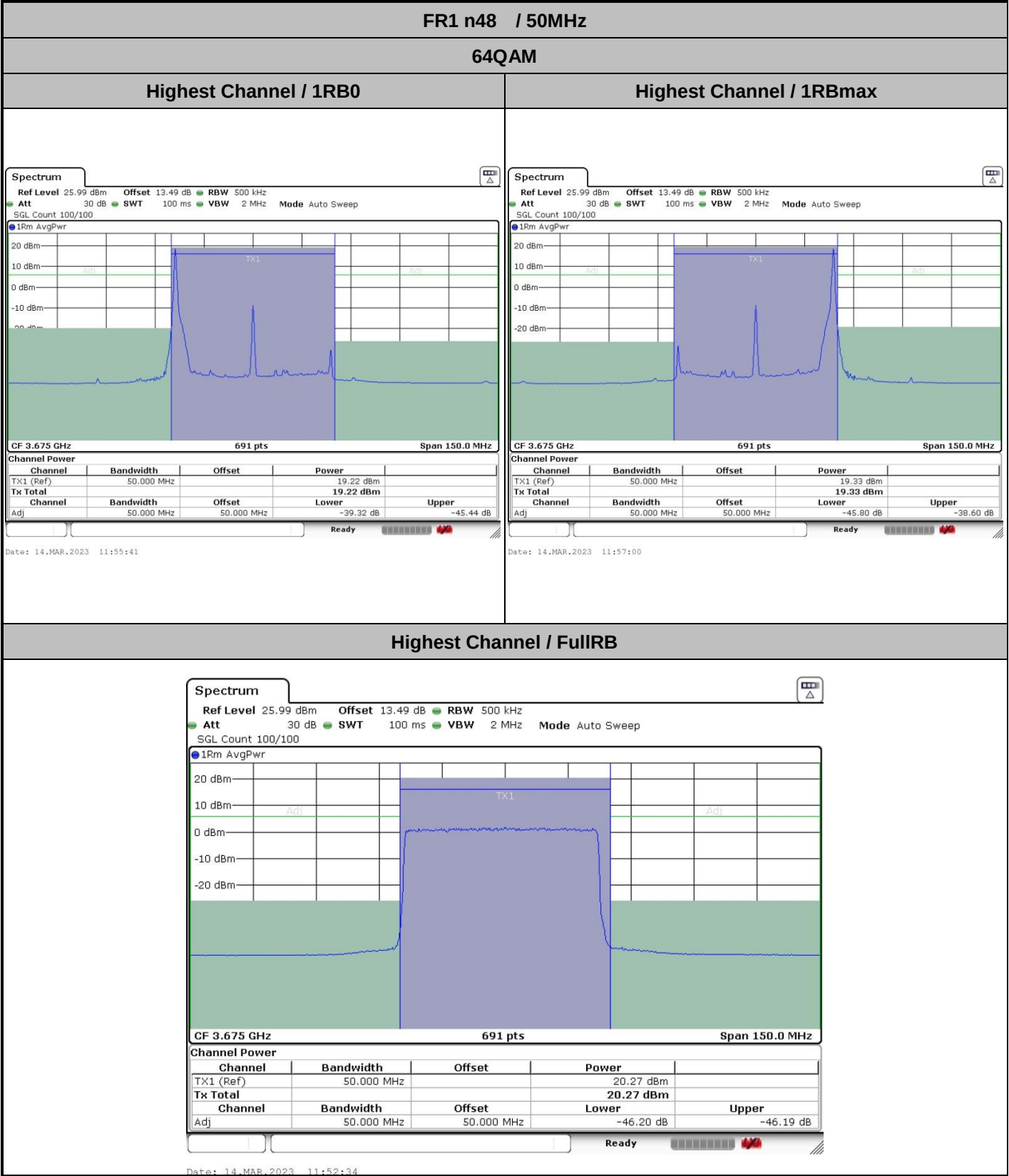


Date: 14.MAR.2023 11:40:00

Middle Channel / FullRB



Date: 14.MAR.2023 11:34:26



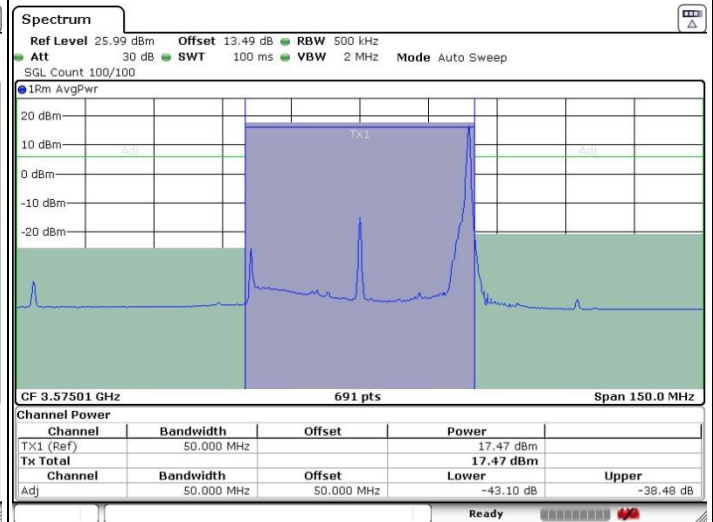
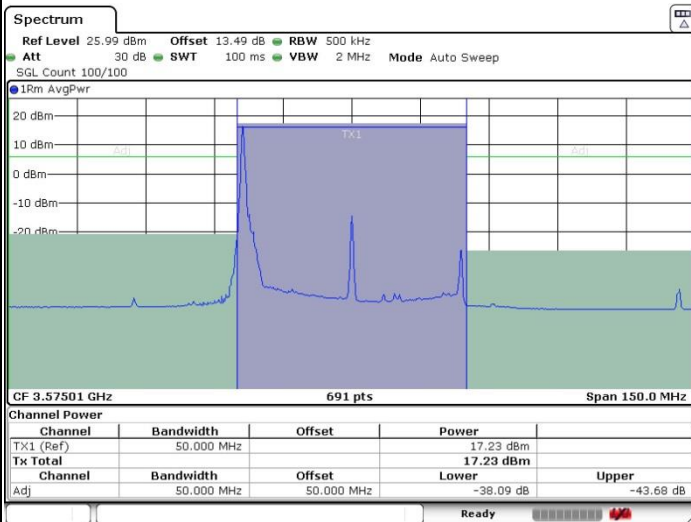


FR1 n48 / 50MHz

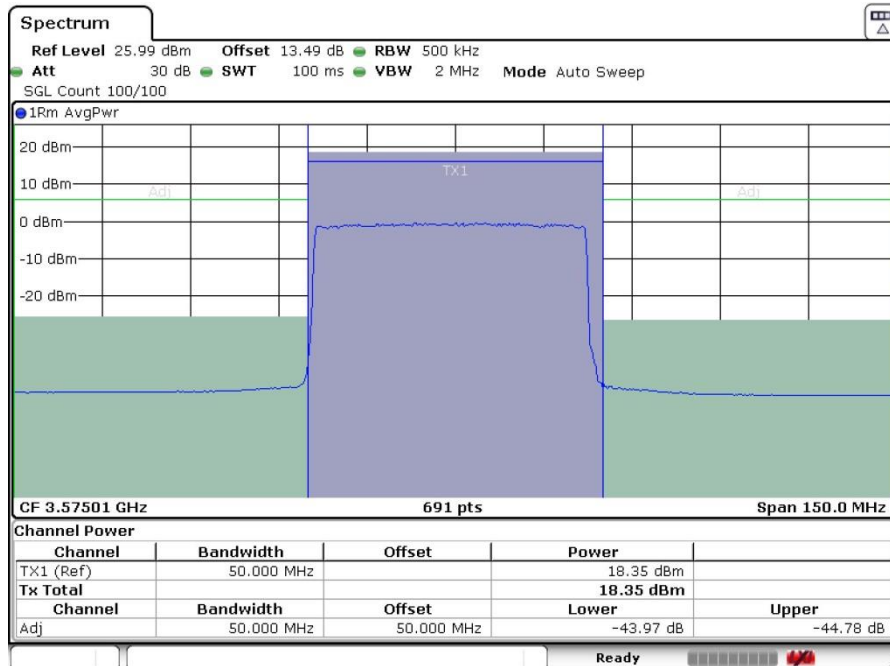
256QAM

Lowest Channel / 1RB0

Lowest Channel / 1RBmax



Lowest Channel / FullRB



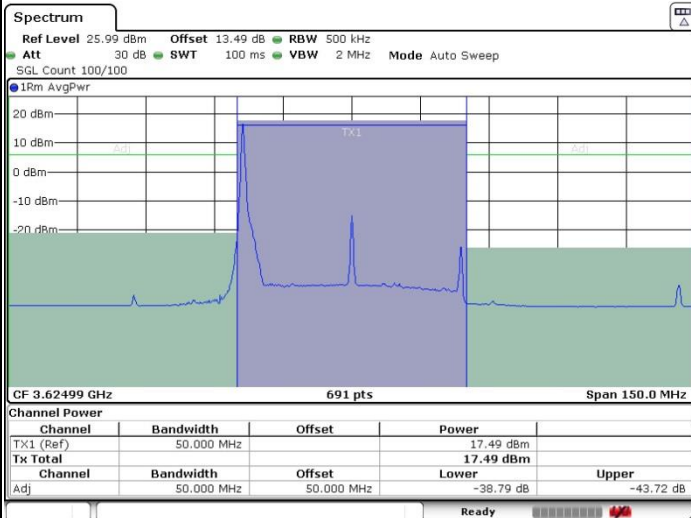


FR1 n48 / 50MHz

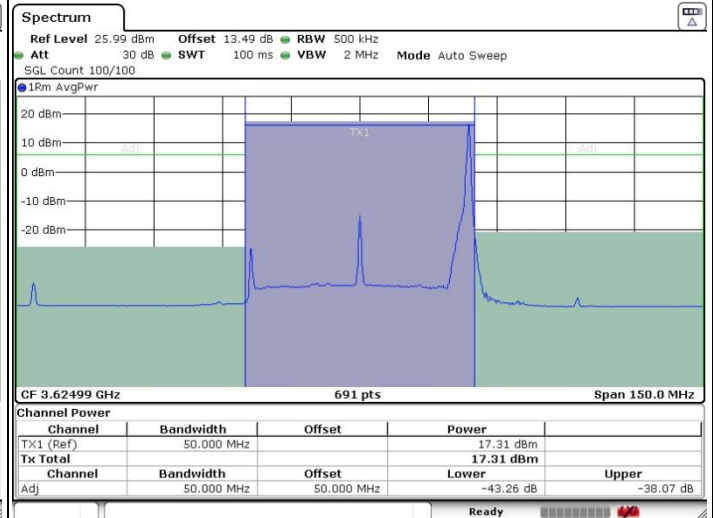
256QAM

Middle Channel / 1RB0

Middle Channel / 1RBmax

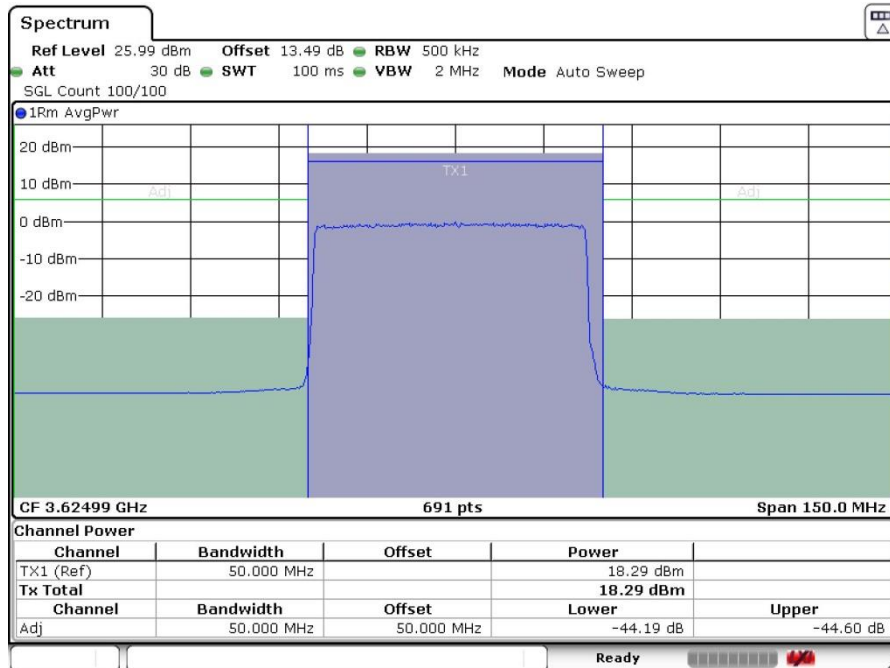


Date: 14.MAR.2023 11:38:50



Date: 14.MAR.2023 11:39:26

Middle Channel / FullRB



Date: 14.MAR.2023 11:32:39



