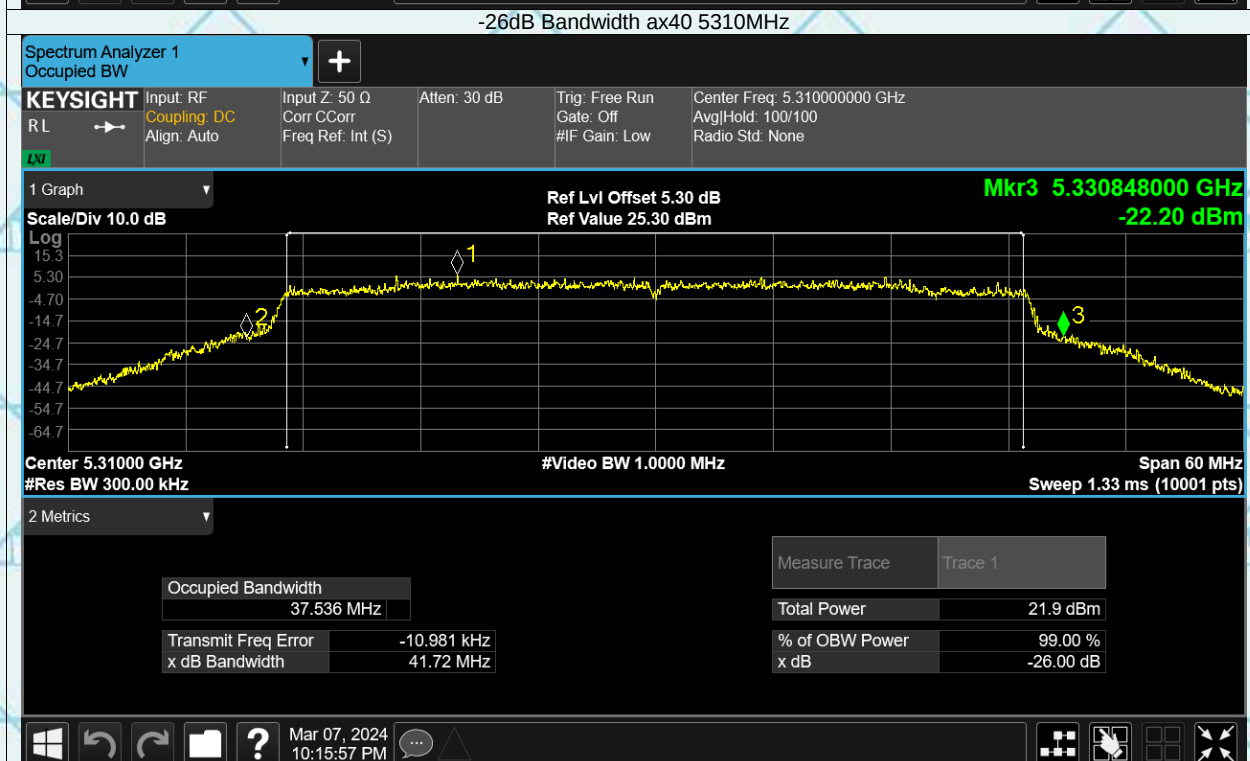
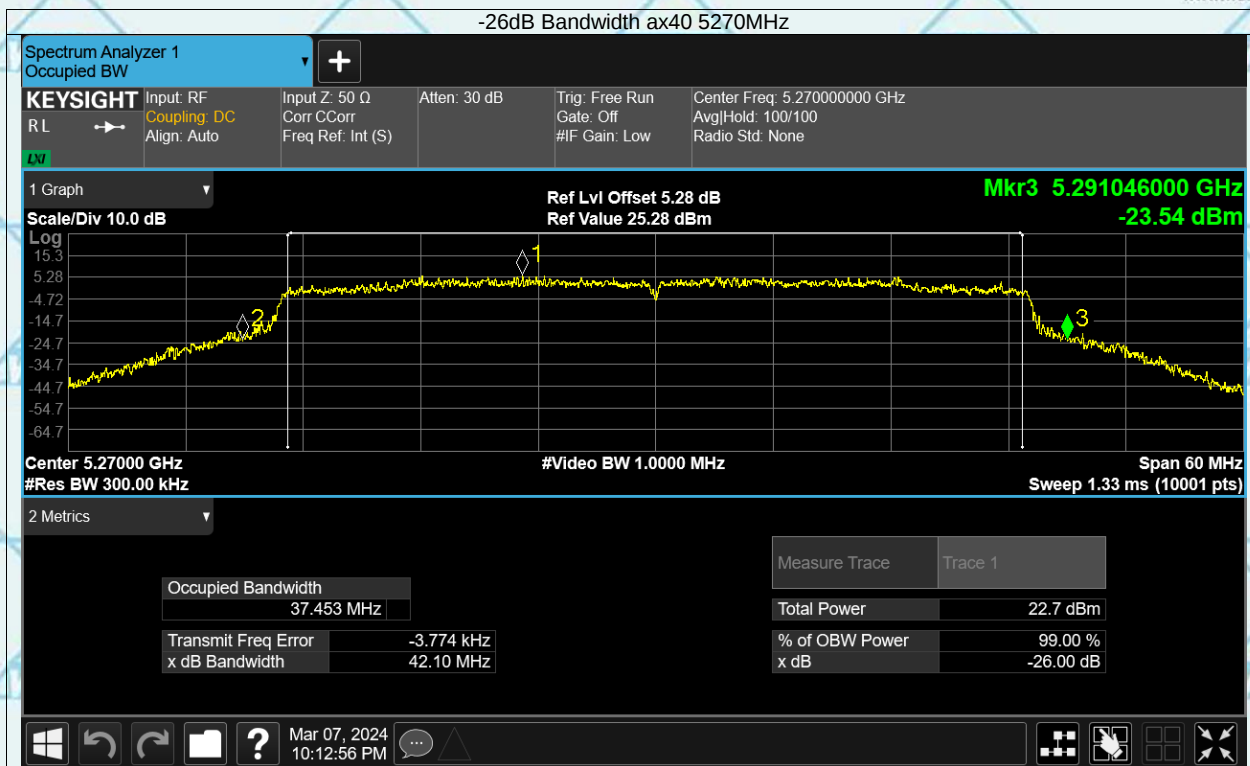




Report No.: WSCT-A2LA-R&E240300011A-Wi-Fi2

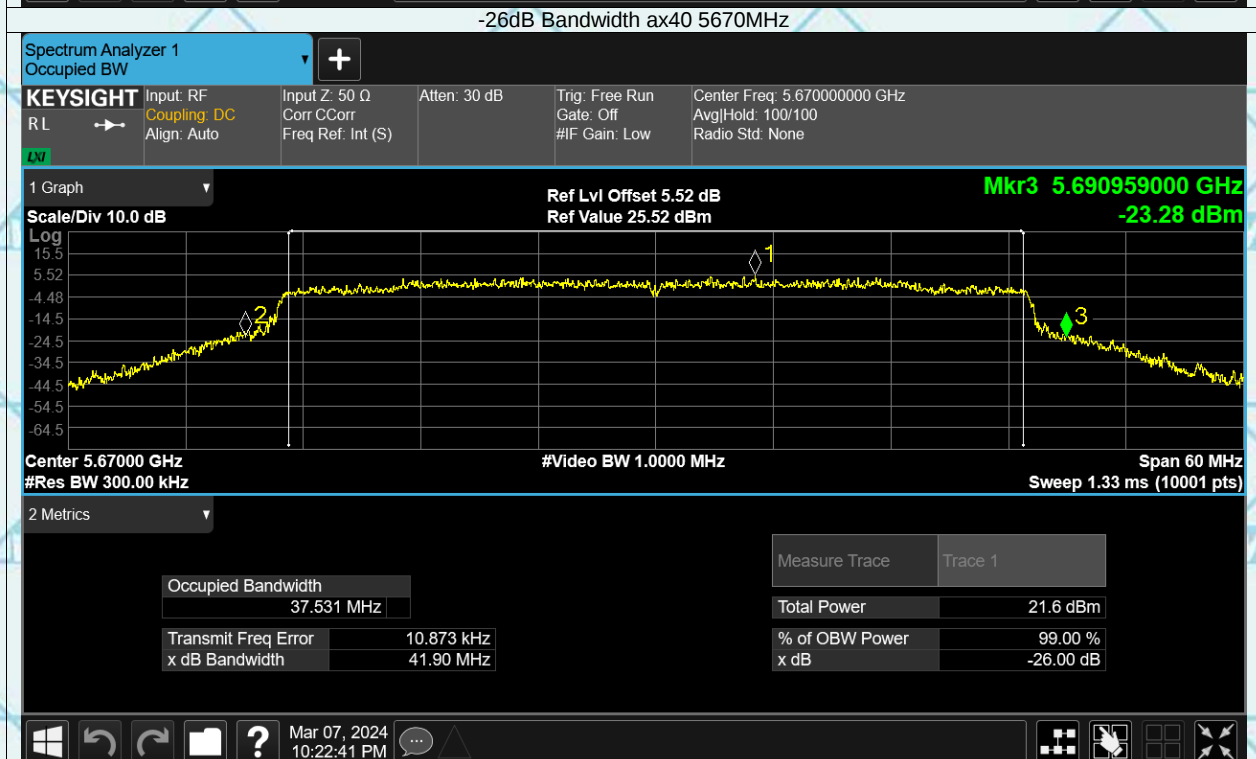
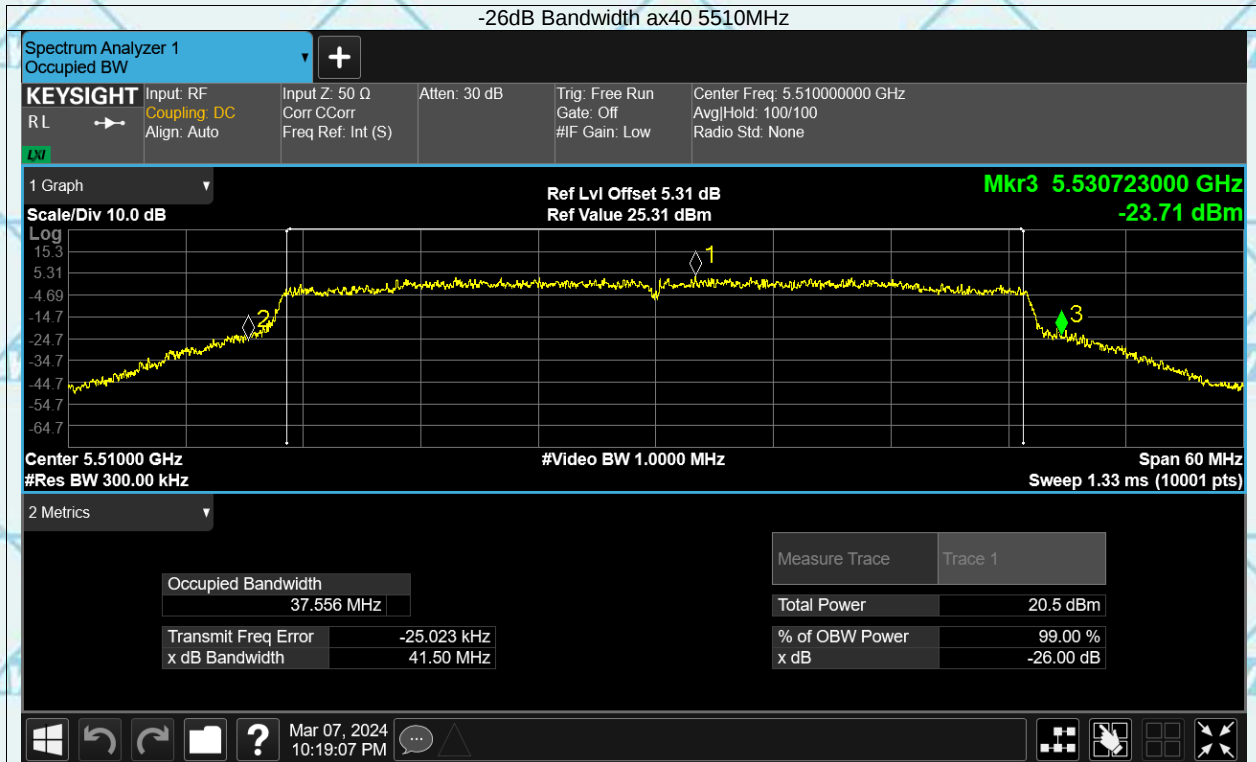
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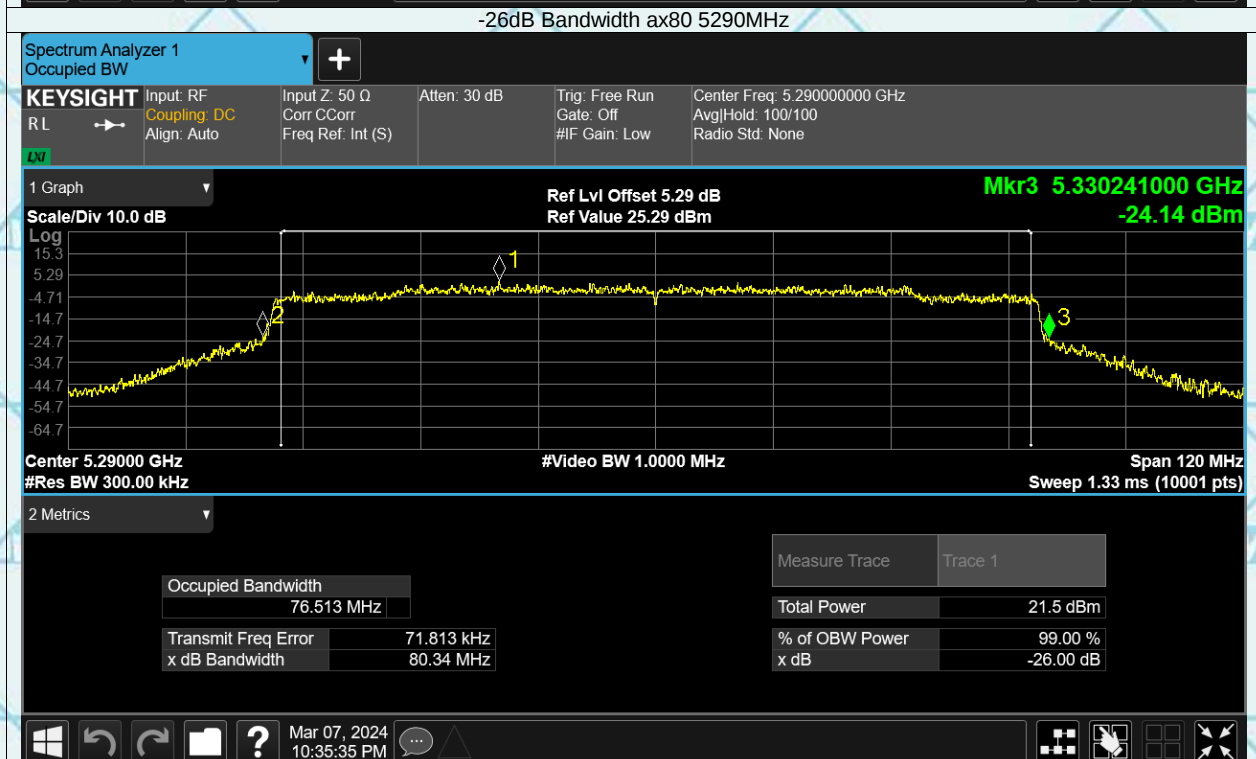
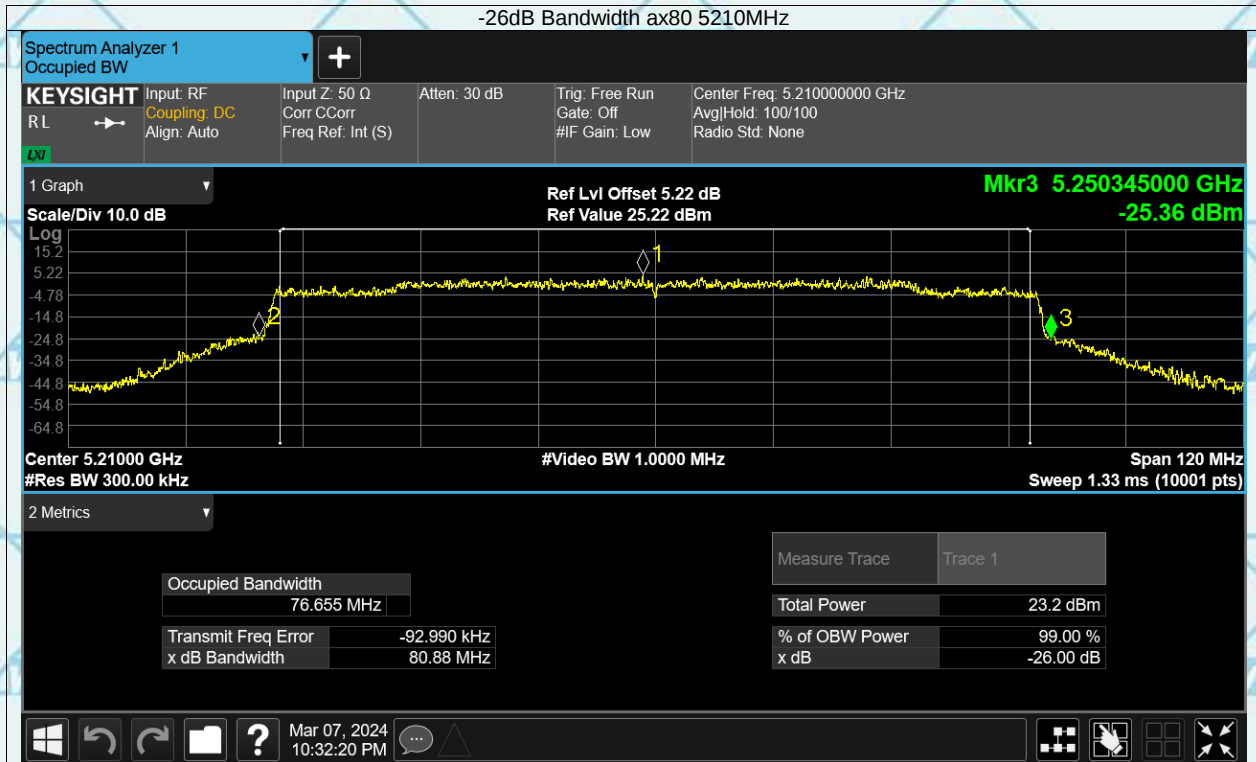
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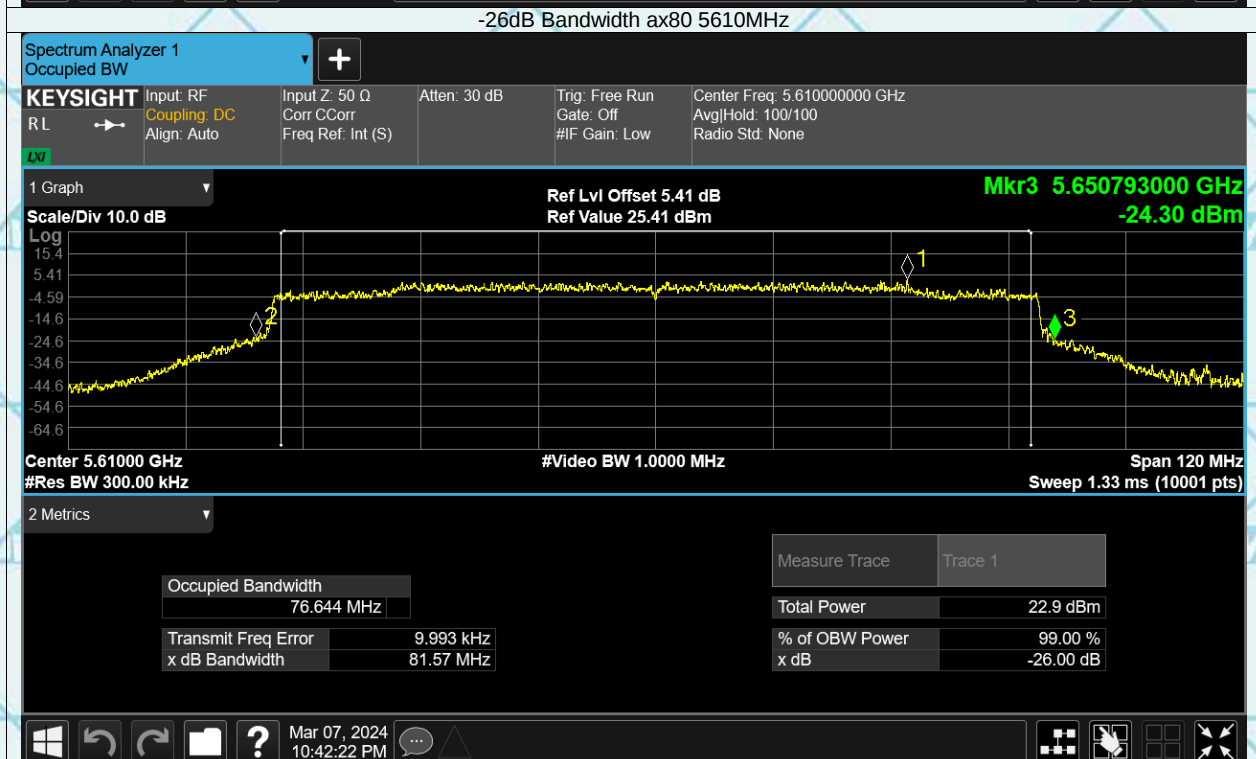
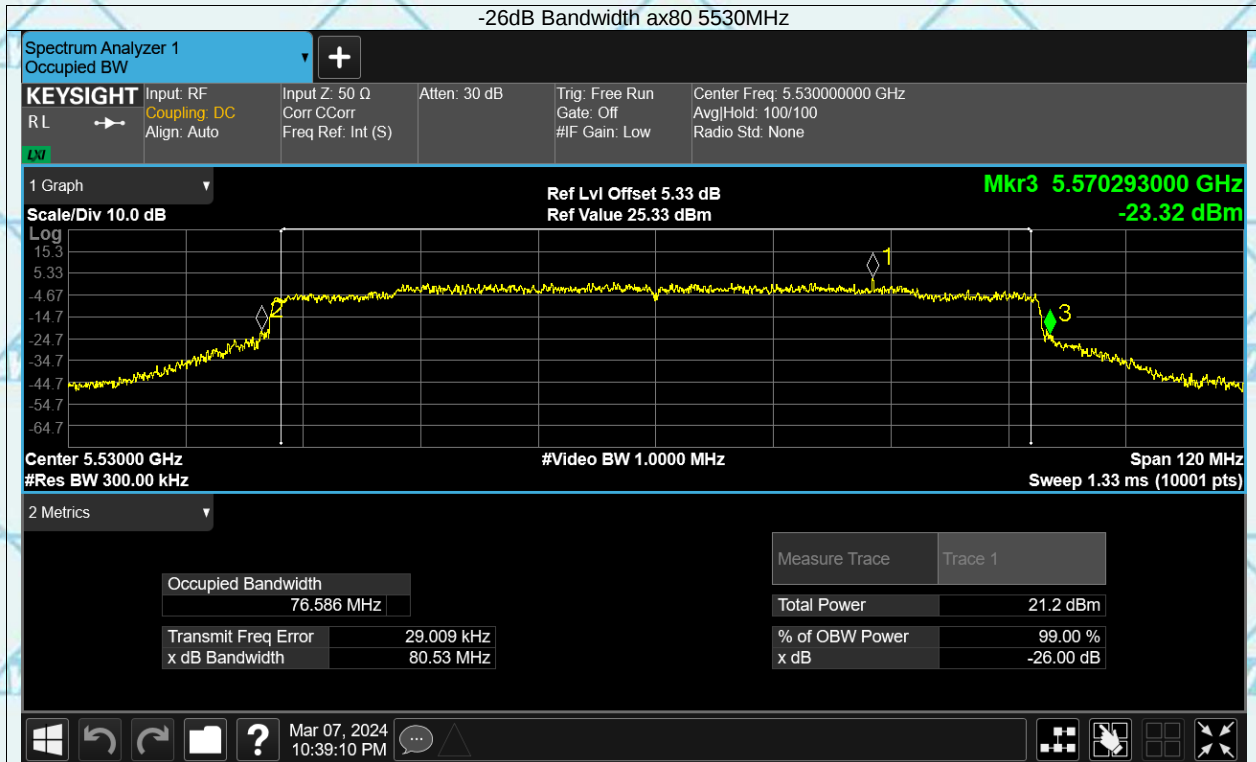
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Report No.: WSCT-A2LA-R&E240300011A-Wi-Fi2

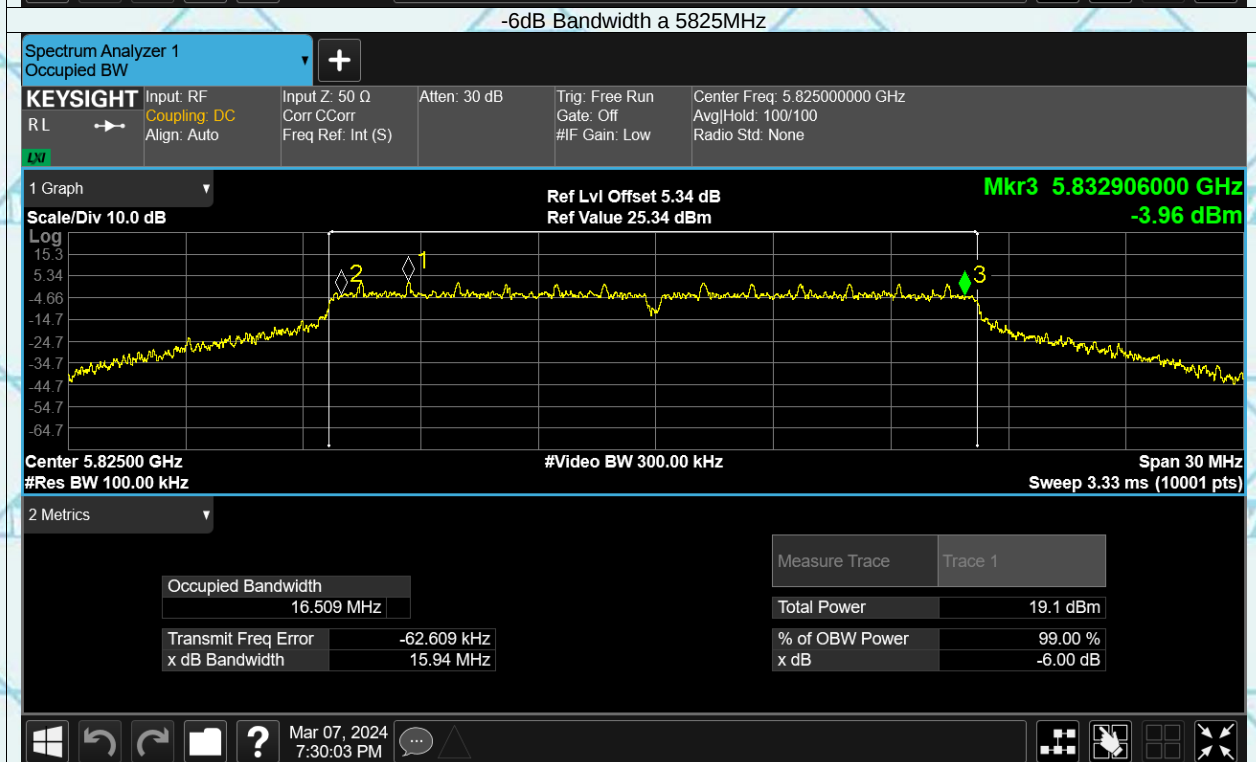
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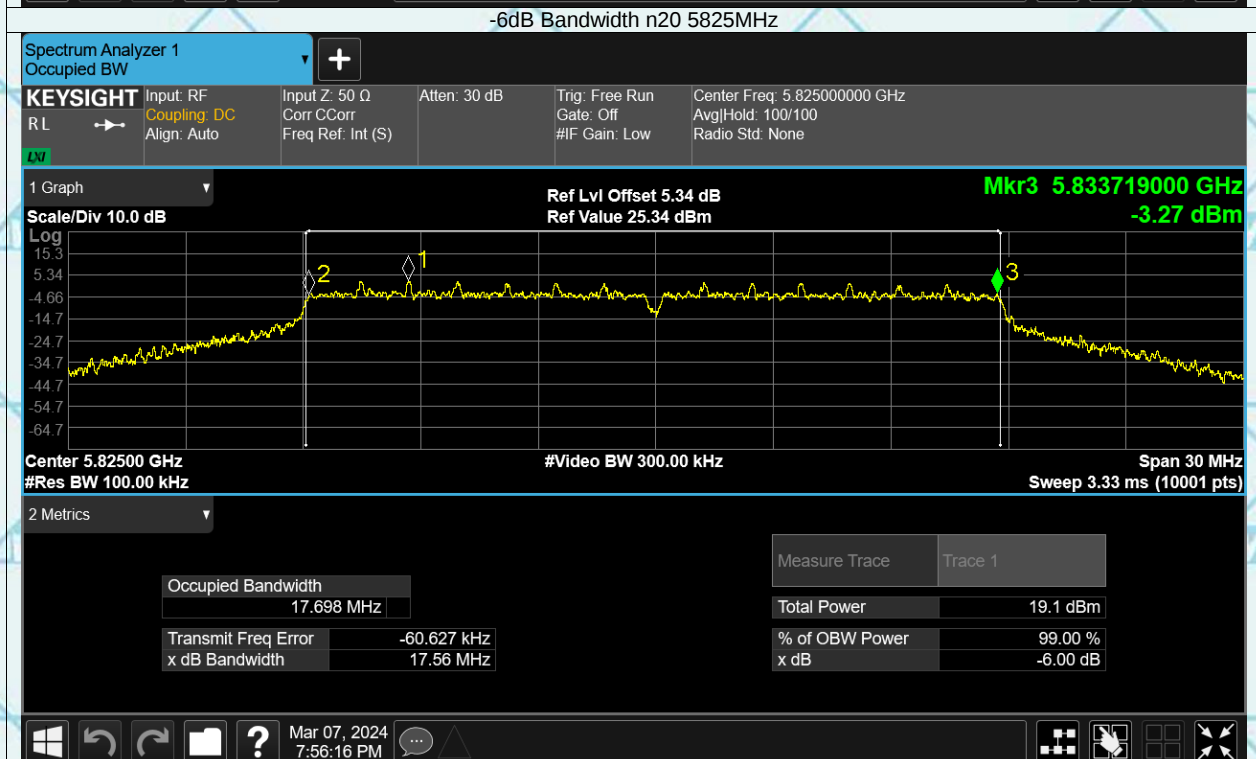
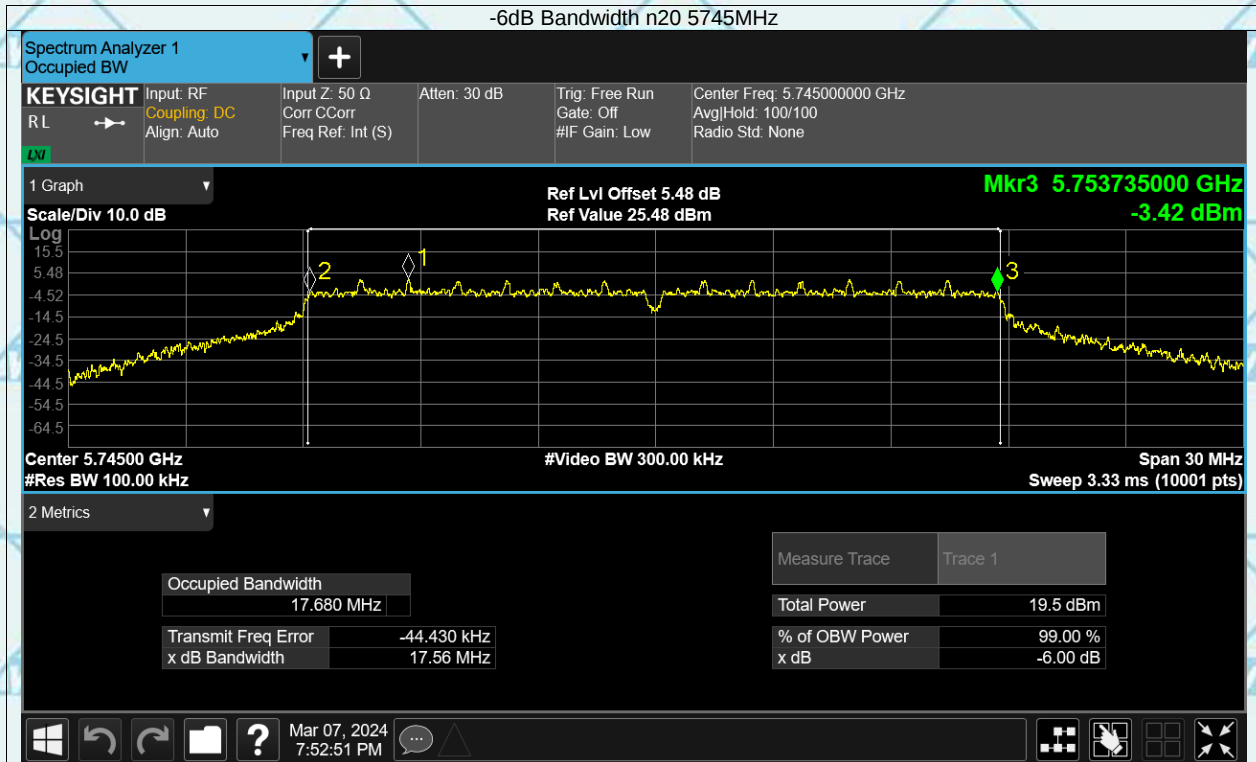
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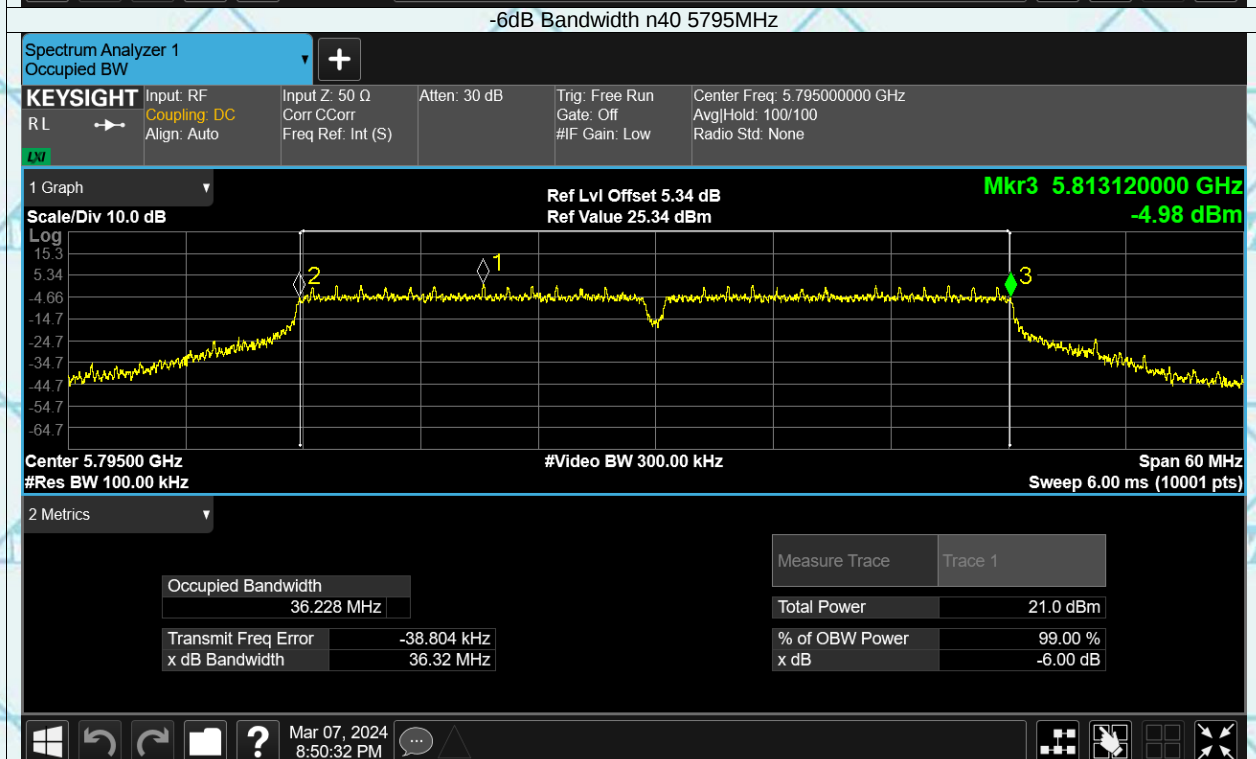
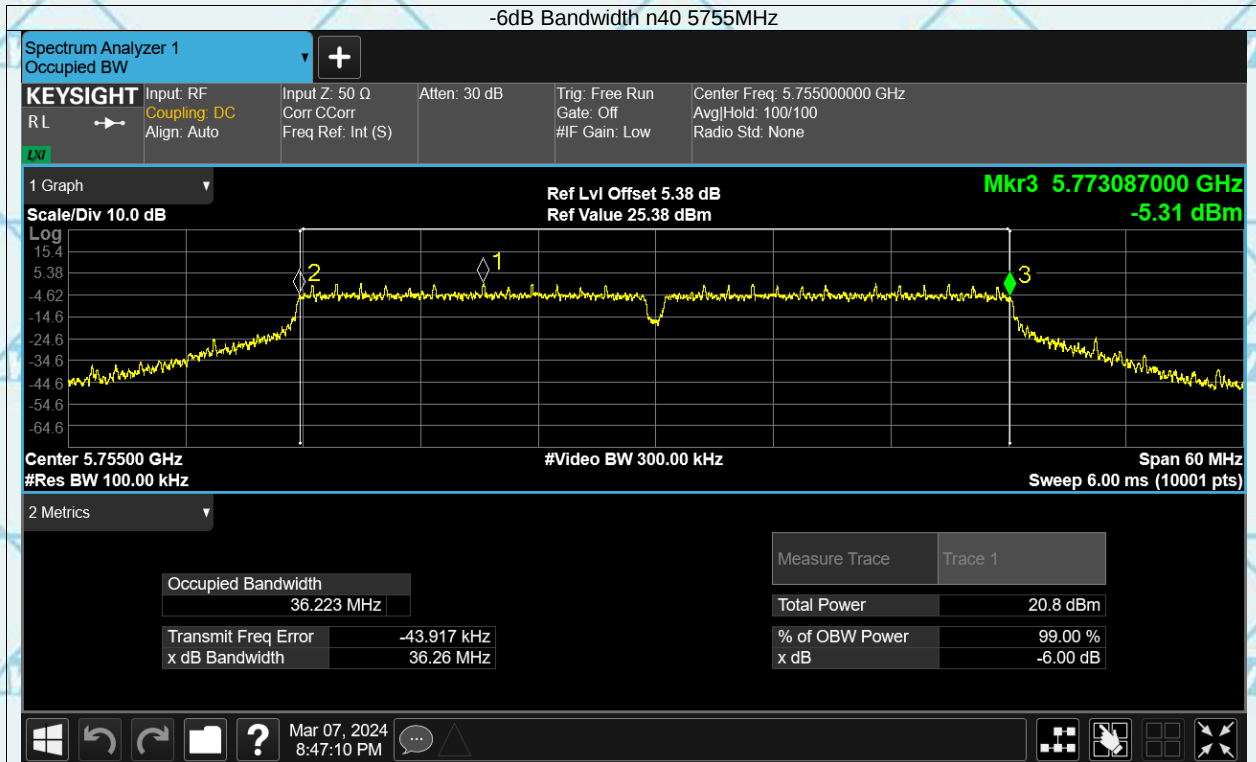
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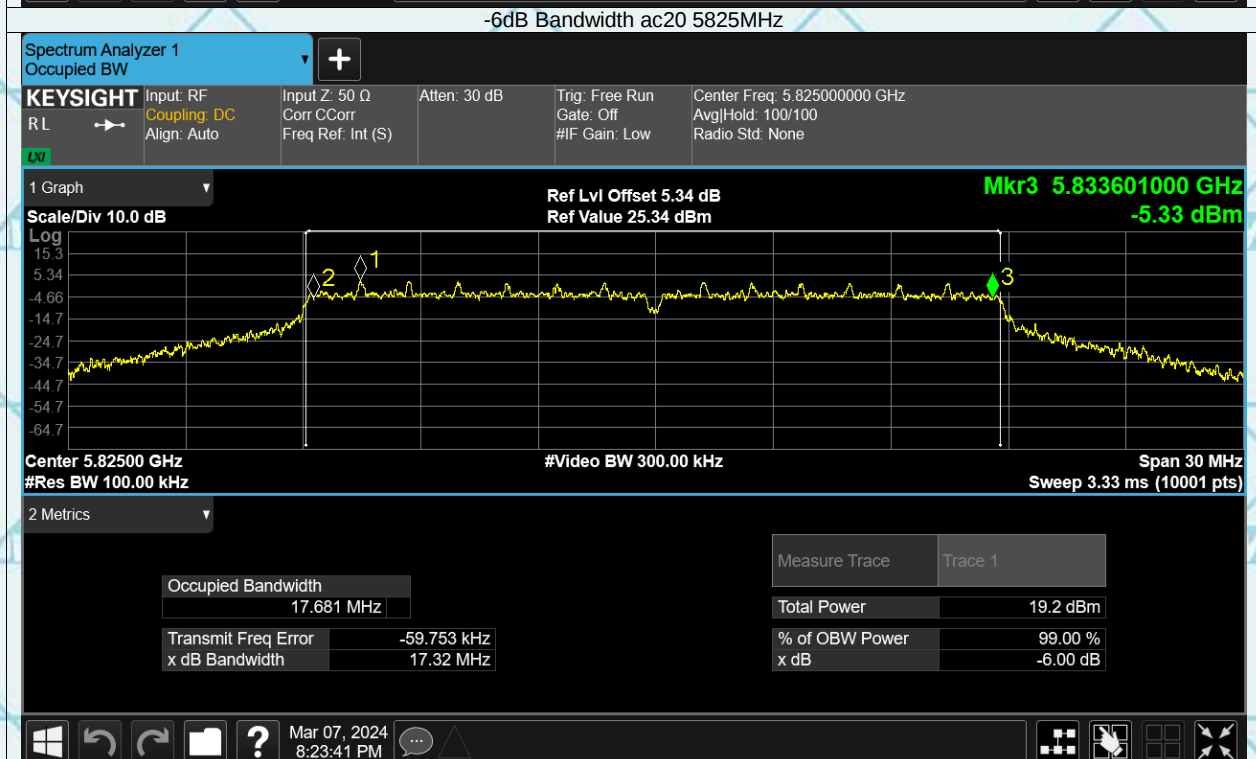
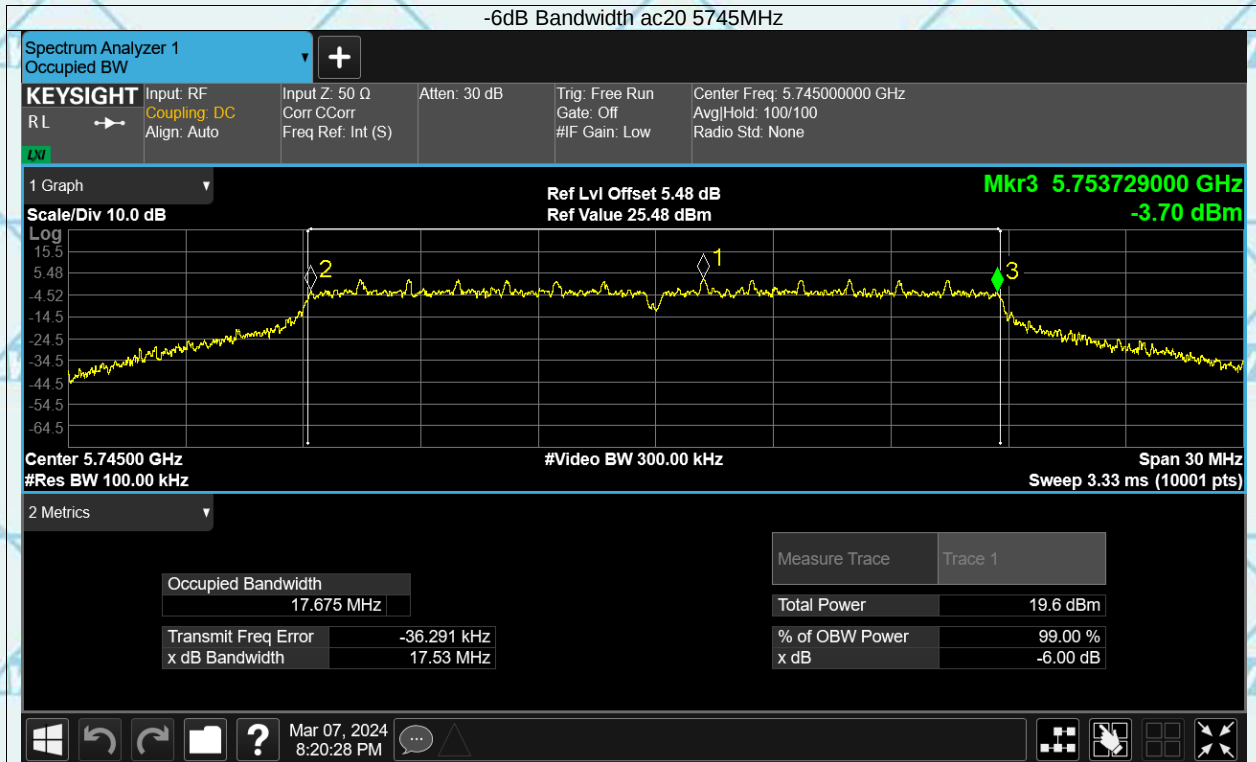
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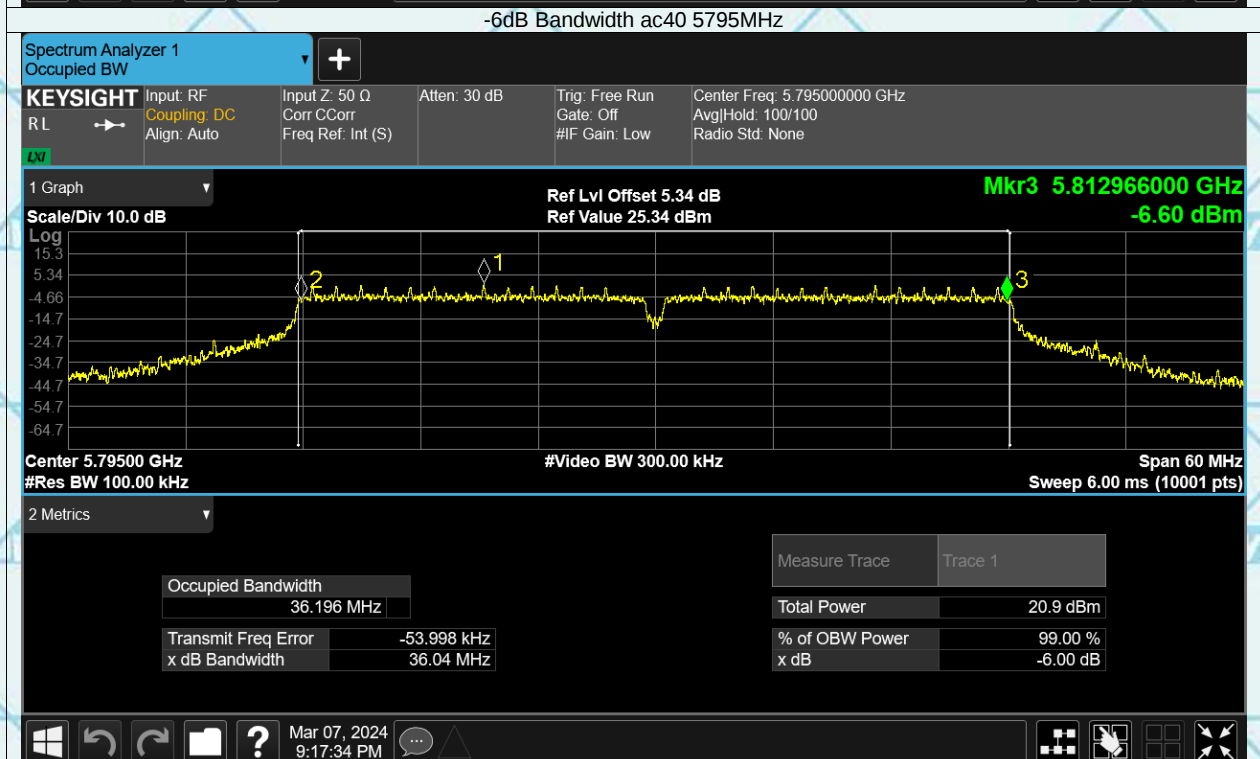
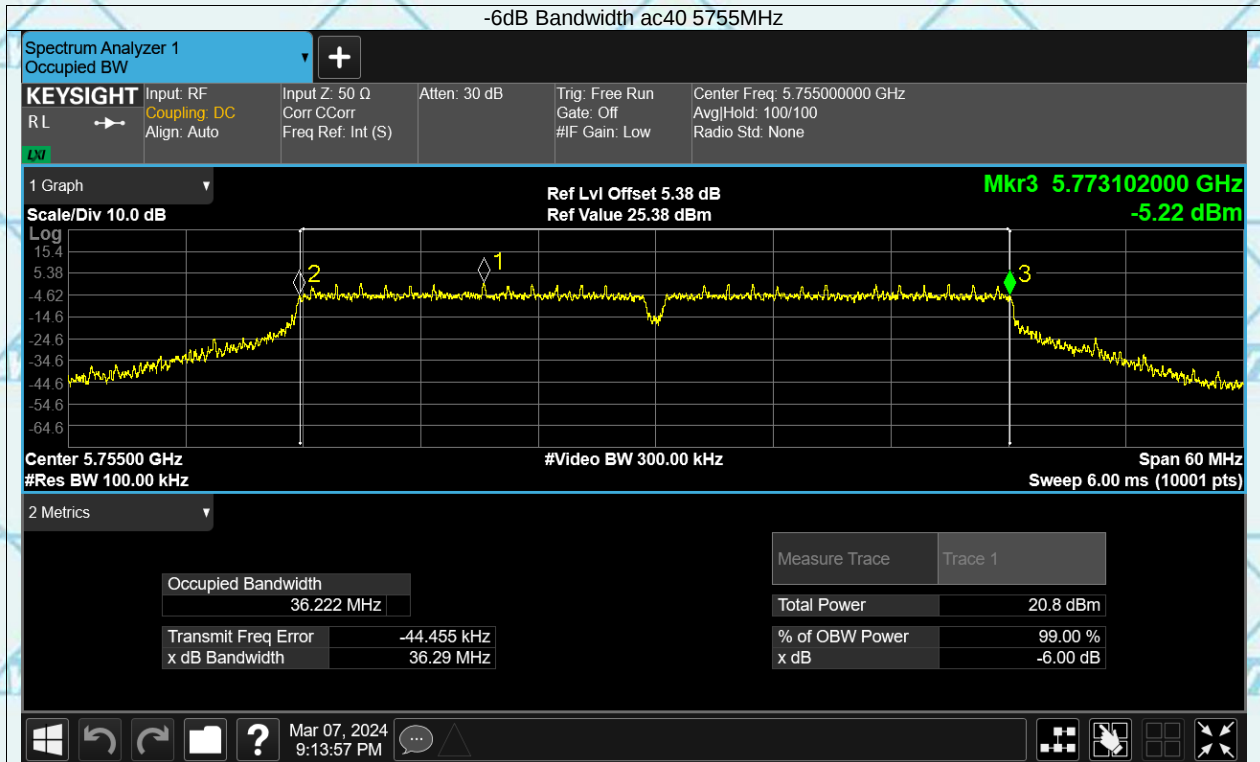
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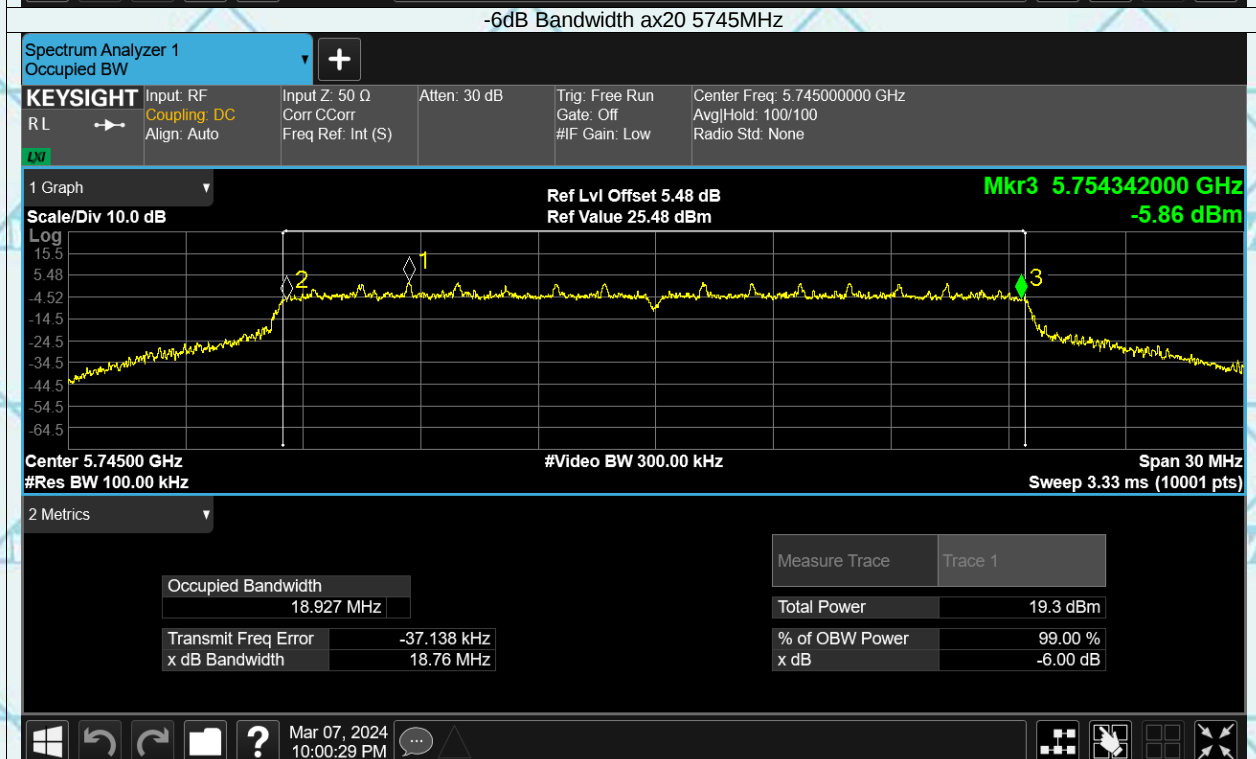
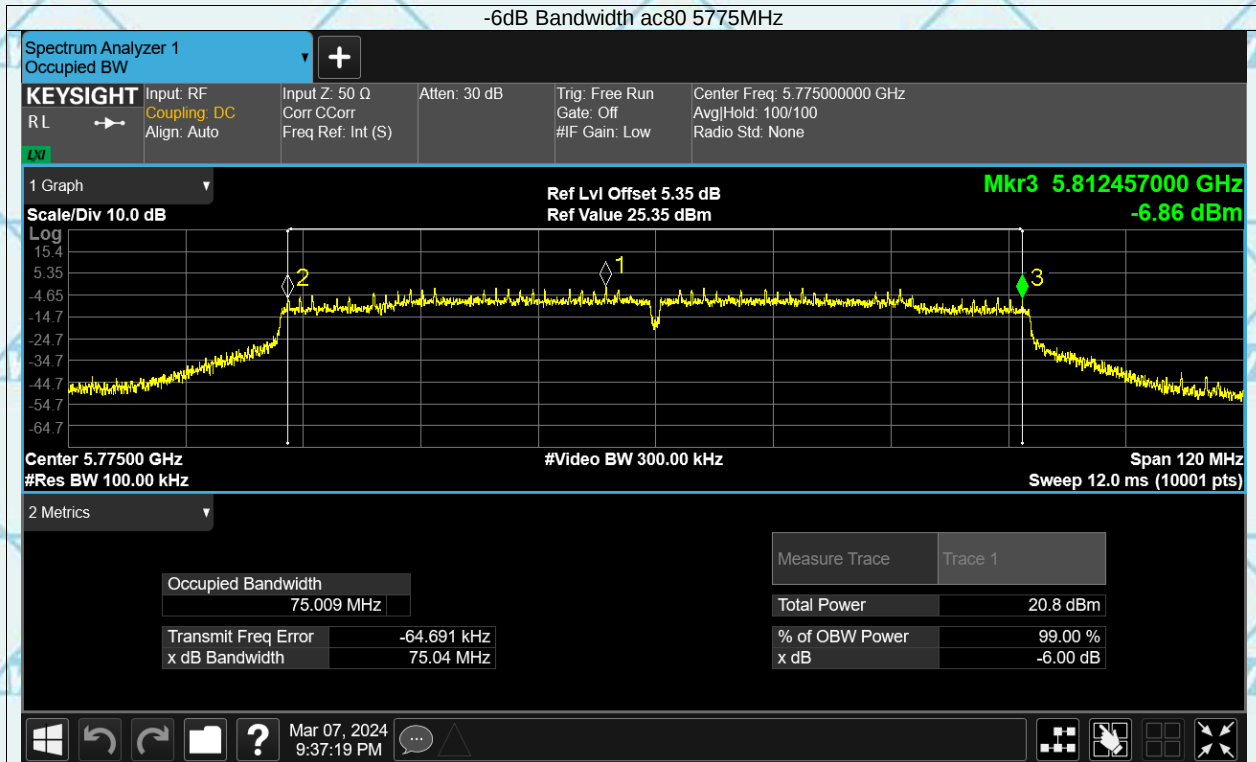
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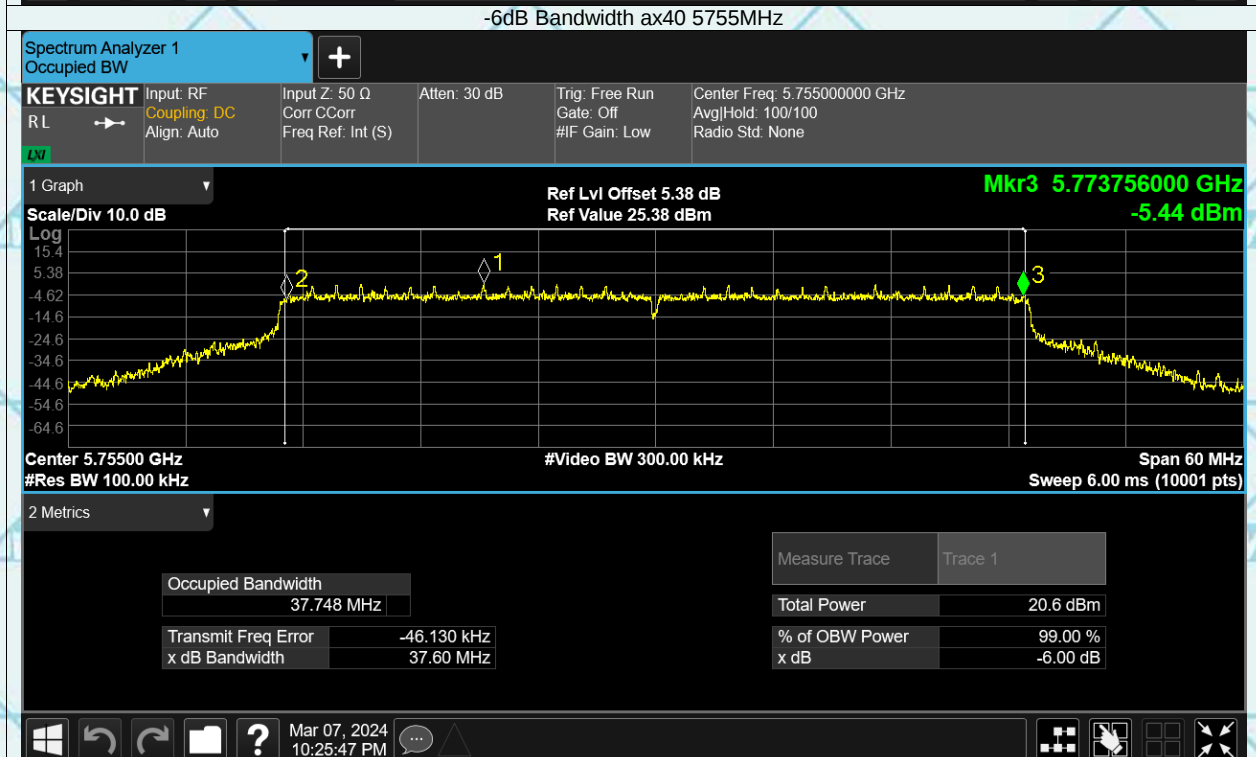
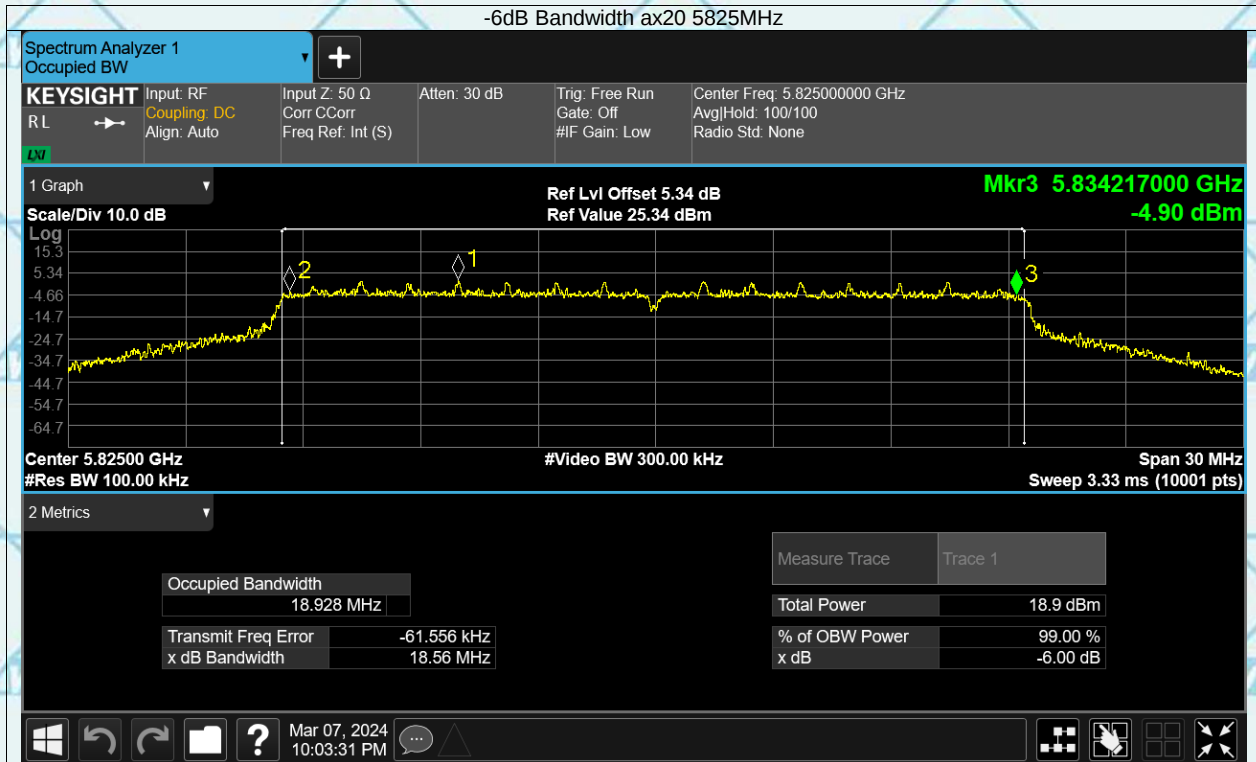
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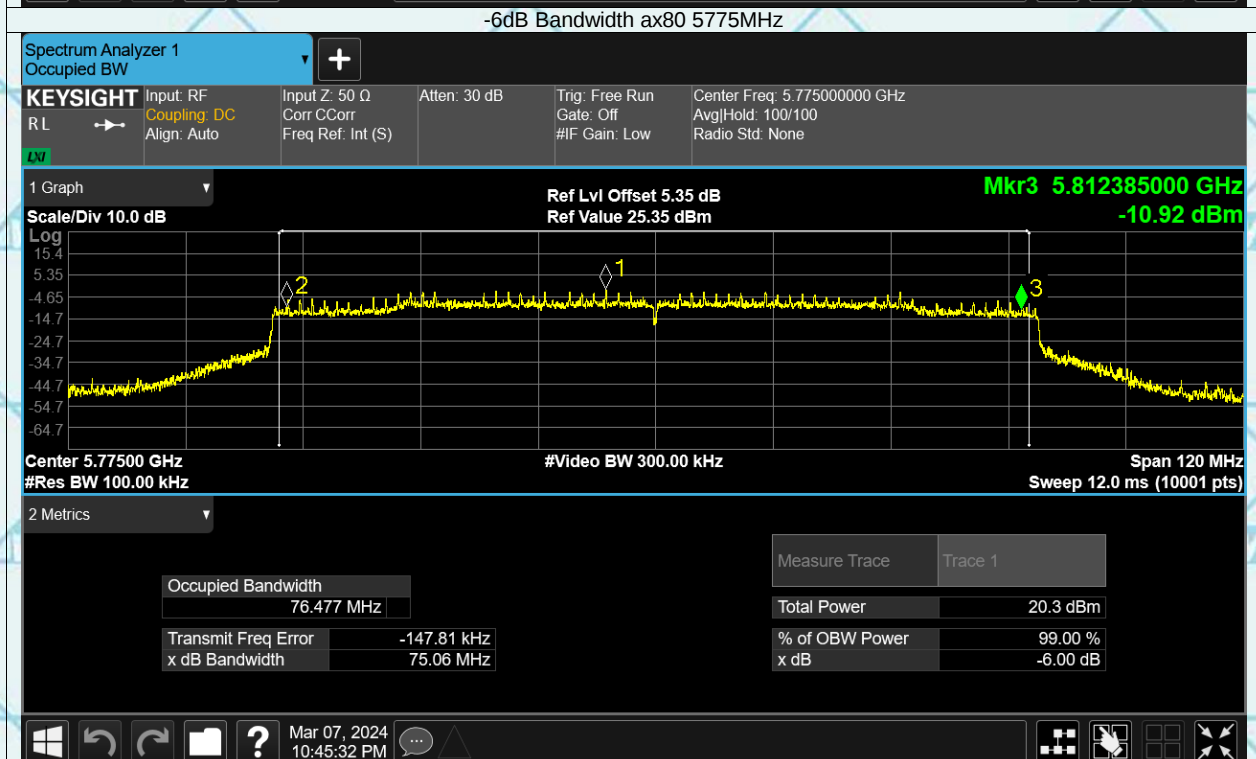
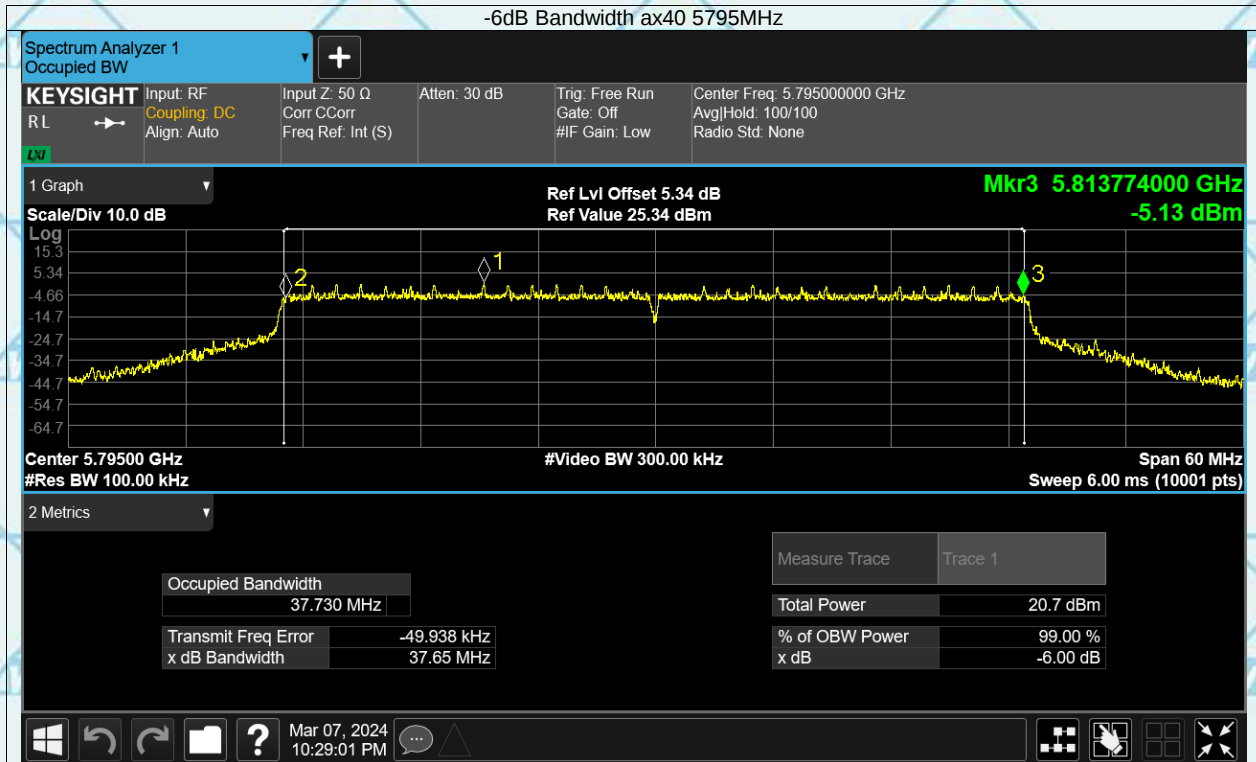
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7.5 MAXIMUM CONDUCTED OUTPUT POWER

- (i) If all antennas have the same gain, G_{ANT} :
Directional gain = $G_{ANT} + 10 \log(N_{ANT}/N_{SS})$ dBi, where N_{SS} = the number of independent spatial streams of data and G_{ANT} is the antenna gain in dBi. (This formula can also be applied when antennas have different gains if the highest antenna gain is substituted for G_{ANT} .)
- (ii) If antenna gains are not equal and each transmit antenna is driven by only one spatial stream, directional gain may be calculated by either of the following two formulas.

- Directional gain = $G_{ANT\ MAX} + 10 \log(N_{ANT}/N_{SS})$ dBi, where N_{SS} = the number of independent spatial streams of data and $G_{ANT\ MAX}$ is the gain of the antenna having the highest gain (in dBi).

Or,

$$\bullet \text{ Directional Gain} = 10 \cdot \log \left[\frac{\sum_{j=1}^{N_{SS}} \left\{ \sum_{k=1}^{N_{ANT}} g_{j,k} \right\}^2}{N_{ANT}} \right]$$

where

Each antenna is driven by no more than one spatial stream;

N_{SS} = the number of independent spatial streams of data;

N_{ANT} = the total number of antennas

$g_{j,k} = 10^{G_k/20}$ if the k th antenna is being fed by spatial stream j , or zero if it is not;
 G_k is the gain in dBi of the k th antenna.

For power measurements on IEEE 802.11 devices, 1,2

Array Gain = 0 dB (i.e., no array gain) for $N_{ANT} \leq 4$;

Array Gain = 0 dB (i.e., no array gain) for channel widths ≥ 40 MHz for any N_{ANT} ;

Array Gain = $5 \log(N_{ANT}/N_{SS})$ dB or 3 dB, whichever is less, for 20-MHz channel widths with $N_{ANT} \geq 5$.

Note: $N_{ANT}=2$, satisfy the condition $N_{ANT} \leq 4$, so Array gain=0dB, Directional gain= G_{ANT} +Array gain=2.91dBi+0dB=2.91dBi, not more than 6, so the power limit is unchanged.



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Product	: EUT-Sample	Test Mode	: See Section 3.4
Test Item	: output power	Temperature	: 25 °C
Test Voltage	: DC 11.61V	Humidity	: 56%RH
Test Result	: PASS		

MAIN

Mode	Frequency (MHz)	Duty Factor (dB)	Total Power (dBm)	Limit (dBm)	Verdict
a	5180	0	14.58	24	Pass
a	5240	0	14.26	24	Pass
a	5260	0	14.12	24	Pass
a	5320	0	14.25	24	Pass
a	5500	0	13.36	24	Pass
a	5700	0	13.2	24	Pass
a	5745	0	12.89	30	Pass
a	5825	0	12.27	30	Pass
n20	5180	0	14.67	24	Pass
n20	5240	0	14.23	24	Pass
n20	5260	0	14.15	24	Pass
n20	5320	0	14.27	24	Pass
n20	5500	0	13.34	24	Pass
n20	5700	0	13.18	24	Pass
n20	5745	0	12.97	30	Pass
n20	5825	0	12.38	30	Pass
n40	5190	0	15.5	24	Pass
n40	5230	0	16.06	24	Pass
n40	5270	0	15.71	24	Pass
n40	5310	0	14.89	24	Pass
n40	5510	0	13.49	24	Pass
n40	5670	0	14.65	24	Pass
n40	5755	0	14.21	30	Pass
n40	5795	0	14.26	30	Pass
ac20	5180	0	14.67	24	Pass
ac20	5240	0	14.25	24	Pass
ac20	5260	0	14.1	24	Pass
ac20	5320	0	14.26	24	Pass
ac20	5500	0	13.3	24	Pass
ac20	5700	0	13.18	24	Pass
ac20	5745	0	12.99	30	Pass
ac20	5825	0	12.33	30	Pass
ac40	5190	0	15.45	24	Pass
ac40	5230	0	15.9	24	Pass
ac40	5270	0	15.62	24	Pass
ac40	5310	0	14.8	24	Pass
ac40	5510	0	13.35	24	Pass
ac40	5670	0	14.54	24	Pass
ac40	5755	0	14.12	30	Pass
ac40	5795	0	14.17	30	Pass
ac80	5210	0	15.71	24	Pass
ac80	5290	0	13.96	24	Pass
ac80	5530	0	13.85	24	Pass
ac80	5610	0	15.21	24	Pass
ac80	5775	0	13.72	30	Pass
ax160	5250	0	10.36	24	Pass
ax160	5570	0	11.36	24	Pass
ax20	5180	0	14.54	24	Pass
ax20	5240	0	14.16	24	Pass
ax20	5260	0	14.04	24	Pass
ax20	5320	0	14.21	24	Pass
ax20	5500	0	13.33	24	Pass
ax20	5700	0	13.05	24	Pass
ax20	5745	0	12.82	30	Pass
ax20	5825	0	12.17	30	Pass
ax40	5190	0	15.1	24	Pass
ax40	5230	0	15.6	24	Pass
ax40	5270	0	15.35	24	Pass
ax40	5310	0	14.47	24	Pass



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ax40	5510	0	13.13	24	Pass
ax40	5670	0	14.26	24	Pass
ax40	5755	0	13.85	30	Pass
ax40	5795	0	13.87	30	Pass
ax80	5210	0	15.39	24	Pass
ax80	5290	0	13.63	24	Pass
ax80	5530	0	13.47	24	Pass
ax80	5610	0	14.96	24	Pass
ax80	5775	0	13.44	30	Pass

AUX Ant2

Mode	Frequency (MHz)	Duty Factor (dB)	Total Power (dBm)	Limit (dBm)	Verdict
a	5180	0	12.33	24	Pass
a	5240	0	11.82	24	Pass
a	5260	0	11.65	24	Pass
a	5320	0	12.06	24	Pass
a	5500	0	11.66	24	Pass
a	5700	0	12.53	24	Pass
a	5745	0	11.91	30	Pass
a	5825	0	11.67	30	Pass
n20	5180	0	12.69	24	Pass
n20	5240	0	12.14	24	Pass
n20	5260	0	11.96	24	Pass
n20	5320	0	12.35	24	Pass
n20	5500	0	12	24	Pass
n20	5700	0	12.85	24	Pass
n20	5745	0	12.23	30	Pass
n20	5825	0	12.11	30	Pass
n40	5190	0	14.05	24	Pass
n40	5230	0	13.7	24	Pass
n40	5270	0	13.51	24	Pass
n40	5310	0	13.63	24	Pass
n40	5510	0	13.06	24	Pass
n40	5670	0	14.35	24	Pass
n40	5755	0	13.64	30	Pass
n40	5795	0	13.77	30	Pass
ac20	5180	0	12.67	24	Pass
ac20	5240	0	12.08	24	Pass
ac20	5260	0	11.92	24	Pass
ac20	5320	0	12.27	24	Pass
ac20	5500	0	11.91	24	Pass
ac20	5700	0	12.8	24	Pass
ac20	5745	0	12.17	30	Pass
ac20	5825	0	12.01	30	Pass
ac40	5190	0	13.91	24	Pass
ac40	5230	0	13.58	24	Pass
ac40	5270	0	13.43	24	Pass
ac40	5310	0	13.48	24	Pass
ac40	5510	0	12.95	24	Pass
ac40	5670	0	14.23	24	Pass
ac40	5755	0	13.55	30	Pass
ac40	5795	0	13.56	30	Pass
ac80	5210	0	13.88	24	Pass
ac80	5290	0	13.1	24	Pass
ac80	5530	0	13.85	24	Pass
ac80	5610	0	14.55	24	Pass
ac80	5775	0	14.4	30	Pass
ax160	5250	0	10.74	24	Pass
ax160	5570	0	12.39	24	Pass
ax20	5180	0	12.57	24	Pass
ax20	5240	0	12.05	24	Pass
ax20	5260	0	11.9	24	Pass
ax20	5320	0	12.21	24	Pass
ax20	5500	0	11.81	24	Pass
ax20	5700	0	12.73	24	Pass
ax20	5745	0	12.15	30	Pass
ax20	5825	0	12.02	30	Pass
ax40	5190	0	13.67	24	Pass
ax40	5230	0	13	24	Pass
ax40	5270	0	12.82	24	Pass
ax40	5310	0	12.92	24	Pass
ax40	5510	0	12.33	24	Pass



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ax40	5670	0	13.64	24	Pass
ax40	5755	0	13	30	Pass
ax40	5795	0	12.96	30	Pass
ax80	5210	0	13.28	24	Pass
ax80	5290	0	12.49	24	Pass
ax80	5530	0	13.28	24	Pass
ax80	5610	0	13.99	24	Pass
ax80	5775	0	13.69	30	Pass

MIMO Mode

Mode	Frequency (MHz)	Total Power (dBm)	Limit (dBm)	Verdict
n20	5180	16.80	24	Pass
n20	5240	16.32	24	Pass
n20	5260	16.20	24	Pass
n20	5320	16.43	24	Pass
n20	5500	15.73	24	Pass
n20	5700	16.03	24	Pass
n20	5745	15.63	30	Pass
n20	5825	15.26	30	Pass
n40	5190	17.85	24	Pass
n40	5230	18.05	24	Pass
n40	5270	17.76	24	Pass
n40	5310	17.32	24	Pass
n40	5510	16.29	24	Pass
n40	5670	17.51	24	Pass
n40	5755	16.94	30	Pass
n40	5795	17.03	30	Pass
ac20	5180	16.79	24	Pass
ac20	5240	16.31	24	Pass
ac20	5260	16.16	24	Pass
ac20	5320	16.39	24	Pass
ac20	5500	15.67	24	Pass
ac20	5700	16	24	Pass
ac20	5745	15.61	30	Pass
ac20	5825	15.18	30	Pass
ac40	5190	17.76	24	Pass
ac40	5230	17.9	24	Pass
ac40	5270	17.67	24	Pass
ac40	5310	17.2	24	Pass
ac40	5510	16.16	24	Pass
ac40	5670	17.4	24	Pass
ac40	5755	16.85	30	Pass
ac40	5795	16.89	30	Pass
ac80	5210	17.9	24	Pass
ac80	5290	16.56	24	Pass
ac80	5530	16.86	24	Pass
ac80	5610	17.90	24	Pass
ac80	5775	17.08	24	Pass
ax160	5250	13.56	24	Pass
ax160	5570	14.92	30	Pass
ax20	5180	16.68	30	Pass
ax20	5240	16.24	24	Pass
ax20	5260	16.11	24	Pass
ax20	5320	16.33	24	Pass
ax20	5500	15.65	24	Pass
ax20	5700	15.90	30	Pass
ax20	5745	15.51	24	Pass
ax20	5825	15.11	24	Pass
ax40	5190	17.45	24	Pass
ax40	5230	17.50	24	Pass
ax40	5270	17.28	24	Pass
ax40	5310	16.77	24	Pass
ax40	5510	15.76	24	Pass
ax40	5670	16.97	24	Pass
ax40	5755	16.46	30	Pass
ax40	5795	16.45	30	Pass
ax80	5210	17.47	24	Pass
ax80	5290	16.11	24	Pass
ax80	5530	16.39	24	Pass
ax80	5610	17.51	24	Pass
ax80	5775	16.58	24	Pass



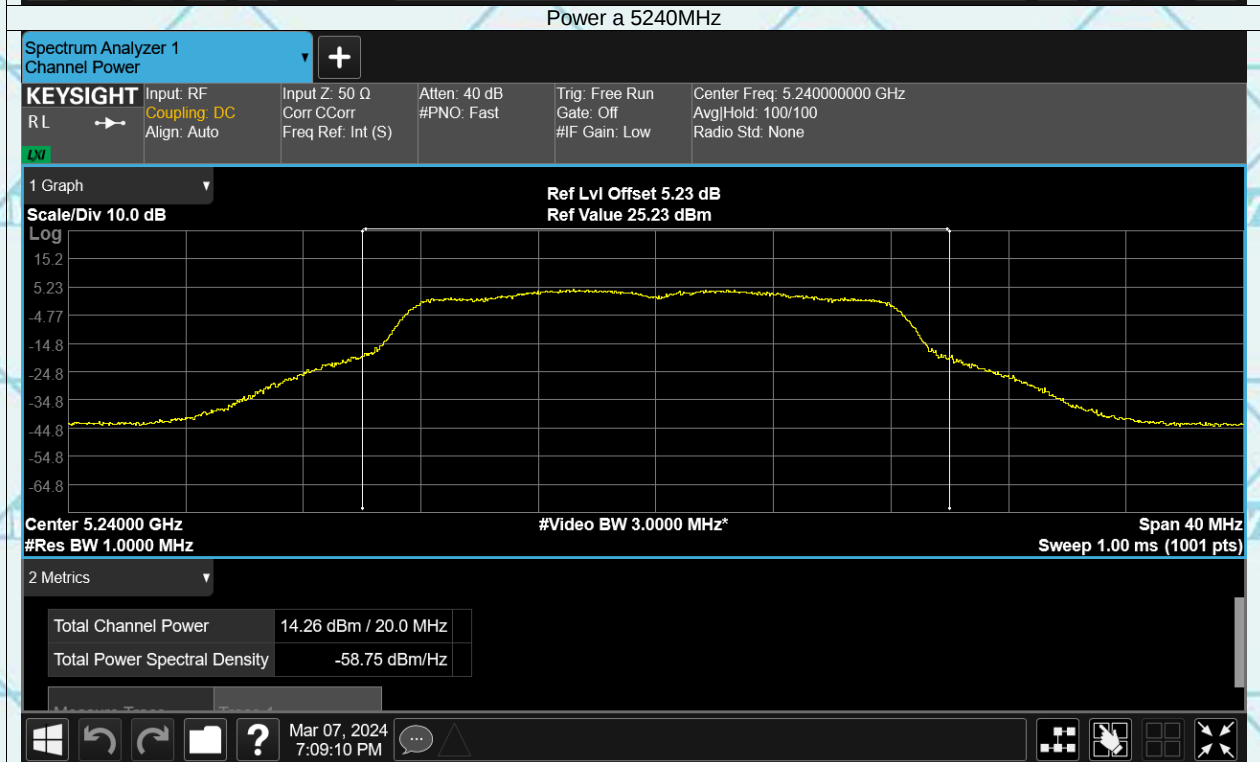


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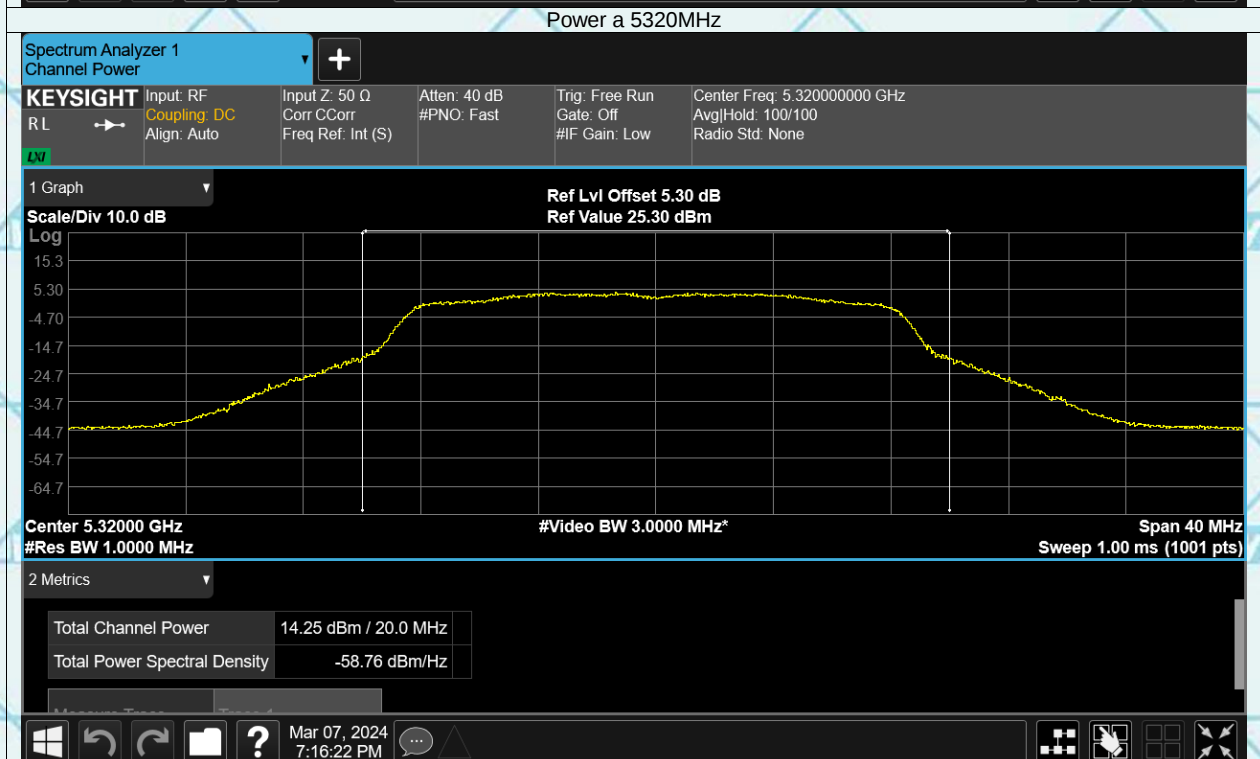
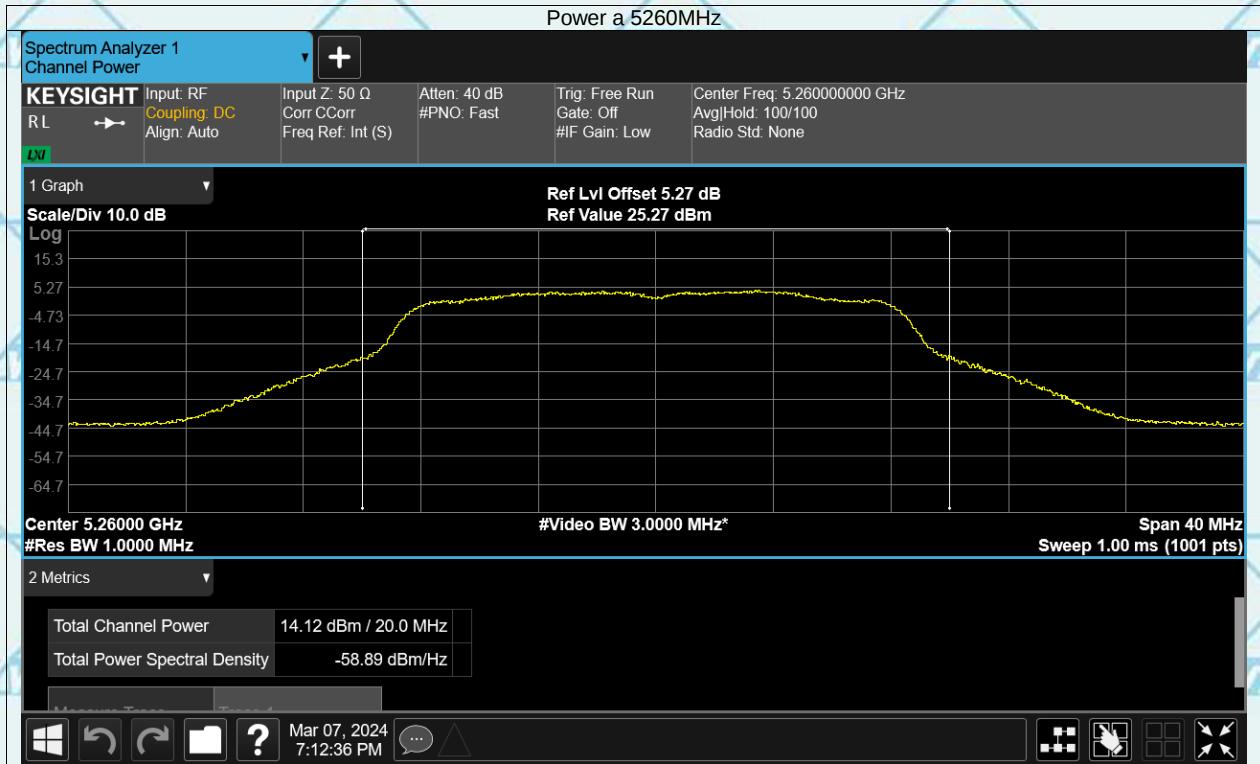
MAIN





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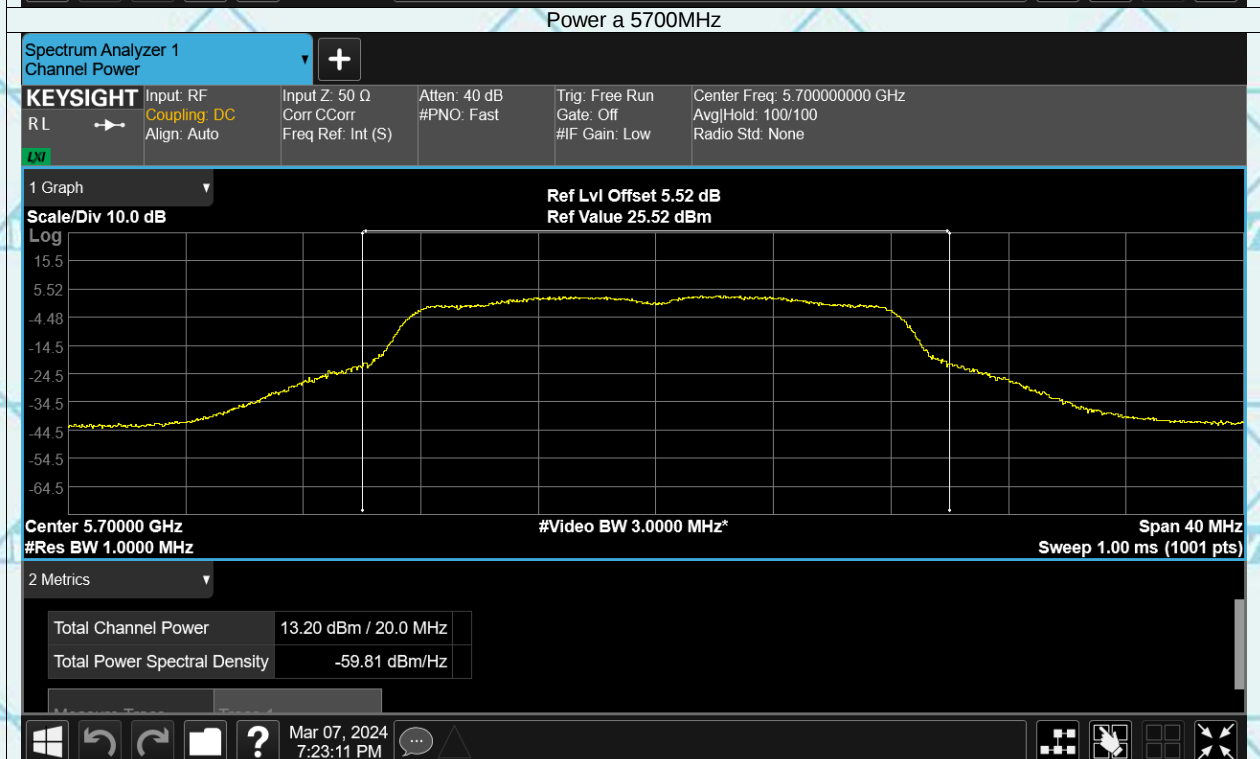
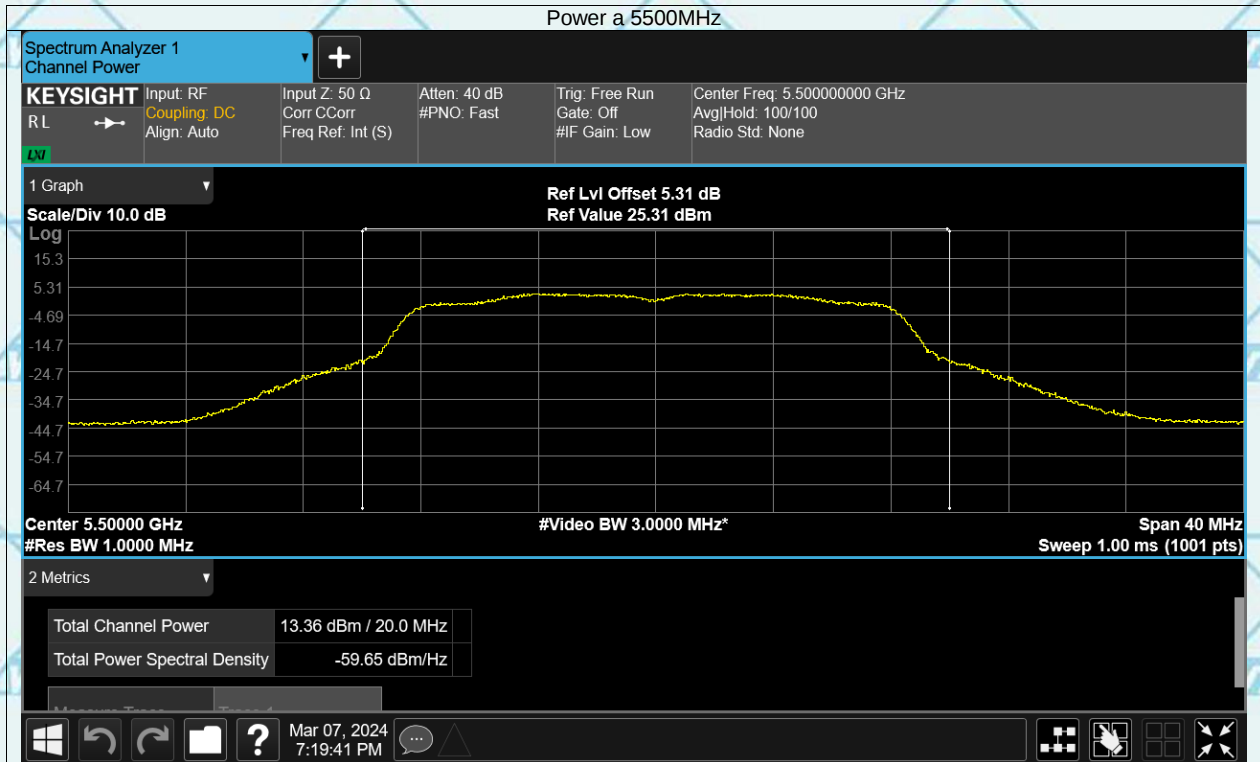
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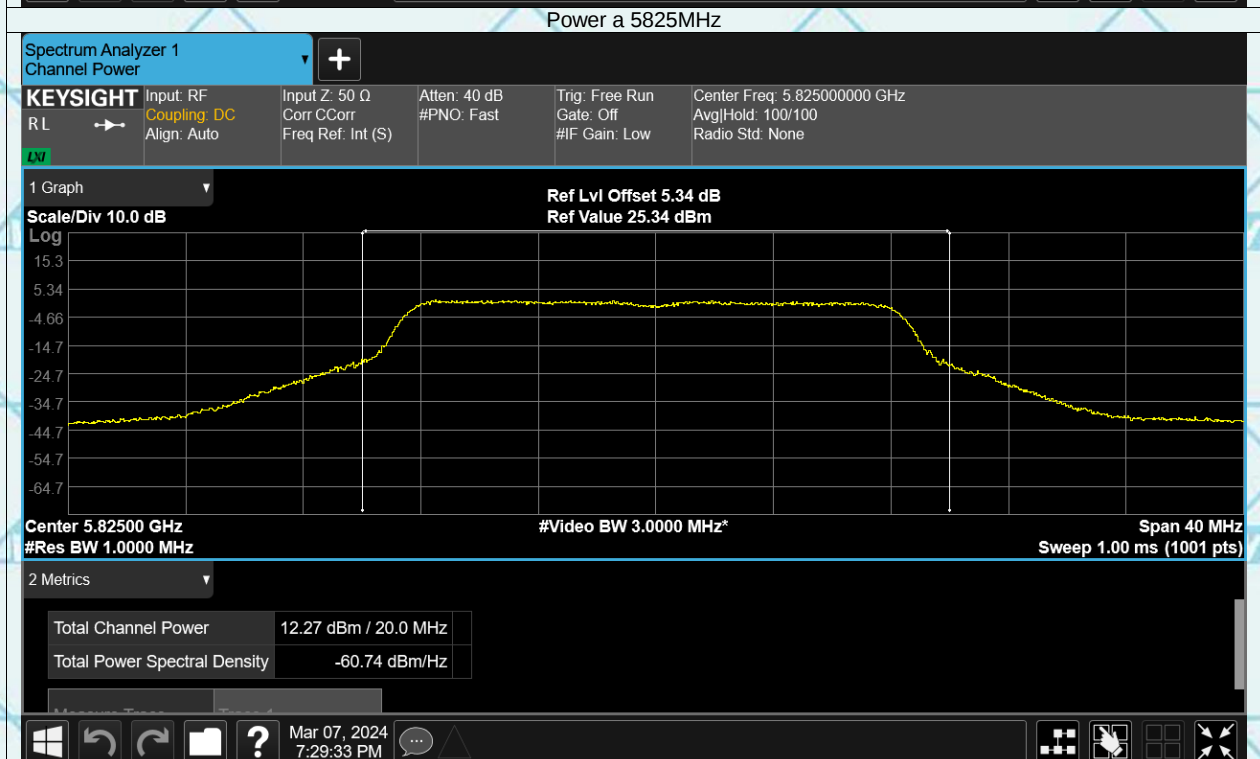
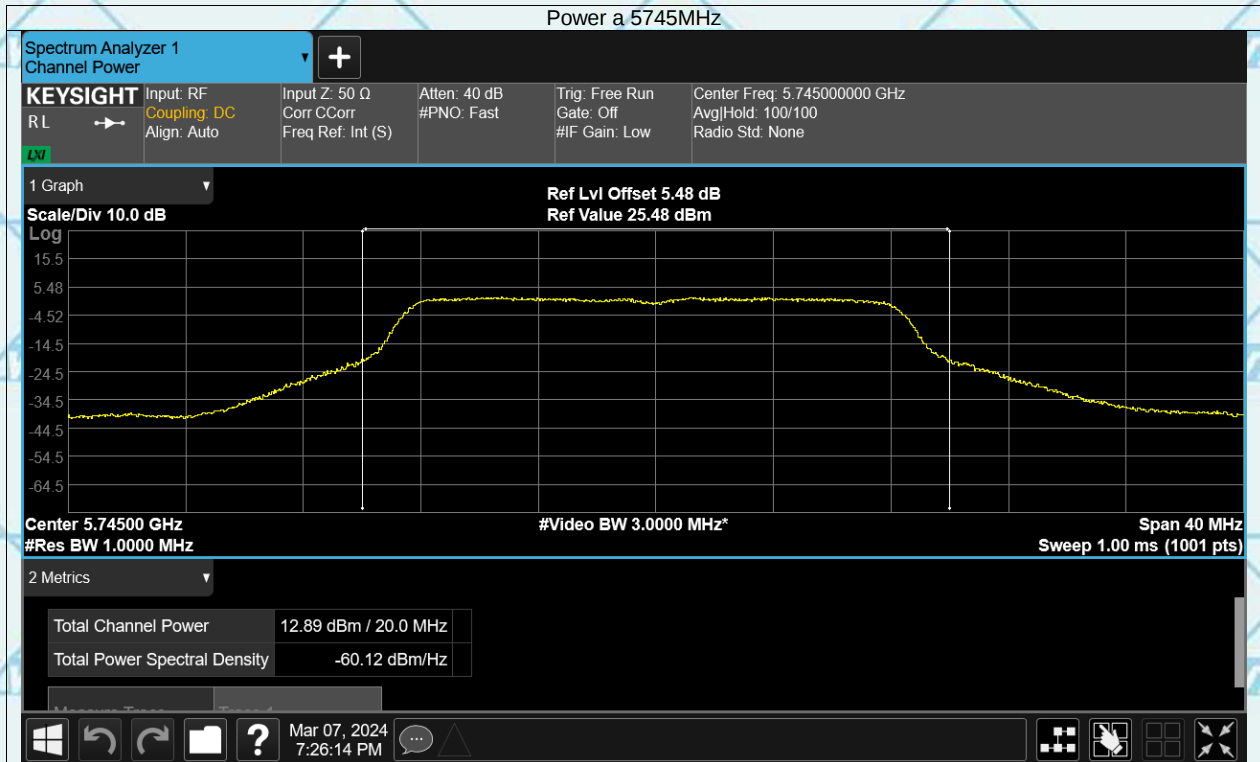
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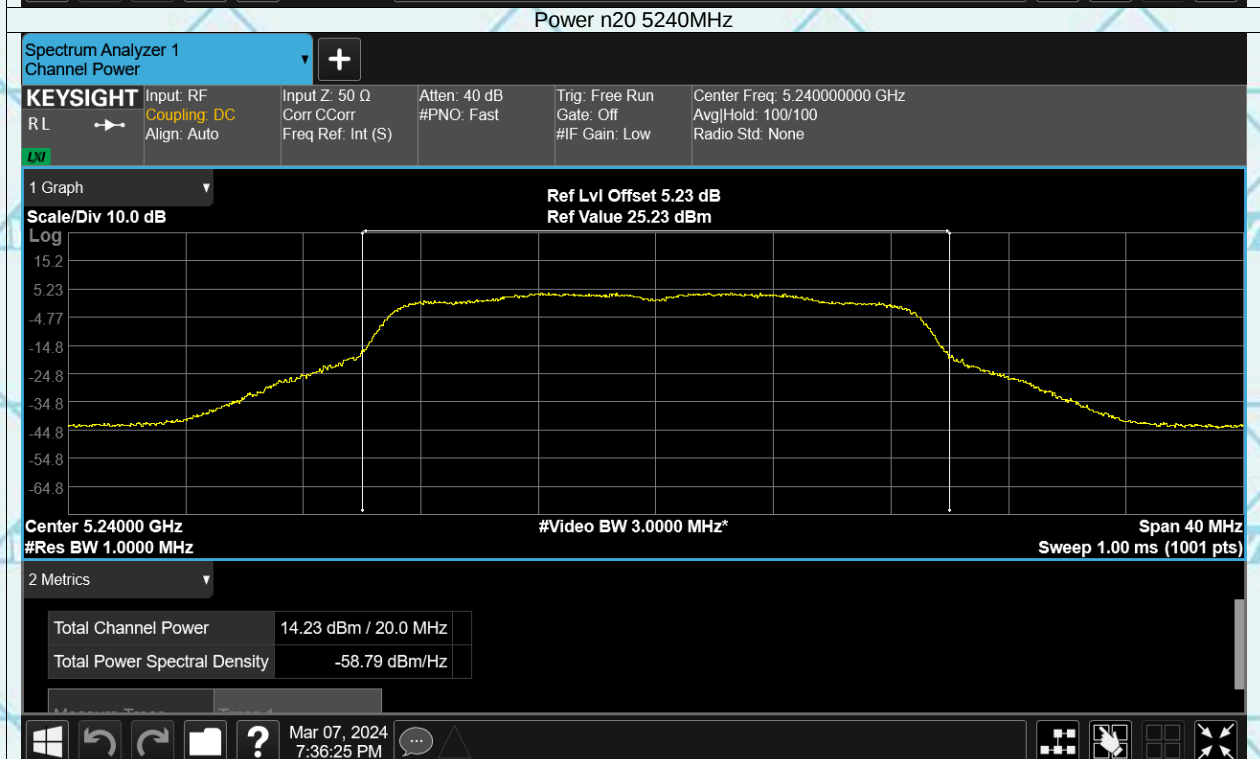
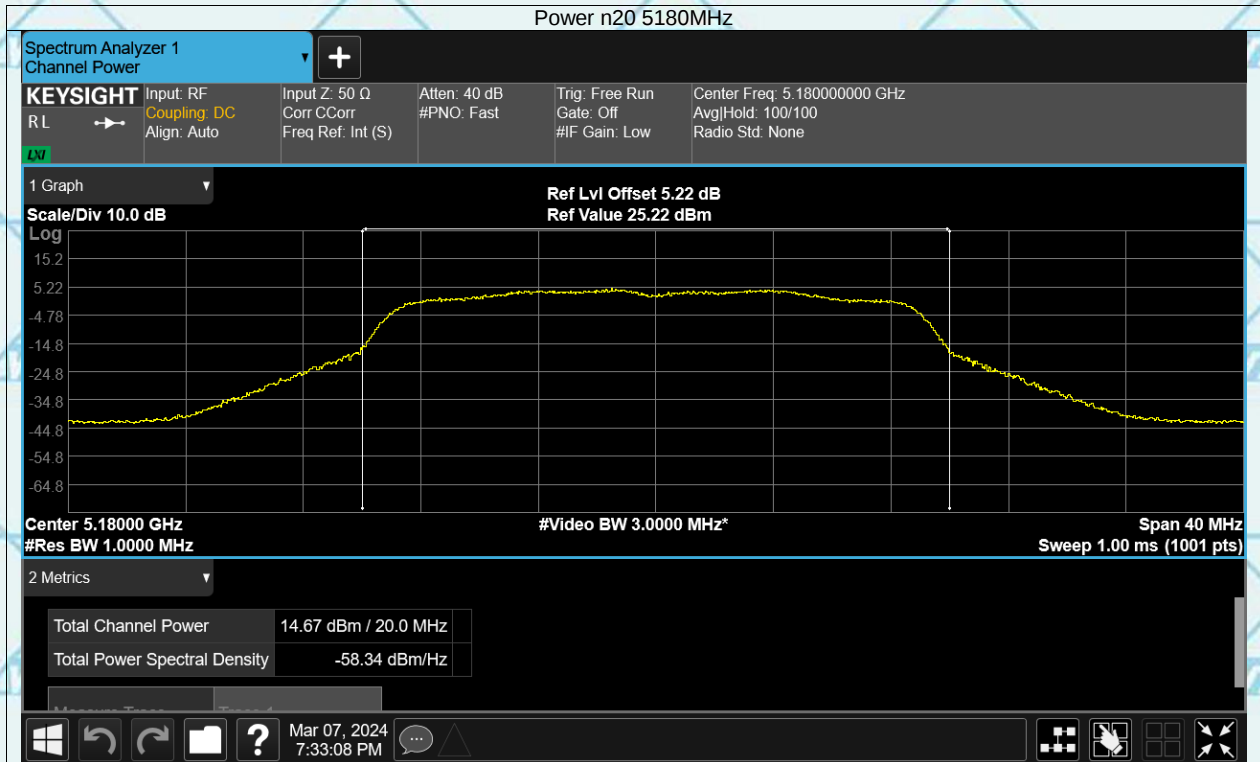
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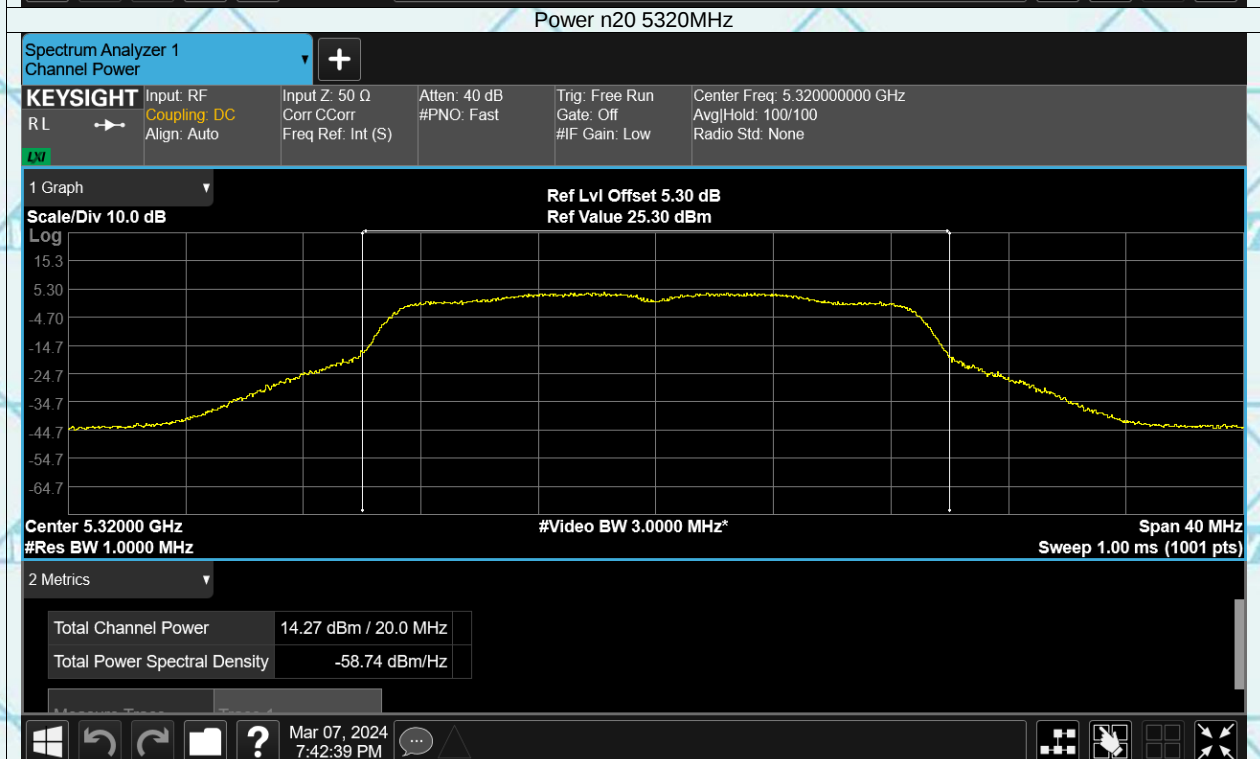
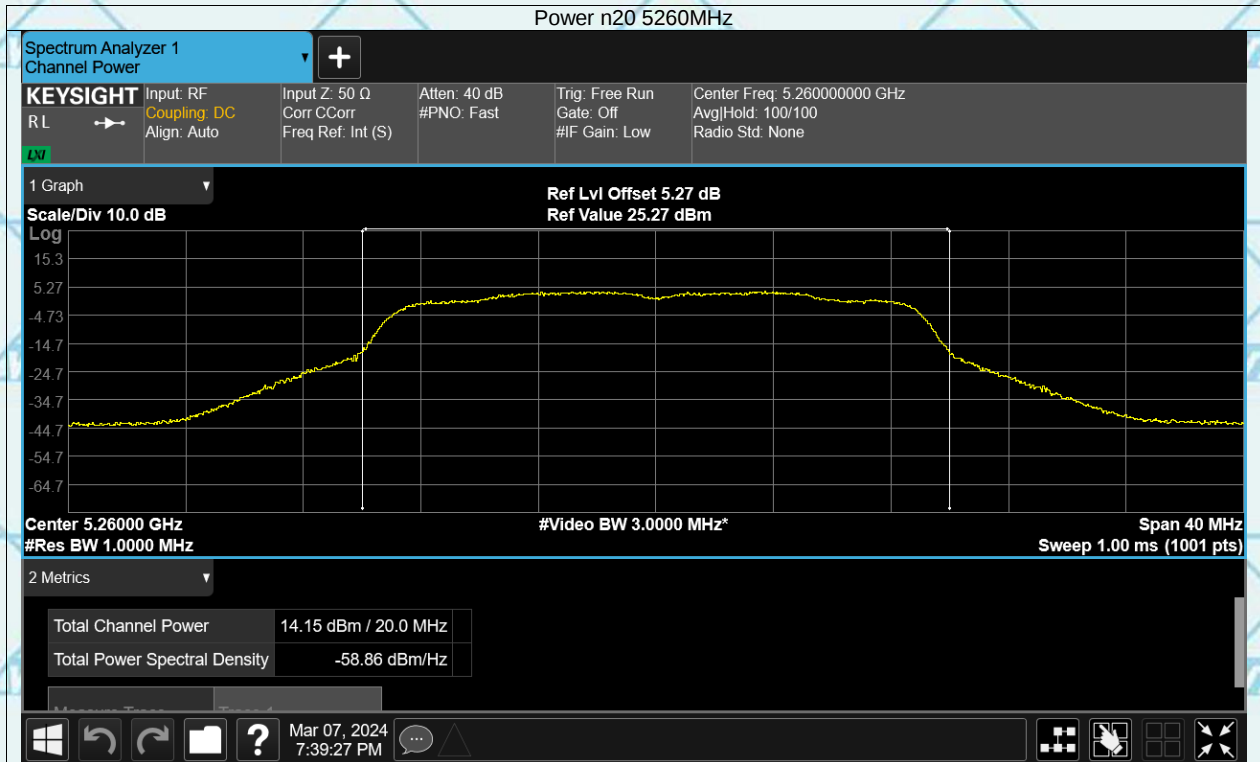
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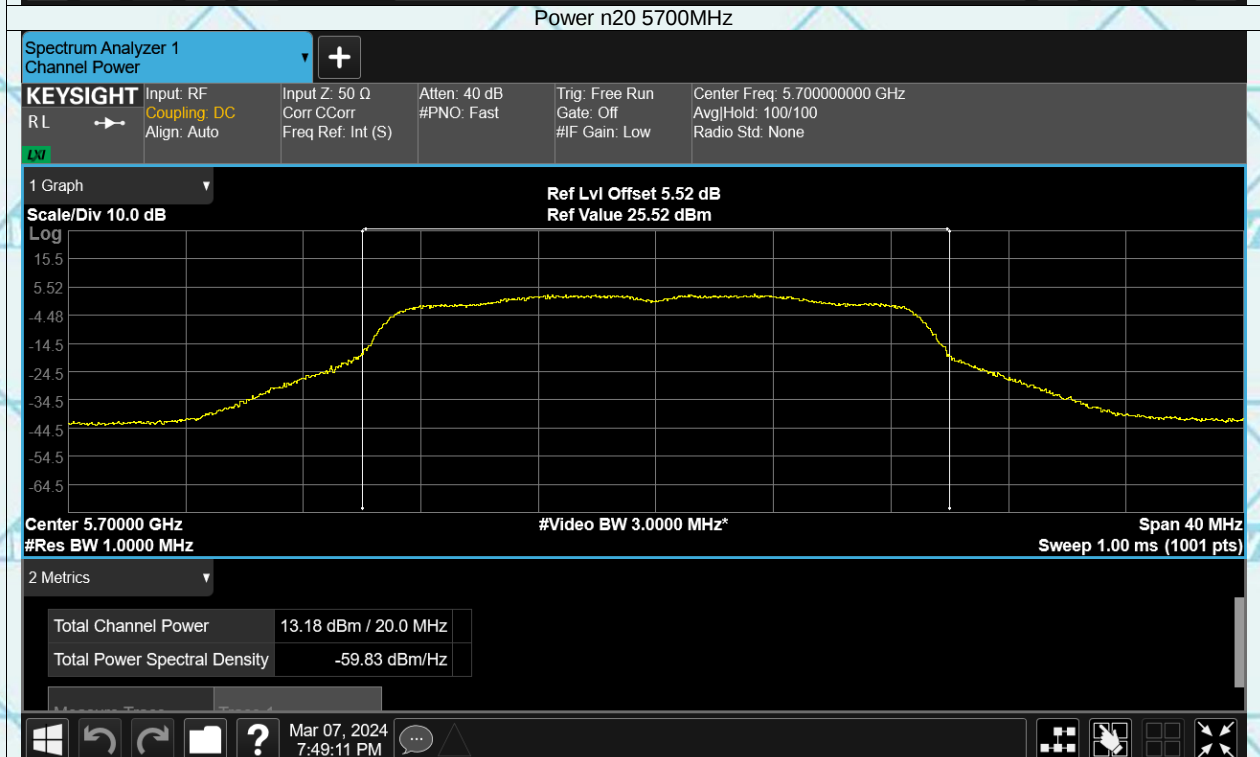
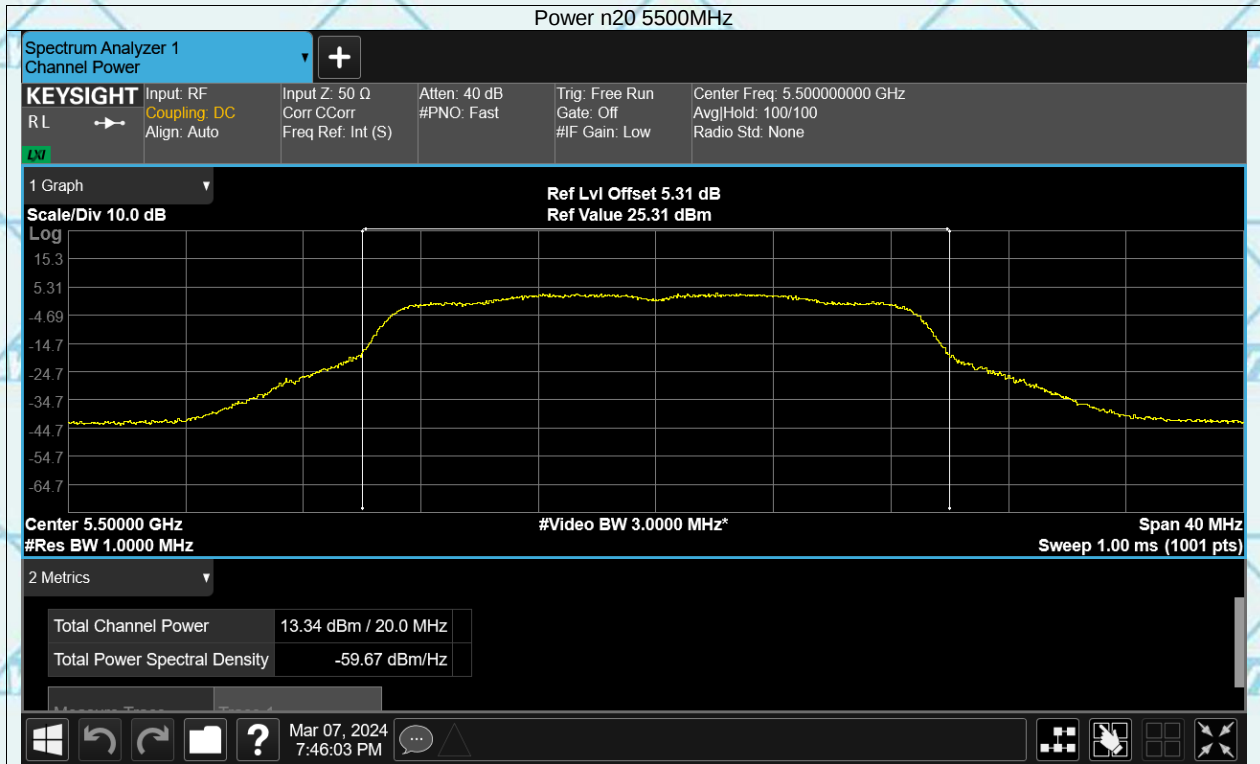
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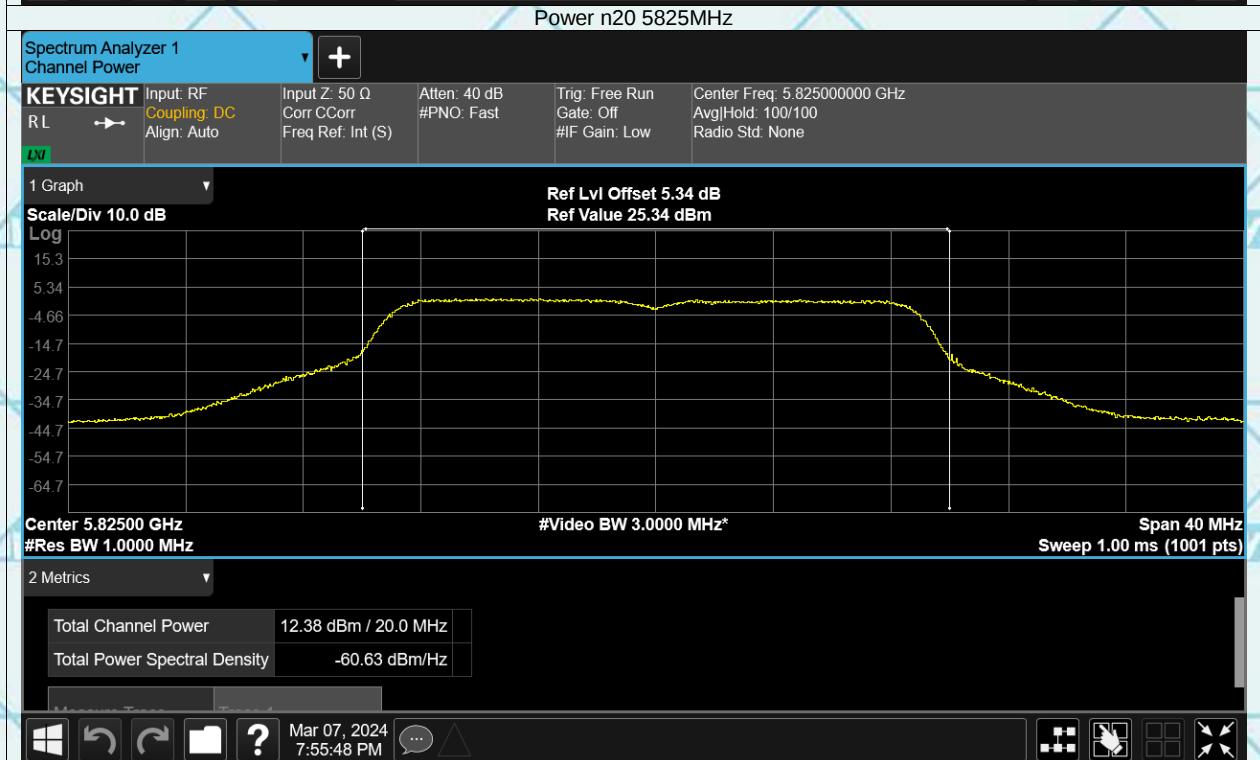
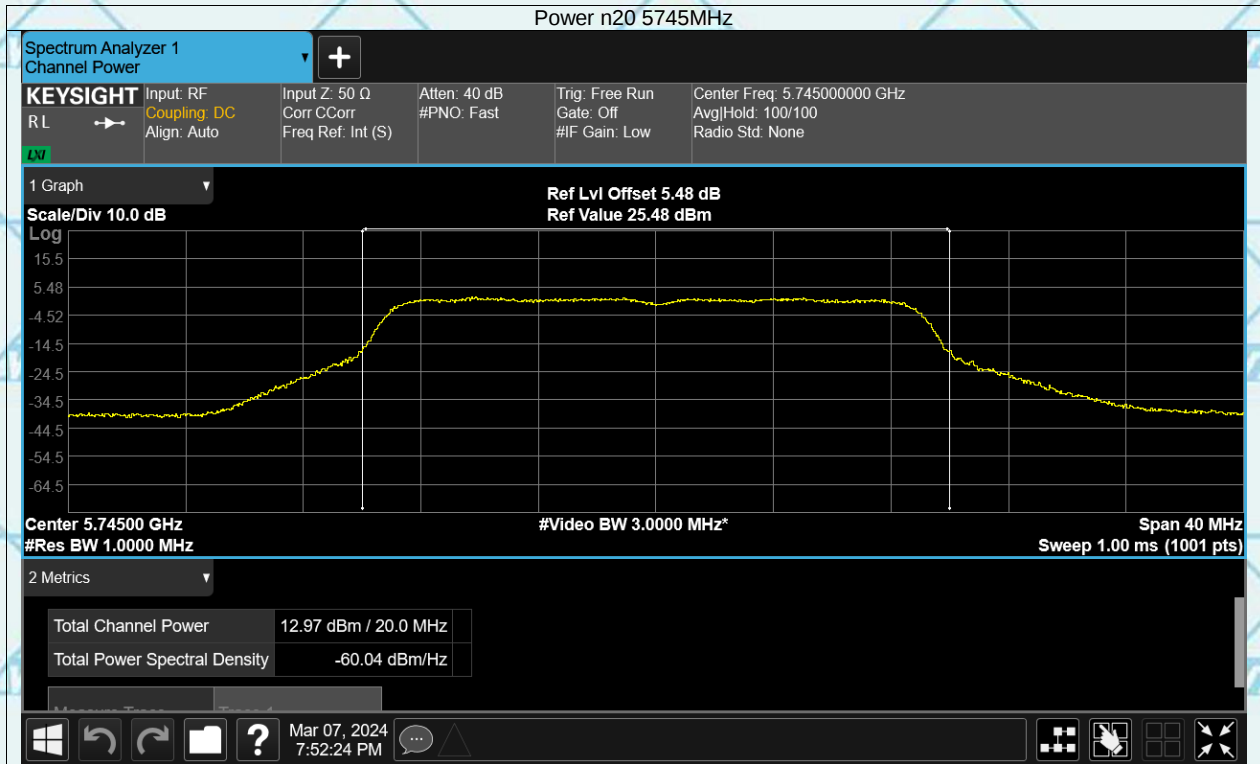




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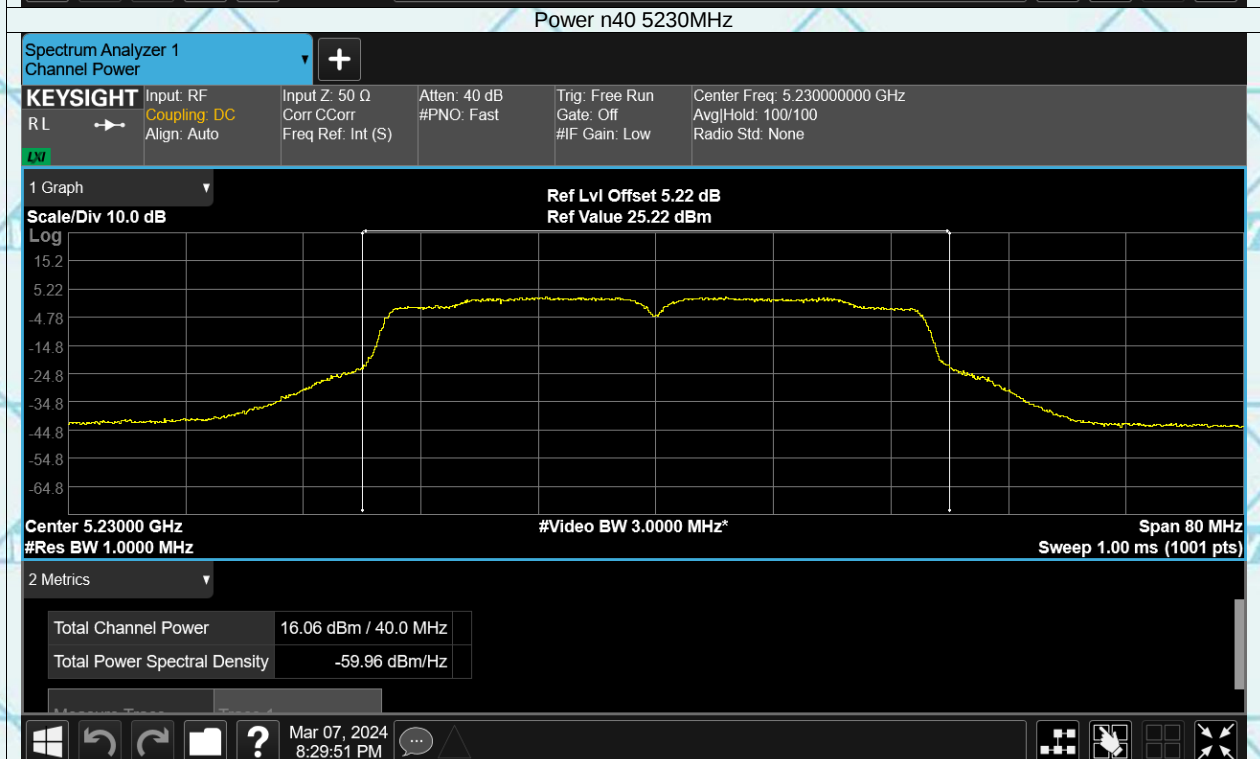
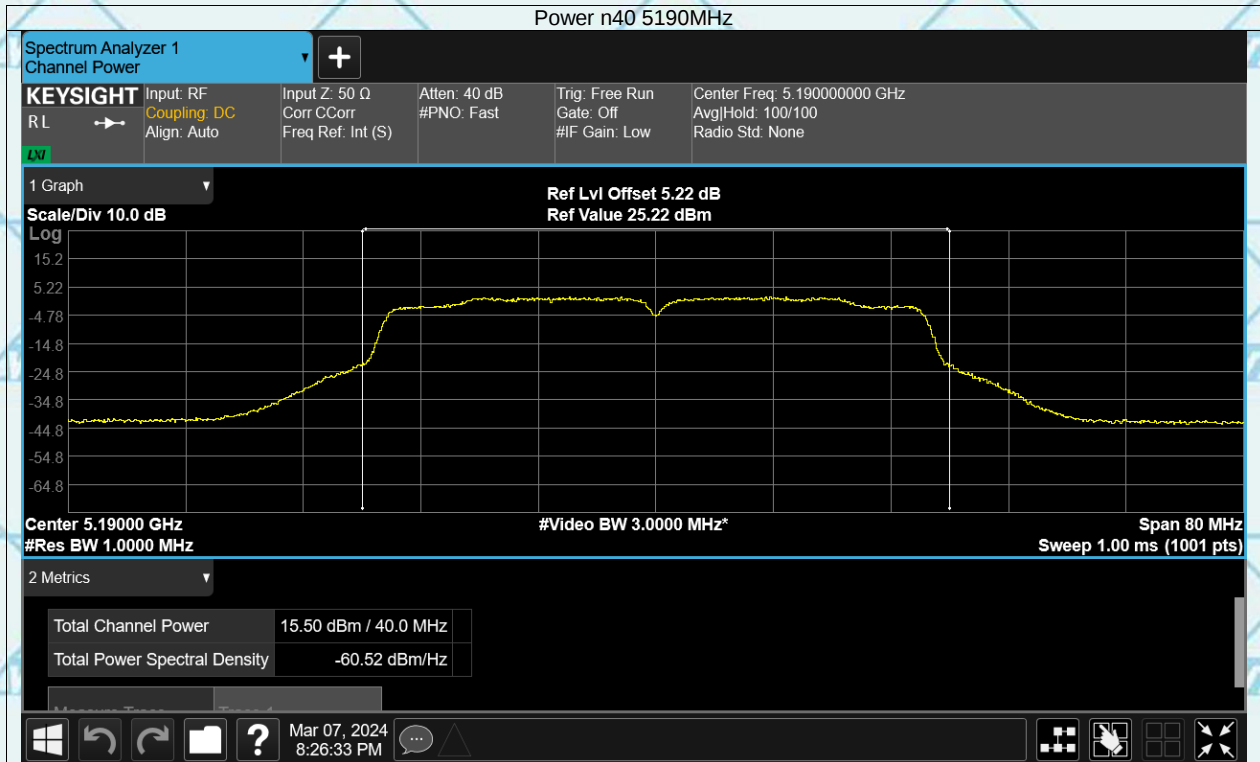




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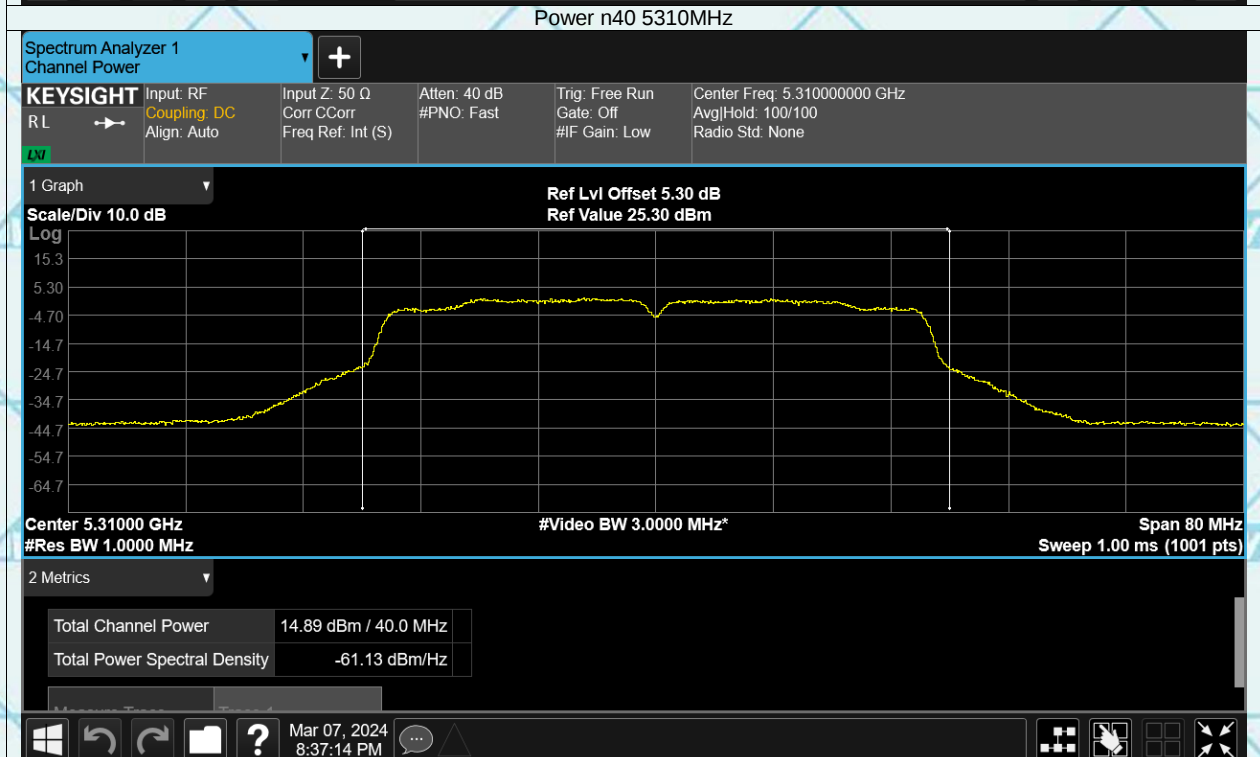
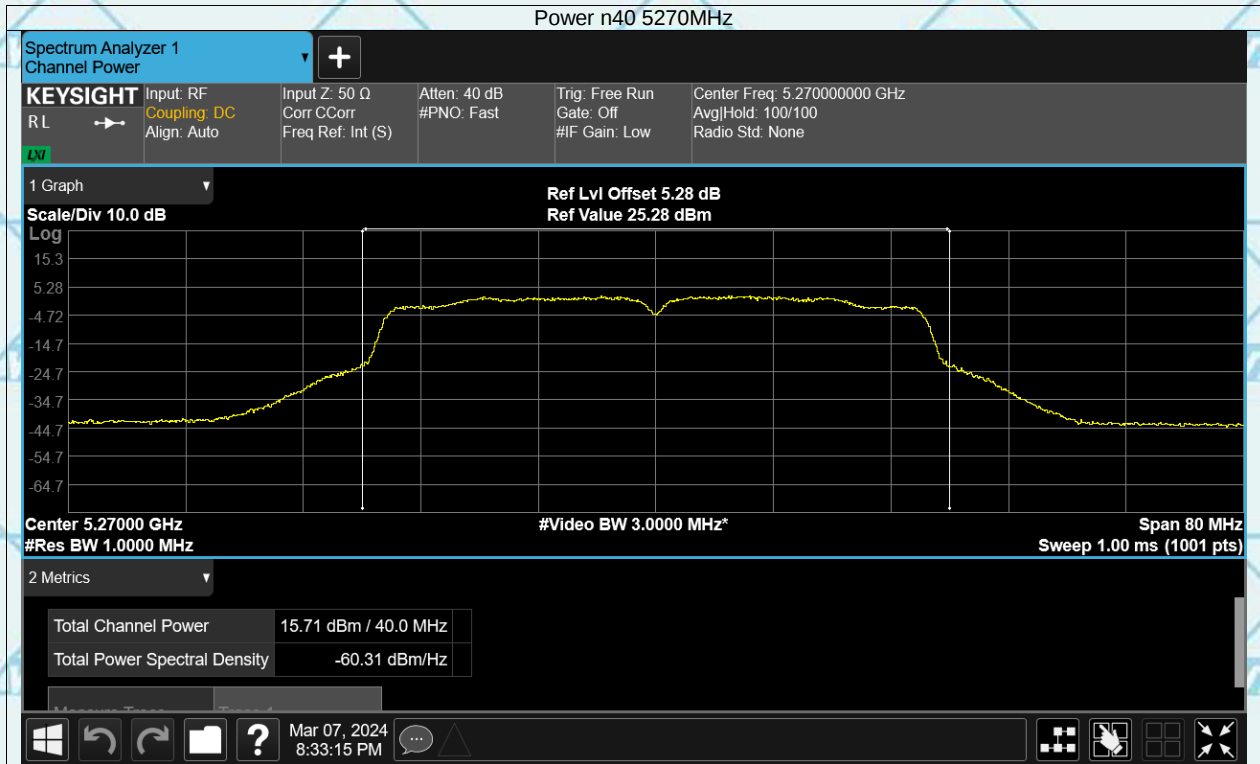




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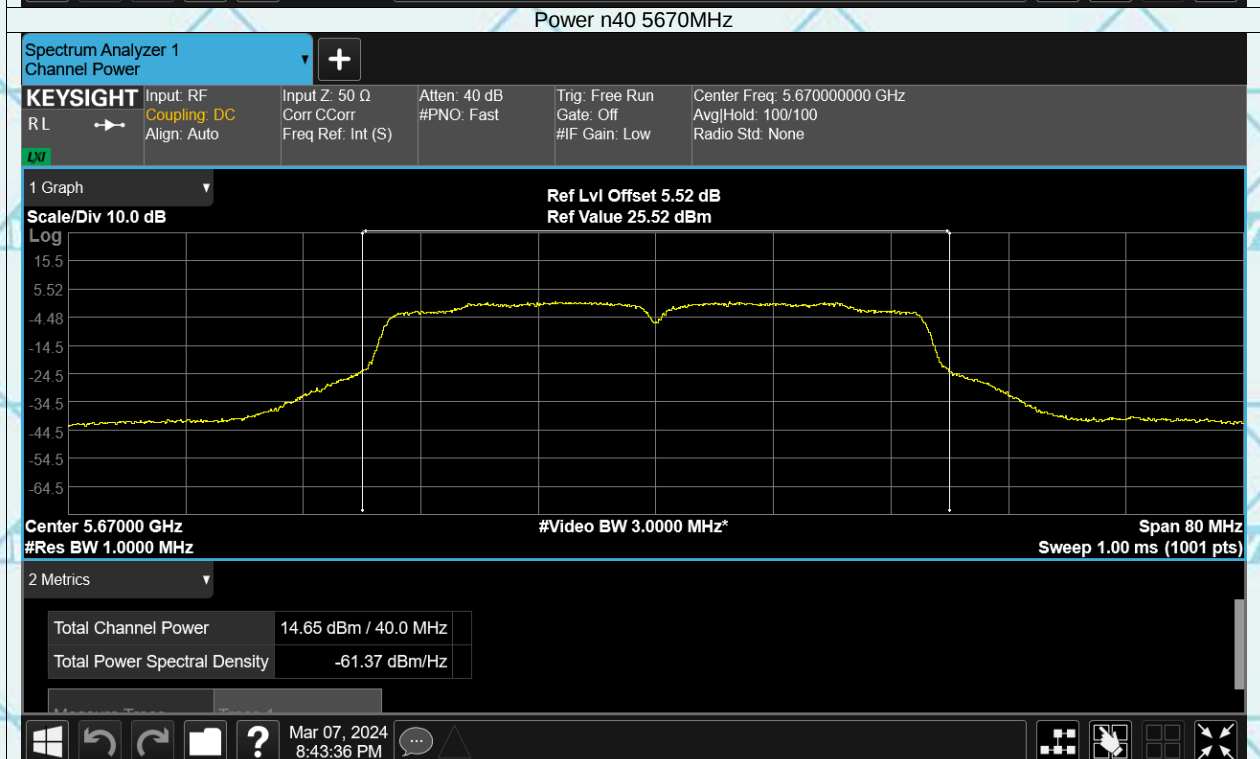
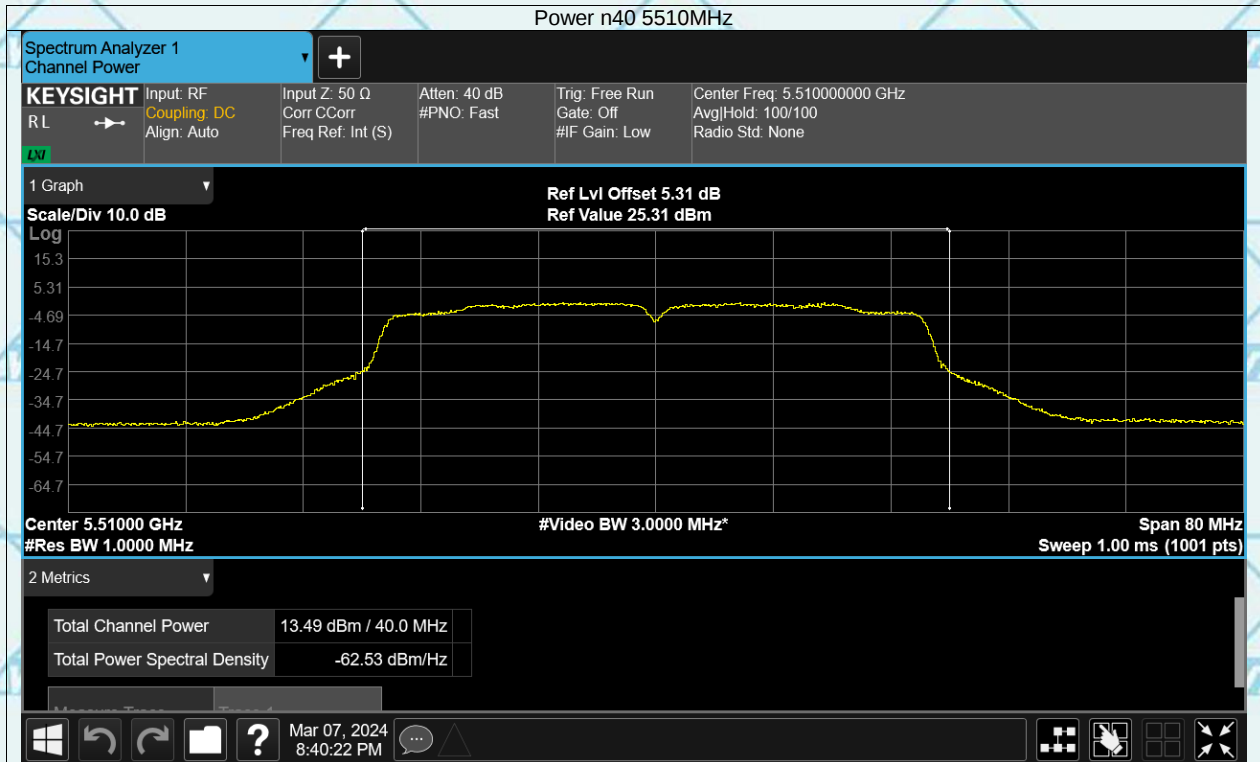
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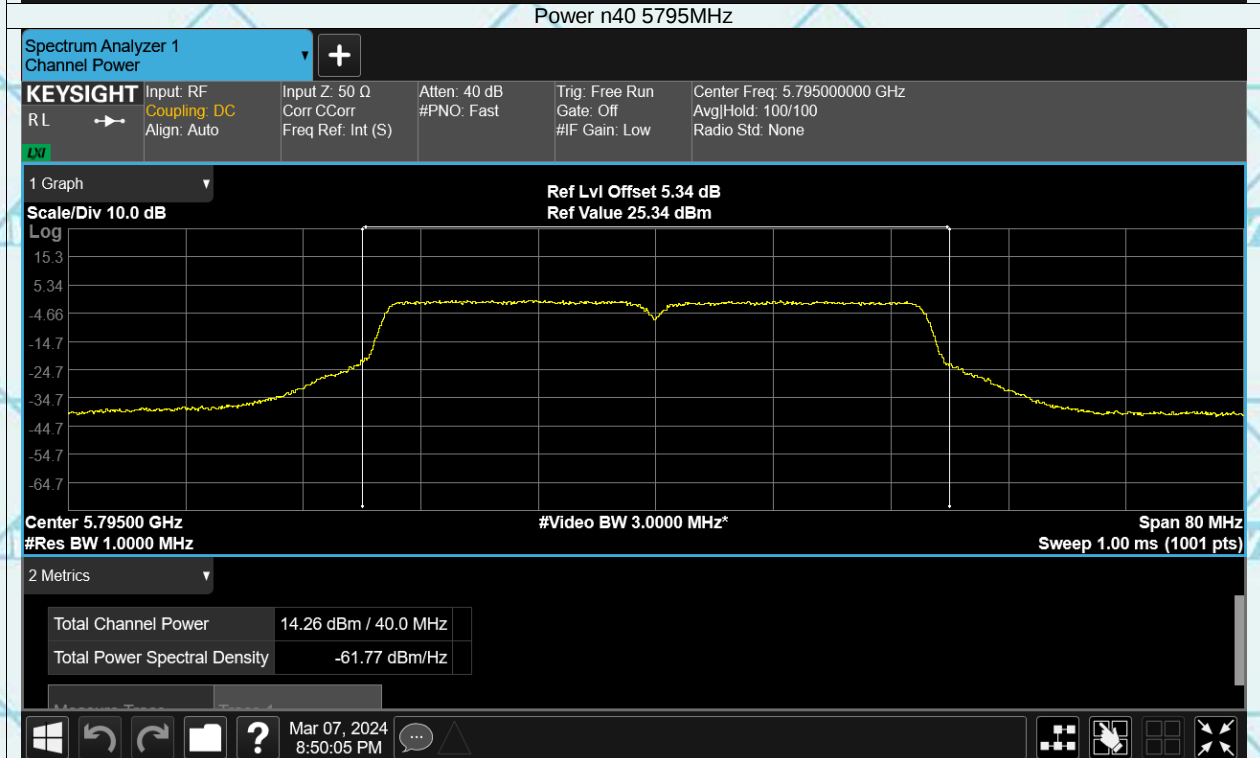
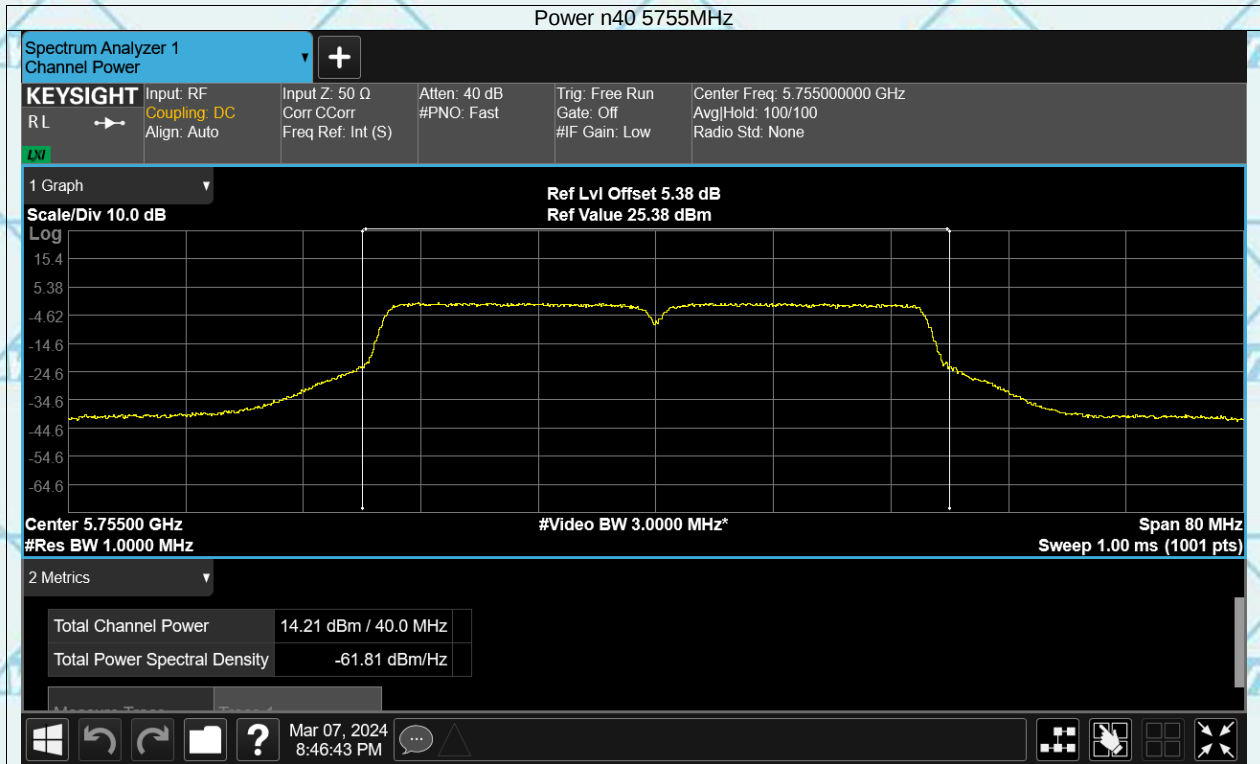
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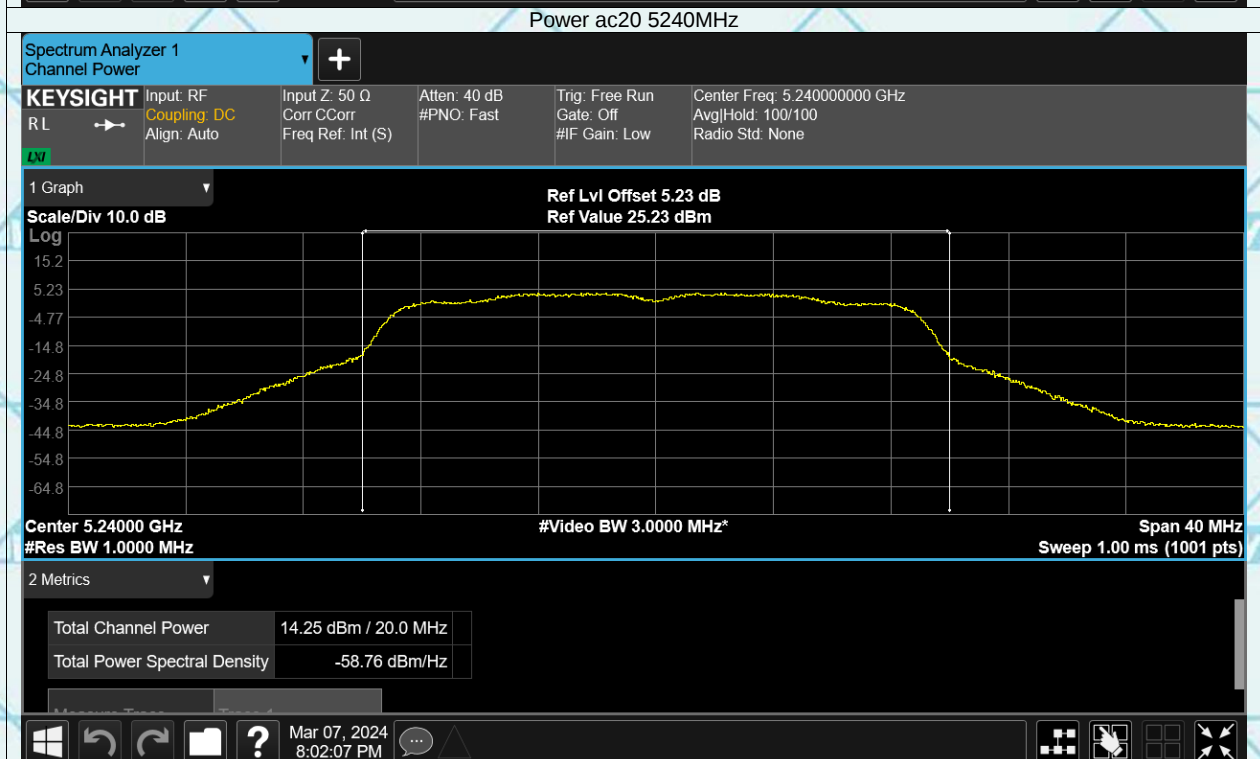
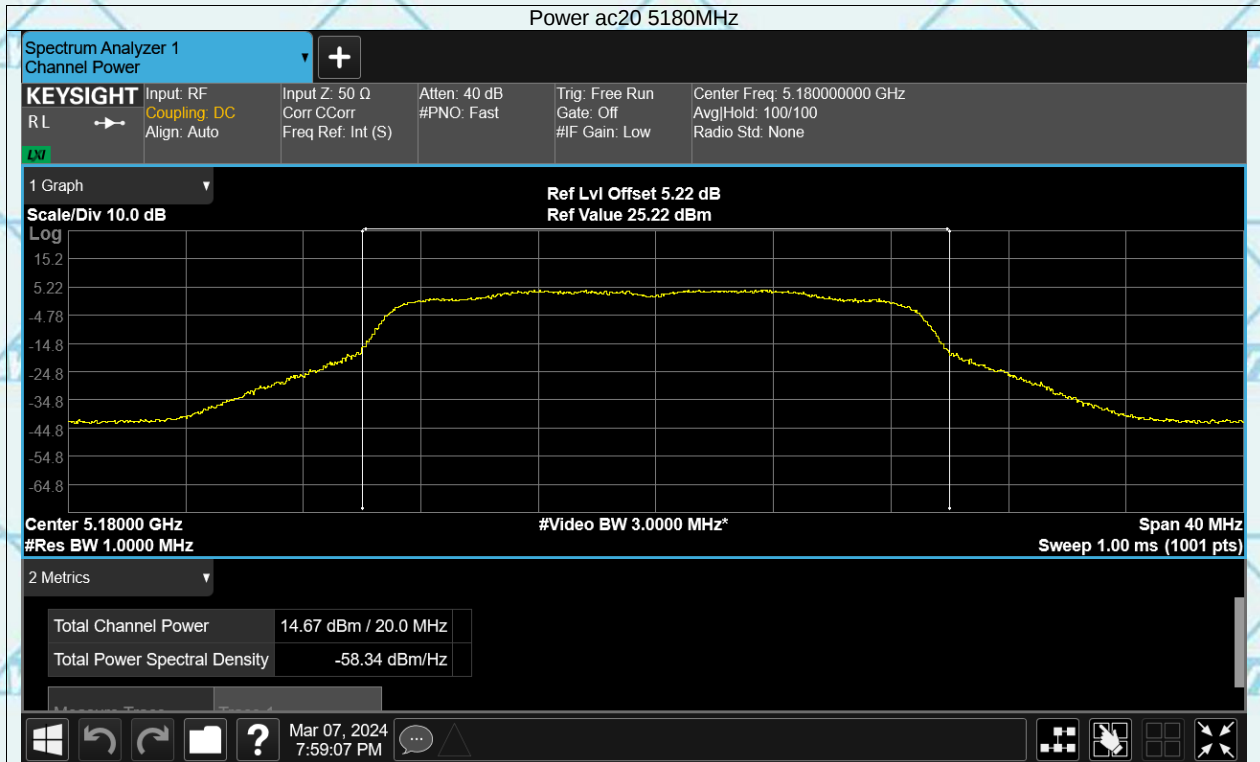
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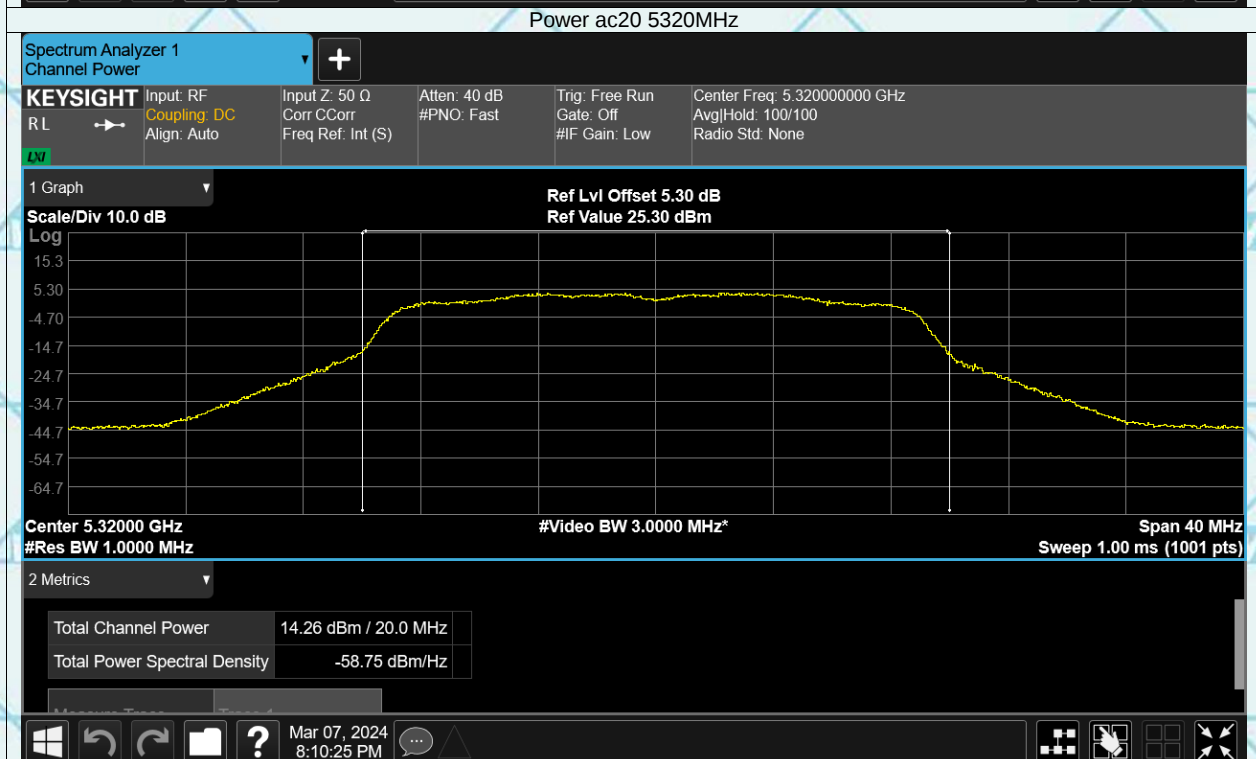
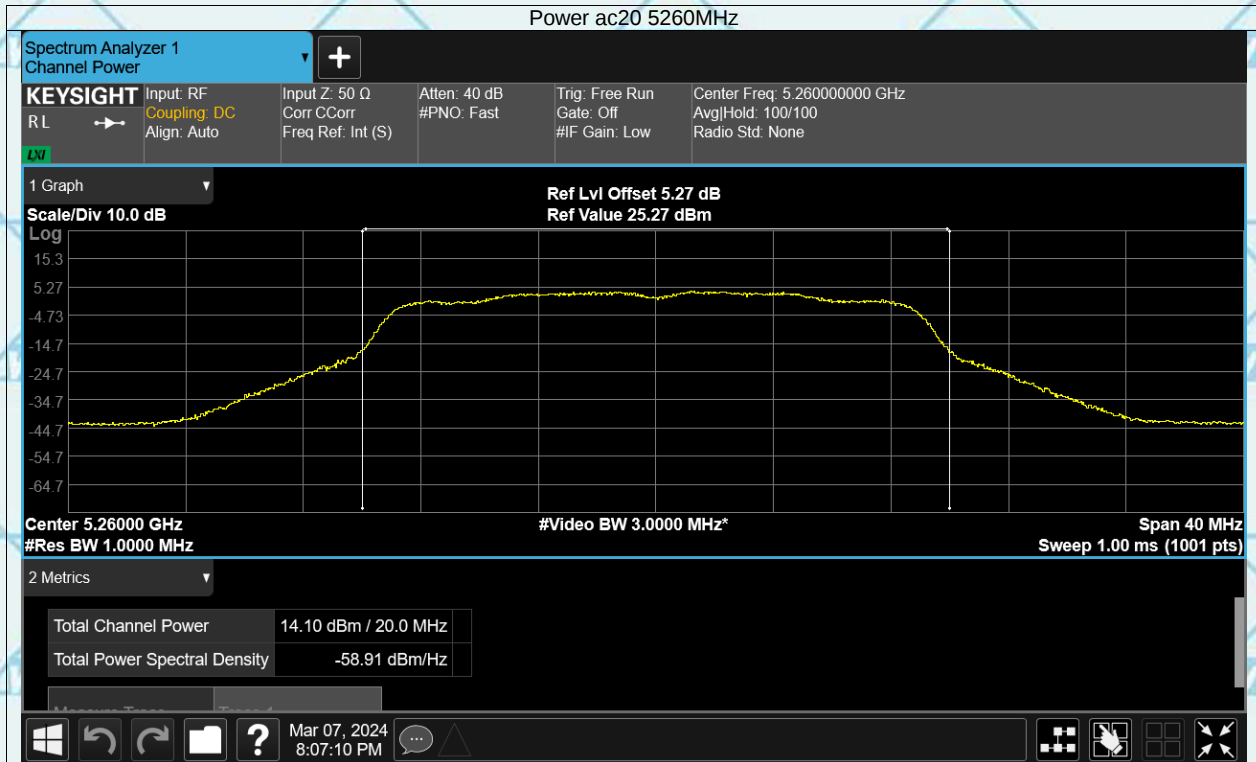
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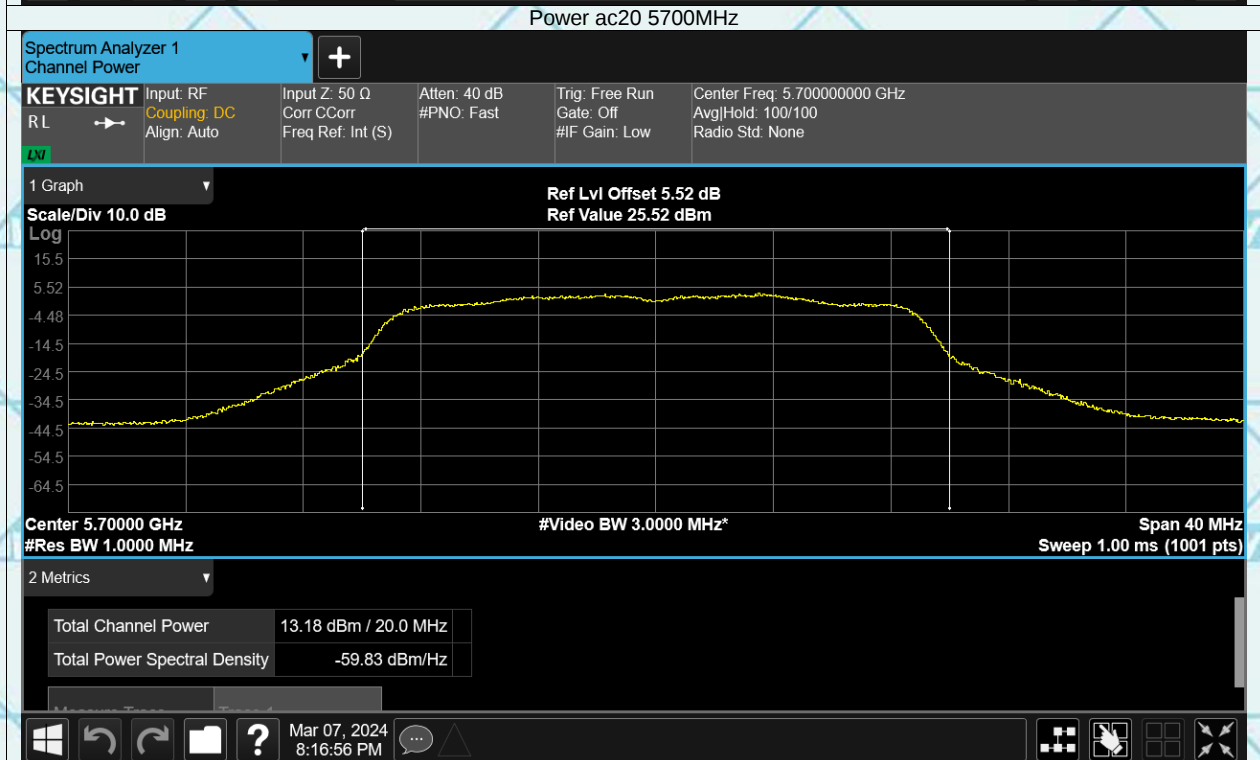
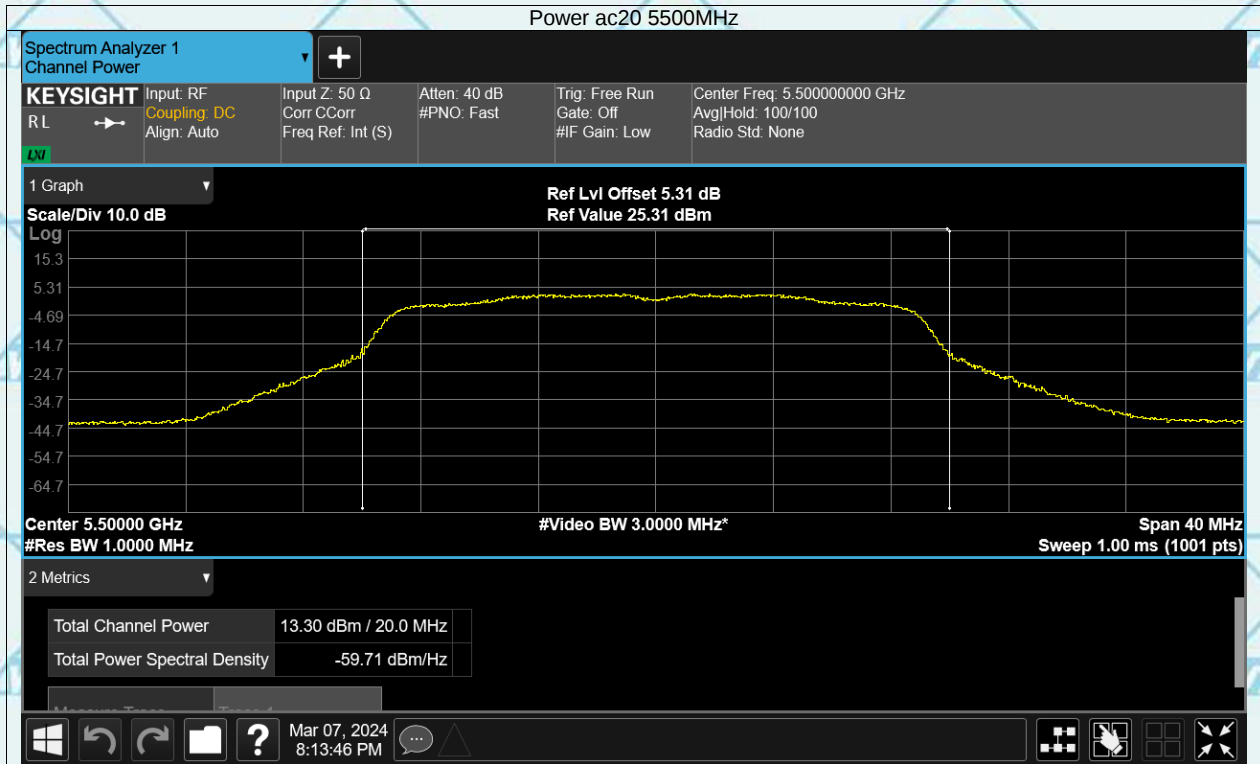
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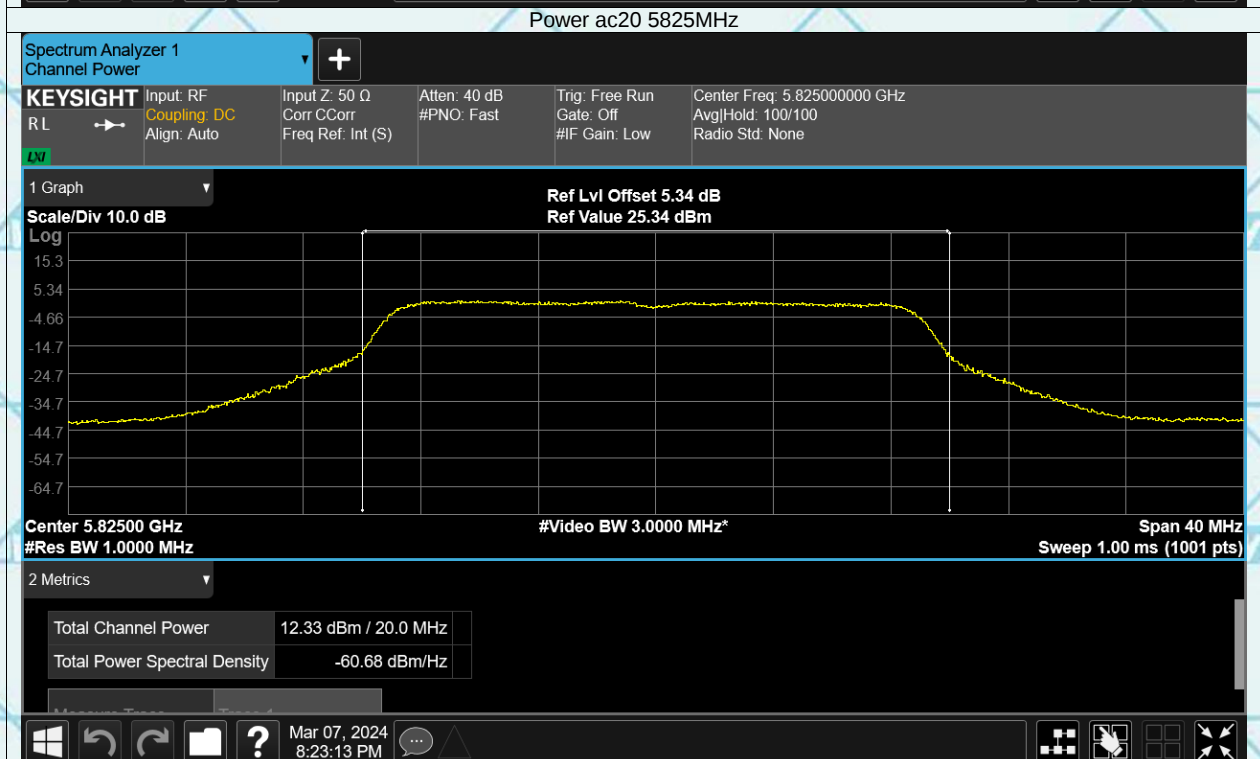
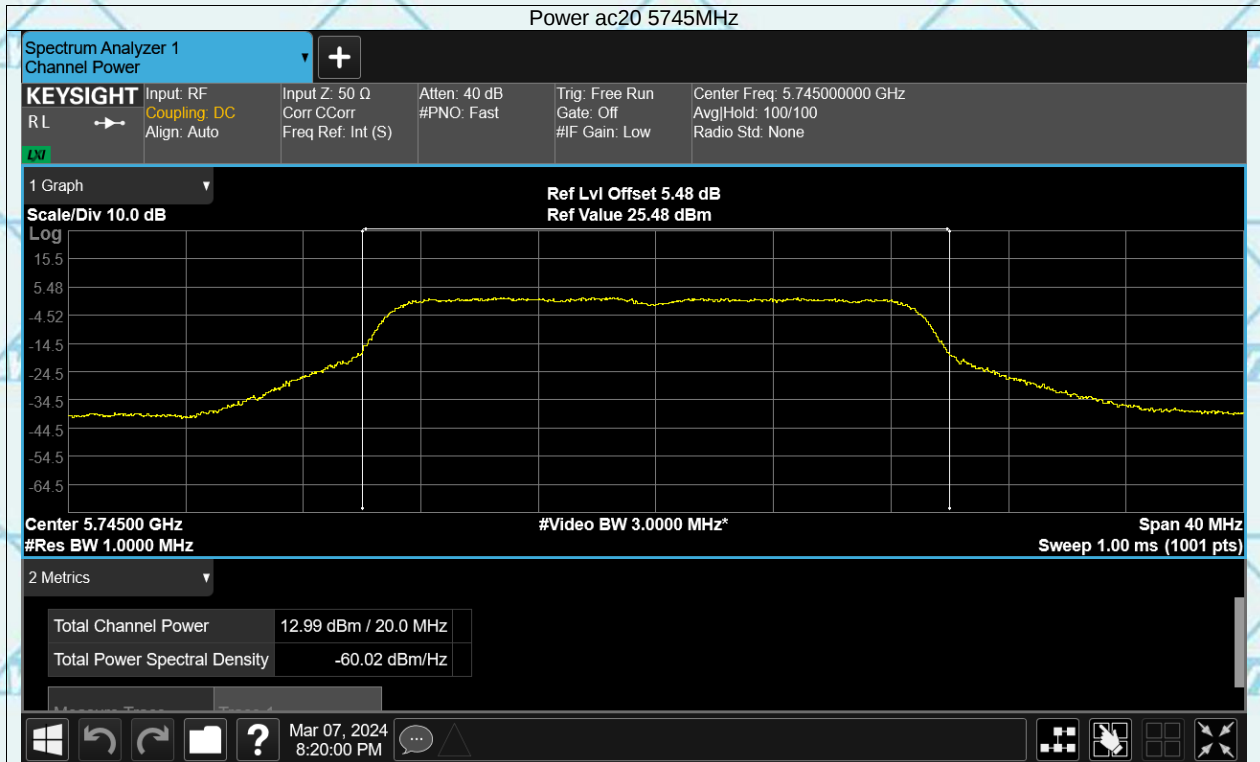
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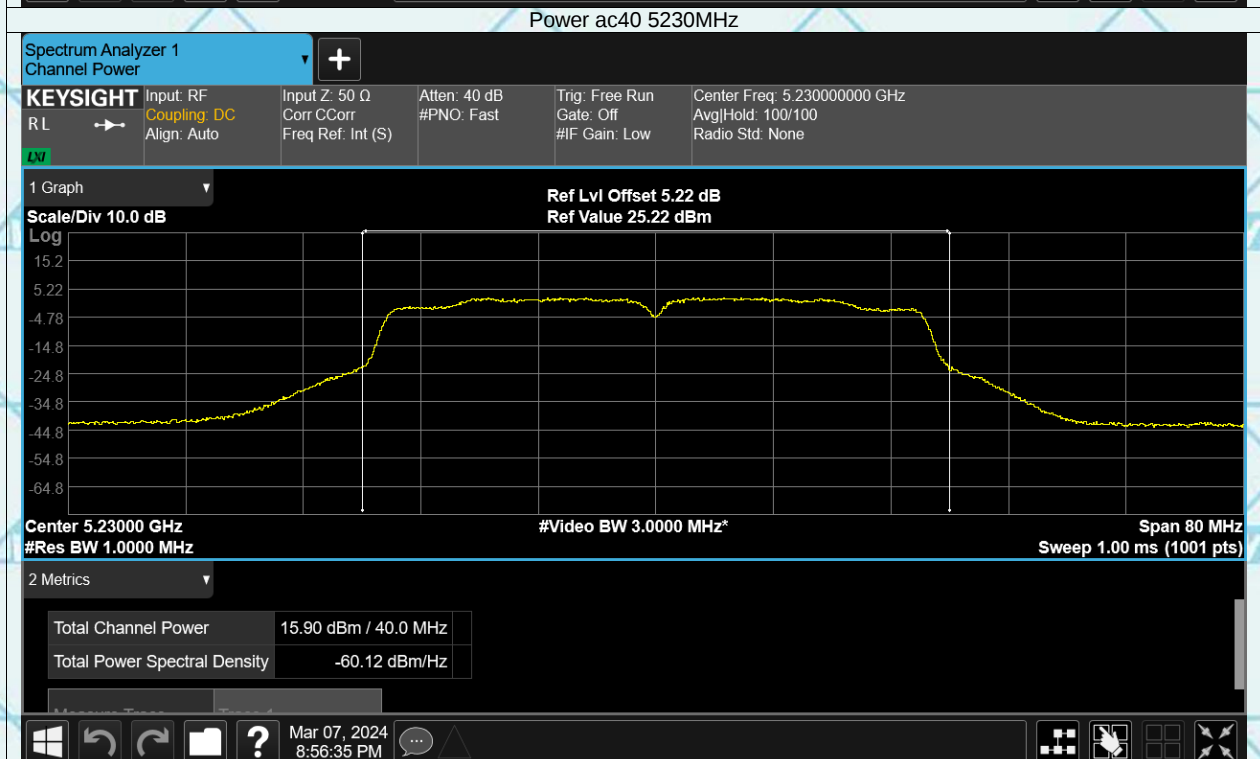
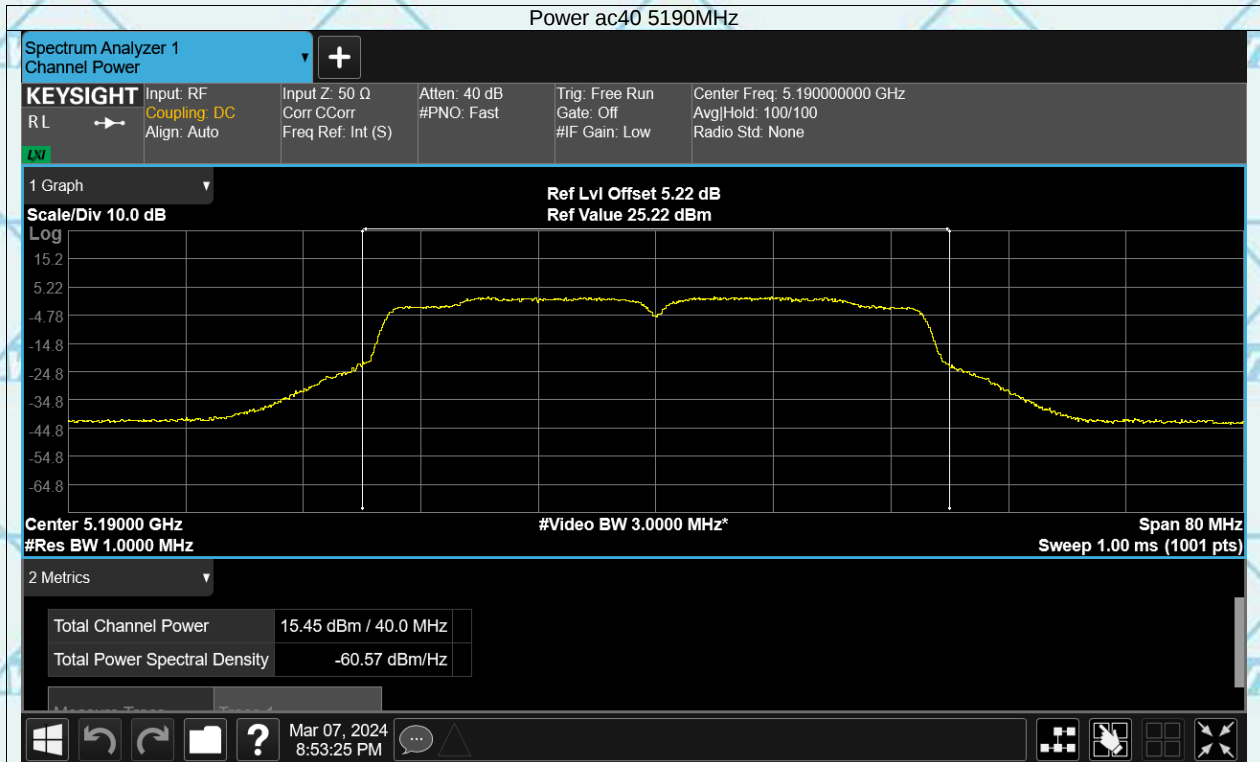
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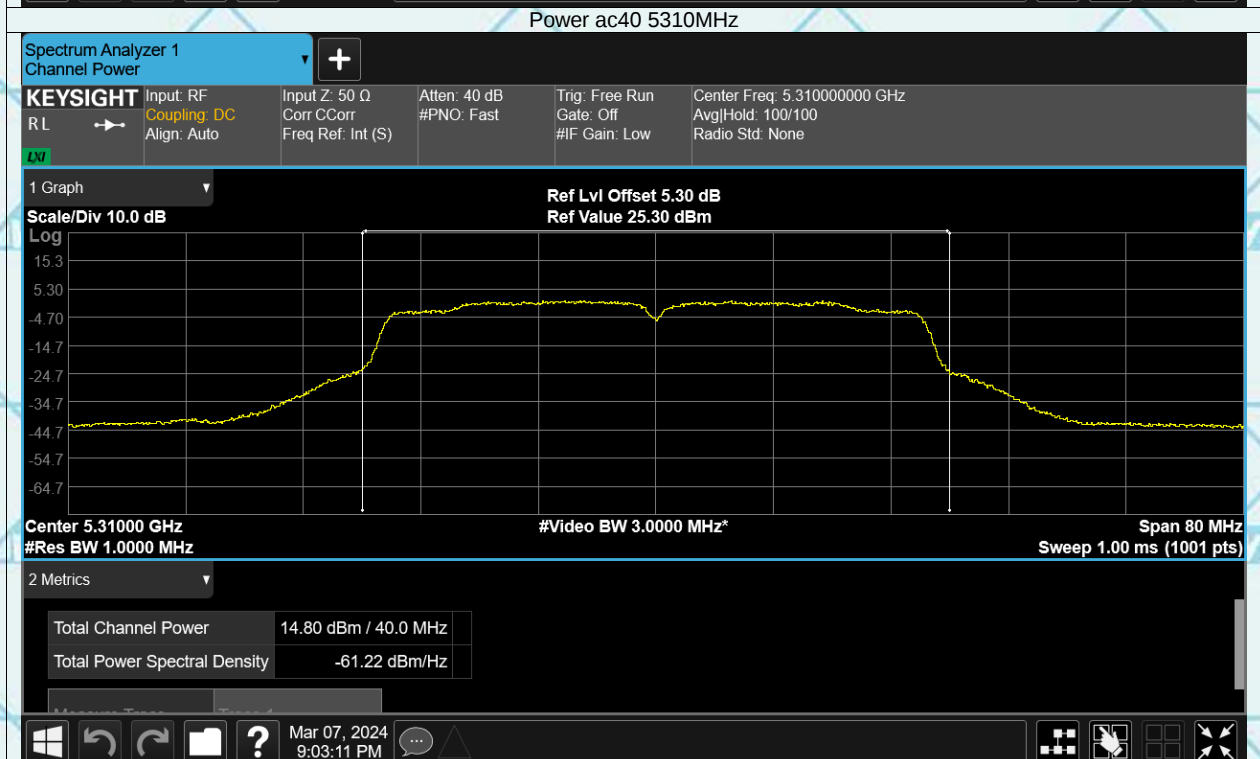
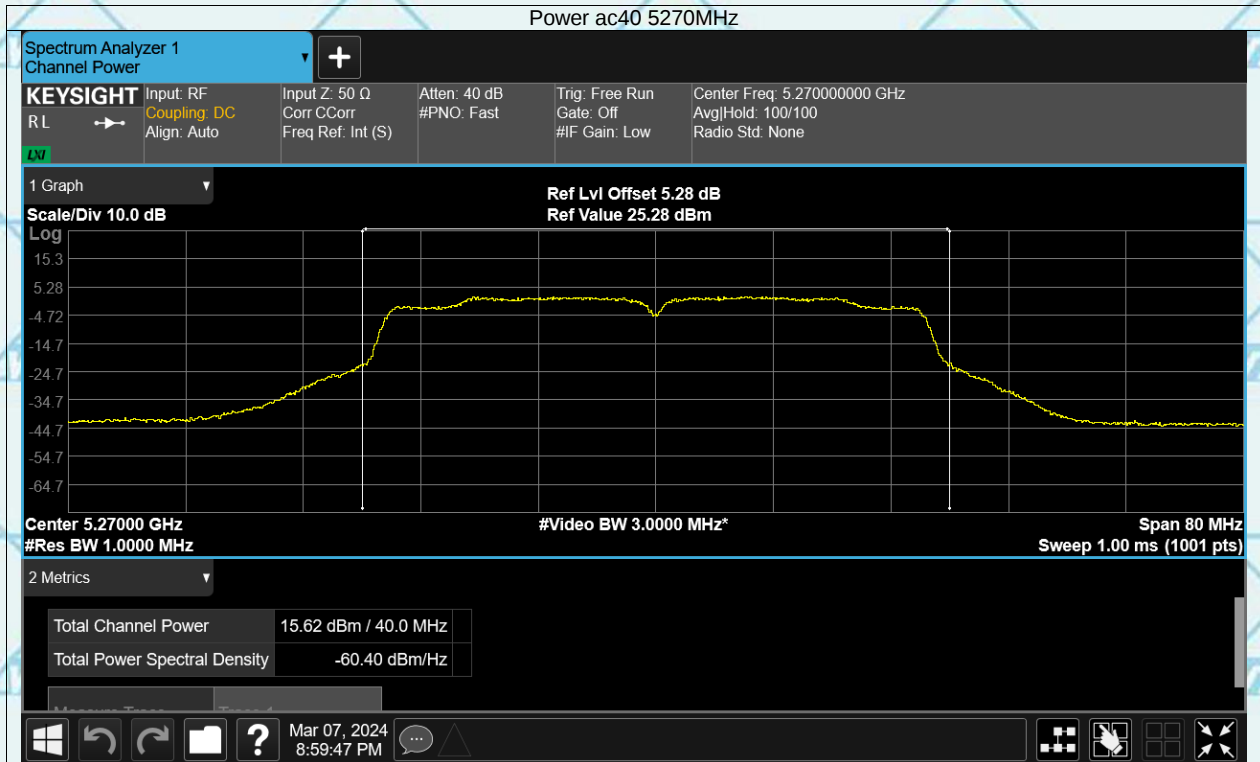
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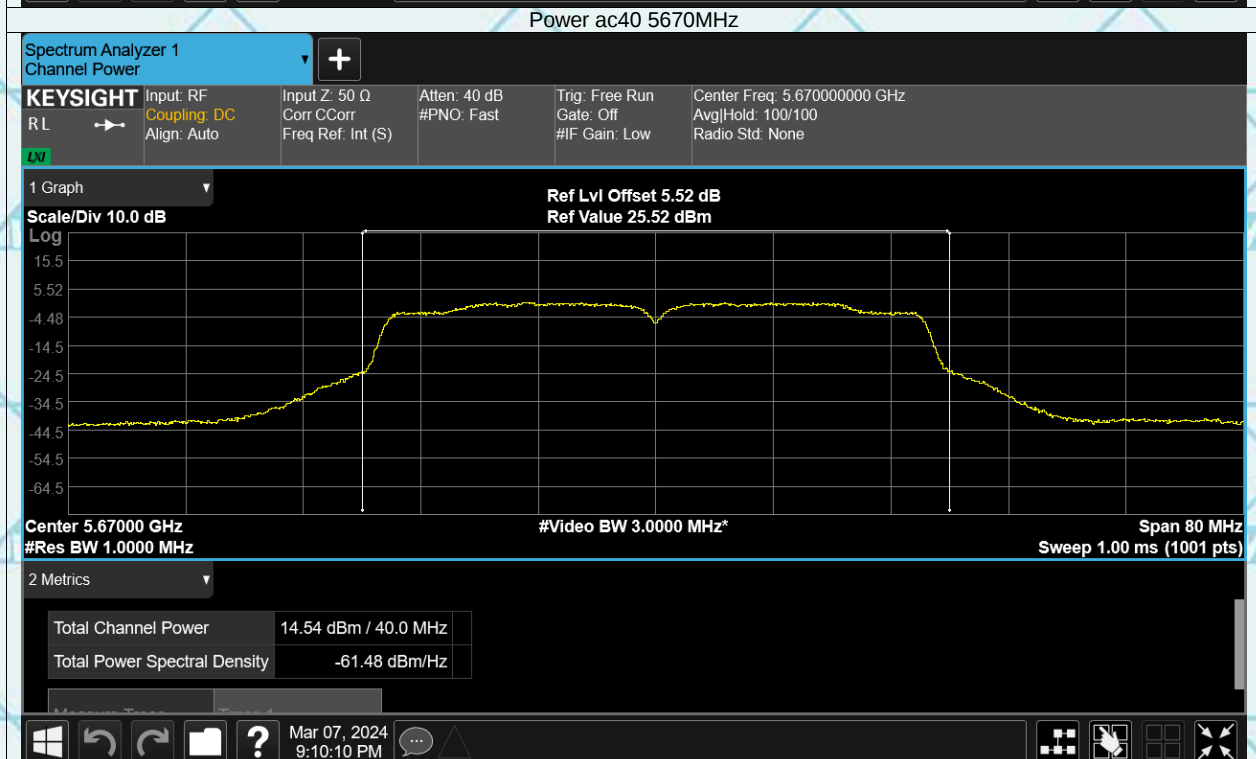
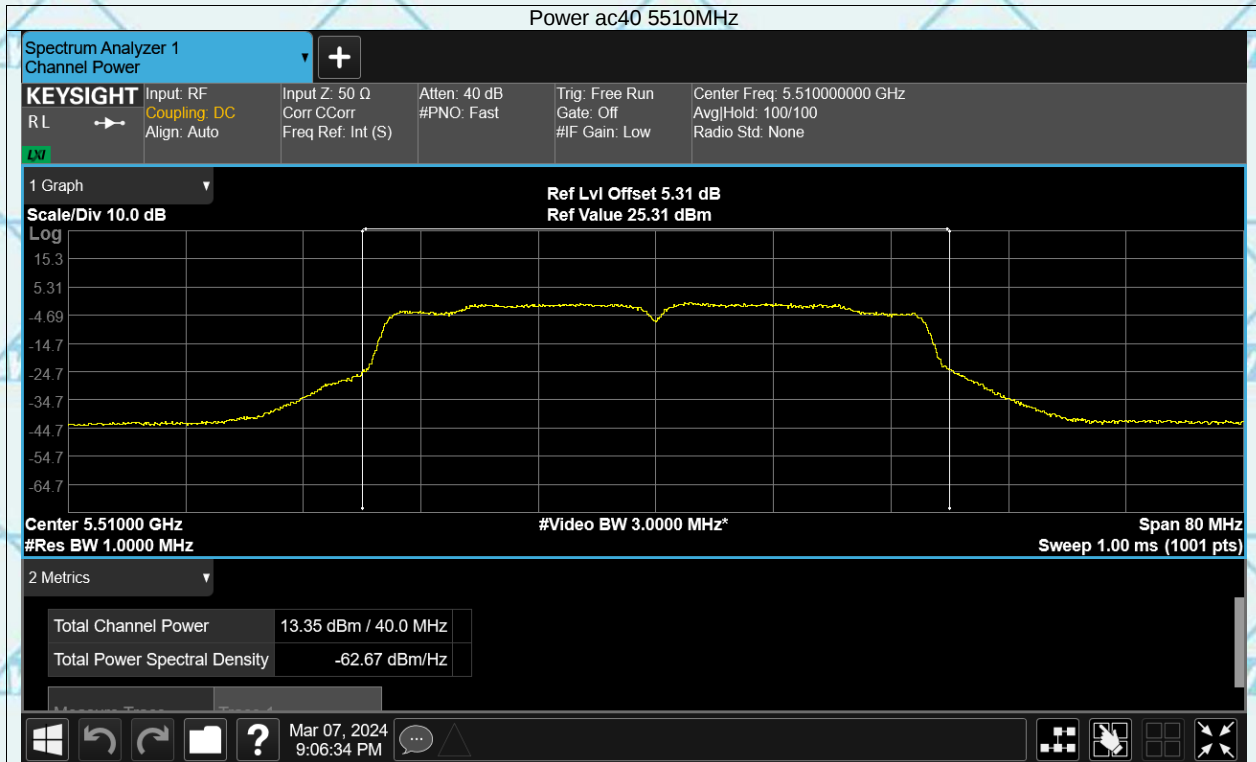
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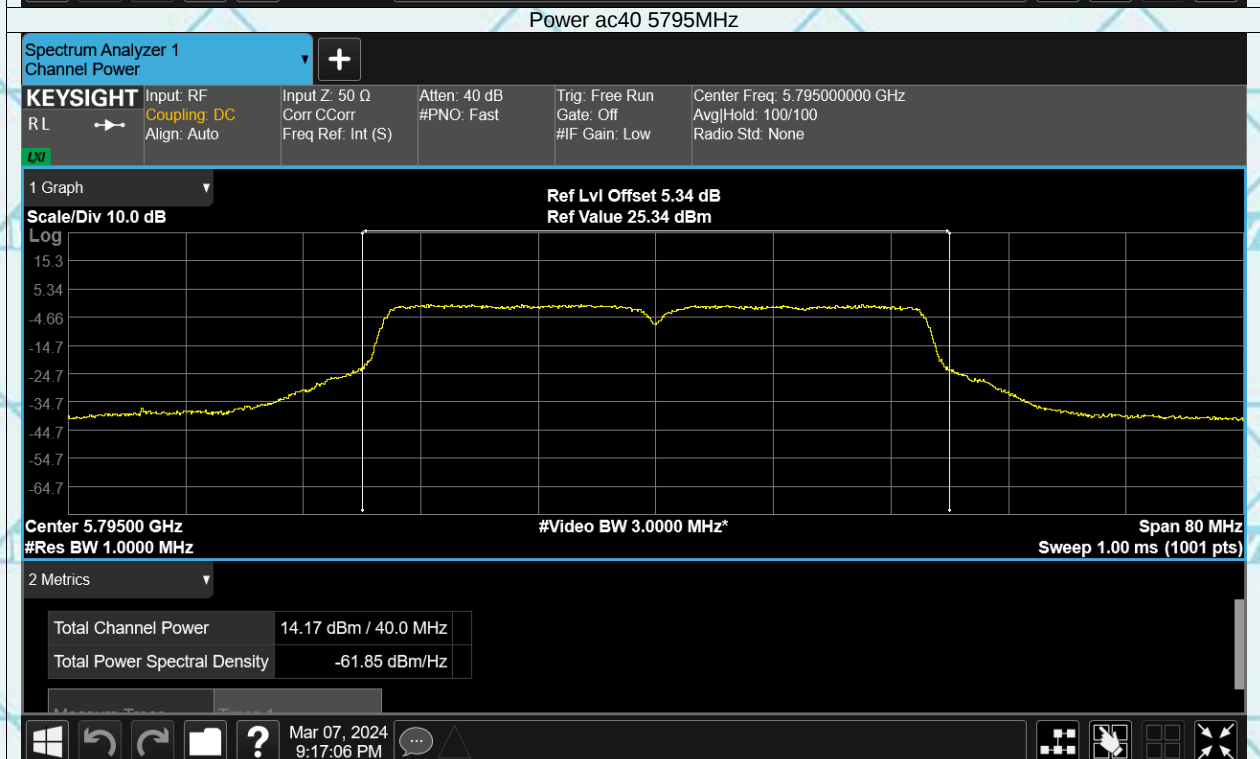
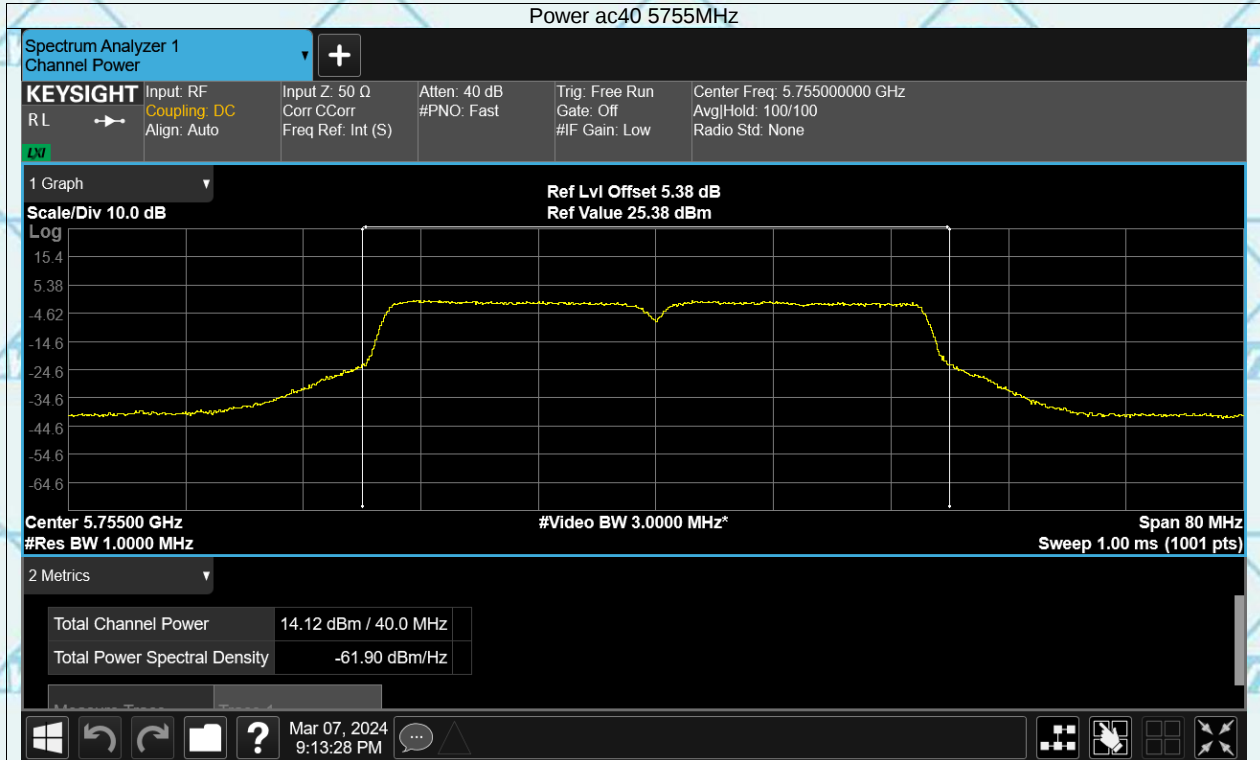
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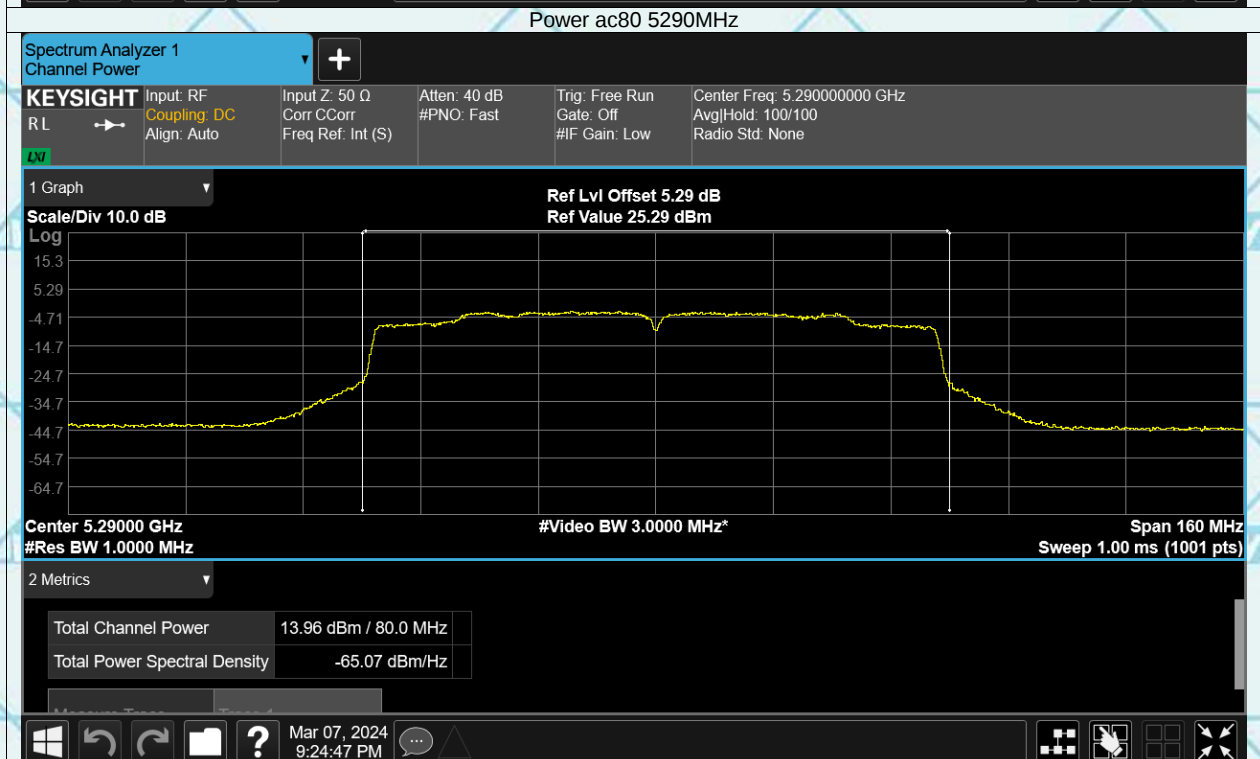
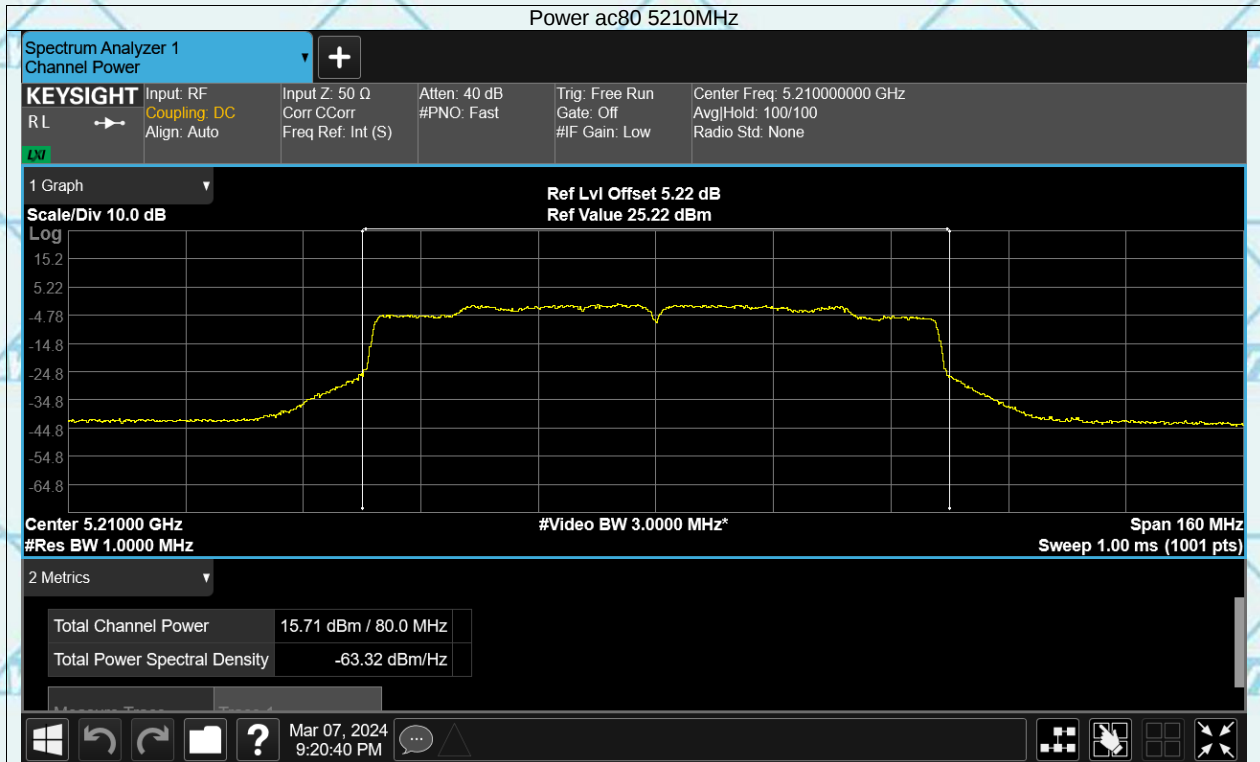
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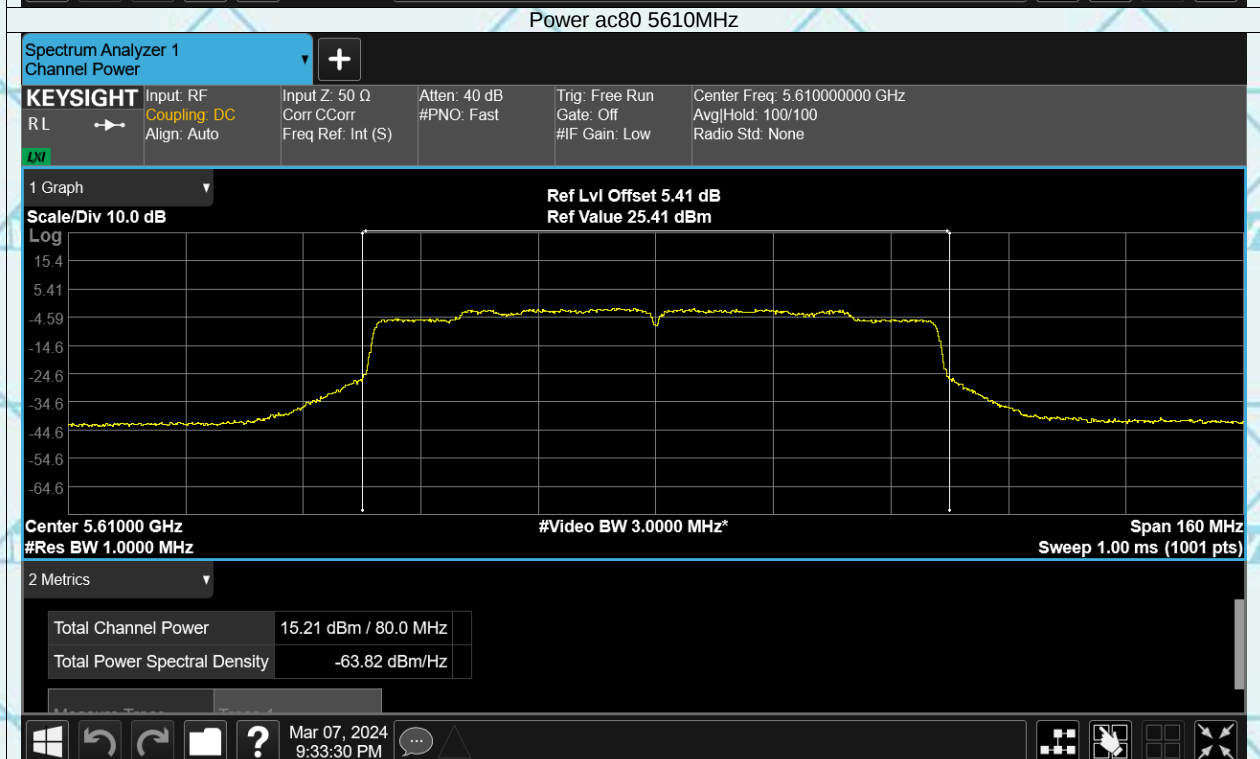
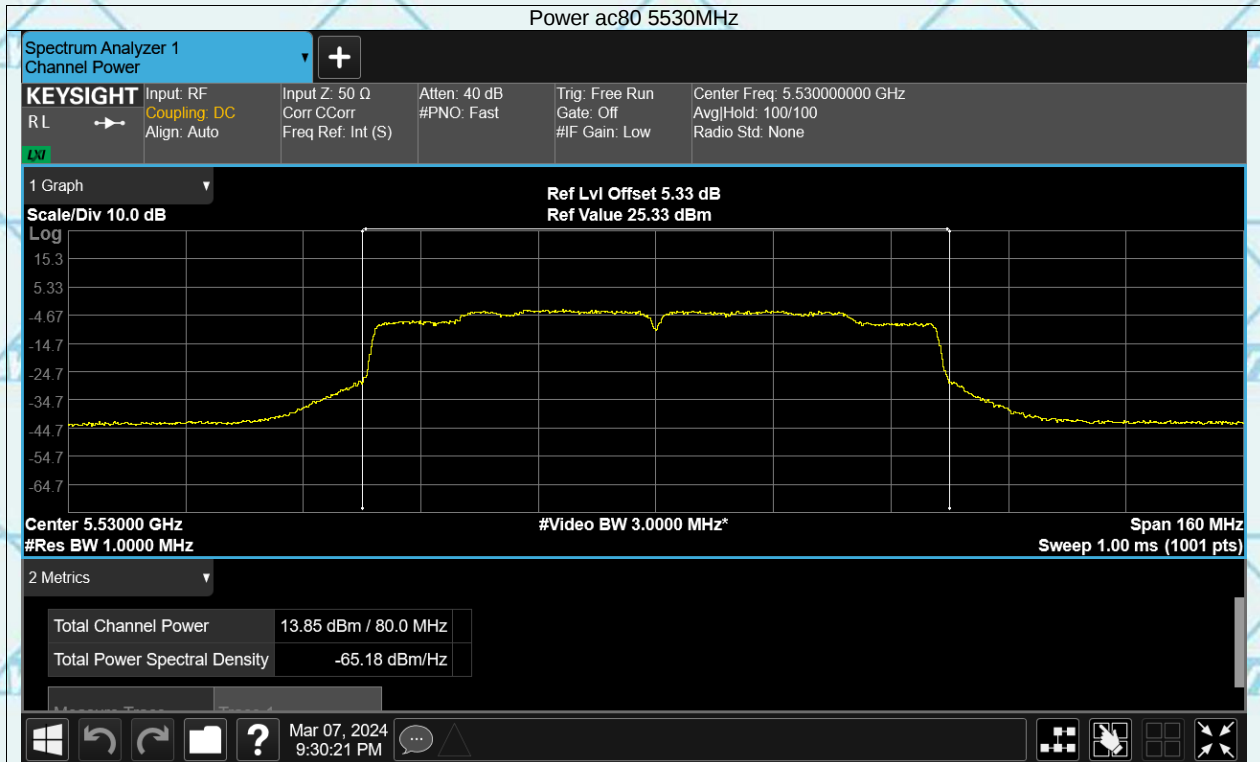
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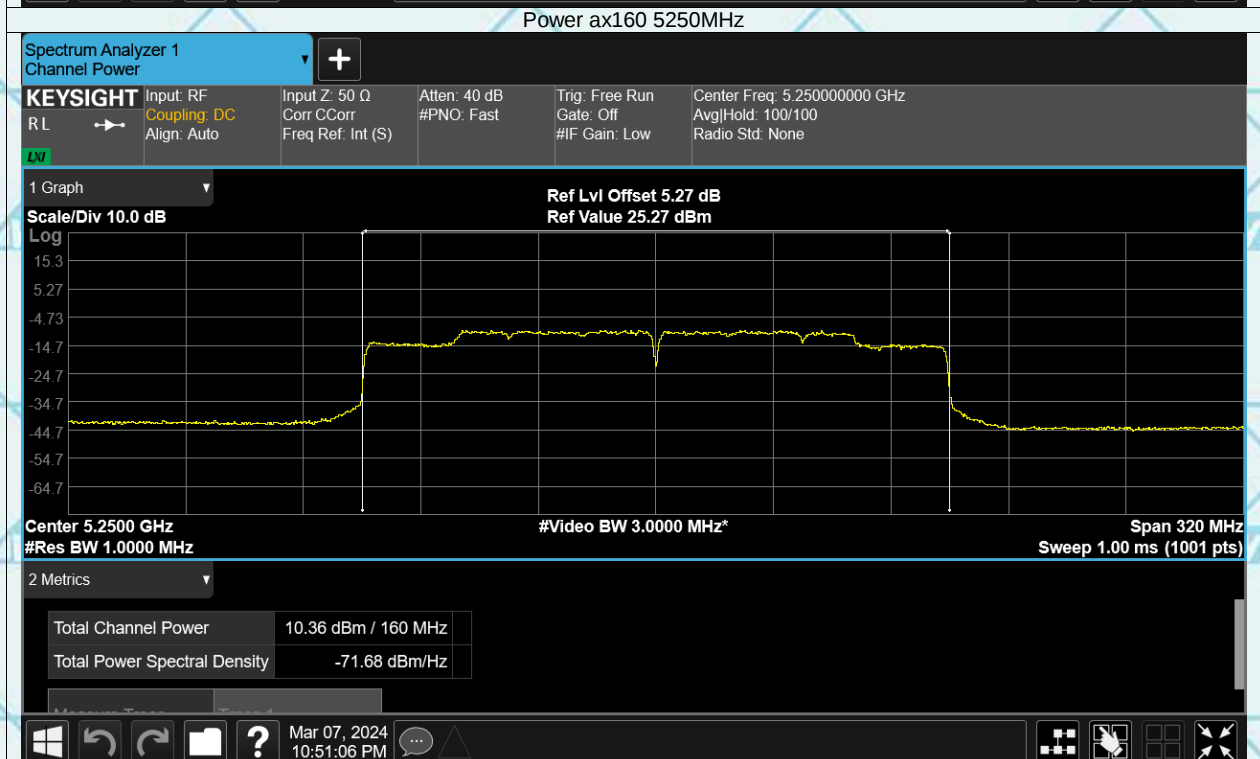
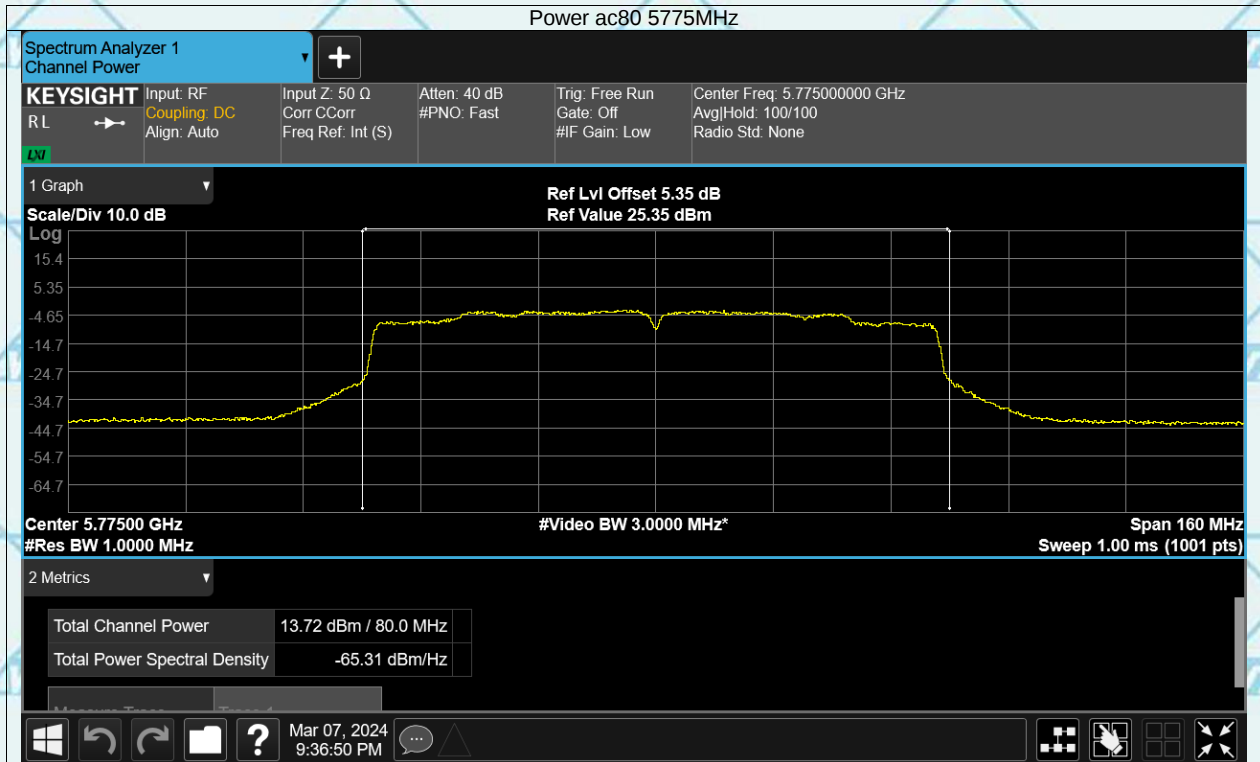
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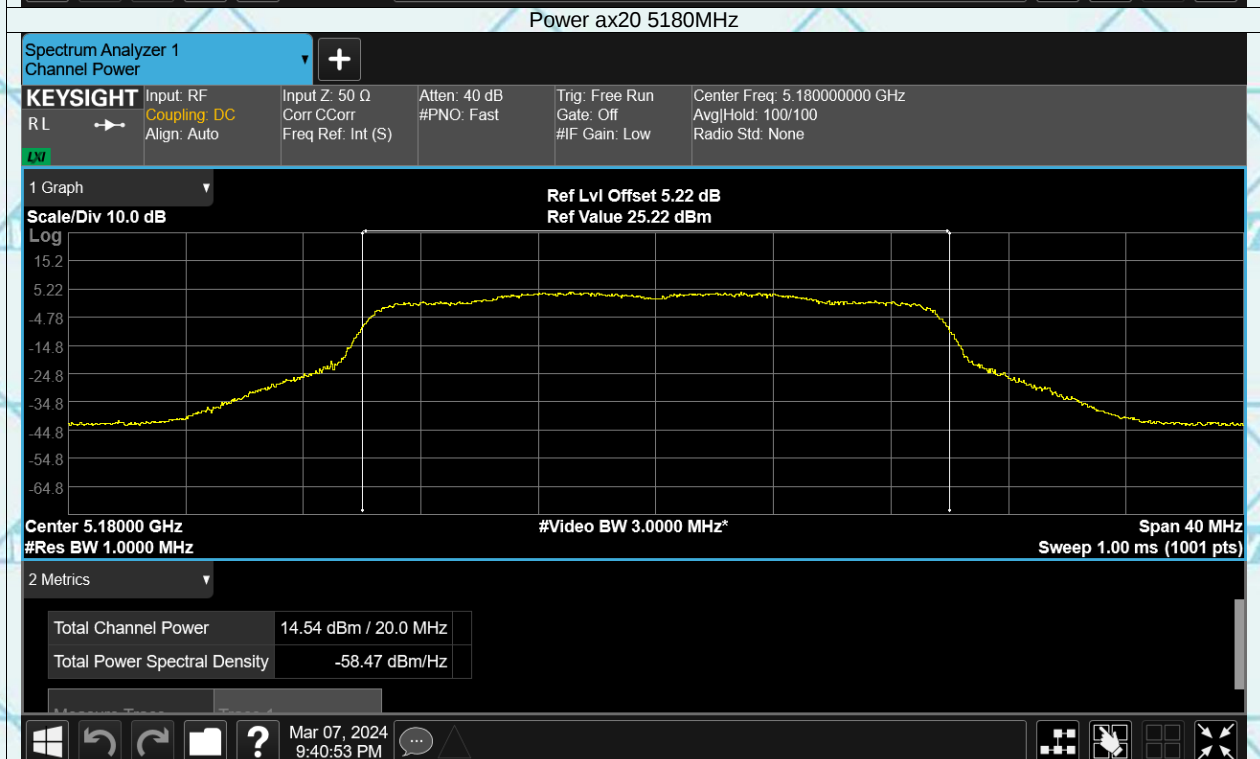
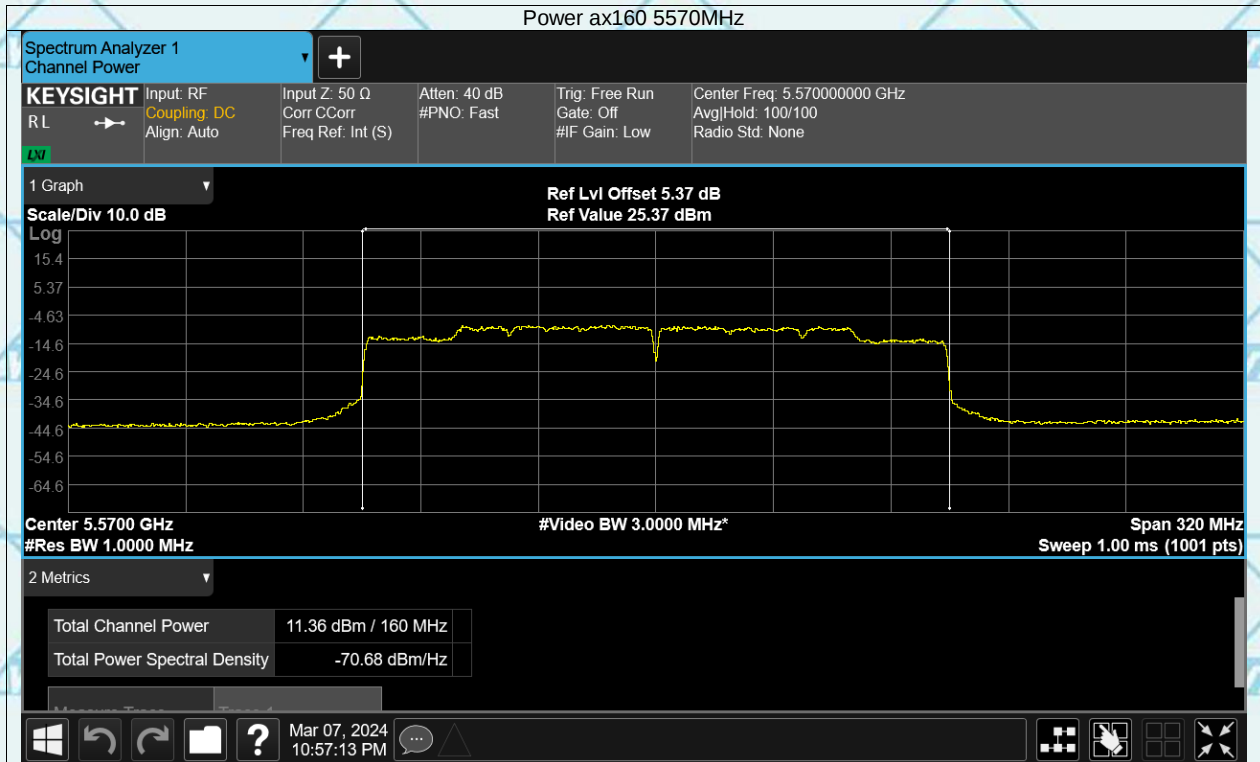
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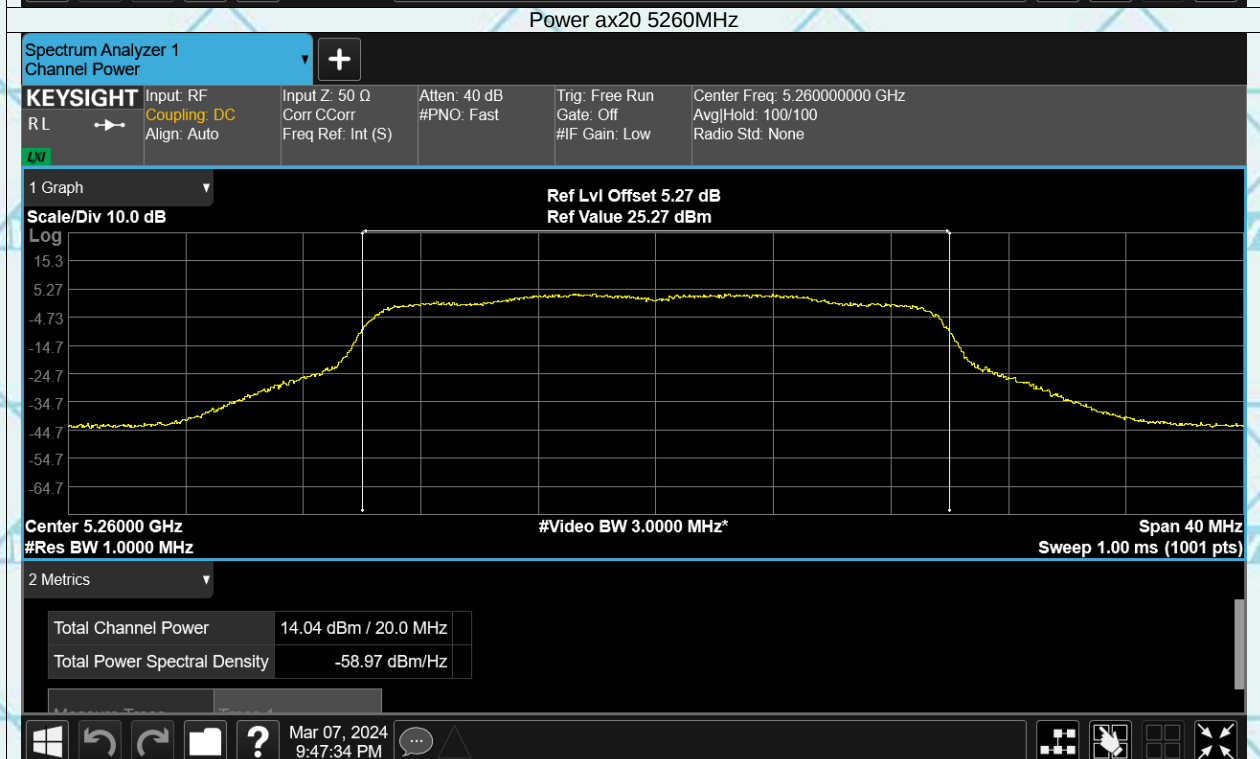
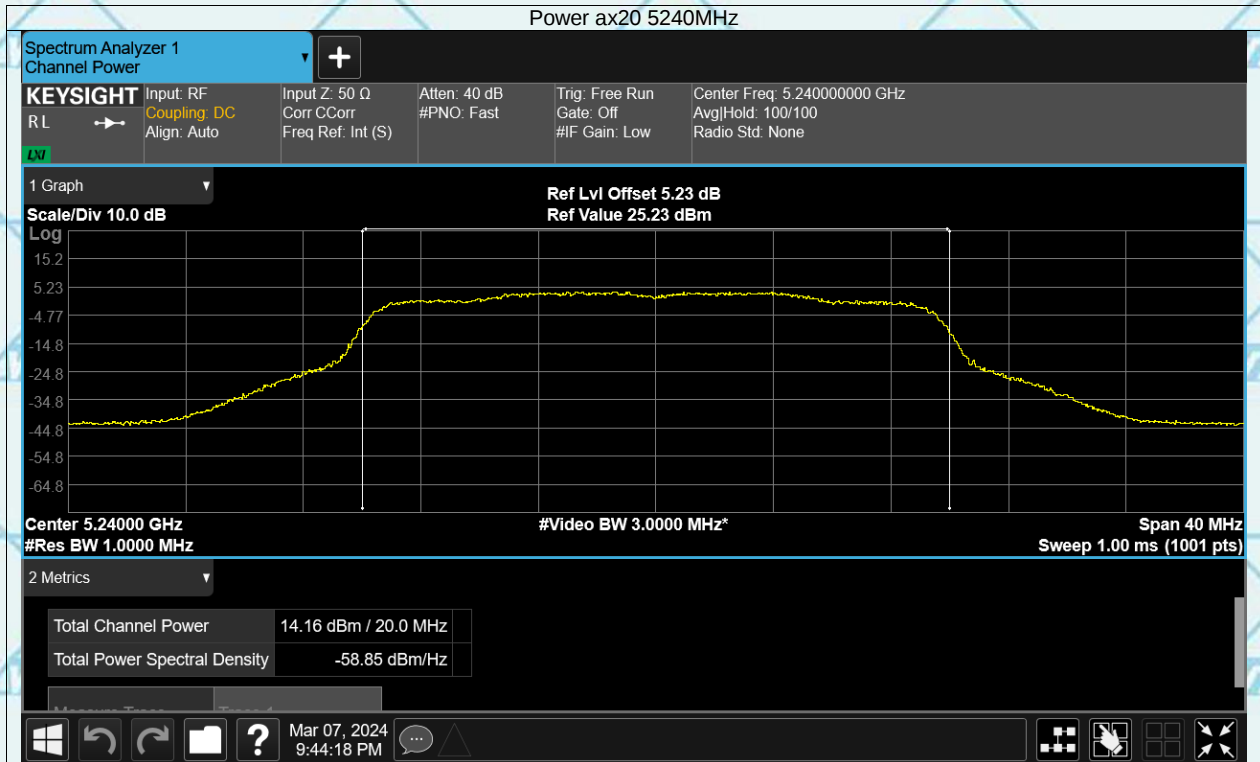
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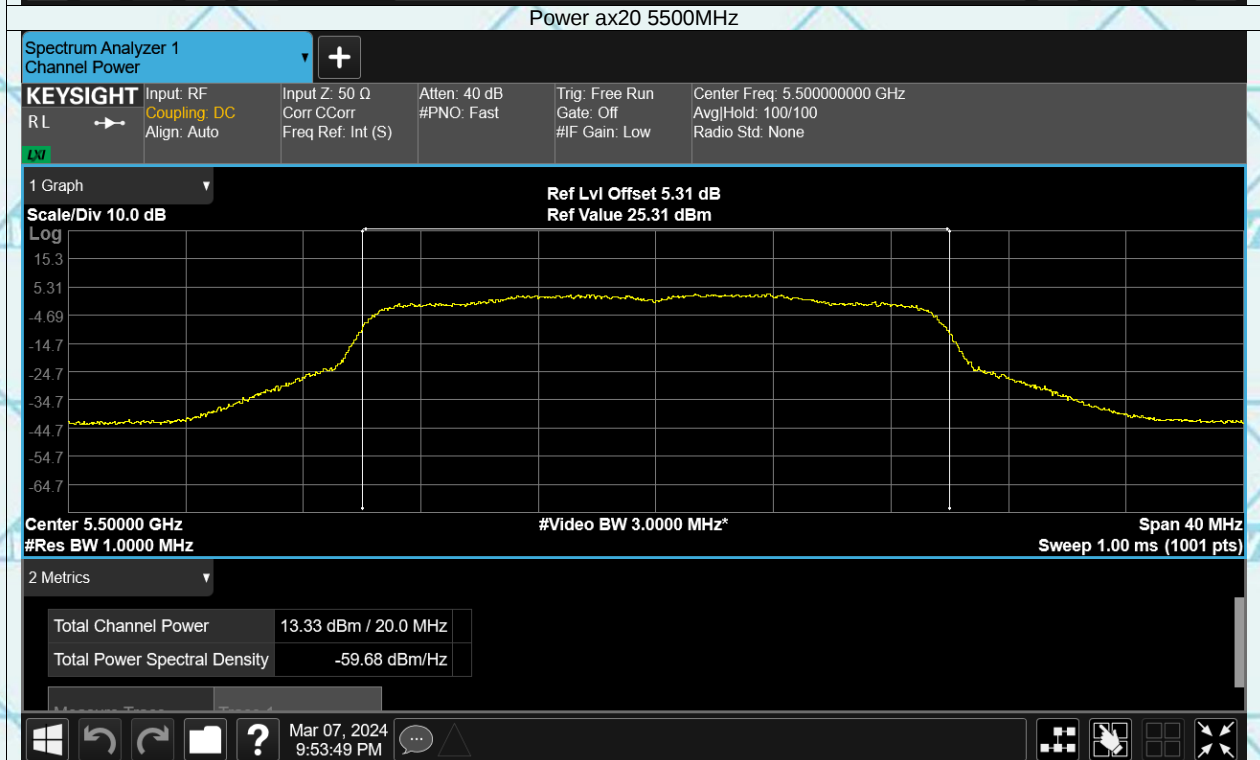
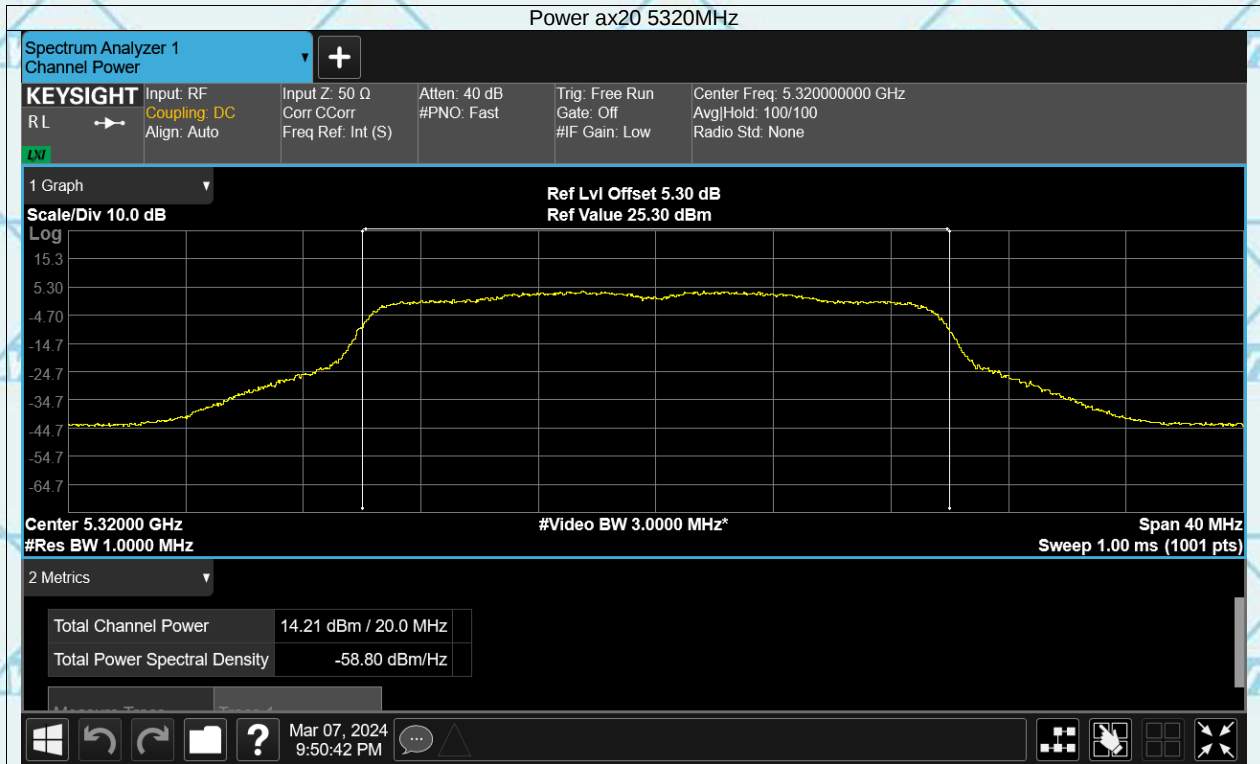
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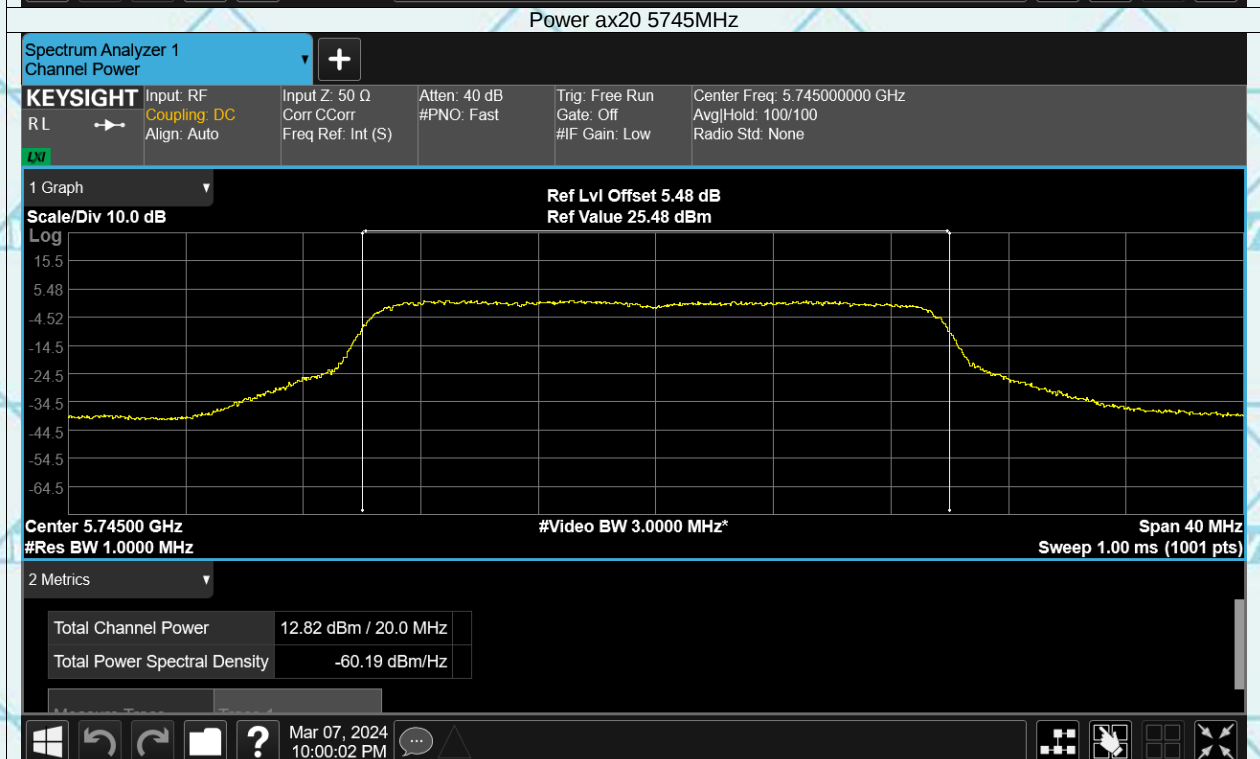
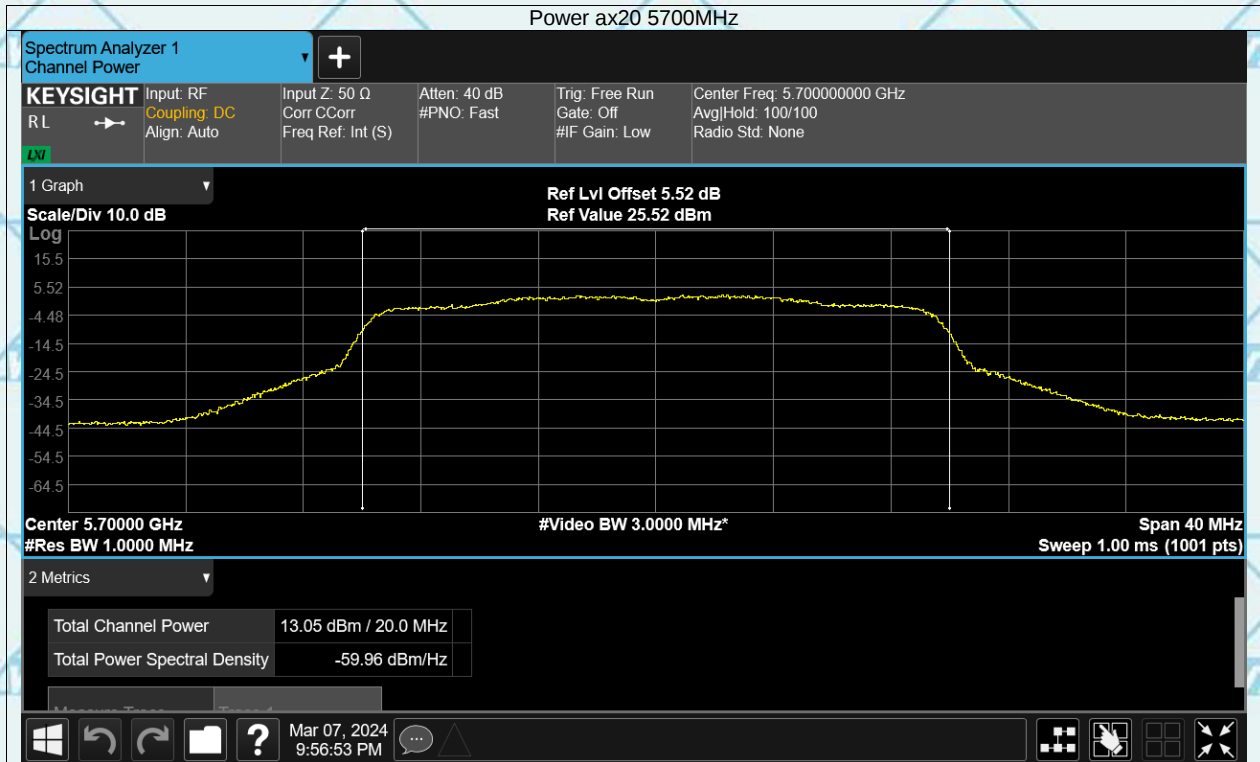
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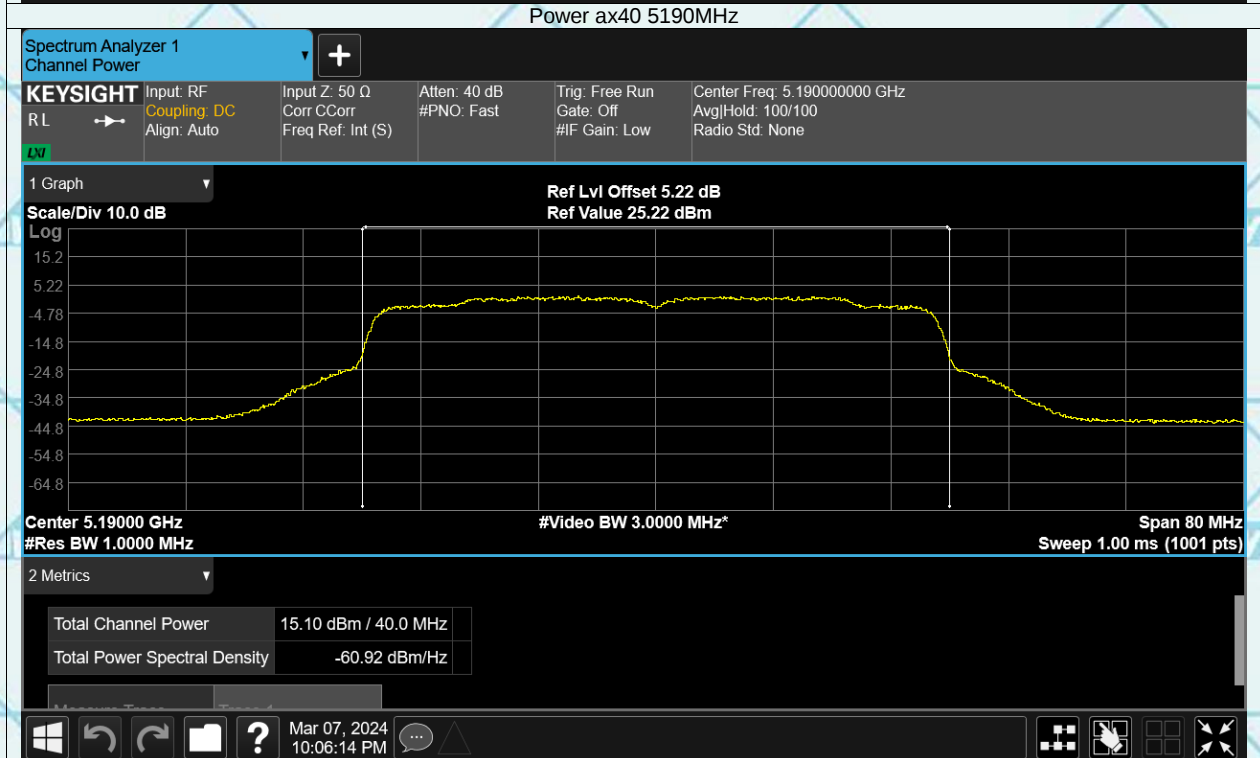
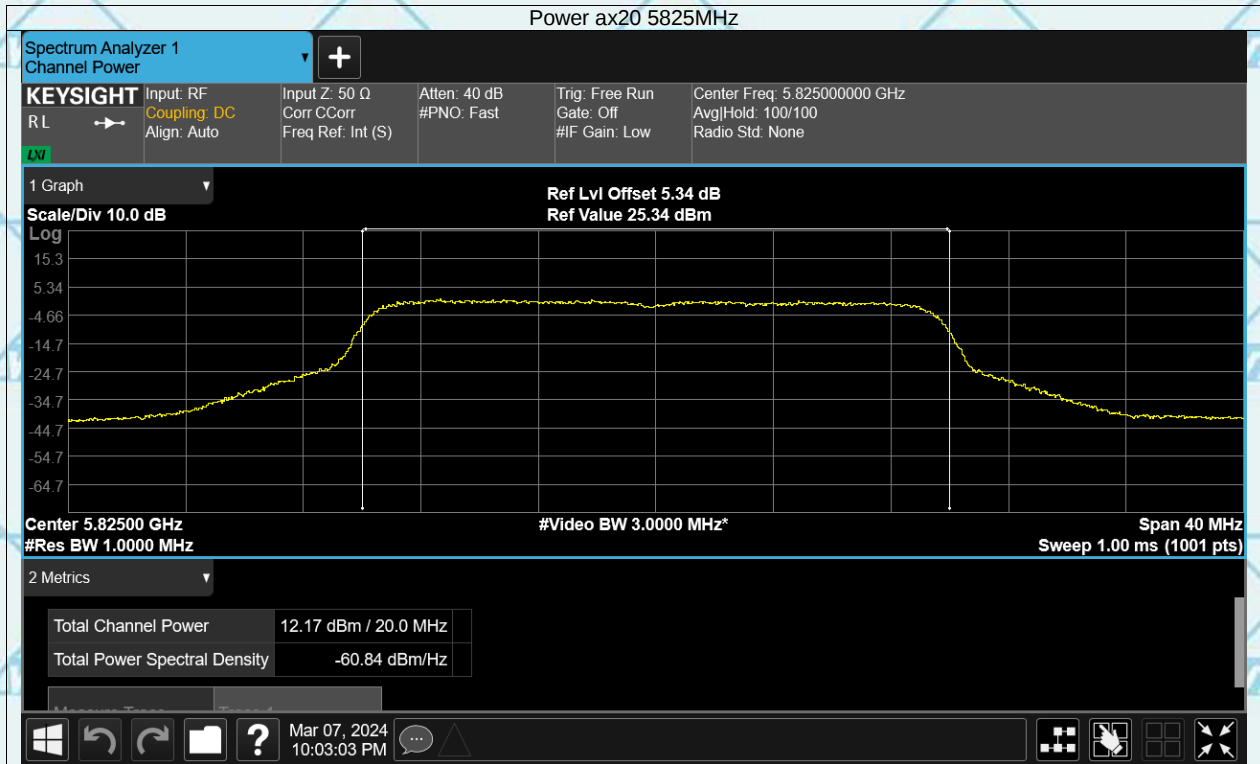
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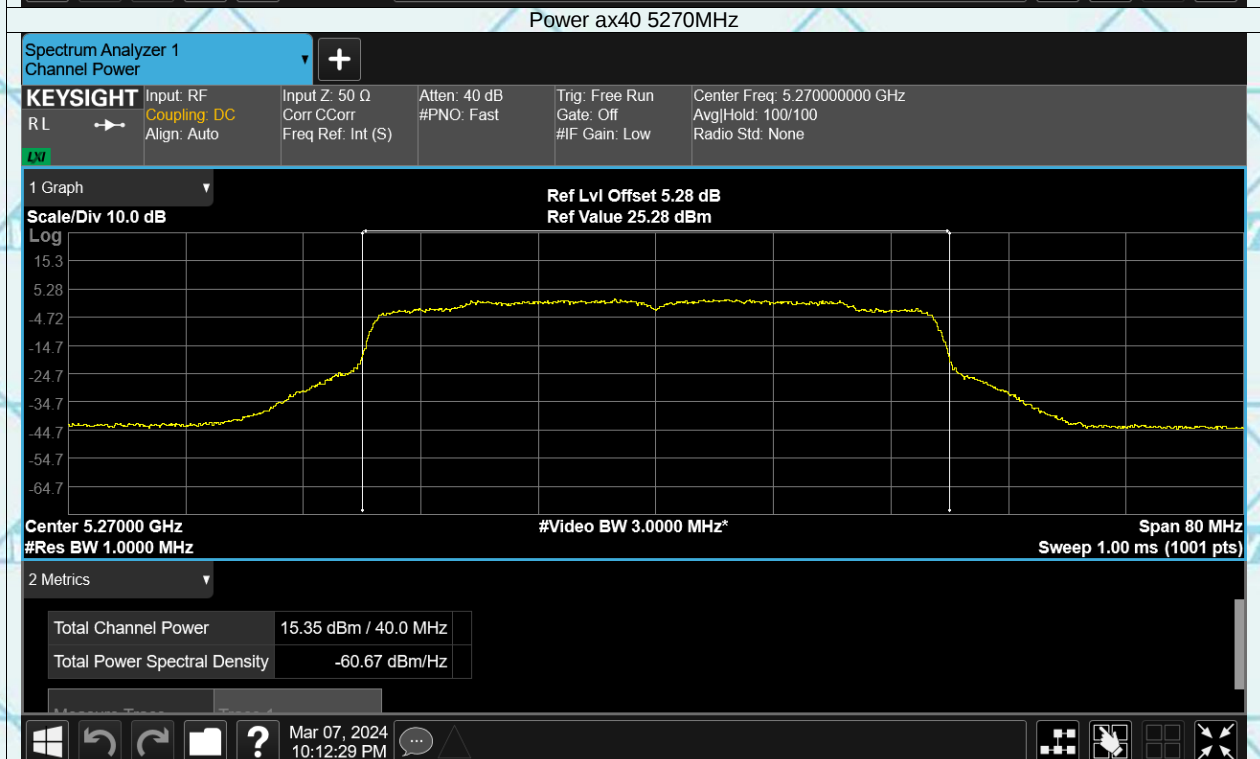
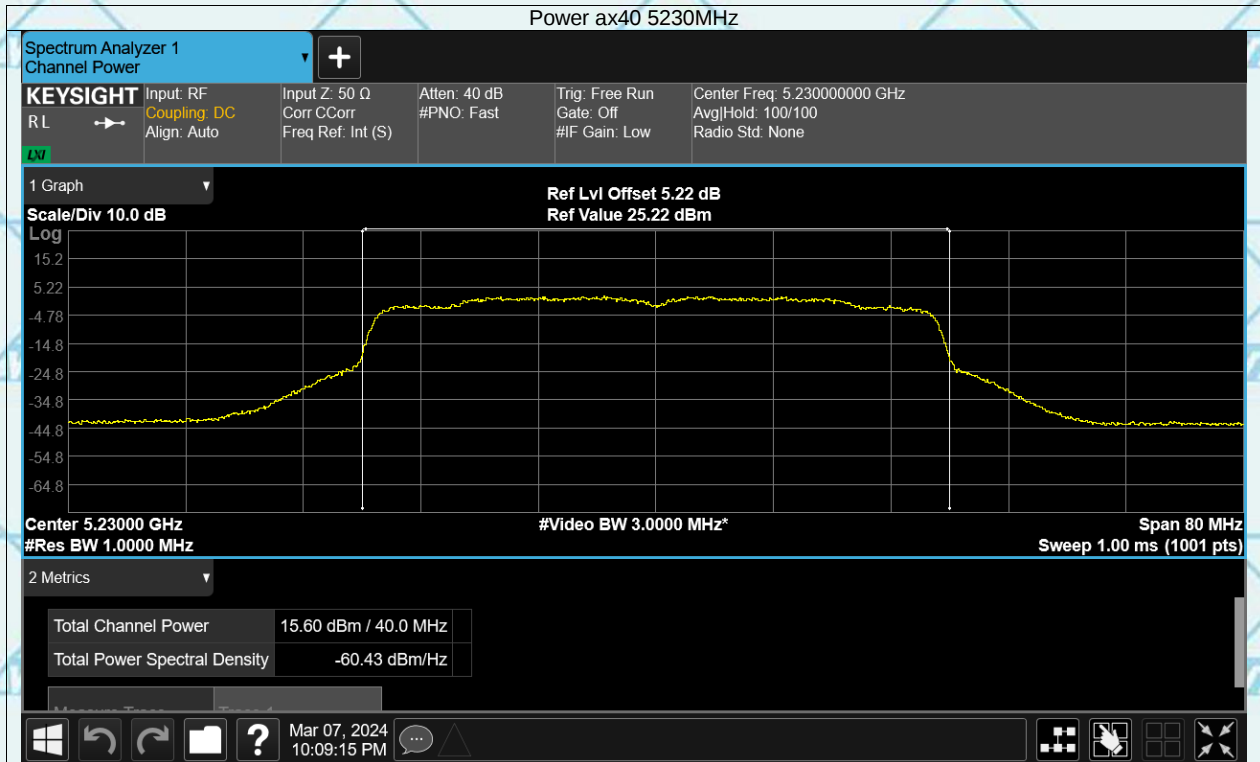




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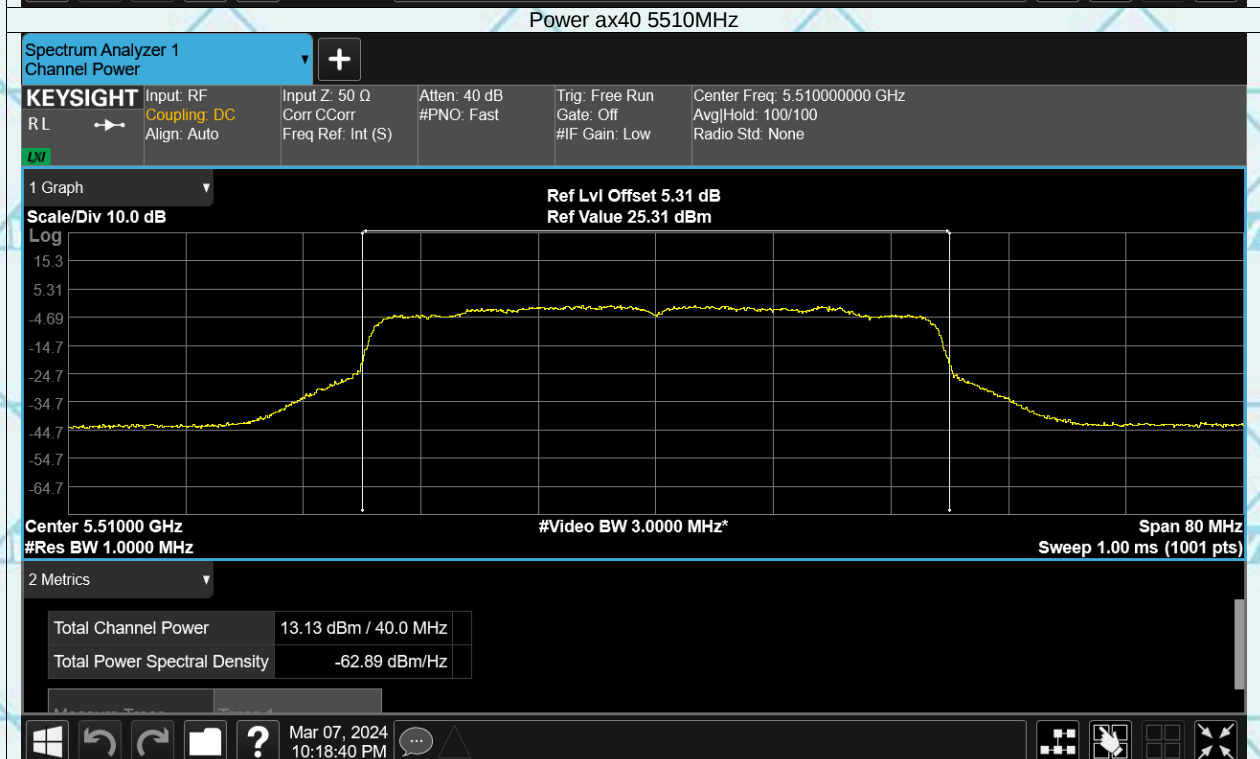
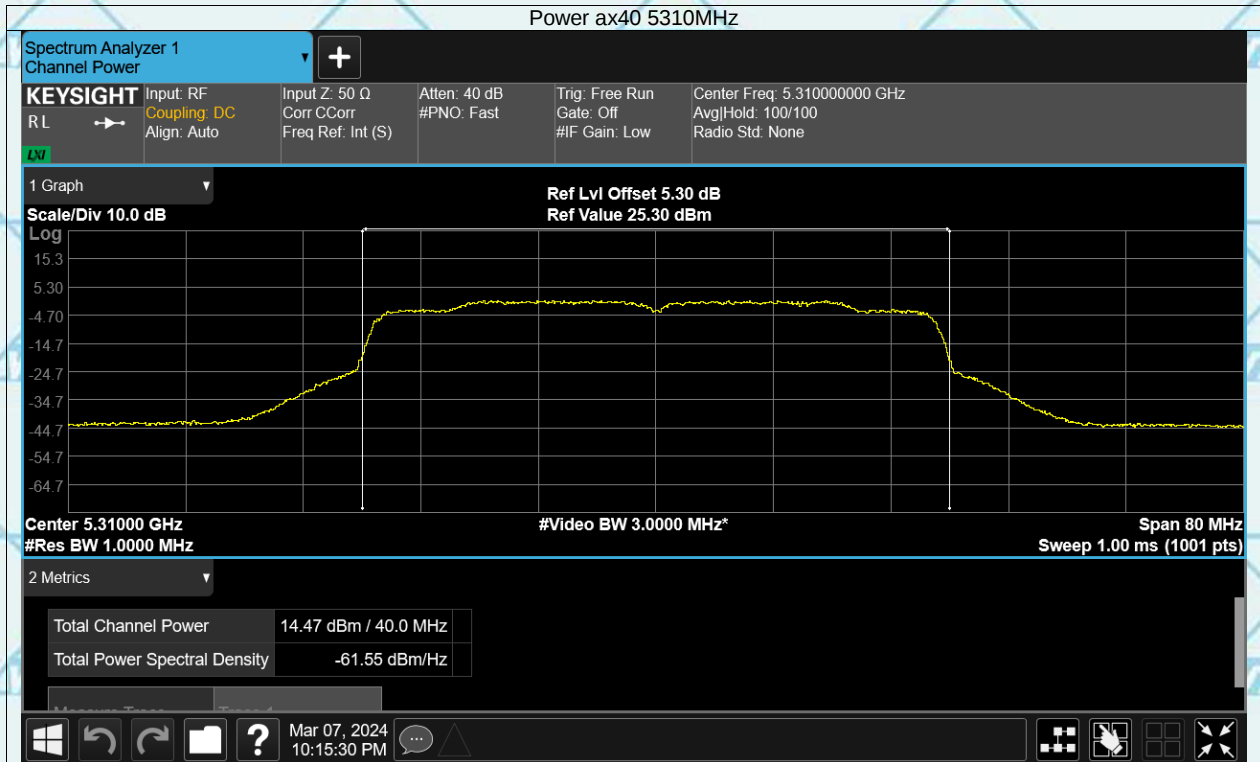
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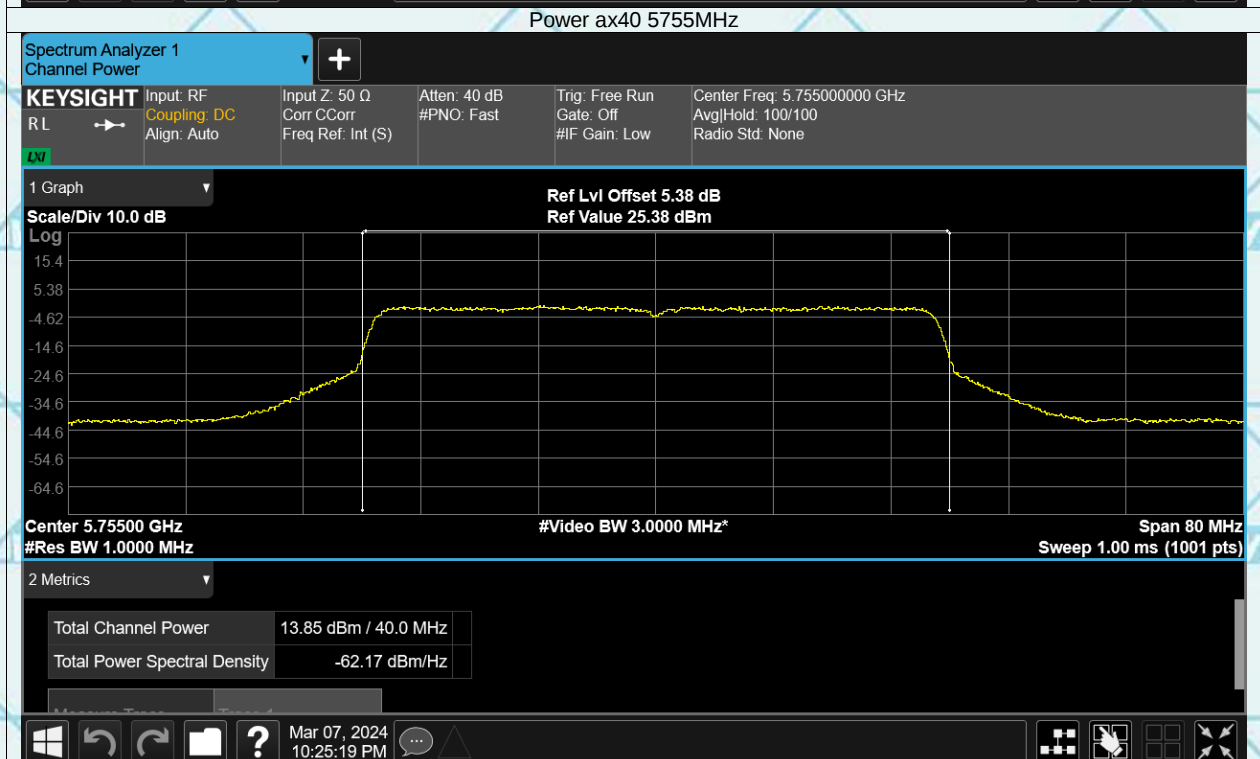
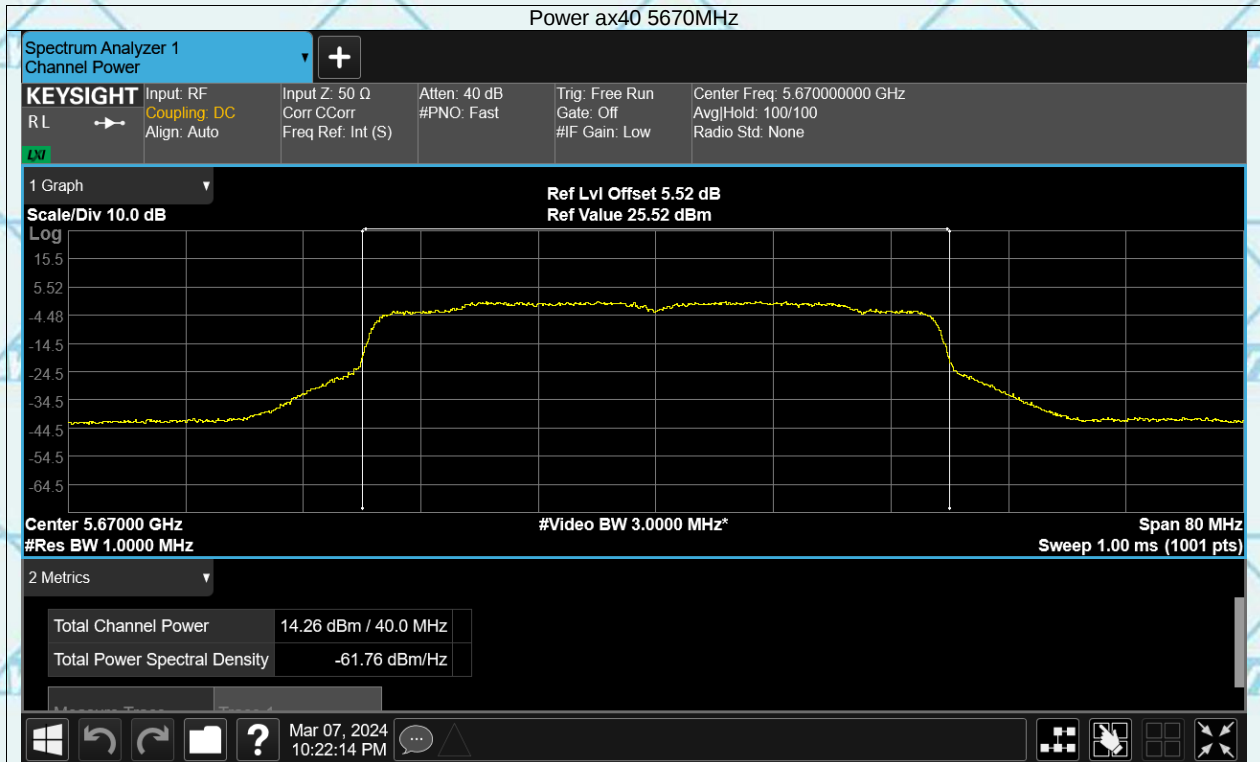
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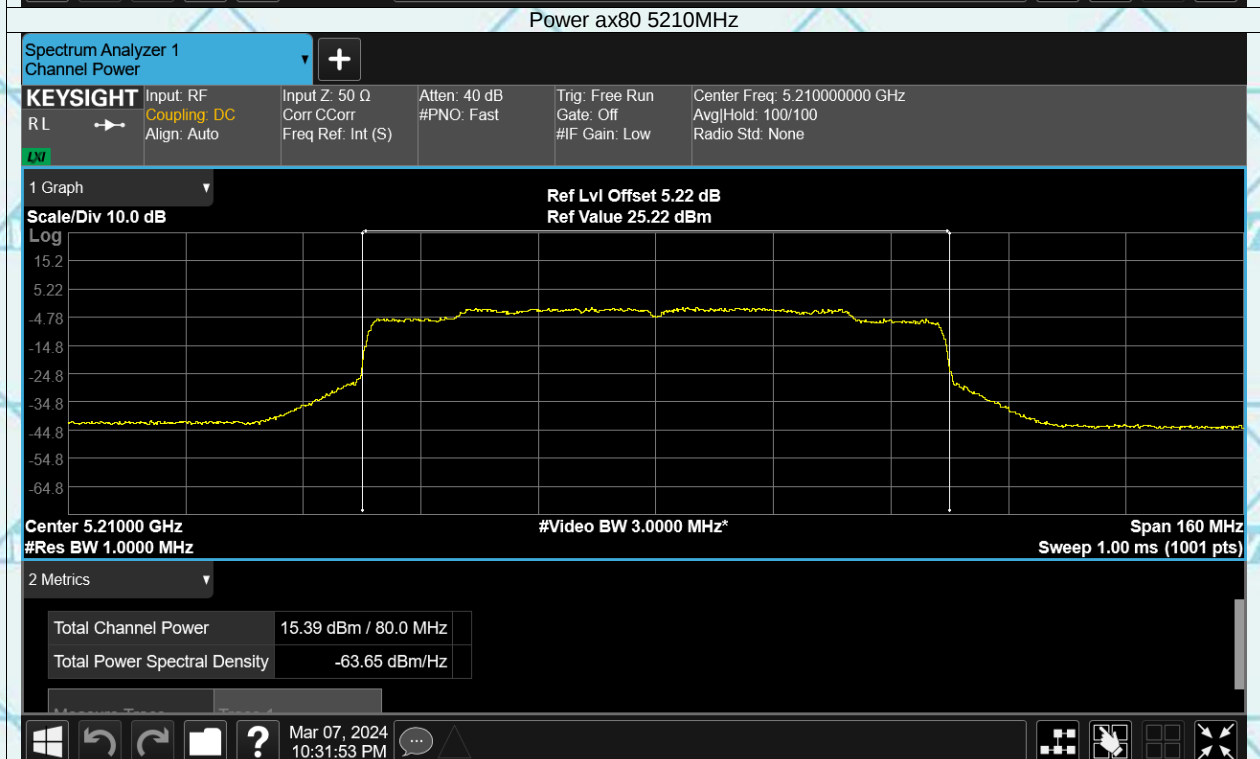
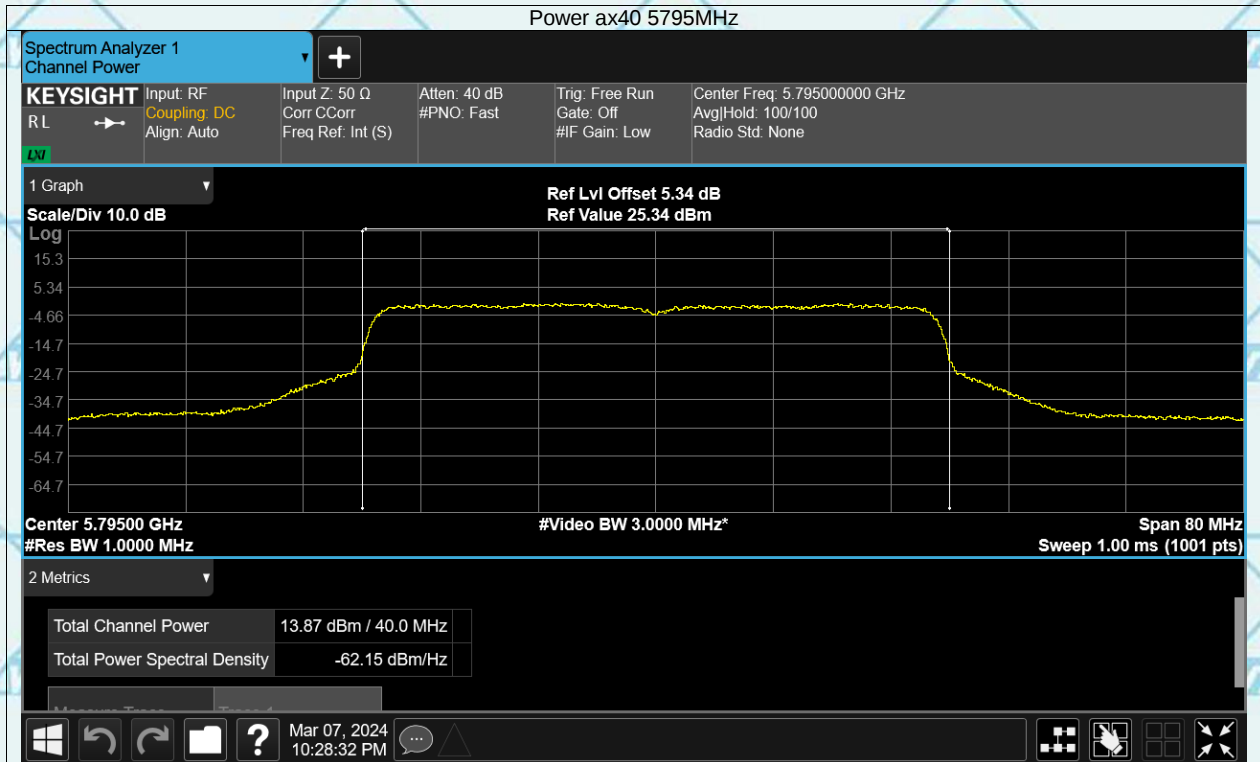
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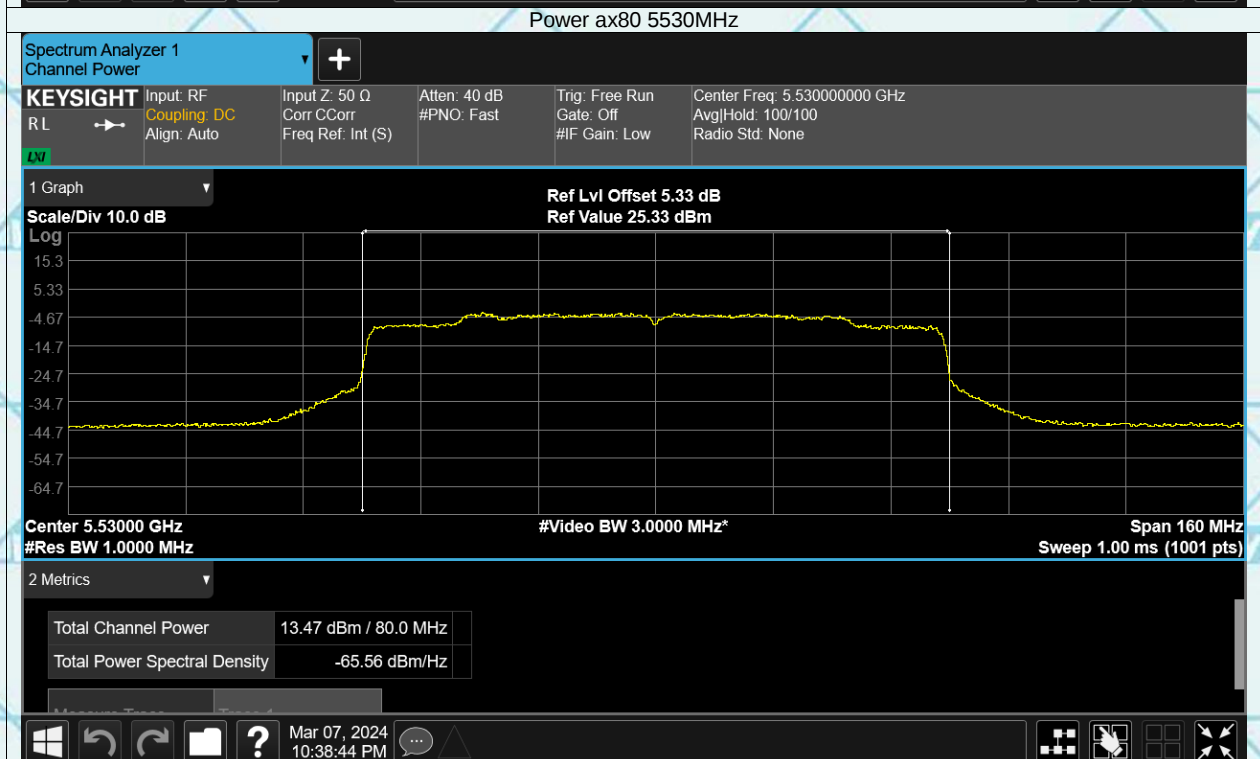
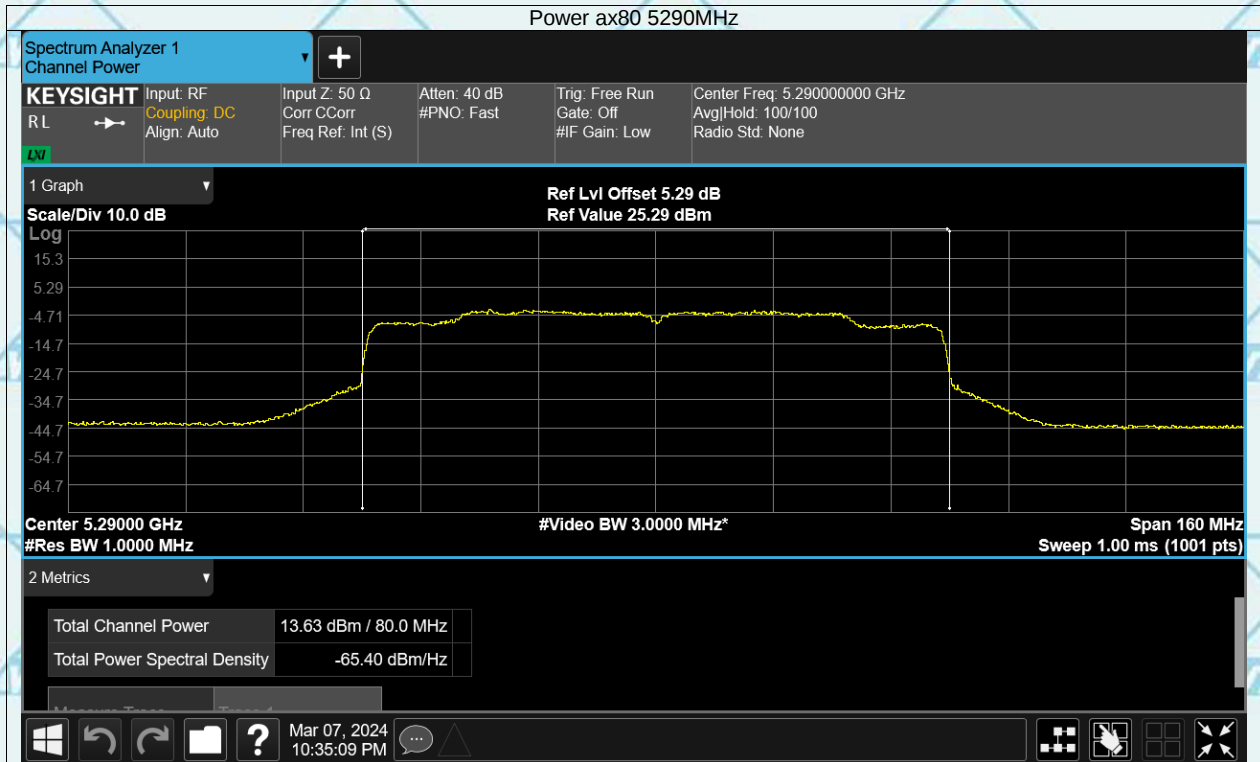
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