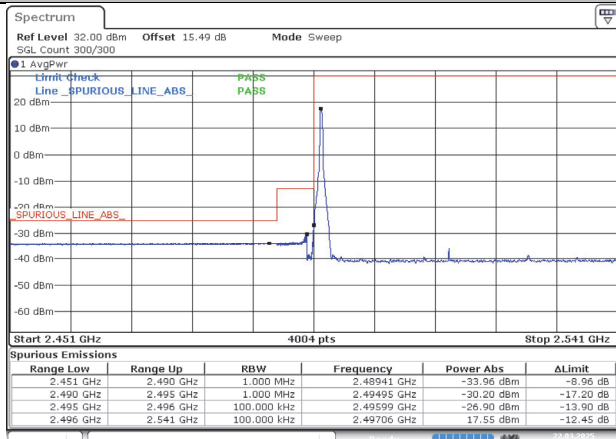


DC_66A_n41A / 30KHz / 40MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



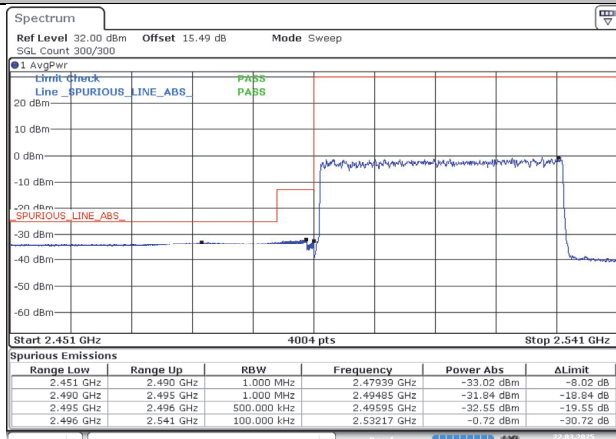
Date: 22 MAR 2025 13:01:42

HCH / DFT-Pi2BPSK_Edge_1RB_Right



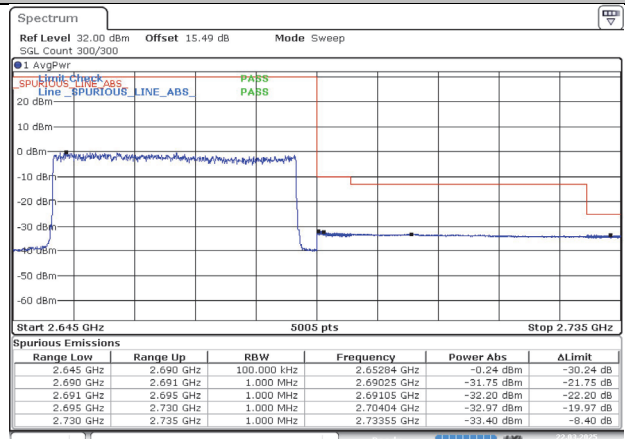
Date: 22 MAR 2025 13:02:50

LCH / DFT-Pi2BPSK / Outer_Full



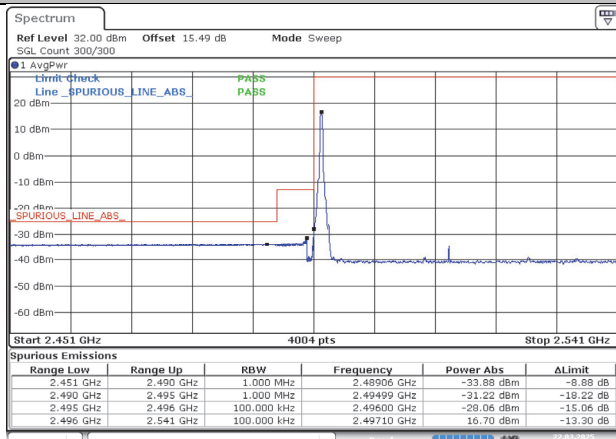
Date: 22 MAR 2025 13:01:55

HCH / DFT-Pi2BPSK / Outer_Full



Date: 22 MAR 2025 13:03:05

LCH / DFT-QPSK_Edge_1RB_Left

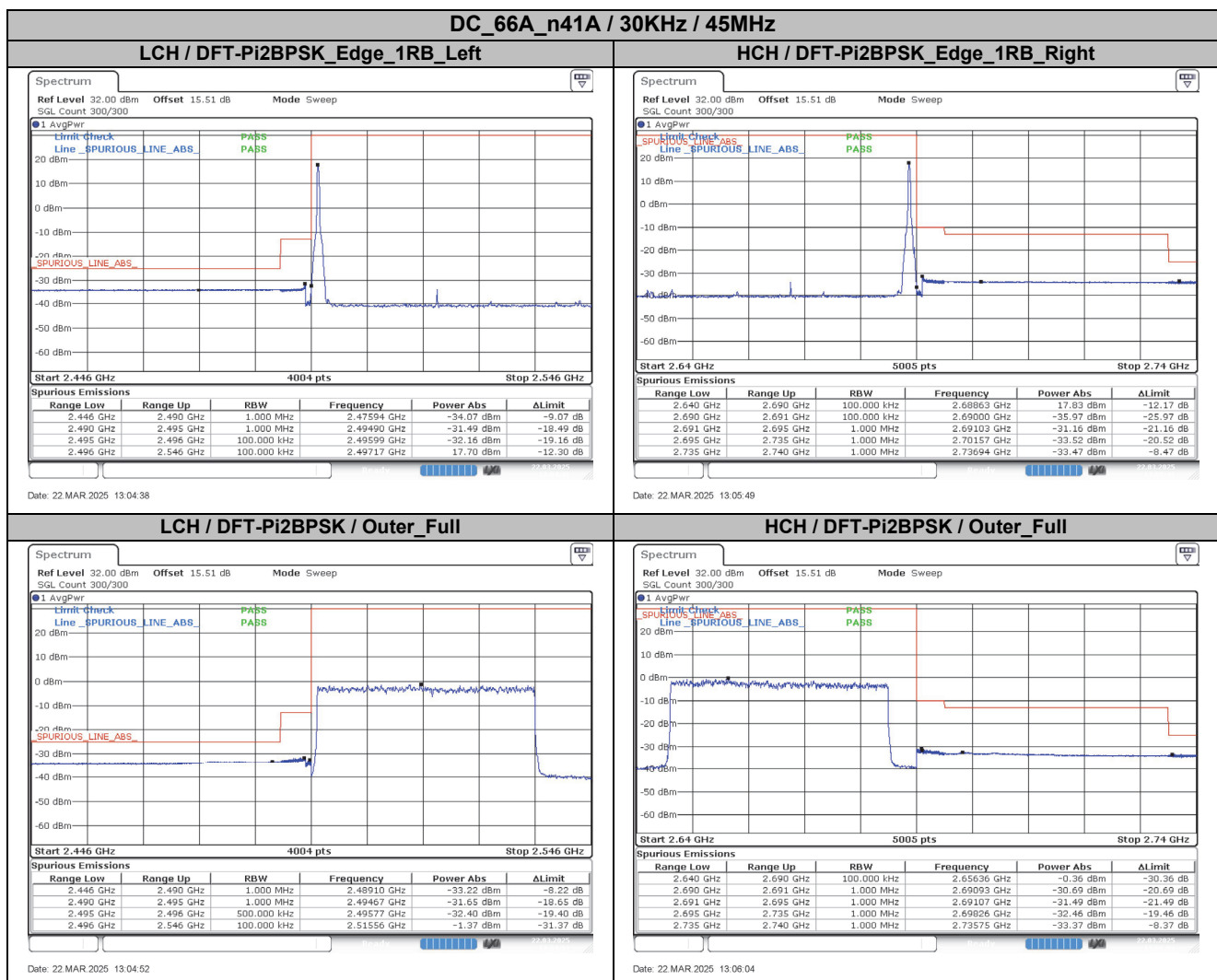
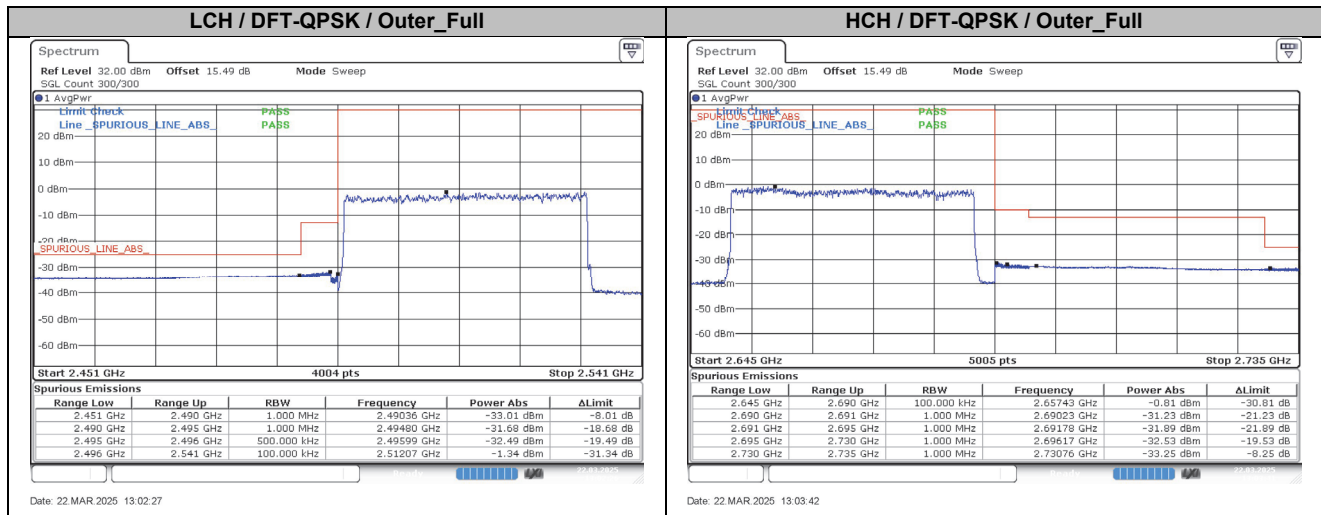


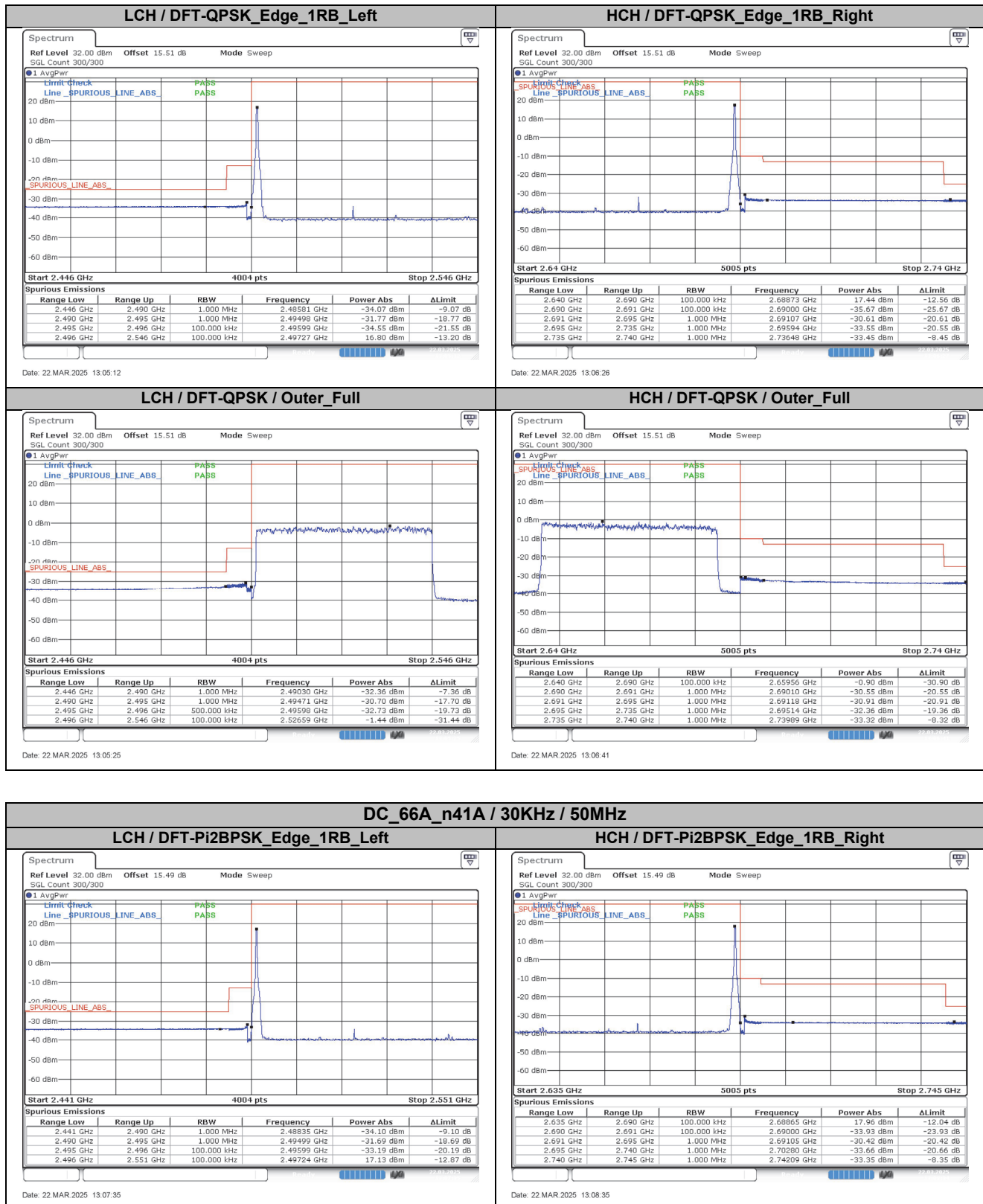
Date: 22 MAR 2025 13:02:14

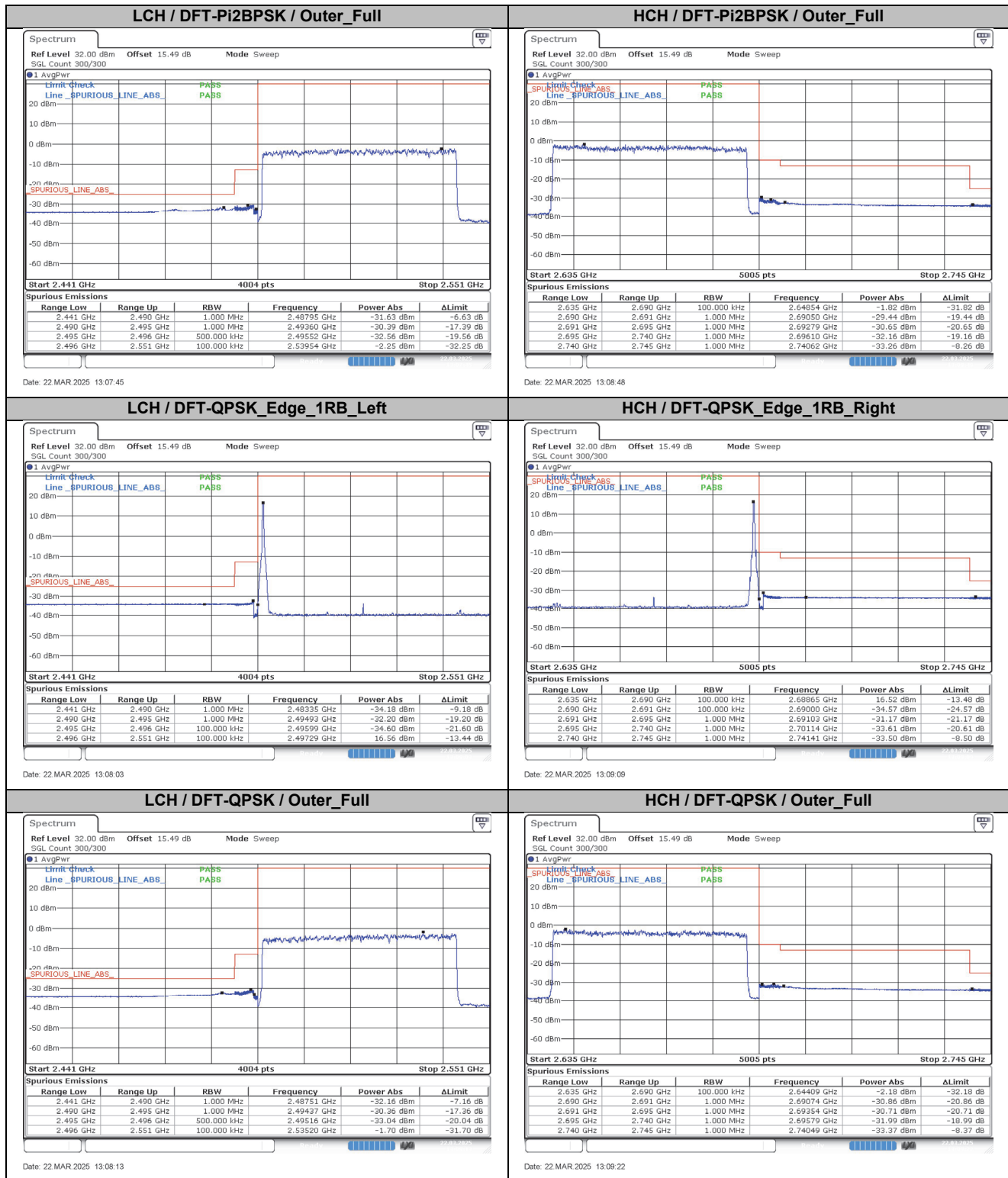
HCH / DFT-QPSK_Edge_1RB_Right



Date: 22 MAR 2025 13:03:27

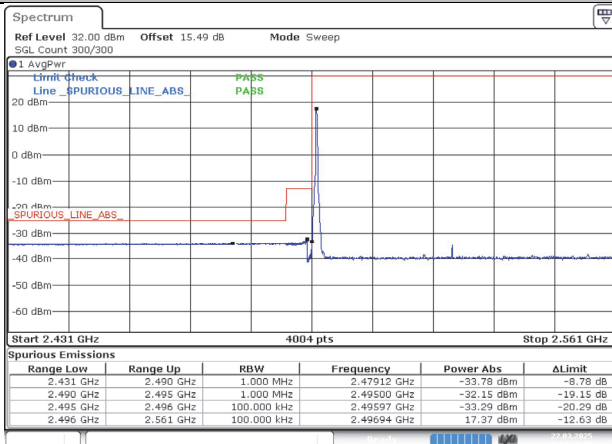






DC_66A_n41A / 30KHz / 60MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



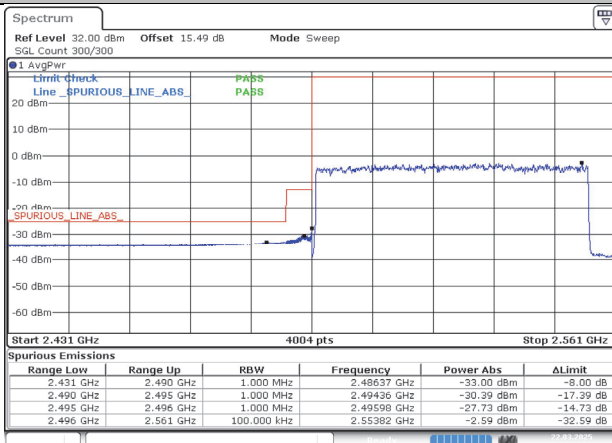
Date: 22 MAR 2025 13:10:17

HCH / DFT-Pi2BPSK_Edge_1RB_Right



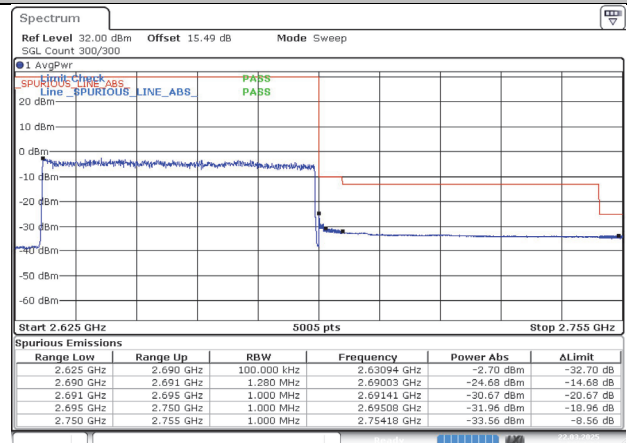
Date: 22 MAR 2025 13:11:20

LCH / DFT-Pi2BPSK / Outer_Full



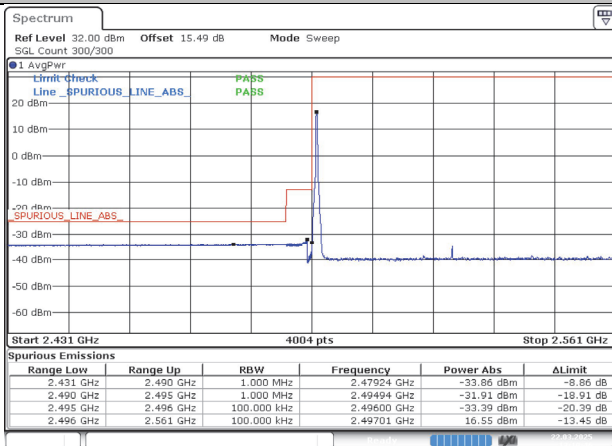
Date: 22 MAR 2025 13:10:28

HCH / DFT-Pi2BPSK / Outer_Full



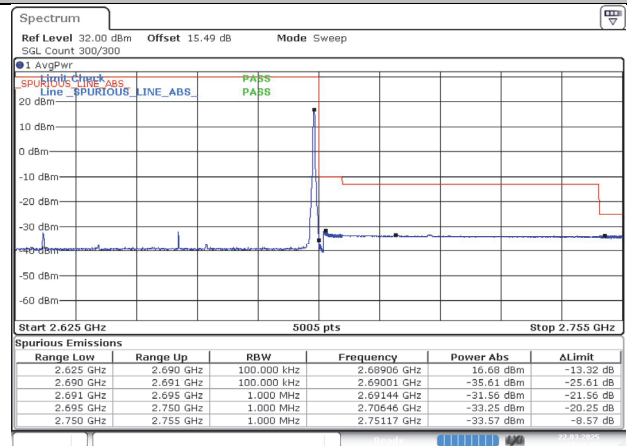
Date: 22 MAR 2025 13:11:34

LCH / DFT-QPSK_Edge_1RB_Left



Date: 22 MAR 2025 13:10:46

HCH / DFT-QPSK_Edge_1RB_Right



Date: 22 MAR 2025 13:11:56