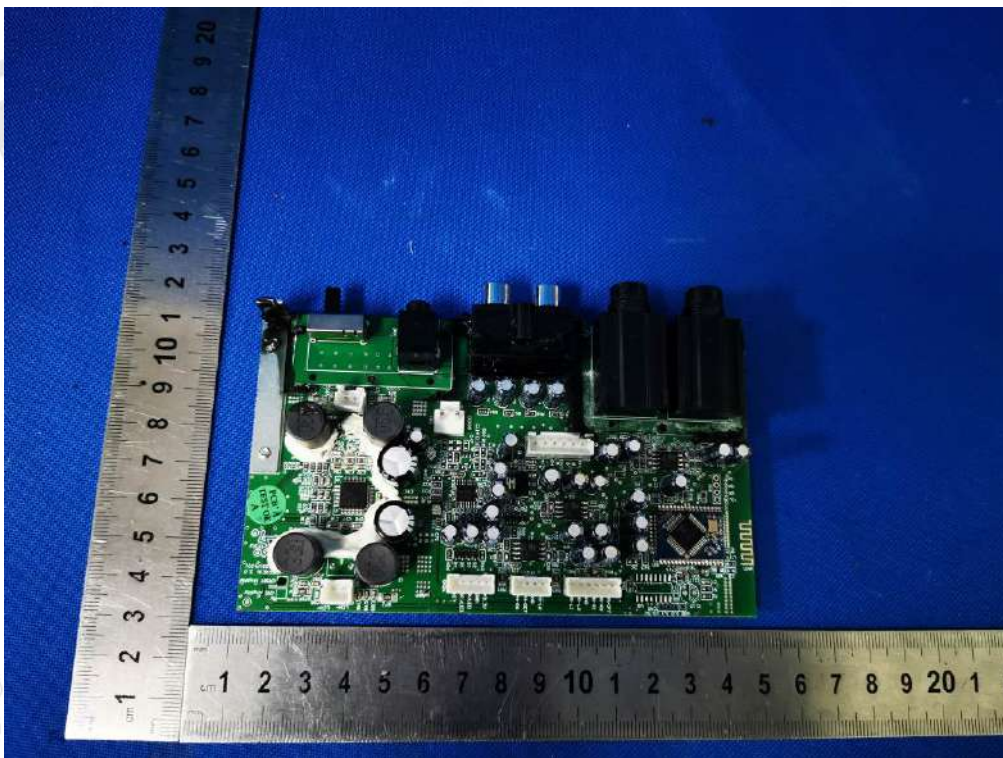
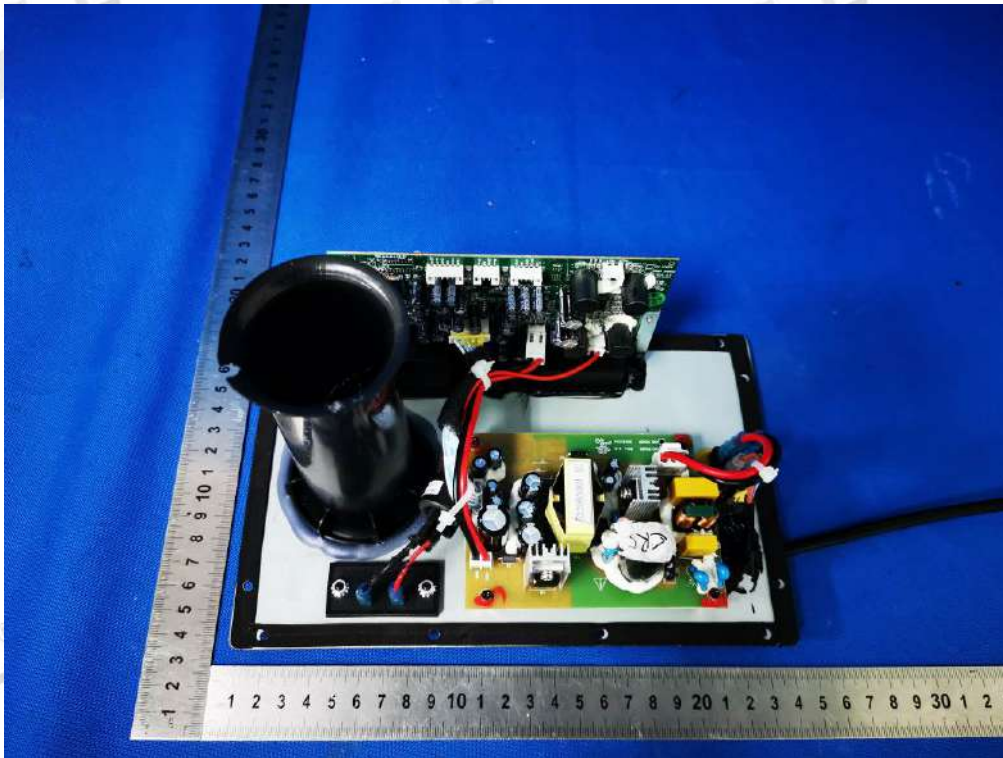
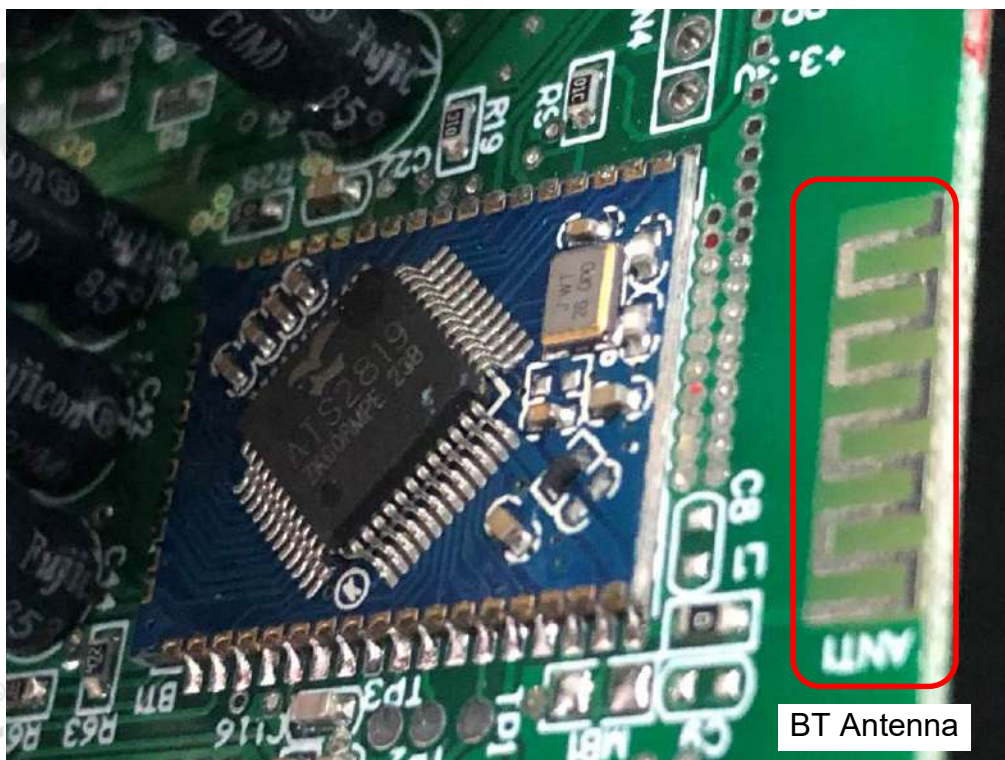
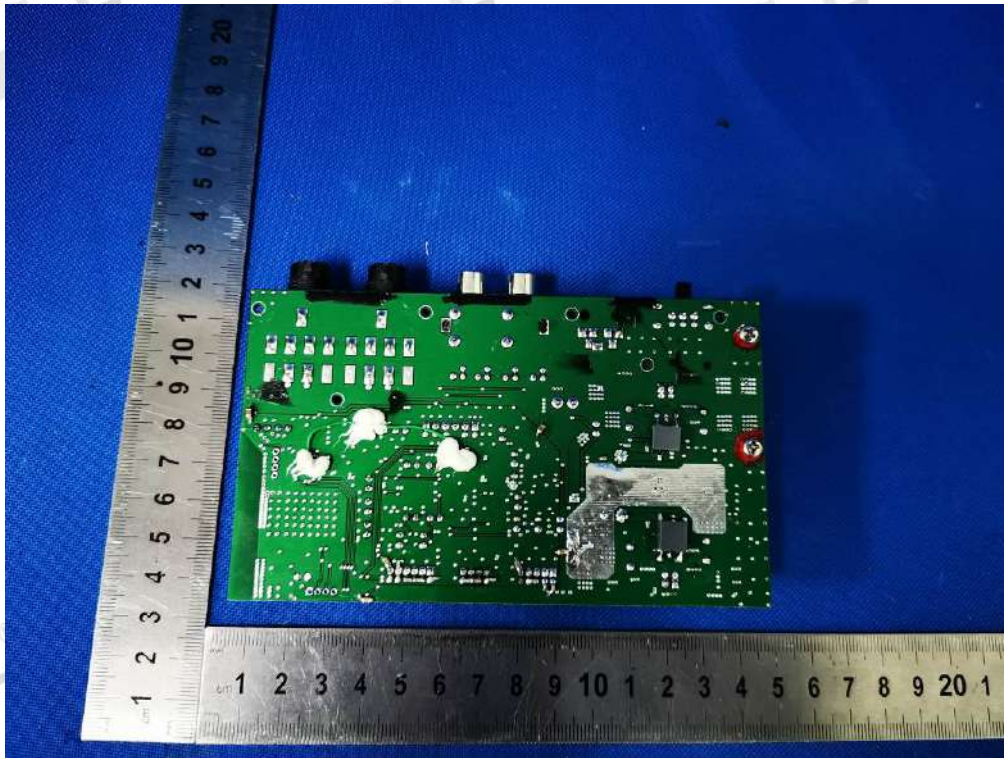
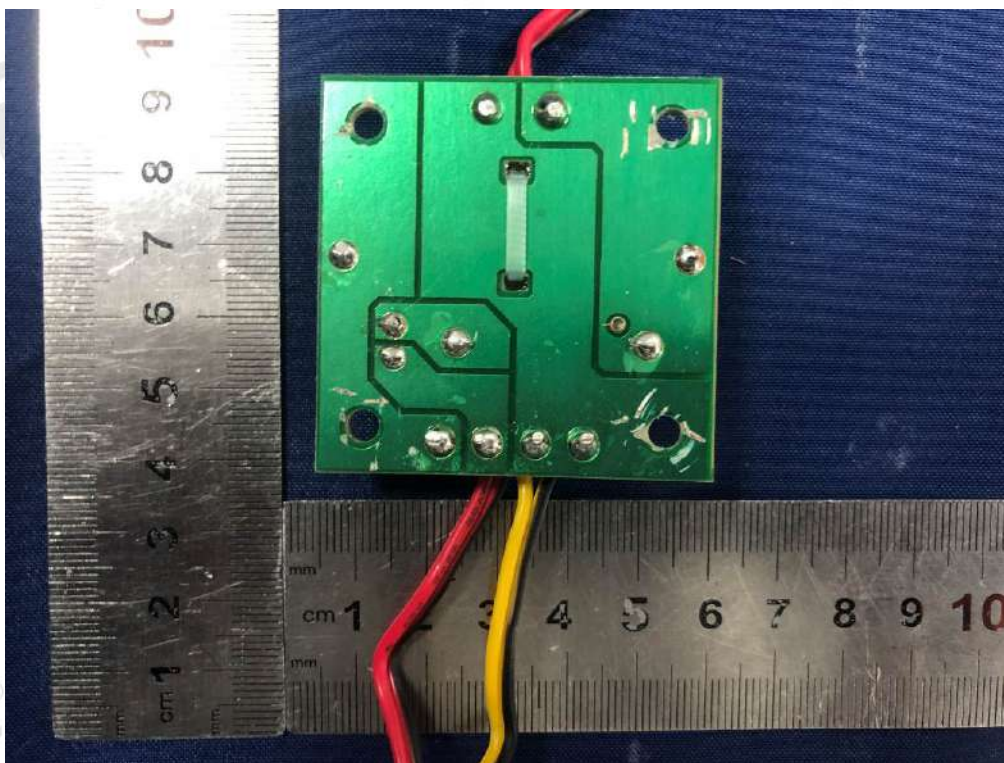
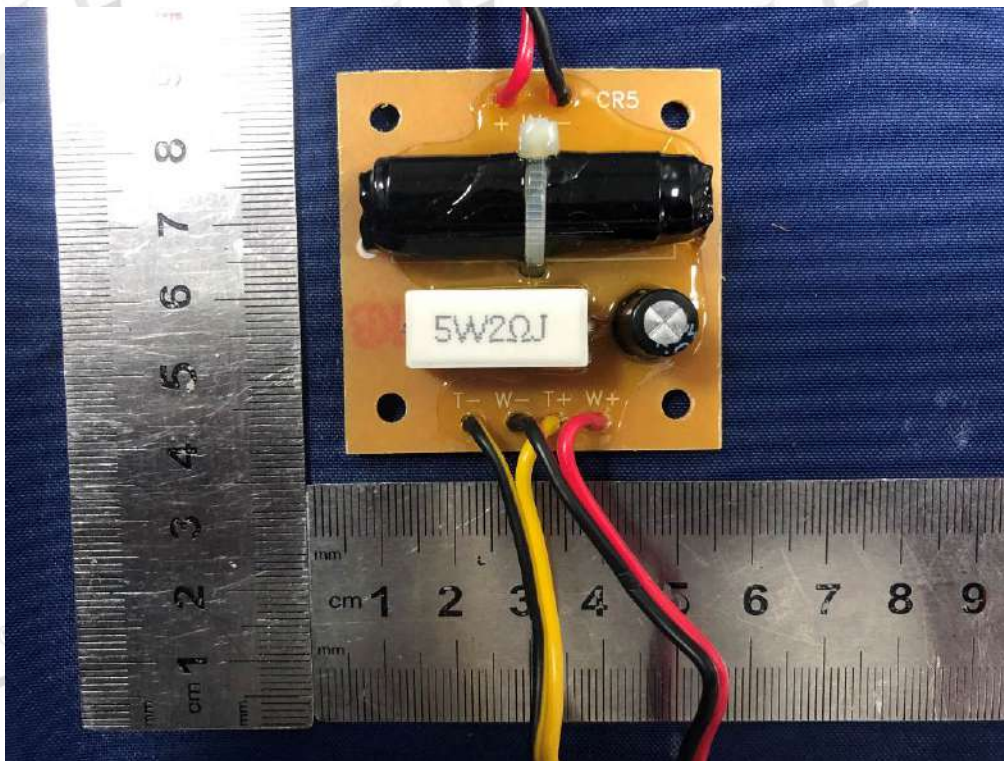
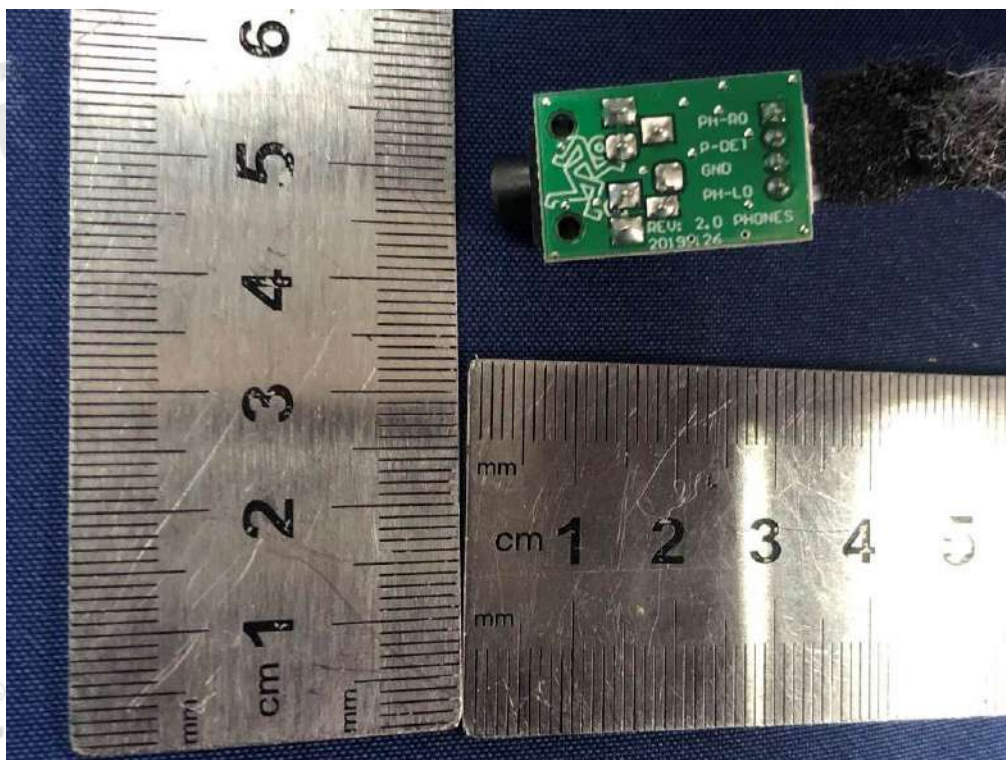
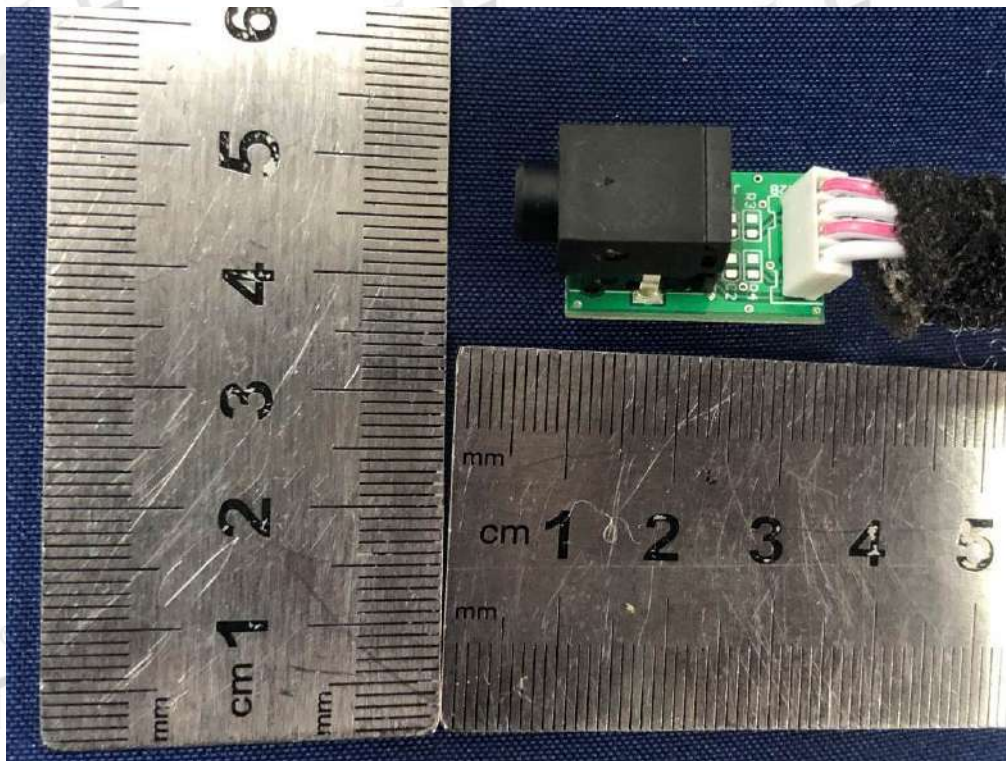


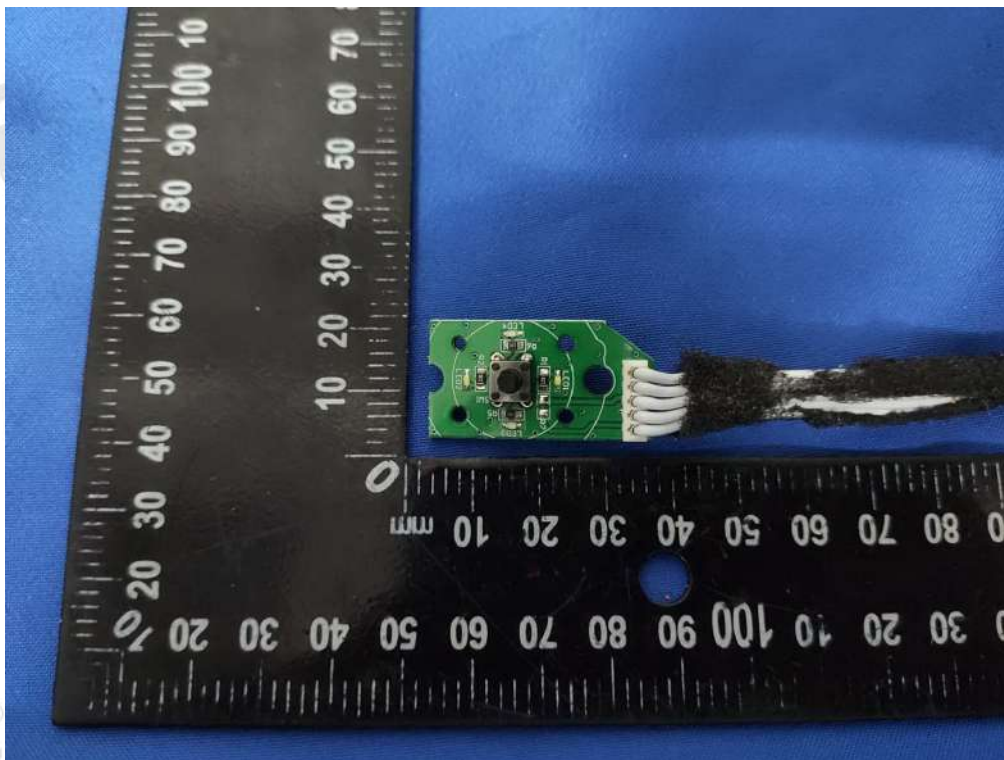
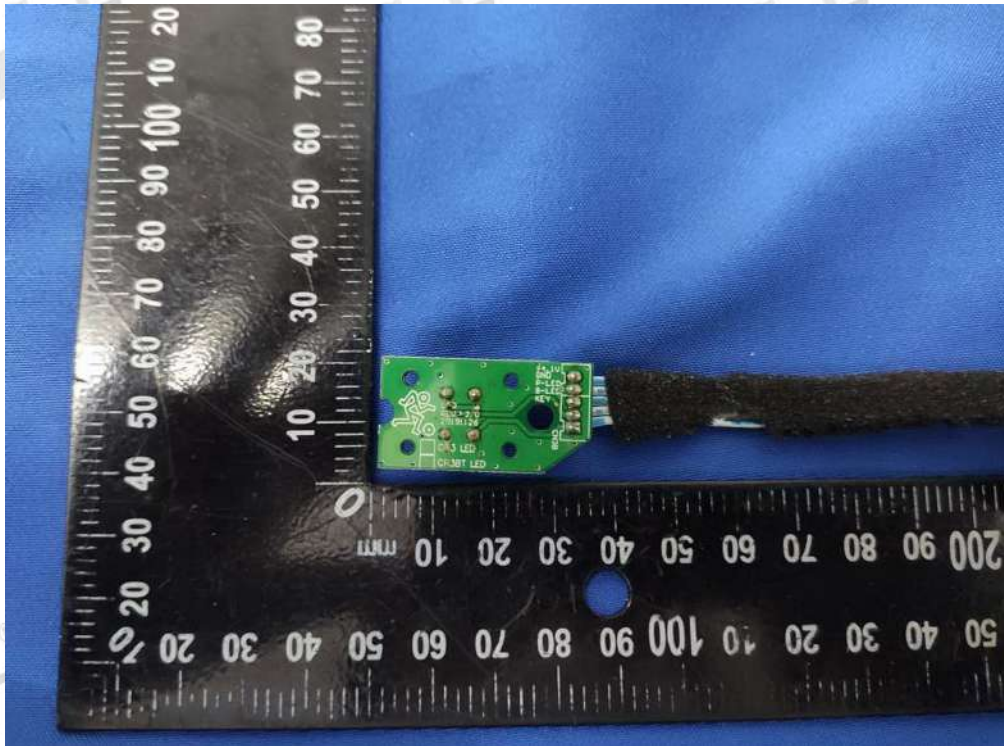


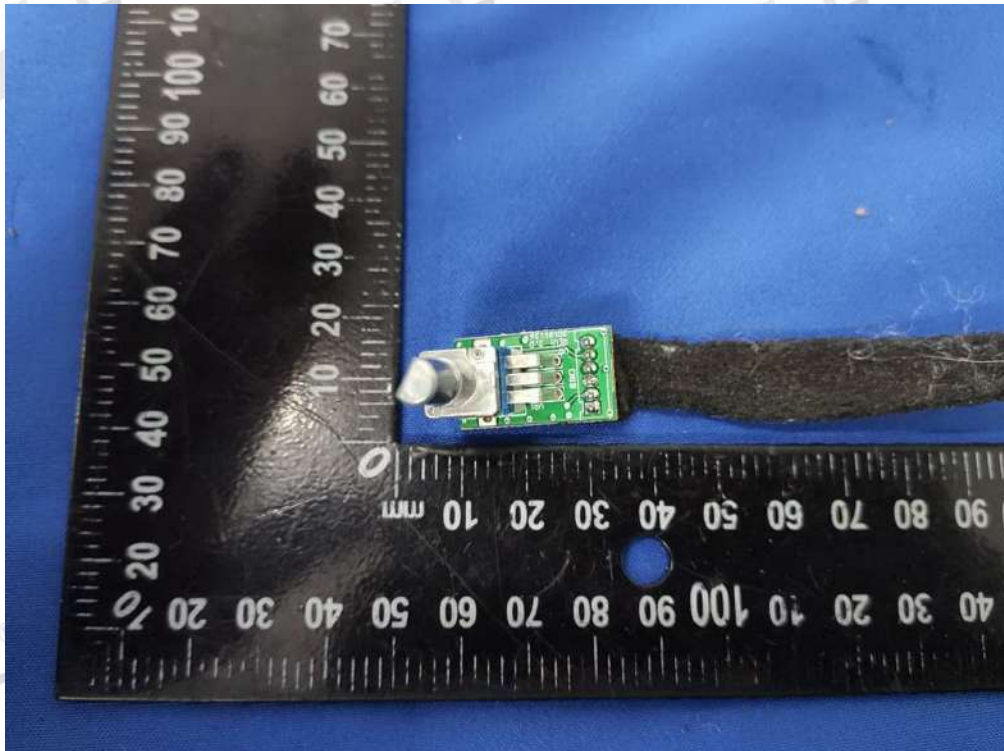


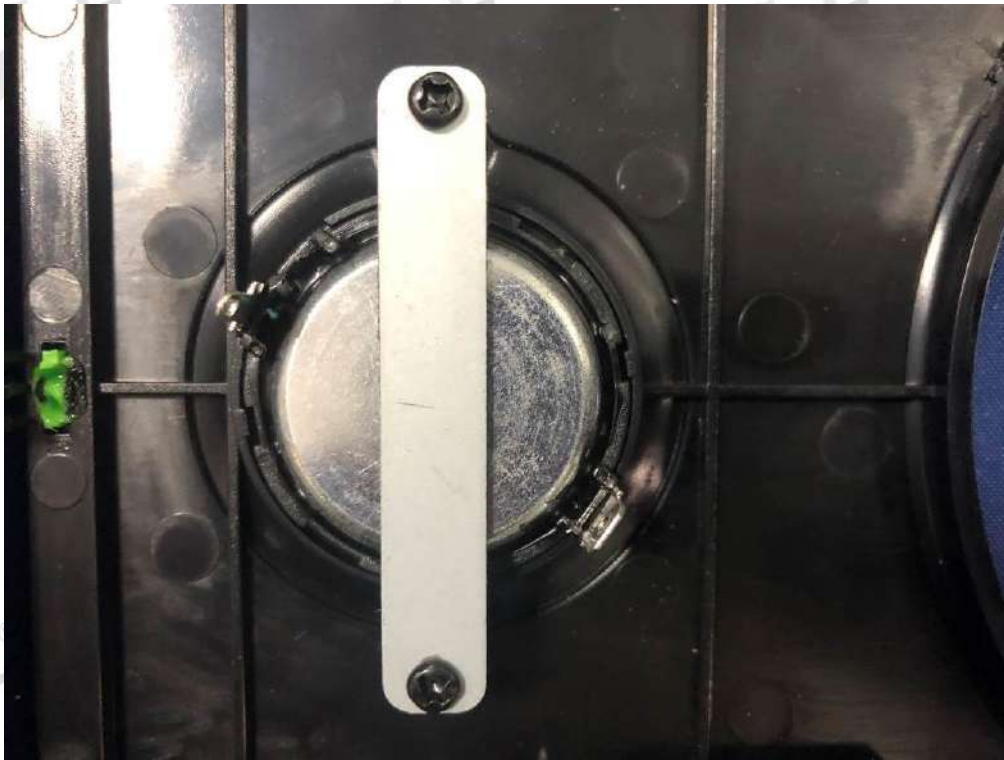


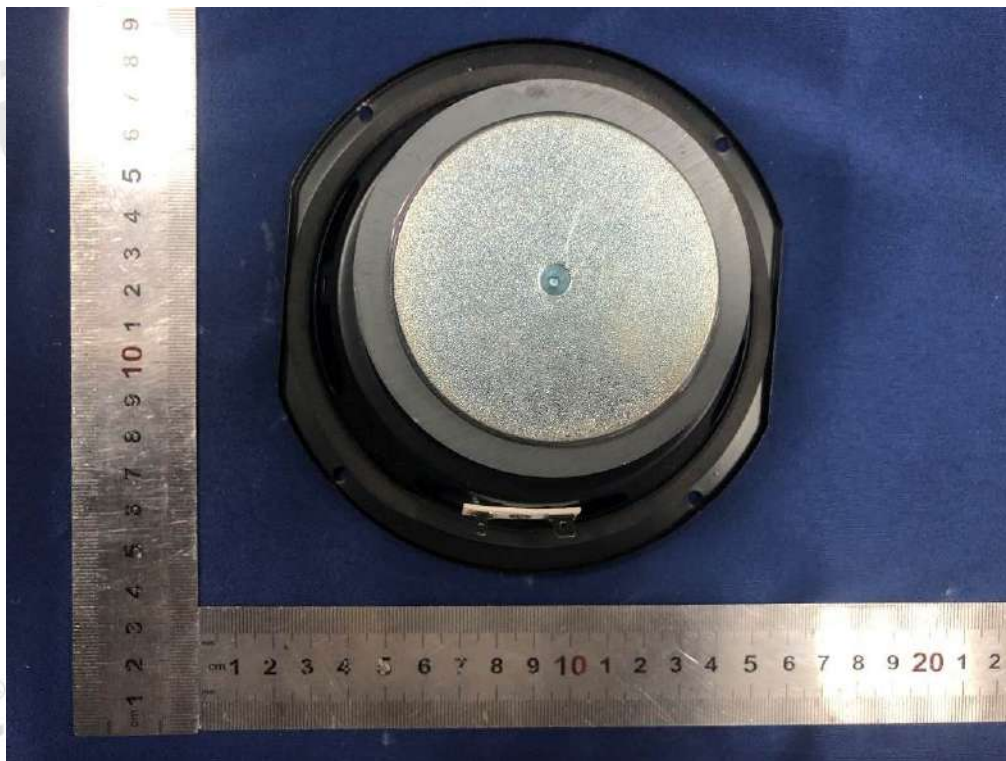






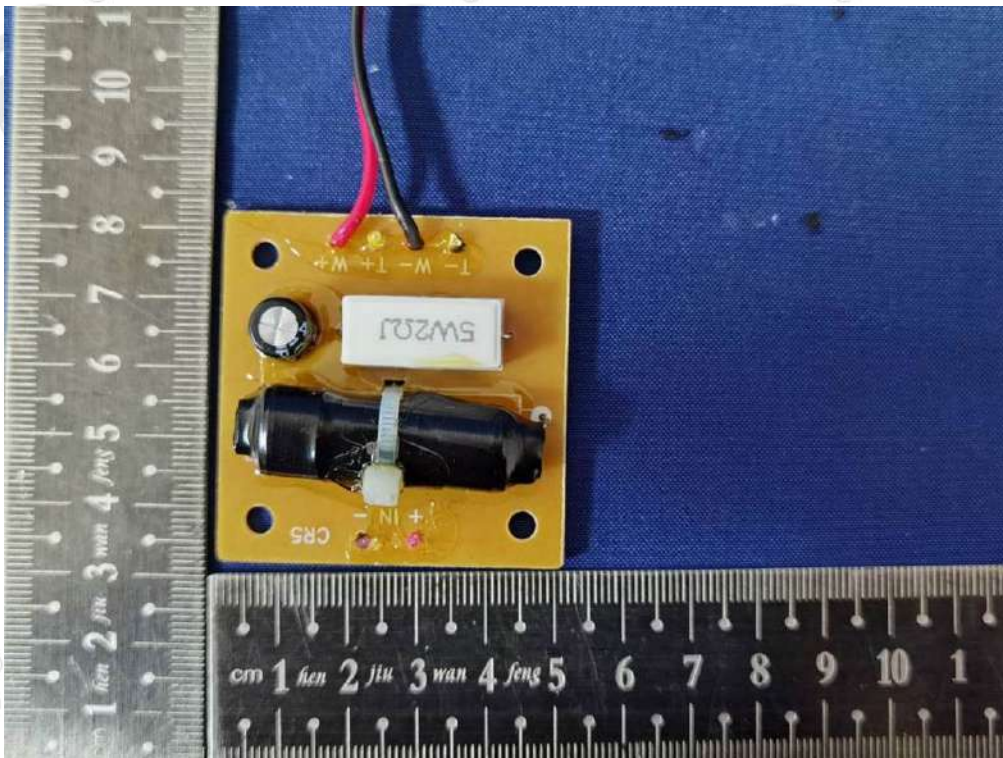
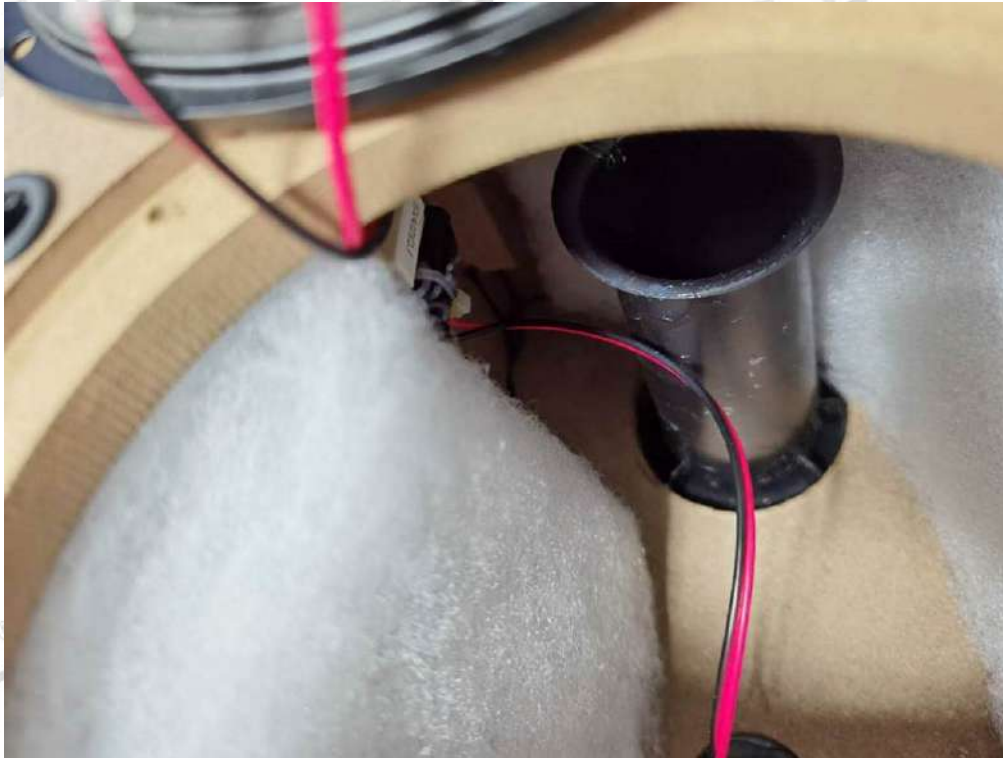


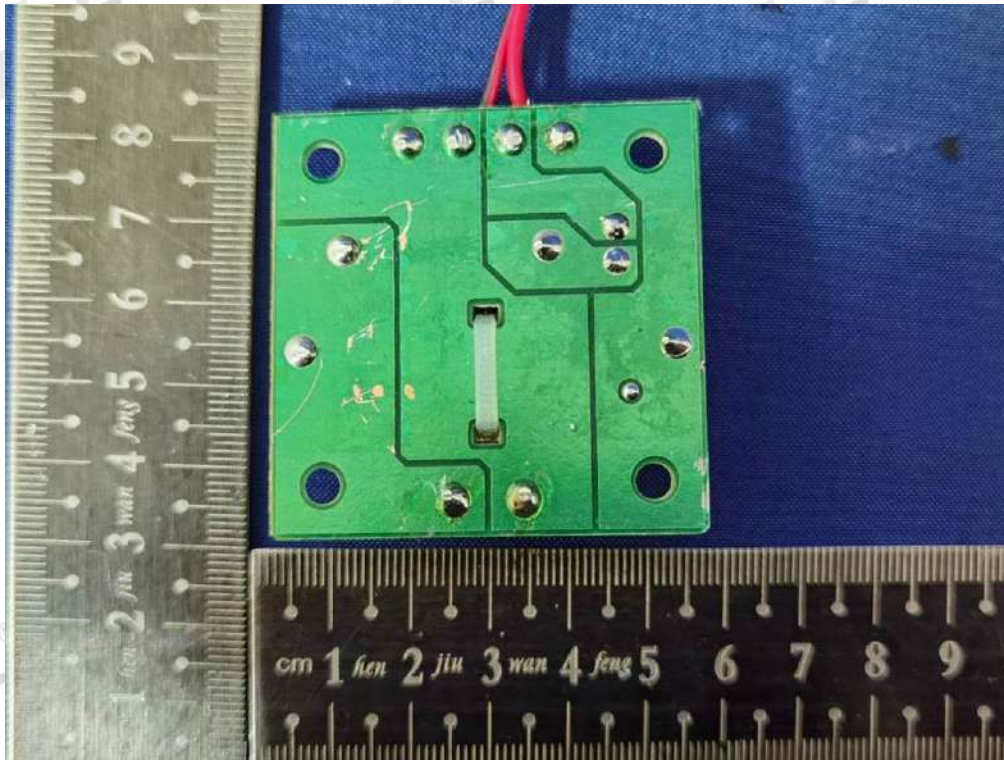


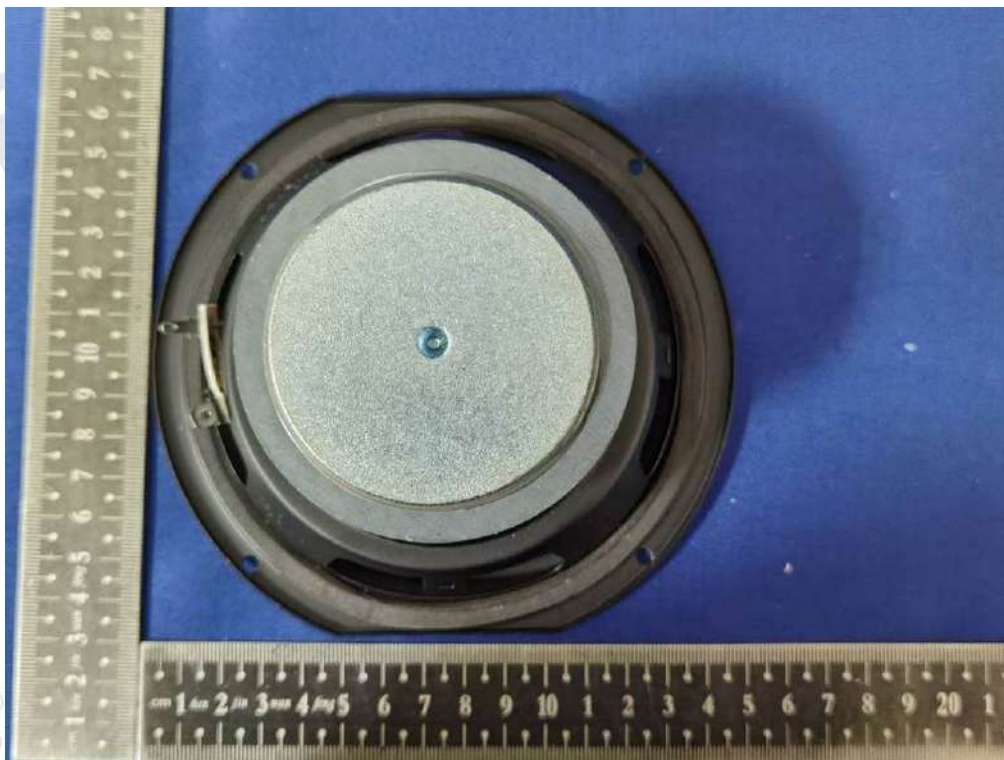


New PCB:



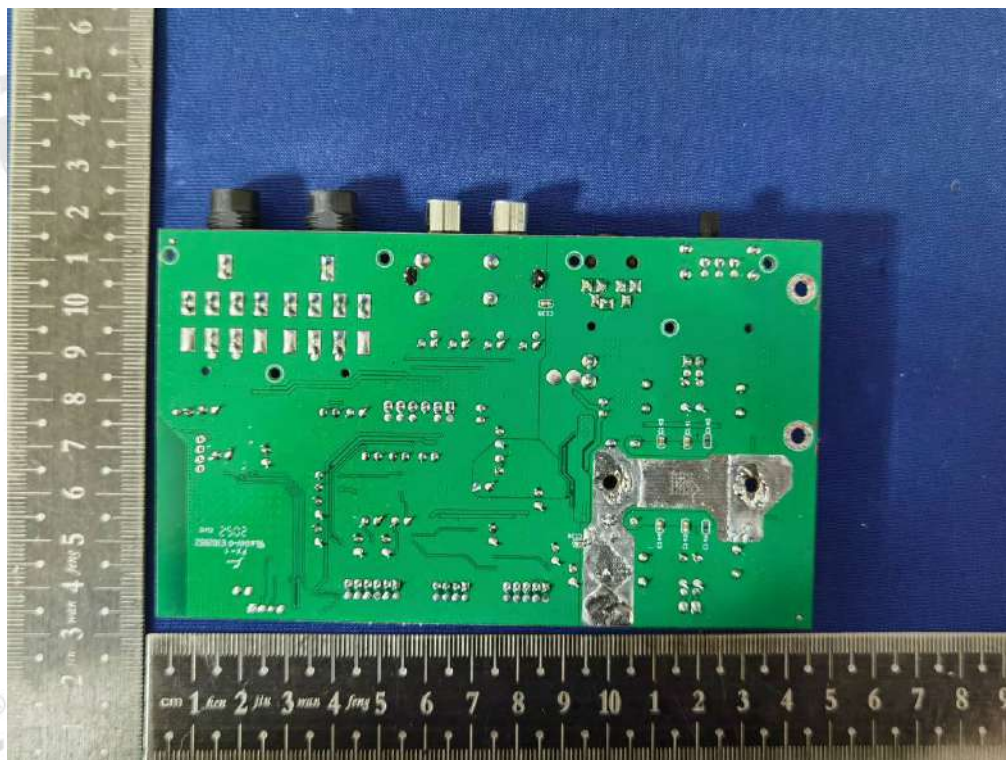
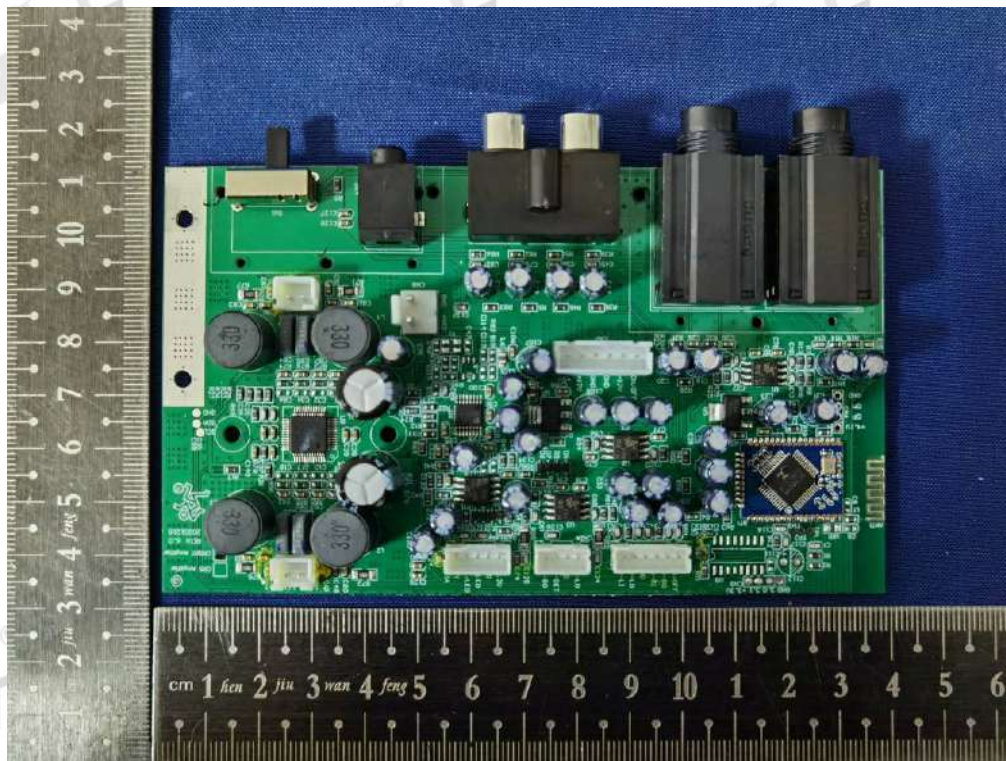


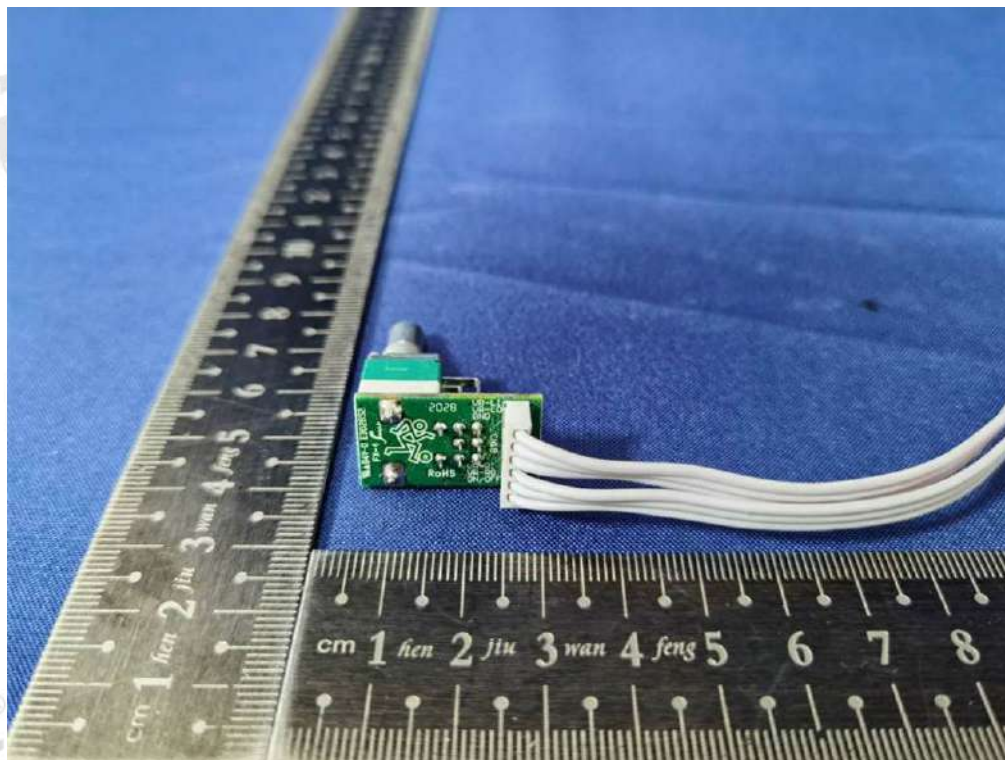
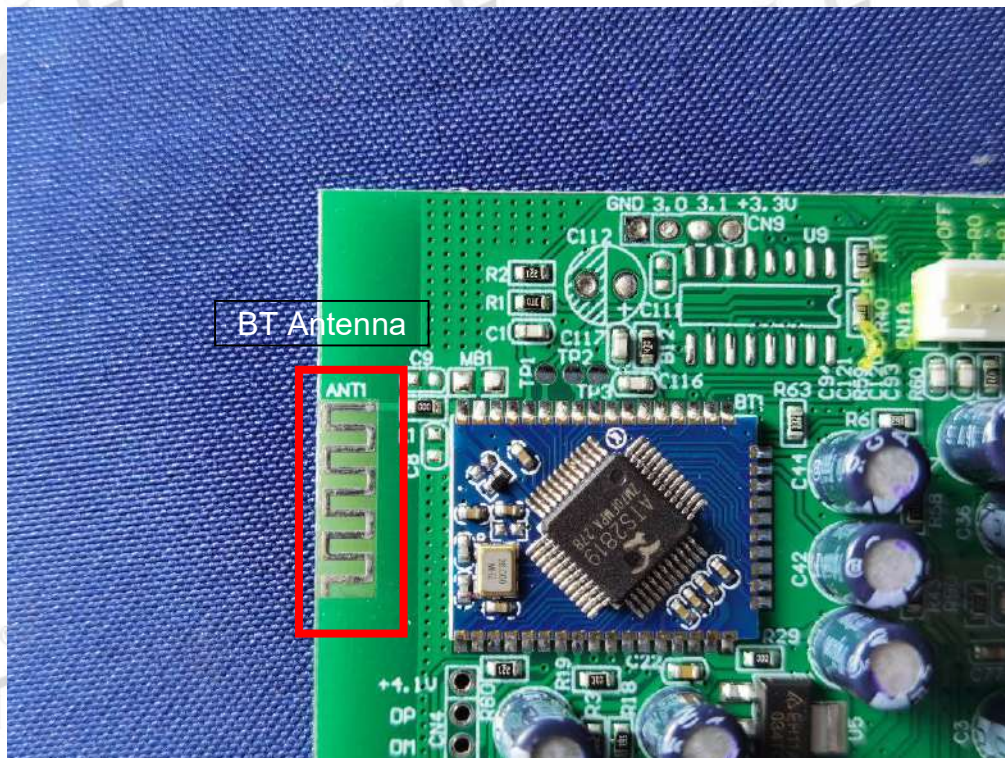




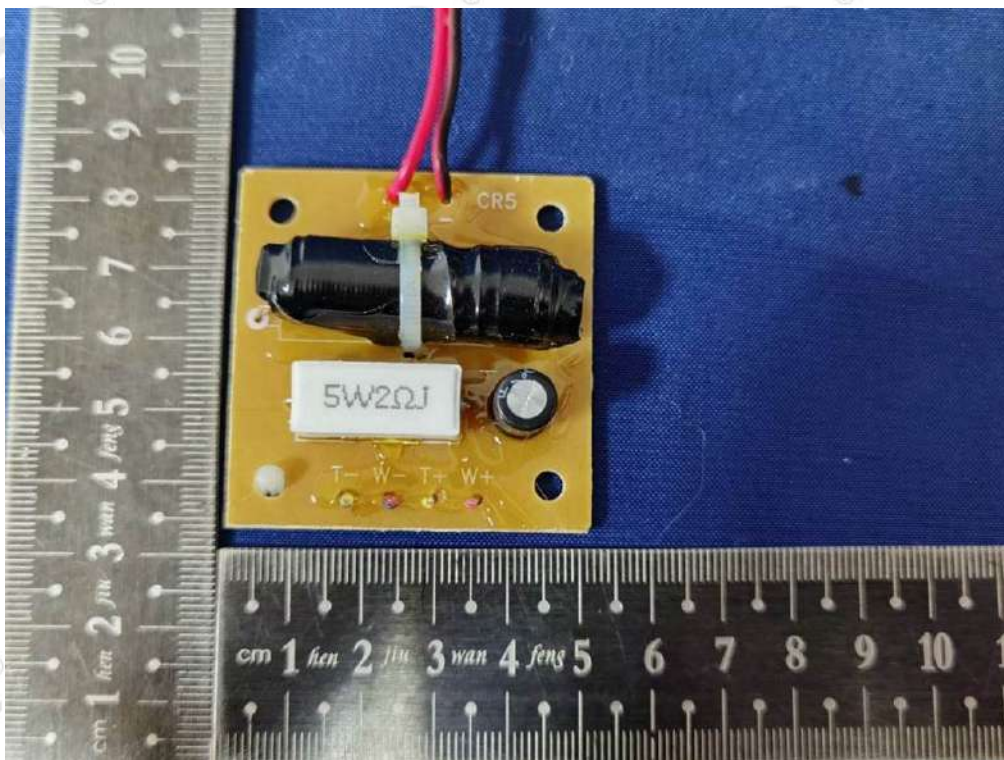
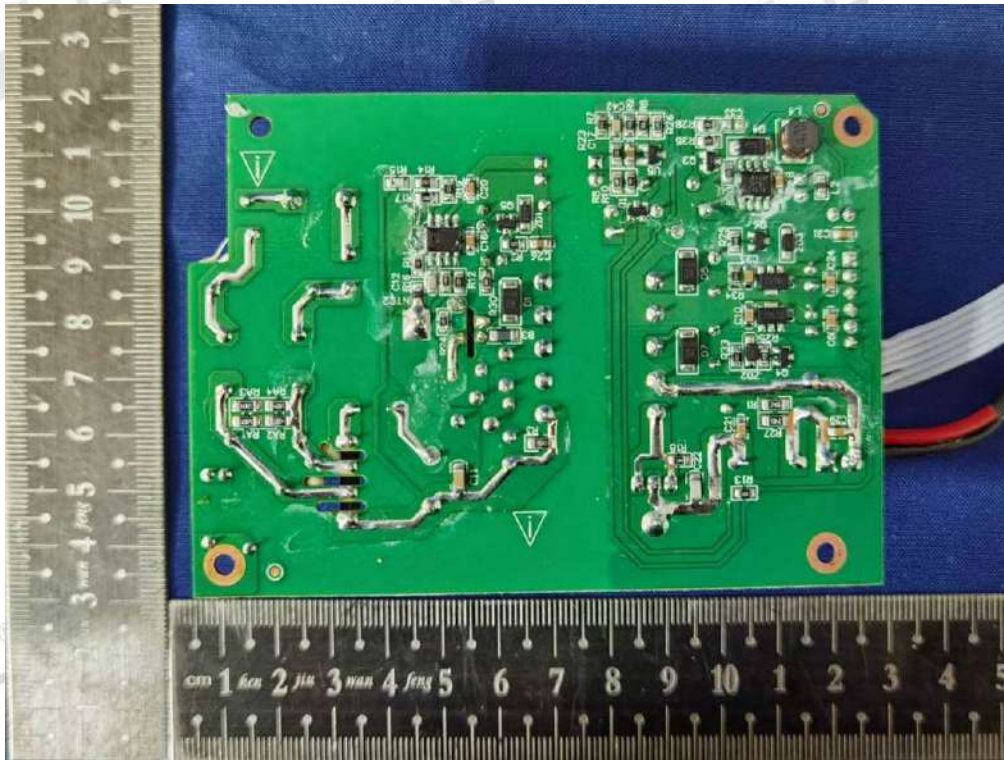


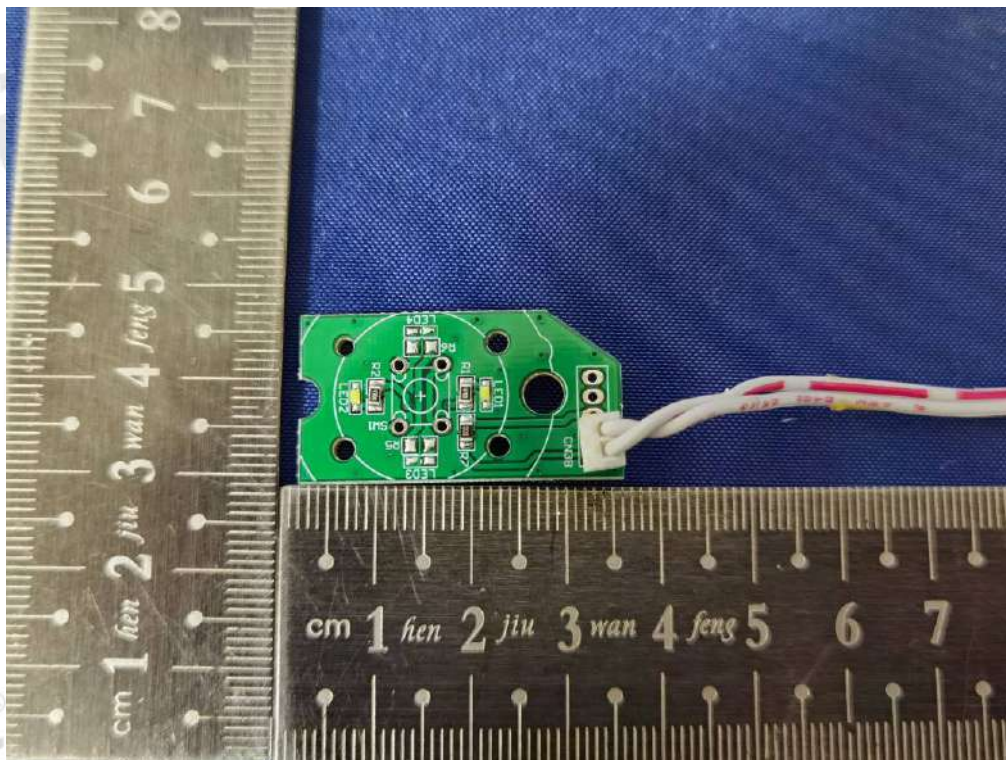
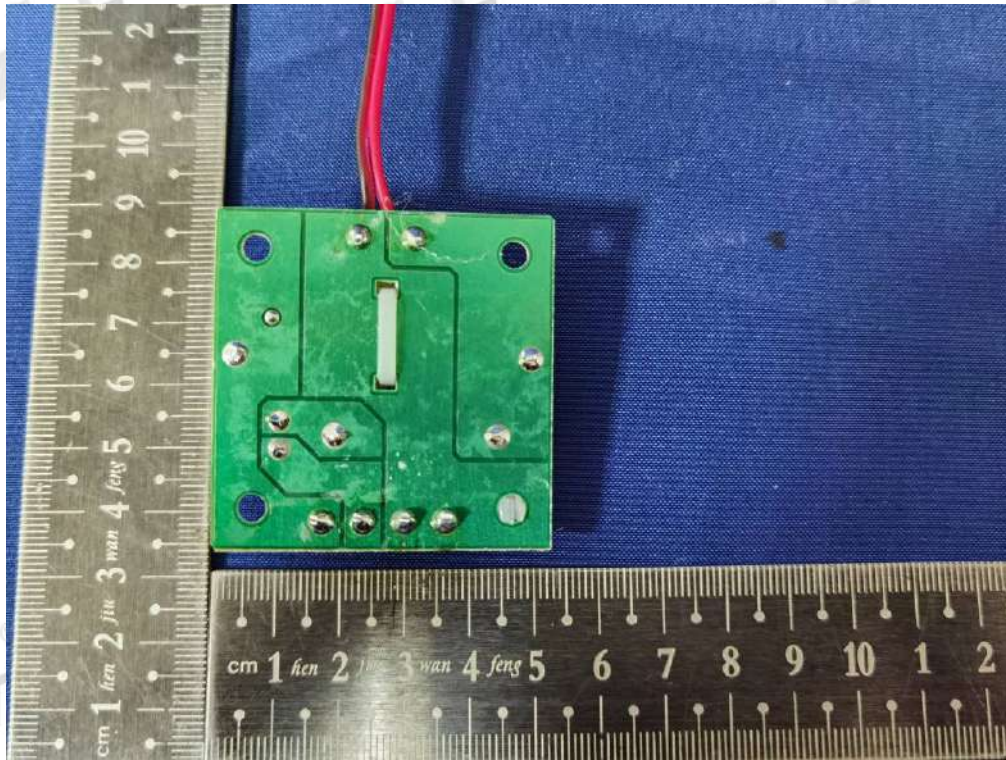


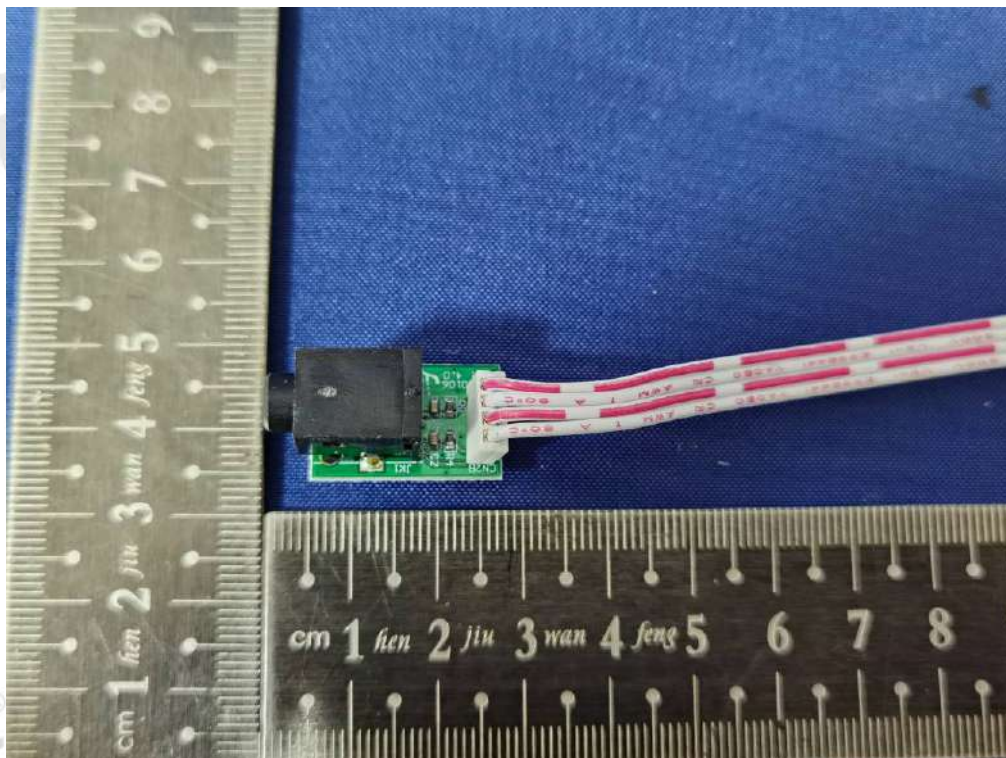
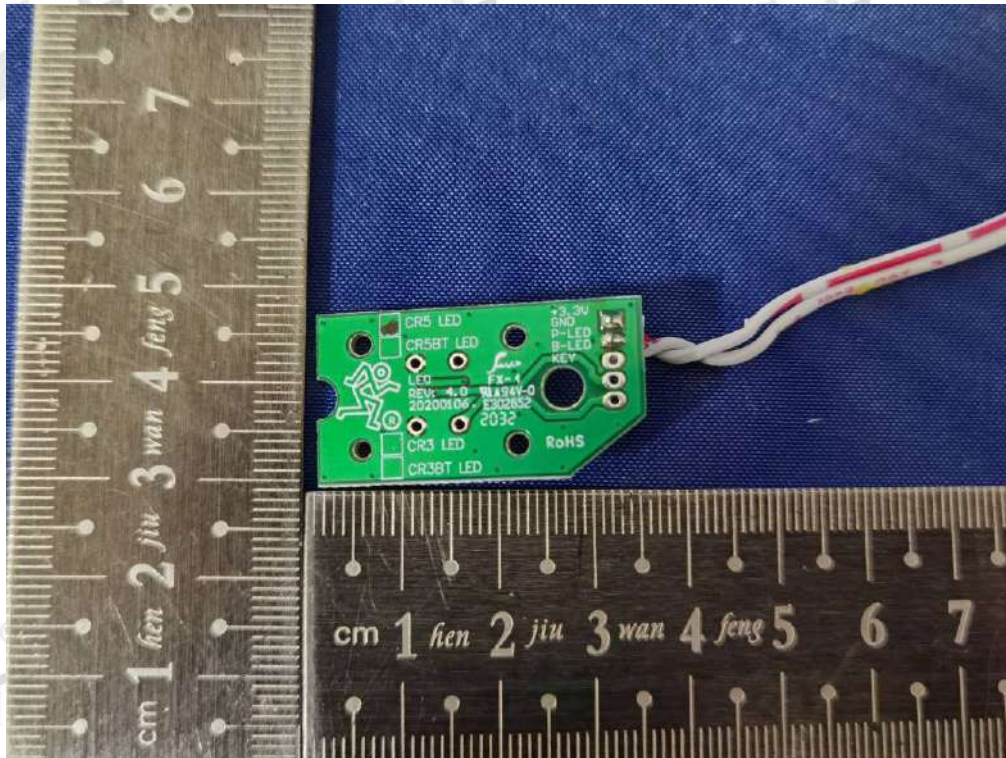


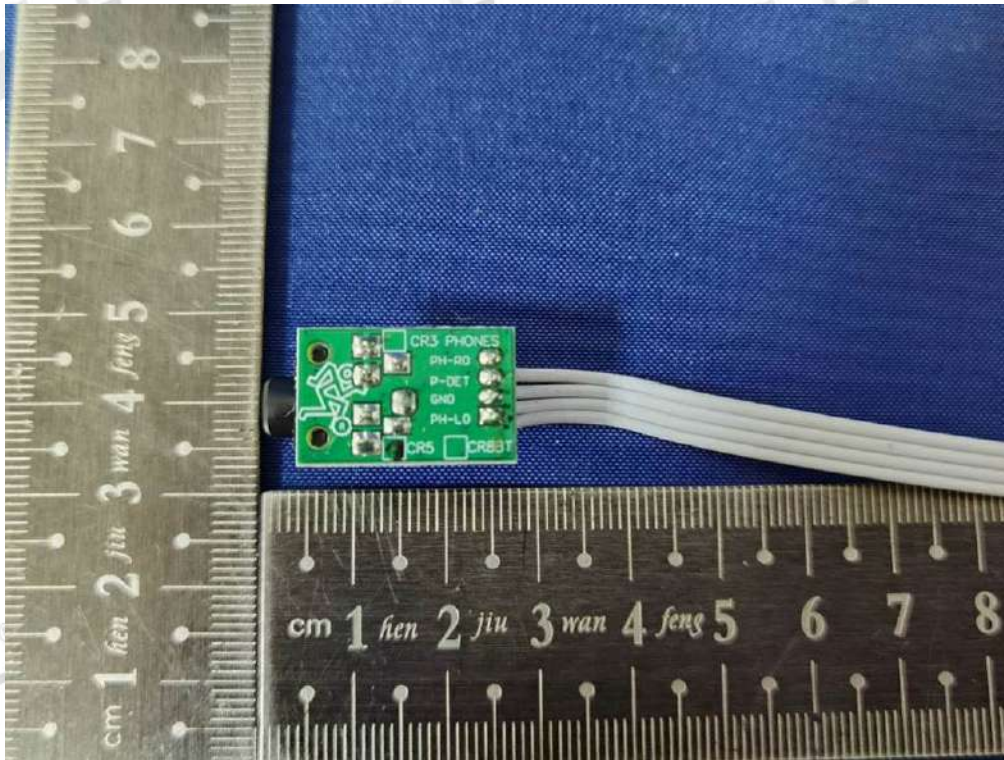




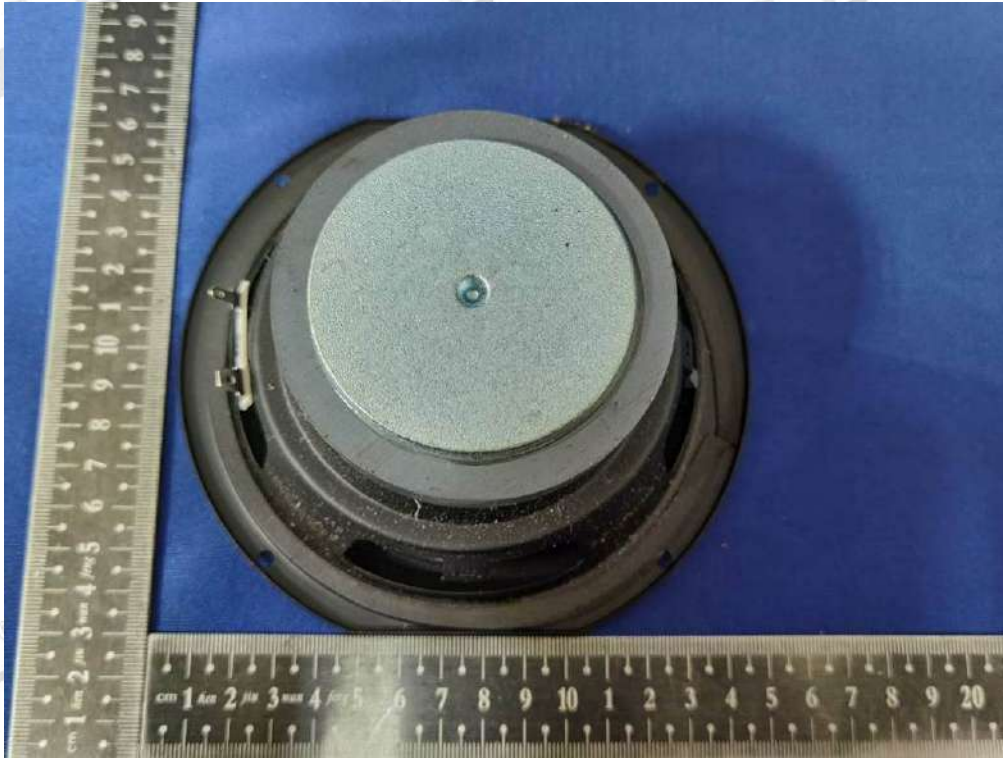












END OF REPORT