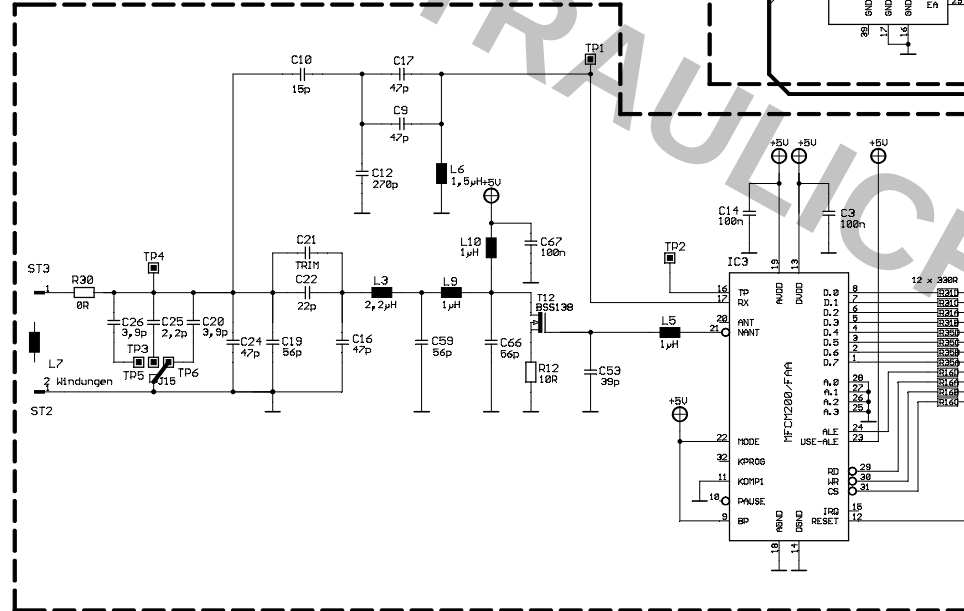
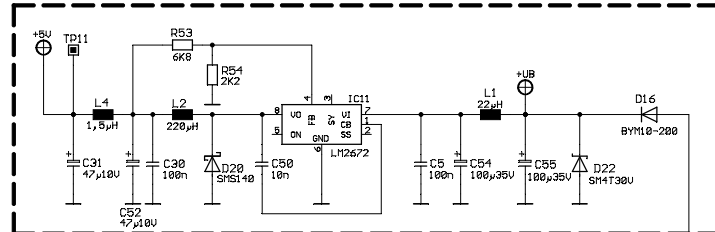


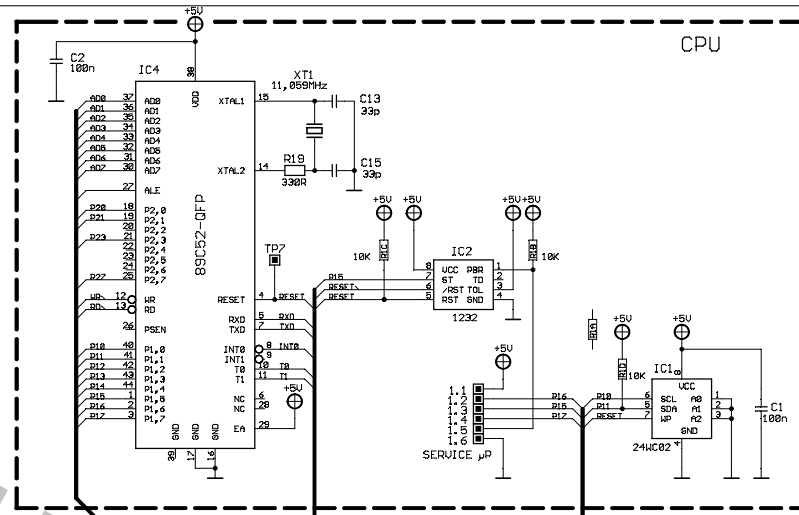
MIFARE TRANSCEIVER



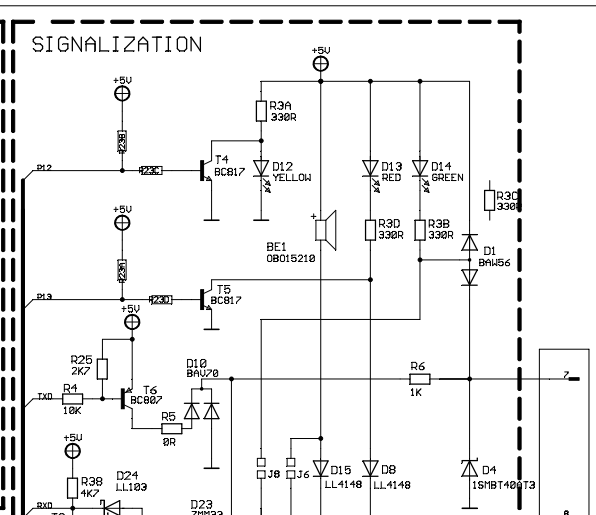
POWER SUPPLY



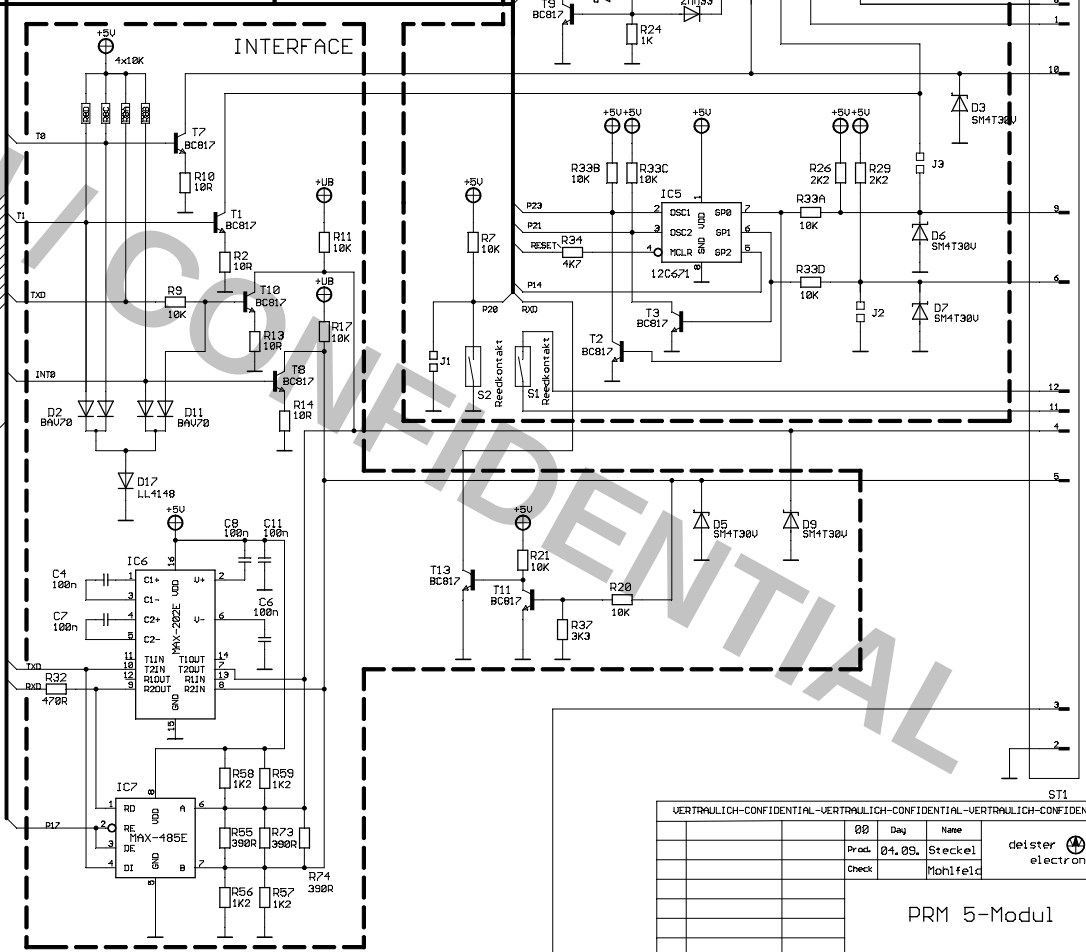
CPU



SIGNALIZATION



INTERFACE



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