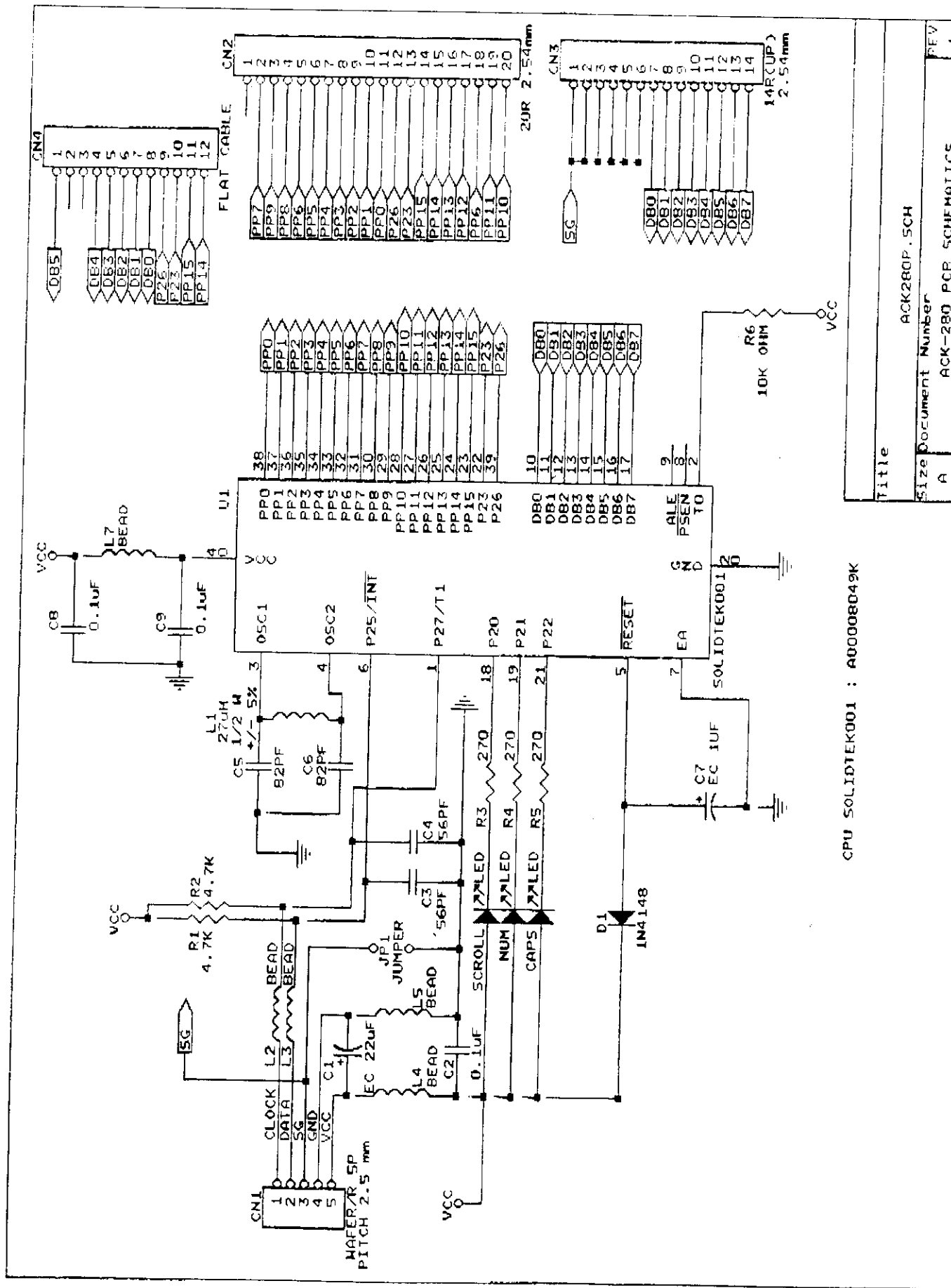




MAX LIGHT

EXHIBIT D

Circuit Diagram



CPU SOLIDTEK001 : A00008049K

Title

ACK280P.SCH

Size Document Number

ACK-280 PCB SCHEMATICS

Date: June 22, 1998

Sheet 1 of 2

REV

1

	PP0	PP1	PP2	PP3	PP4	PP5	PP6	PP7	PP8	PP9	PP10	PP11	PP12	PP13	PP14	PP15	PP16	PP17
DB0	110	17	46	6	12	115	14	79	37	91	93				VOL -	MUTE	STOP	VEDIO
DB1	112	18	47	21	13	116	75	83	38	96	98				NEXT/ FWARD	MENU	PLAY HIN	EJECT
DB2	113	19	48	35	15	117	76	84	39	101	103				VOL +		EMAIL	SLEEP
DB3	114	20	49	50	27	118	90	89	40	100	106				PREV/ RWIND		WHN	
DB4	1	2	31	7	28	119	29	45	43	92	108	80			PLAY/ PAUSE	GAME	CALC	30
DB5	16	3	32	22	41	121	124	56	52	97	99	85			STOP	REC	TV	
DB6	61	4	33	36	42	122	125	107	54	102	104	86			STOP	150	151	
DB7	53	5	34	51	120	123	126	APP	55	105	95	81			131	132	133	STOP MIN

NOTE :

Title	ACK280M2.SCH
Size	Document Number
REV	A
	ACK-280 MATRIX FOR VOYETRA DRIVER
Date:	June 24, 1998
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