ALINCO

6/1/01

Federal Communications Commissions

RE: PH3 DJ-596T / 800MHz analog cellular telephone band blocking

Dear Sir or Madam,

This is to declare that the device in application PH3 DJ-596T has been blocked for any and all access of 824.00 to 849.9975MHz and 869.00 to 894.9975MHz.

The device uses double super heterodyne as a receiver and frequency is generated with PLL synthesizer circuitry. The first local oscillation frequencies are:

- VHF circuit: 175.150 to 213.145MHz
- UHF circuit: 360.850 to 472.845MHz

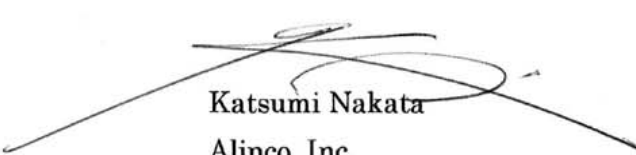Actual receiving frequencies of this device is:

VHF TX: 144.00 to 147.995MHz   VHF RX: 136.00MHz to 173.995MHz

UHF TX: 430.00 to 439.995MHz   UHF RX: 400.00MHz to 511.995MHz

VHF circuit uses upper side of local oscillation while UHF circuit uses Lower side. In addition, band-pass filters are used to receive both VHF and UHF frequencies to filter-out the above declared bands. The CPU used in this device, our parts code IC6 XA0844, vender's code M38267M8L275GP is exclusively programmed and burned for US export model. Alinco, Inc exports solely this version to the US market, and this CPU can't be modified by any means to receive the cellular frequencies declared above. Moreover, the entire circuitry of this device is not designed to cover the cellular frequencies anyway.

To my best of knowledge informed from the chief-engineer in charge of PH3 DJ-596T, above declared is true.

Sincerely,

Katsumi Nakata

Alinco, Inc.

Electronics Div., Headquarters