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Report number: 2002159
FCC: Part 15.121
Industry Canada: RSS-215
FCC ID: PH3DR-620T
M/N: DR-620T VHF/UHF
Twin Band FM Transceiver

APPENDIX D: ATTESTATION LETTER(S) (IF APPLICABLE)

Please refer to the following page.

23 July, 2002

Federal Communications Commissions

RE: PH3 DR-620T / 800MHz analog cellular telephone band blocking

Dear Sir or Madam,

This is to declare that the device in application PH3 DR-620T has been blocked for any and all access of 824.00 to 849.9975MHz and 869.00 to 894.9975MHz.

The device uses double super heterodyne PLL synthesizer circuitry as a receiver circuit and its first oscillation frequency is determined by a [n] figure generated in CPU. The first local oscillation frequencies determined by the [n] figures are as follows:

FM broadcasting band: 98.2 ~ 118.7MHz

AM aviation band: 129.7 ~ 157.7MHz

VHF band: 114.3 ~ 152.3MHz

UHF LOW band: (400-420MHz) 445.1 ~ 465.1MHz

UHF HIGH band: (420-480MHz) 374.9 ~ 434.9MHz

VHF-side 360MHz band 356.7 ~ 421.7MHz

UHF-side 360MHz band 380.1 ~ 445.1MHz

SUB-band VHF: 181.1 ~ 219.1MHz

SUB-band UHF: 378.3 ~ 458.3MHz

In addition, the harmonics are filtered out with Low-pass filter circuit before the signal goes into the mixer circuit, therefore 800MHz range can't be received. These oscillation frequencies can't be altered. Also the Radio Frequency circuits for above declared bands are all composed of Low-pass filters and synthesizer. The CPU used in this device, our parts code XA0913, vender's code M30620FCAGP is exclusively programmed and burned for US export model. Alinco, Inc exports solely this version to the US market, and this CPU can't be modified by any means to receive the cellular frequencies declared above.

Respectfully,

Kazuhiro Kusuhara

General manager, Production Section,

Alinco, Inc. Electronics Division