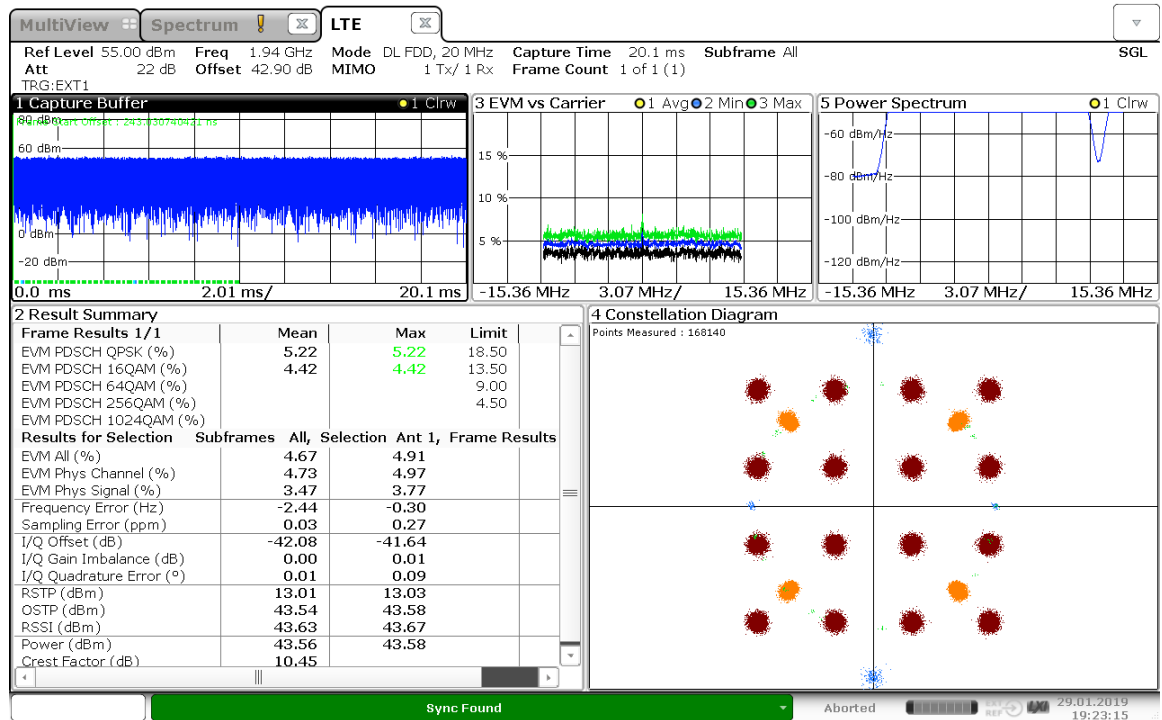
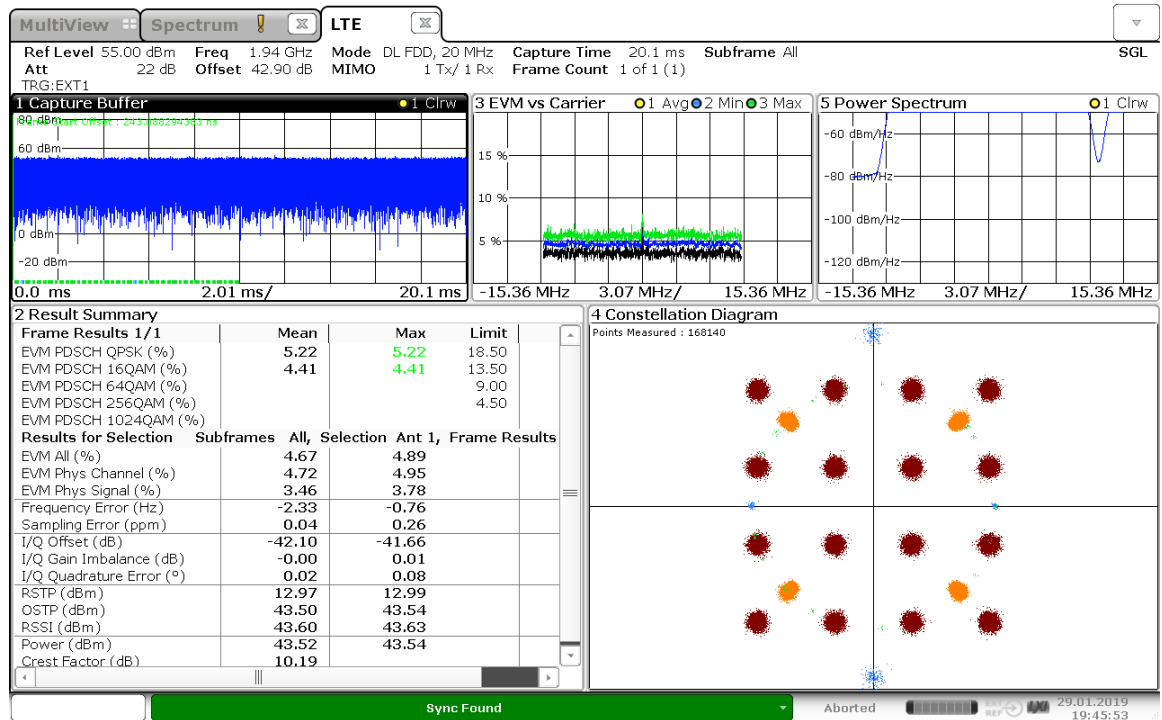


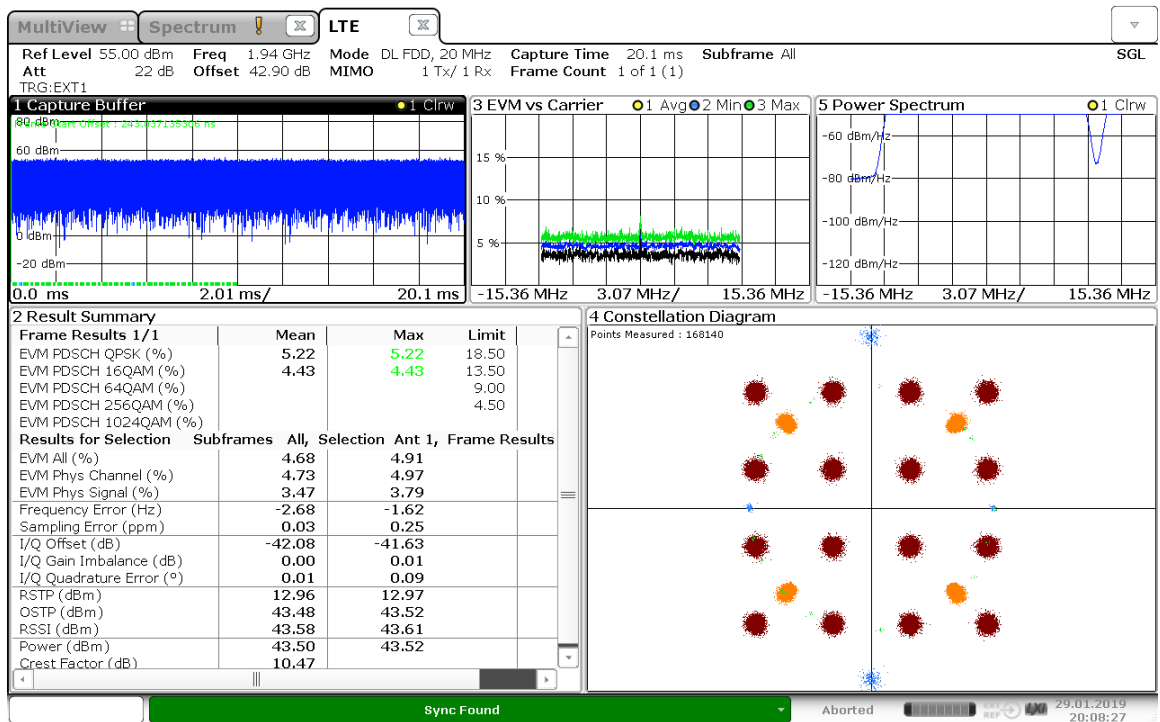
## 20M -1940MHz-TM3.2-Port 1 ~4:



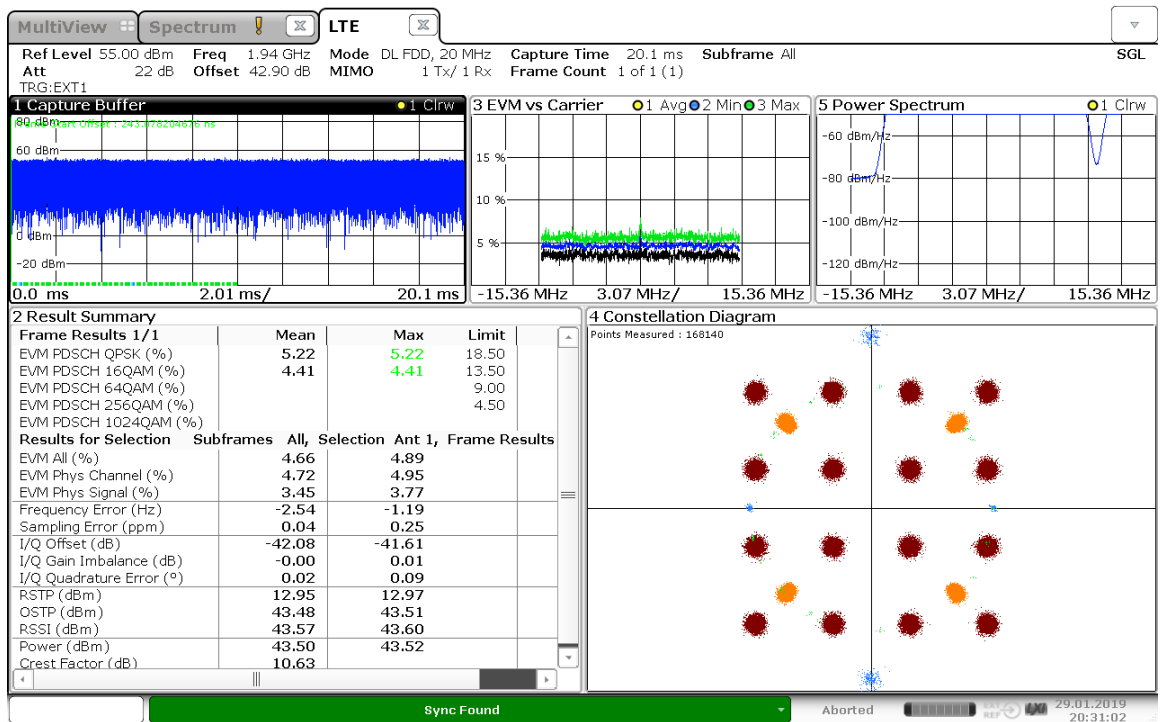
19:23:15 29.01.2019



19:45:53 29.01.2019

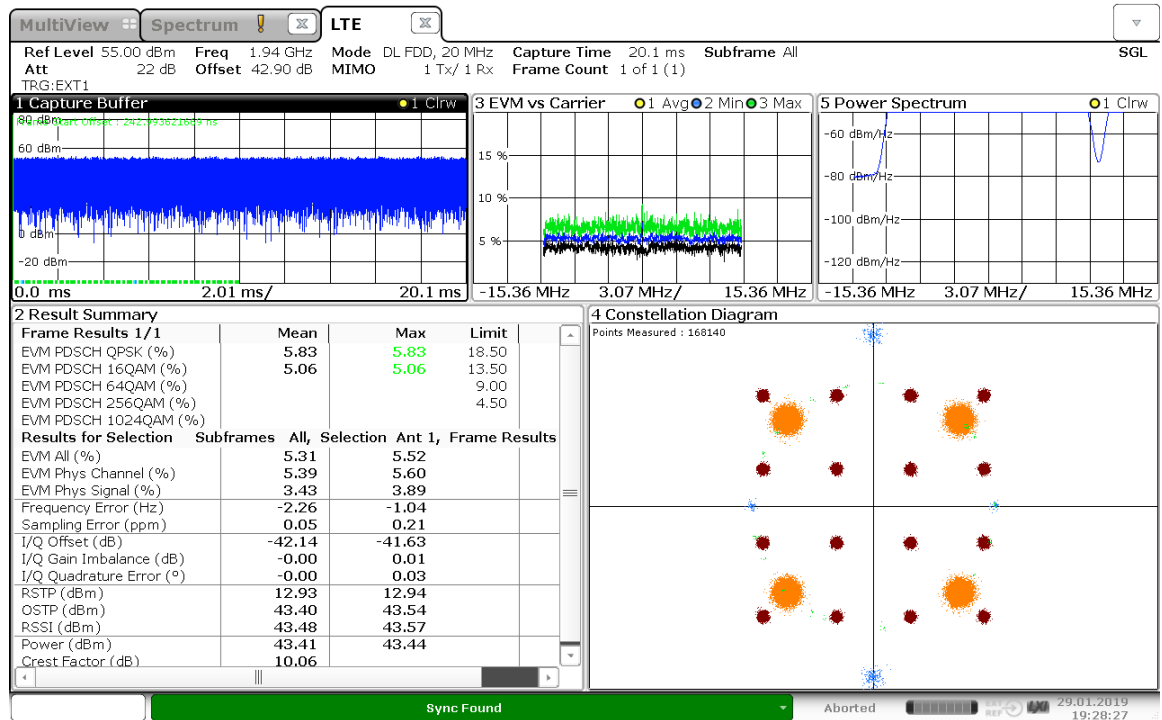


20:08:27 29.01.2019

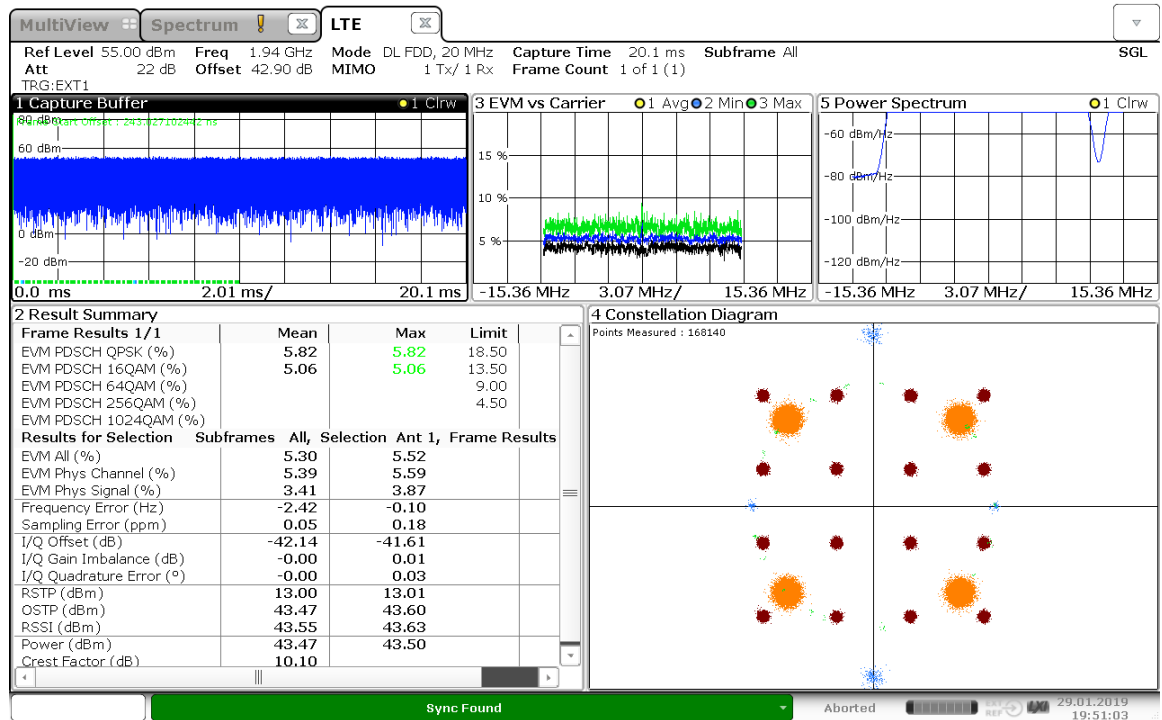


20:31:02 29.01.2019

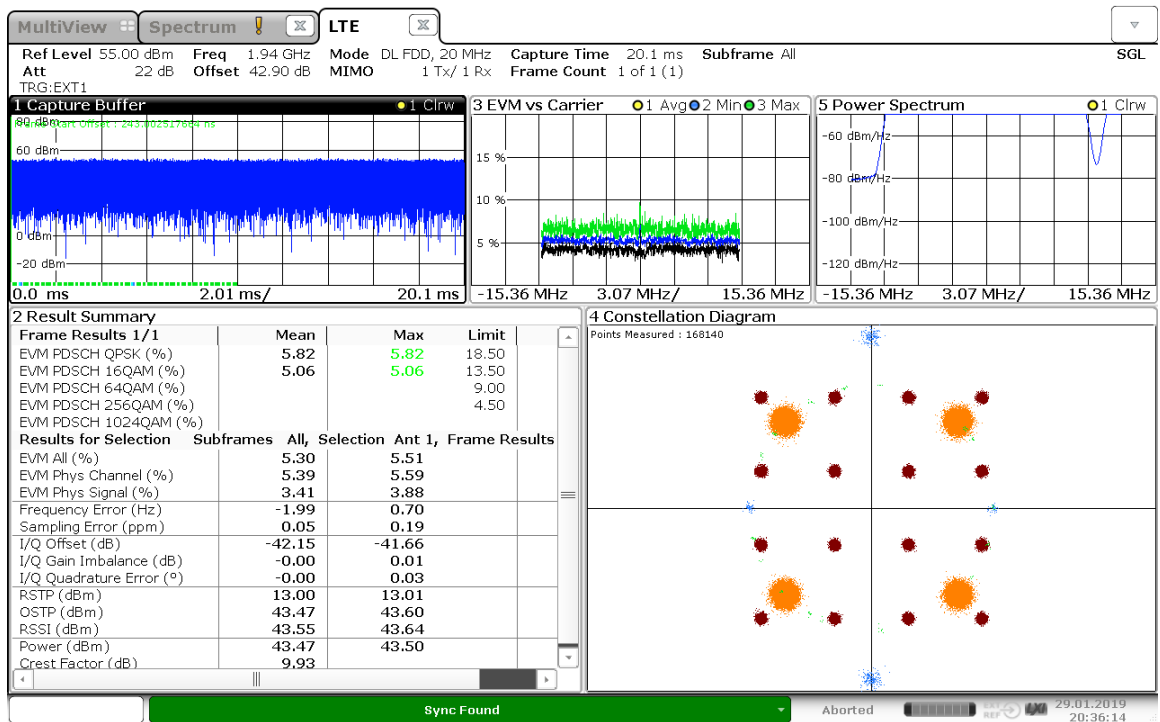
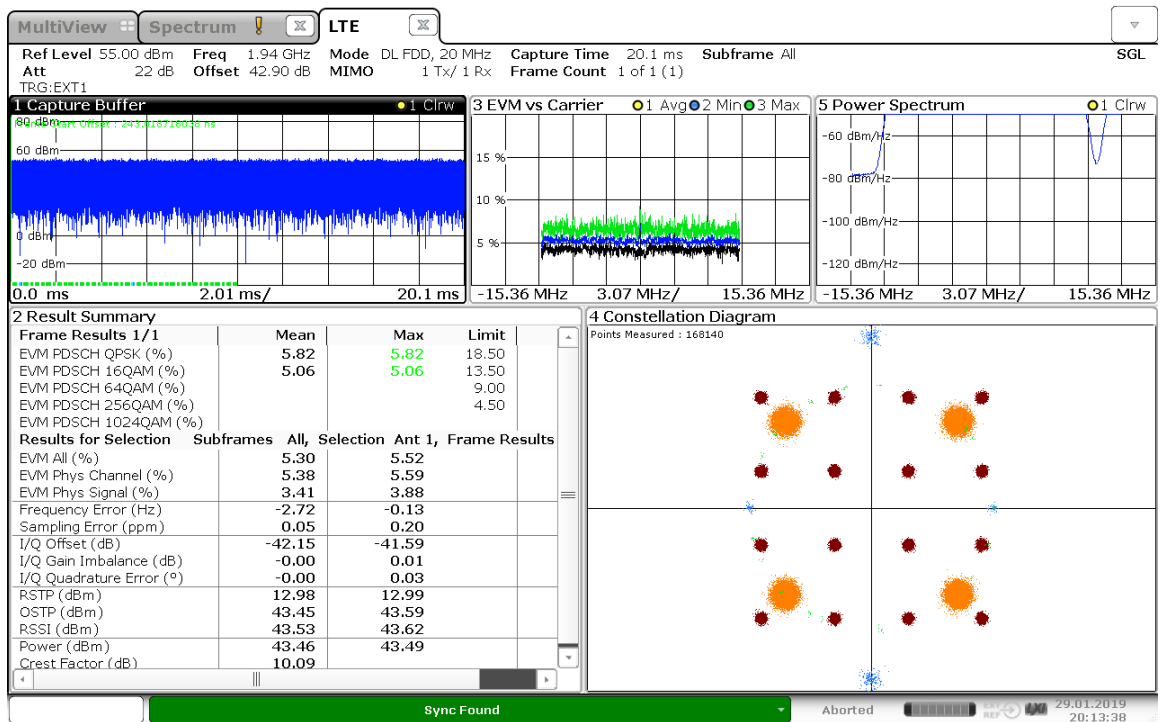
## 20M -1940MHz-TM3.3-Port 1 ~4:



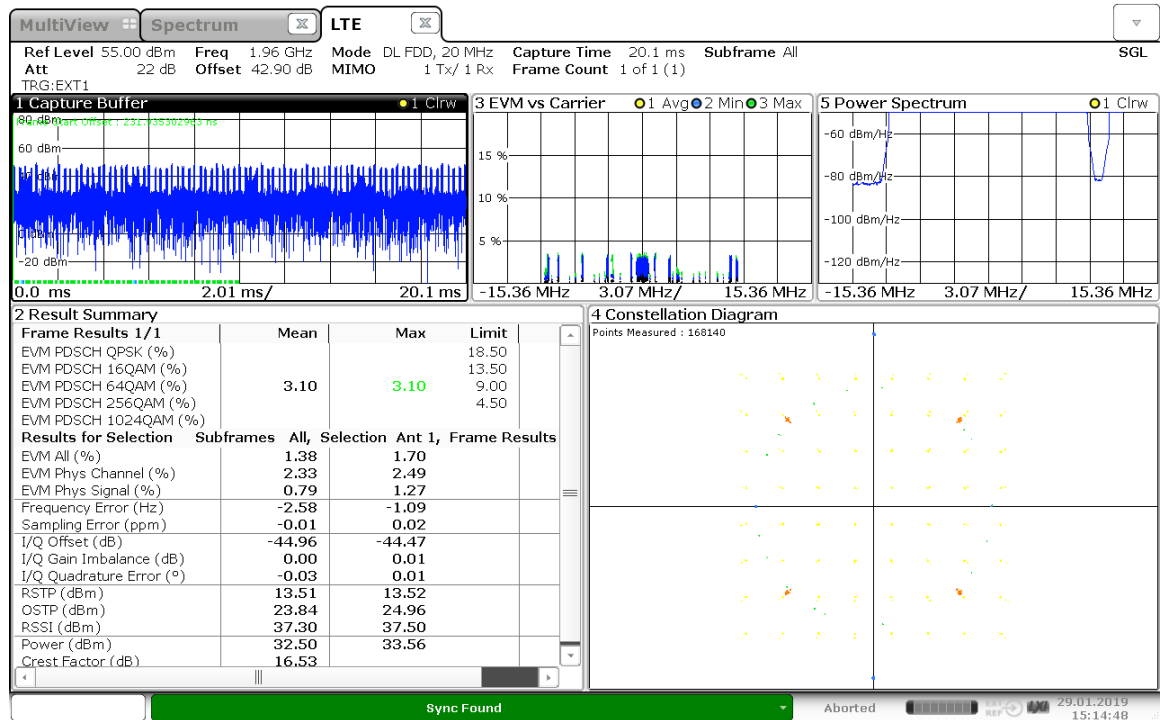
19:28:27 29.01.2019



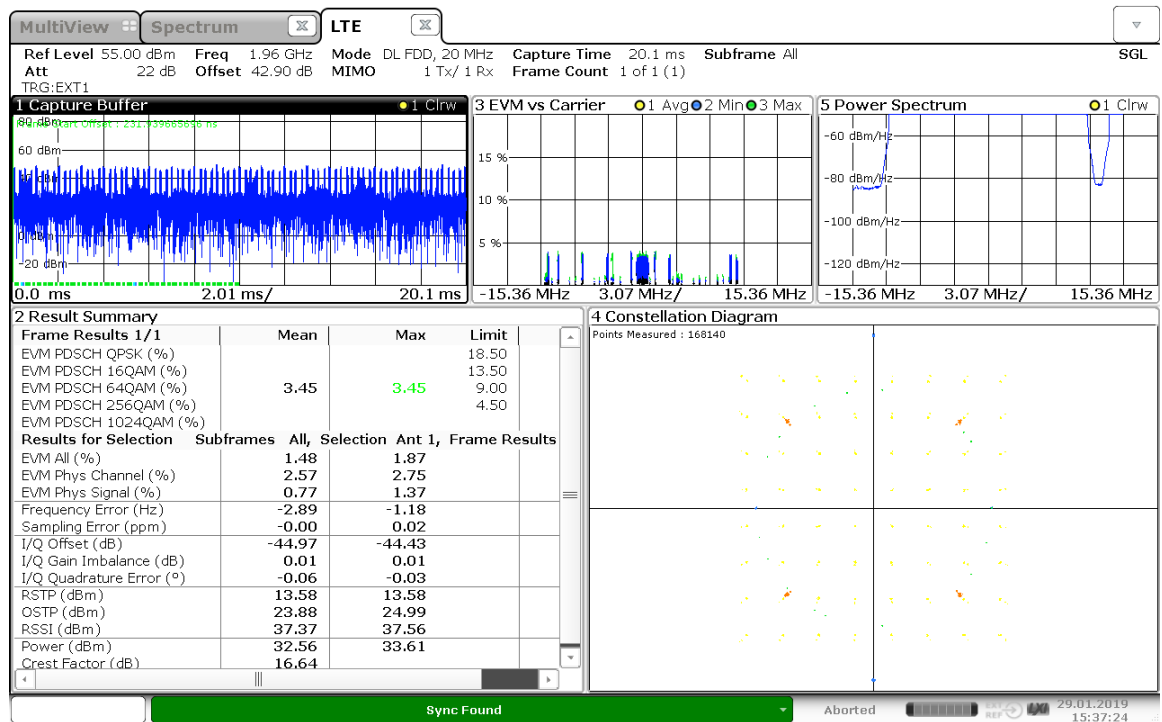
19:51:04 29.01.2019



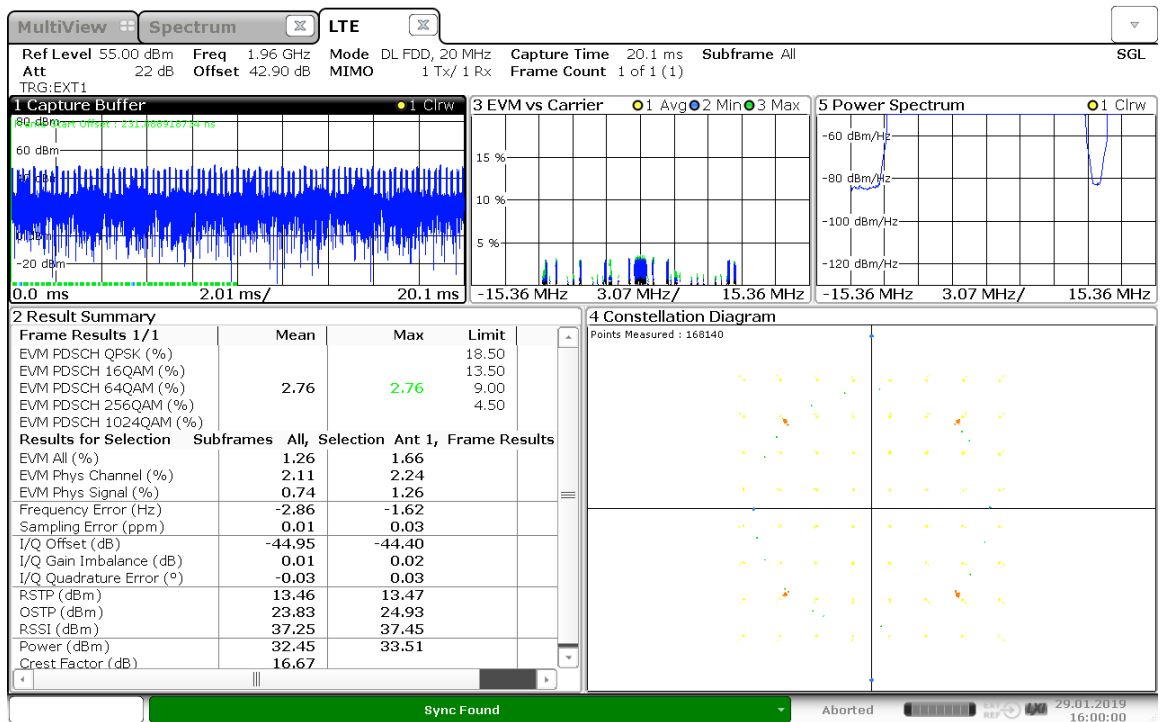
## 20M -1960MHz-TM2.0-Port 1 ~4:



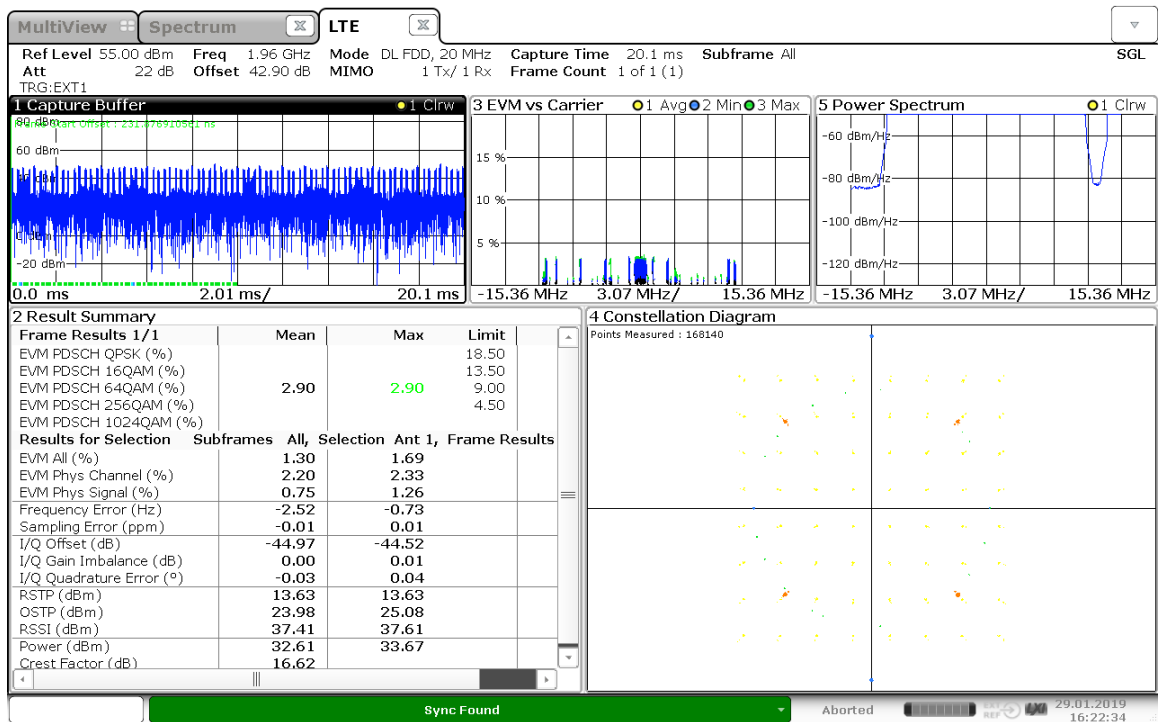
15:14:48 29.01.2019



15:37:24 29.01.2019

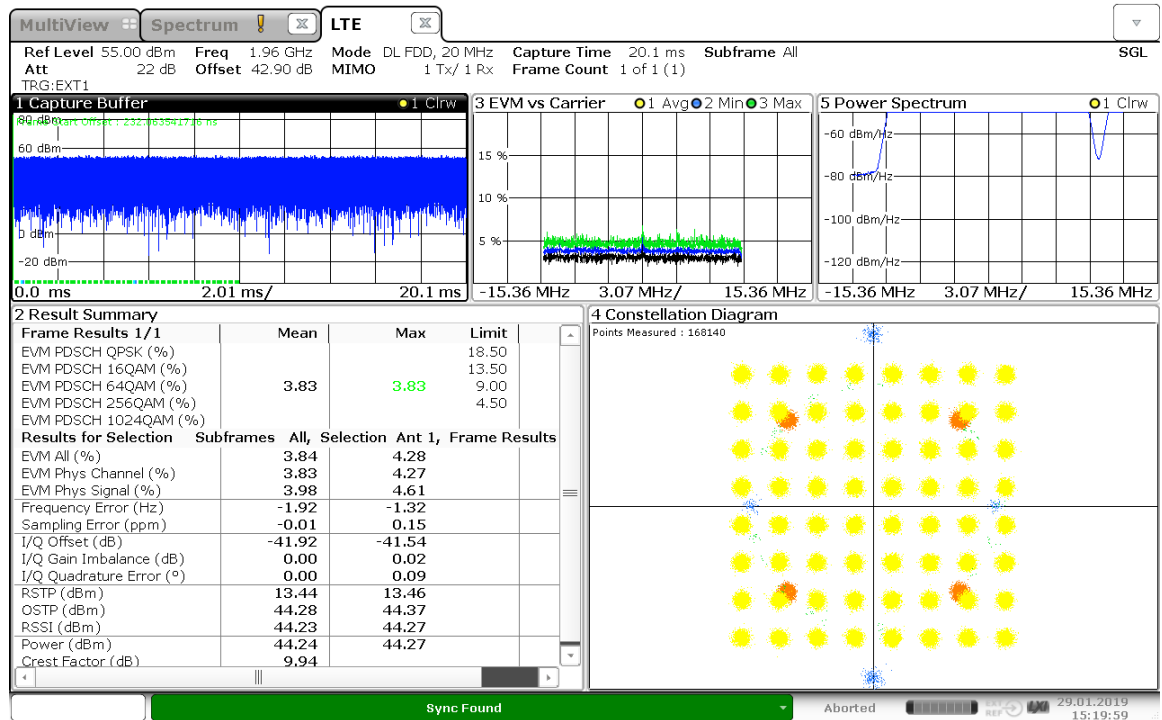


16:00:00 29.01.2019

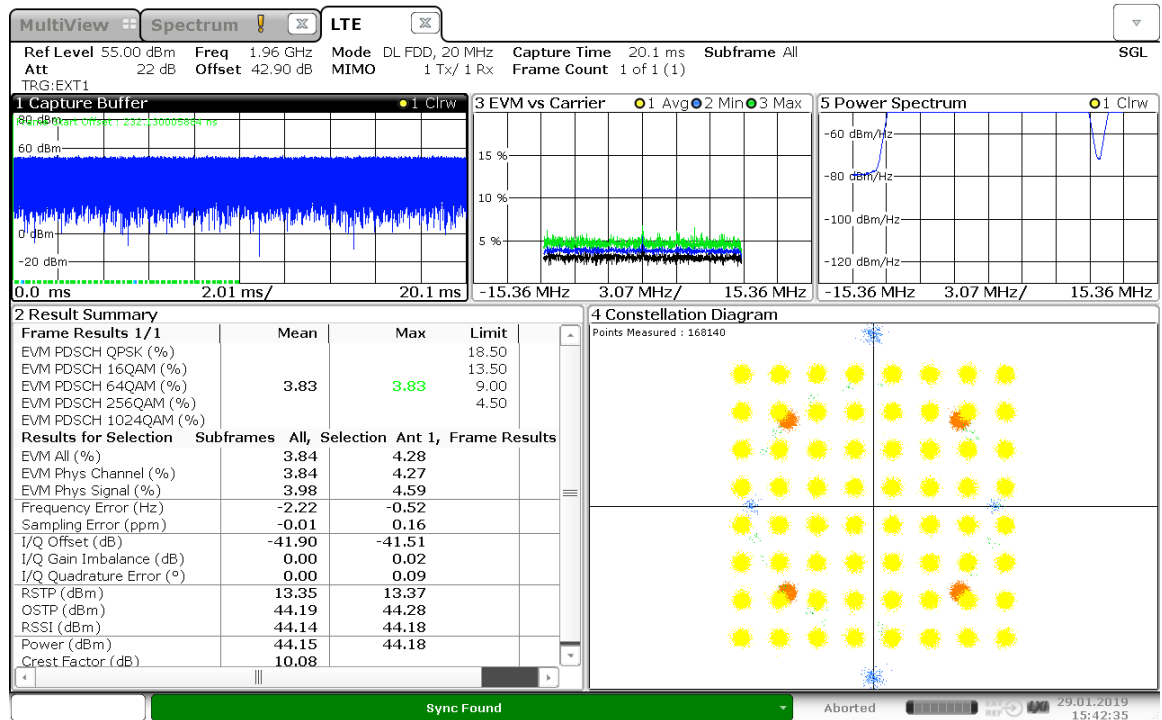


16:22:35 29.01.2019

## 20M -1960MHz-TM3.1-Port 1 ~4:

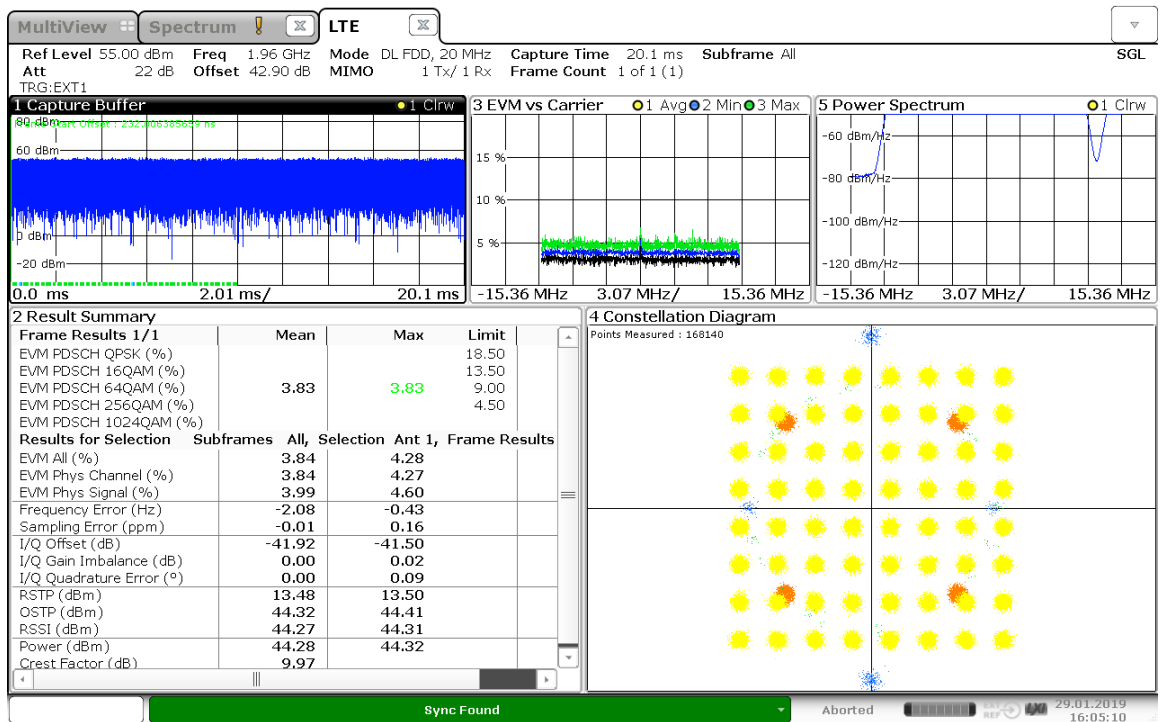


15:19:59 29.01.2019

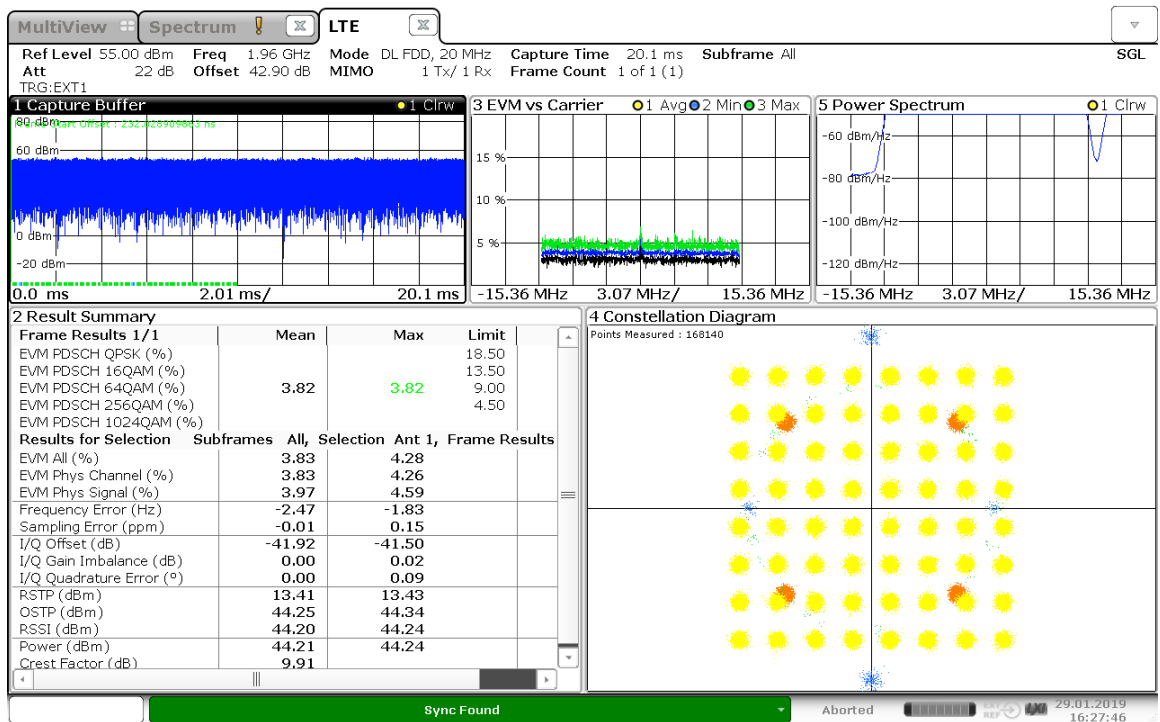


15:42:36 29.01.2019



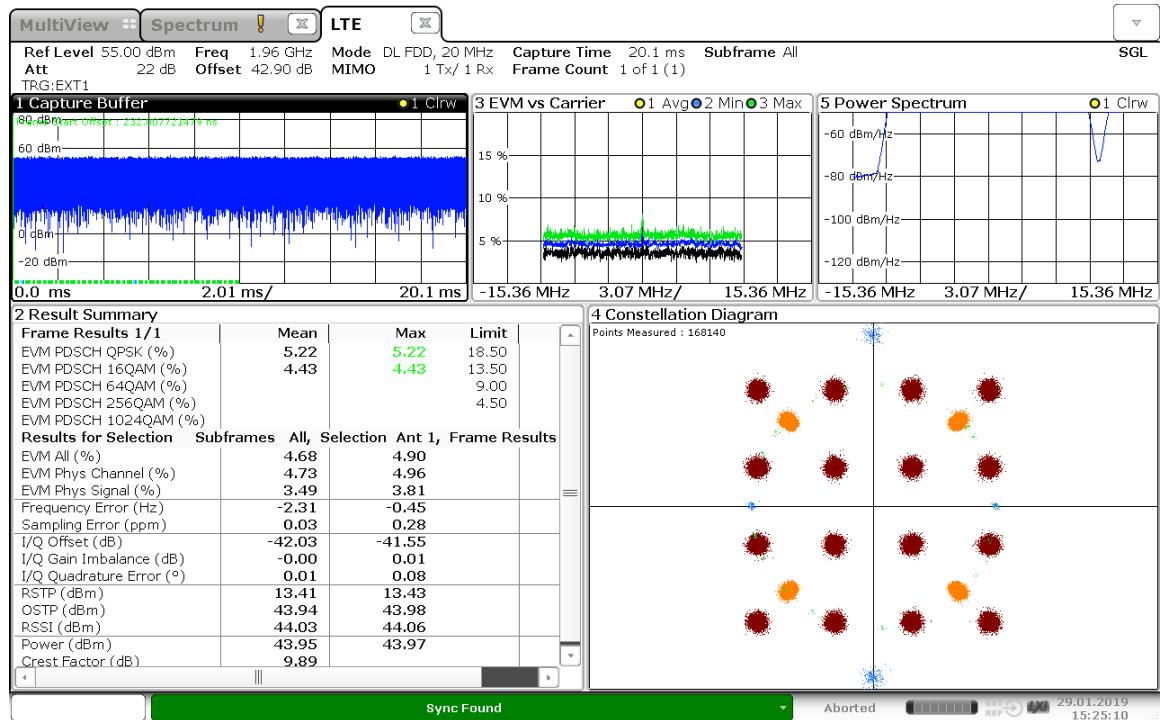


16:05:11 29.01.2019

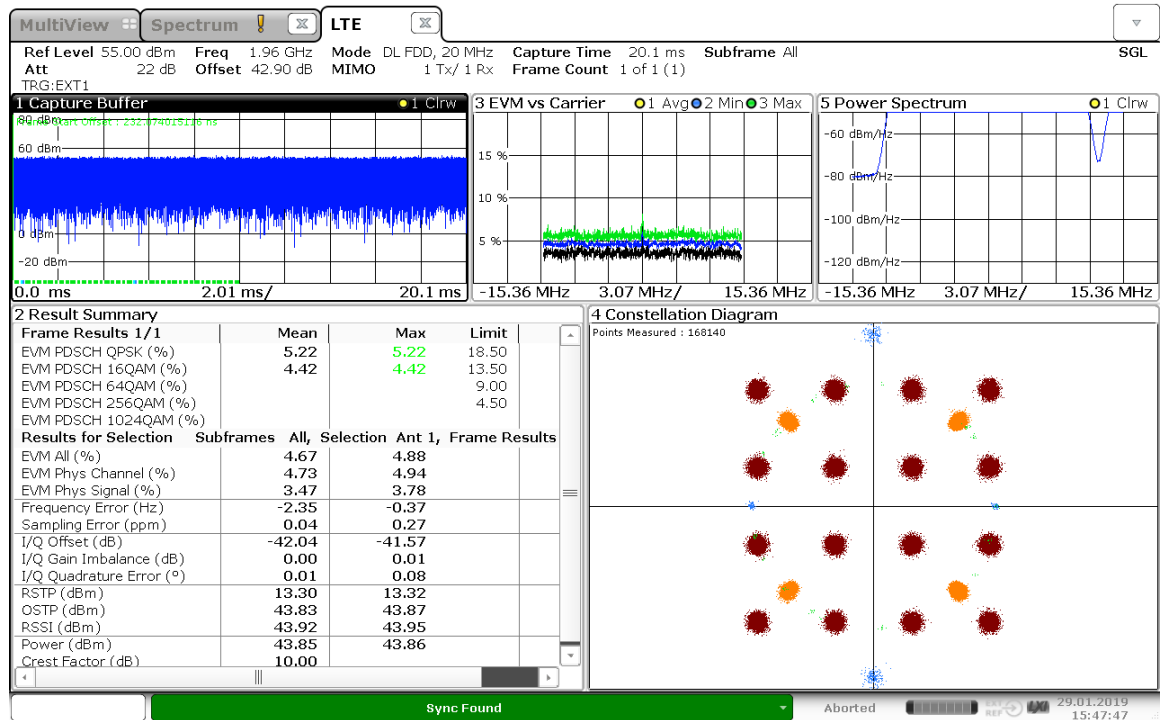


16:27:46 29.01.2019

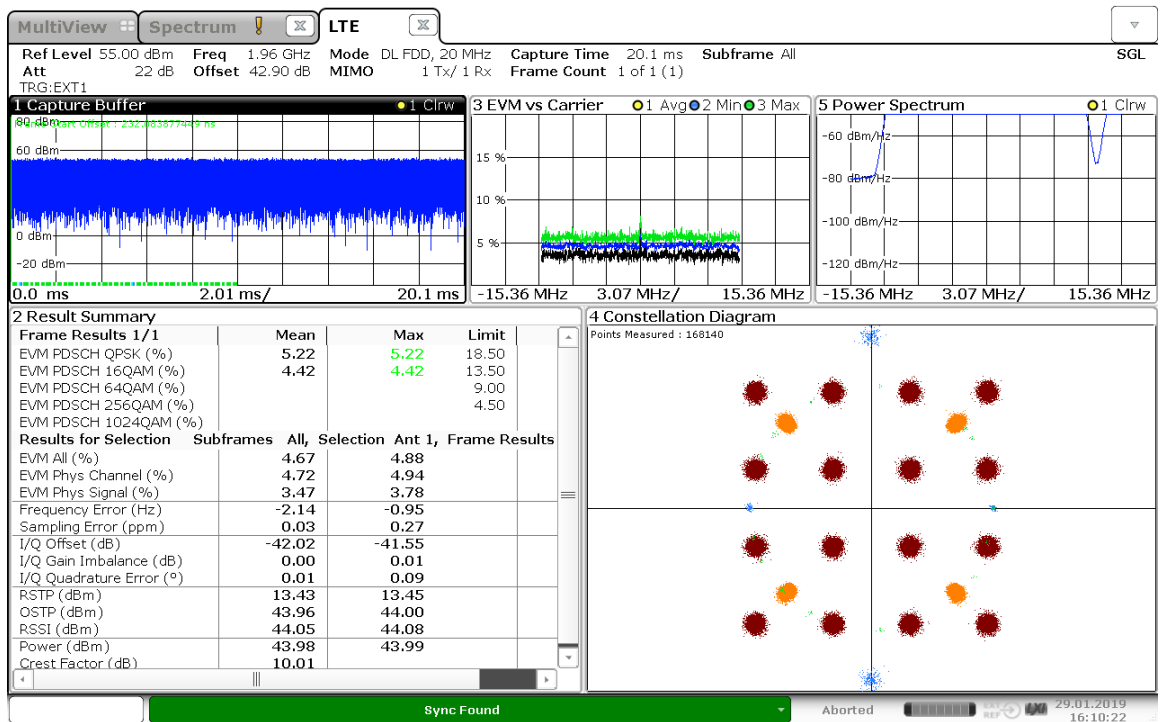
## 20M -1960MHz-TM3.2-Port 1 ~4:



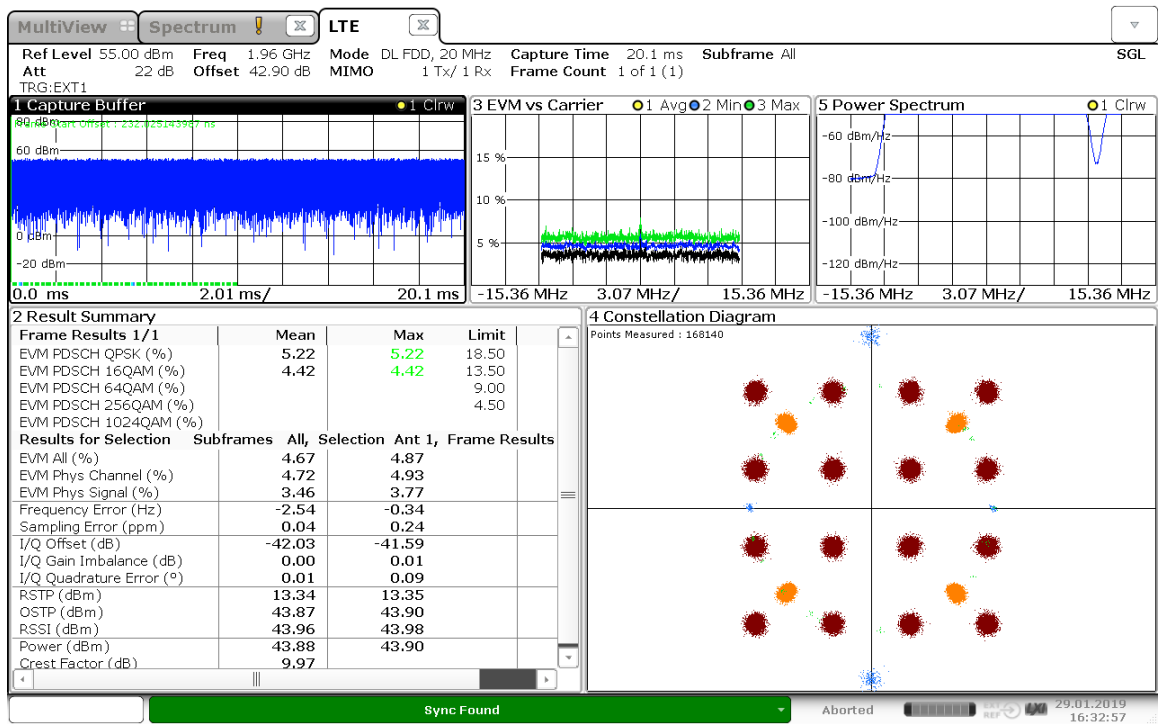
15:25:11 29.01.2019



15:47:47 29.01.2019

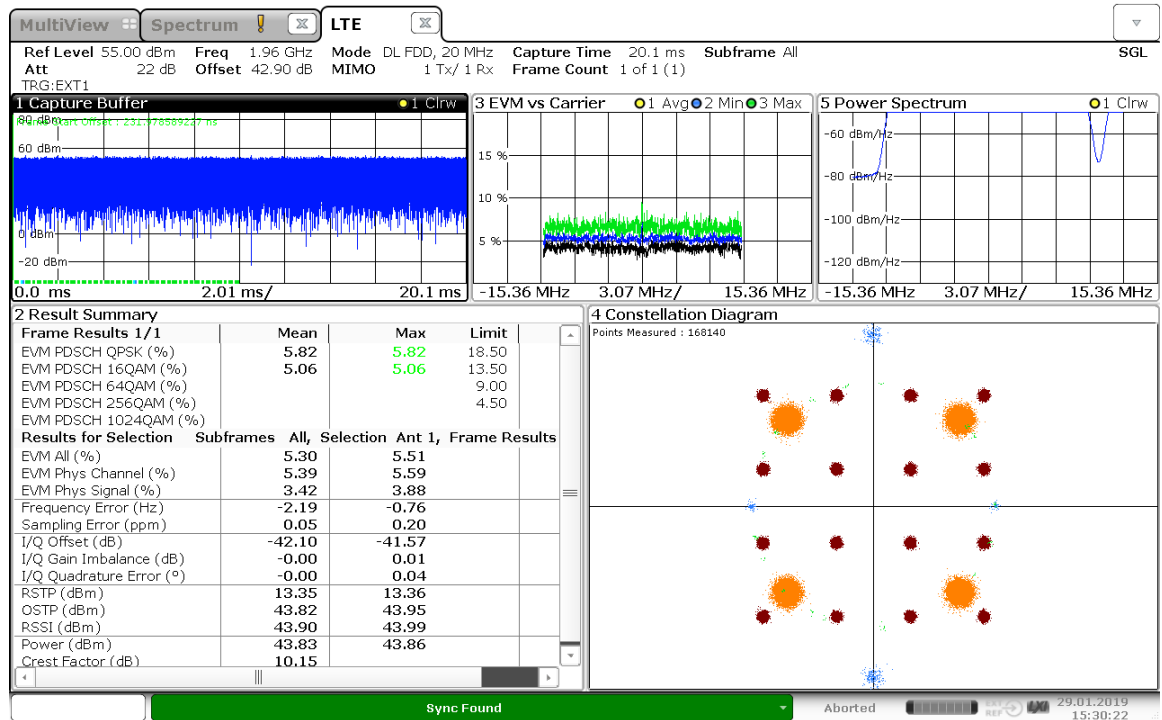


16:10:22 29.01.2019

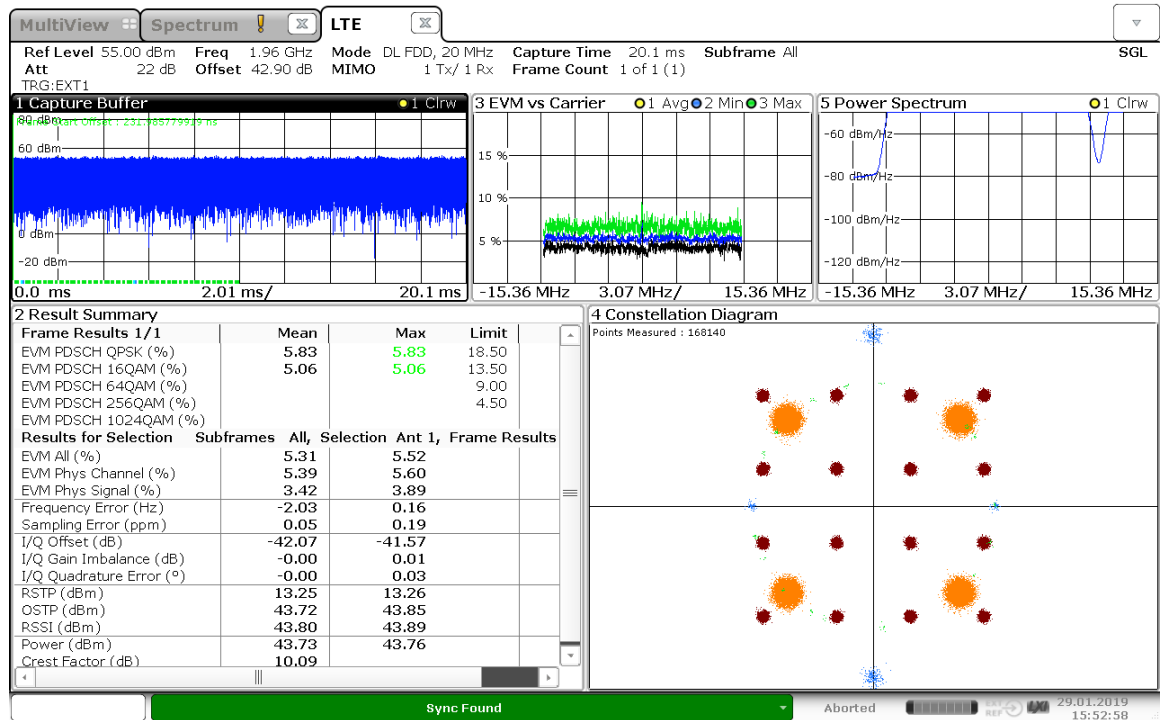


16:32:57 29.01.2019

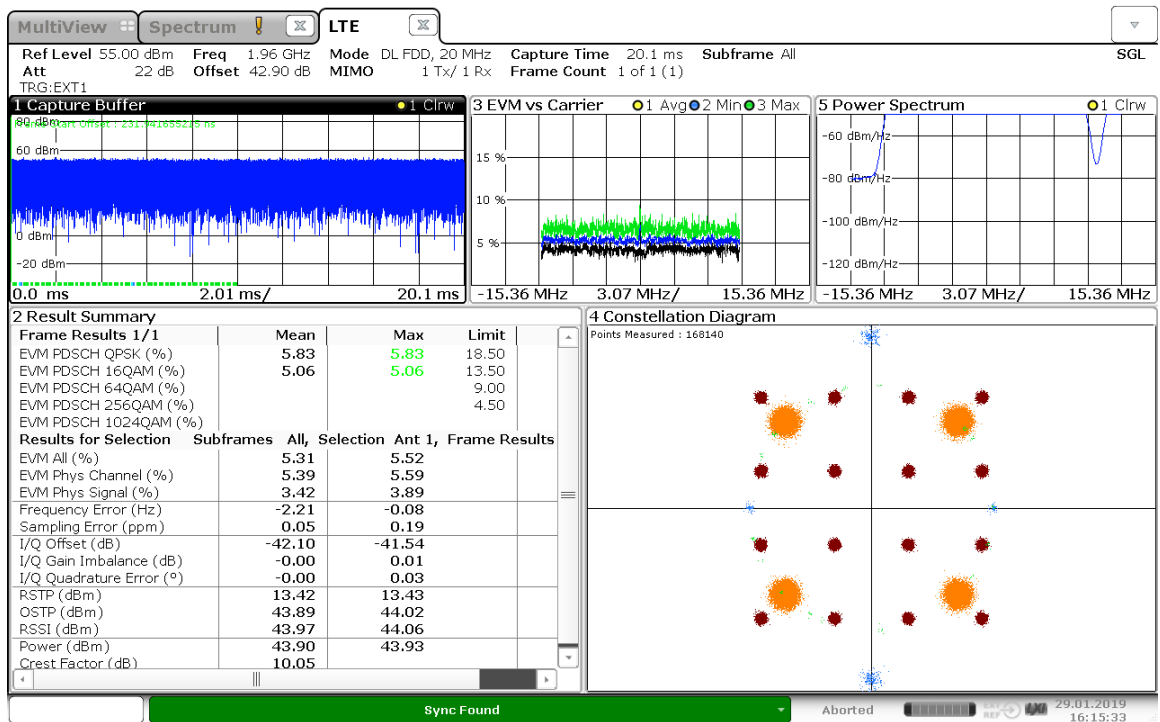
## 20M -1960MHz-TM3.3-Port 1 ~4:



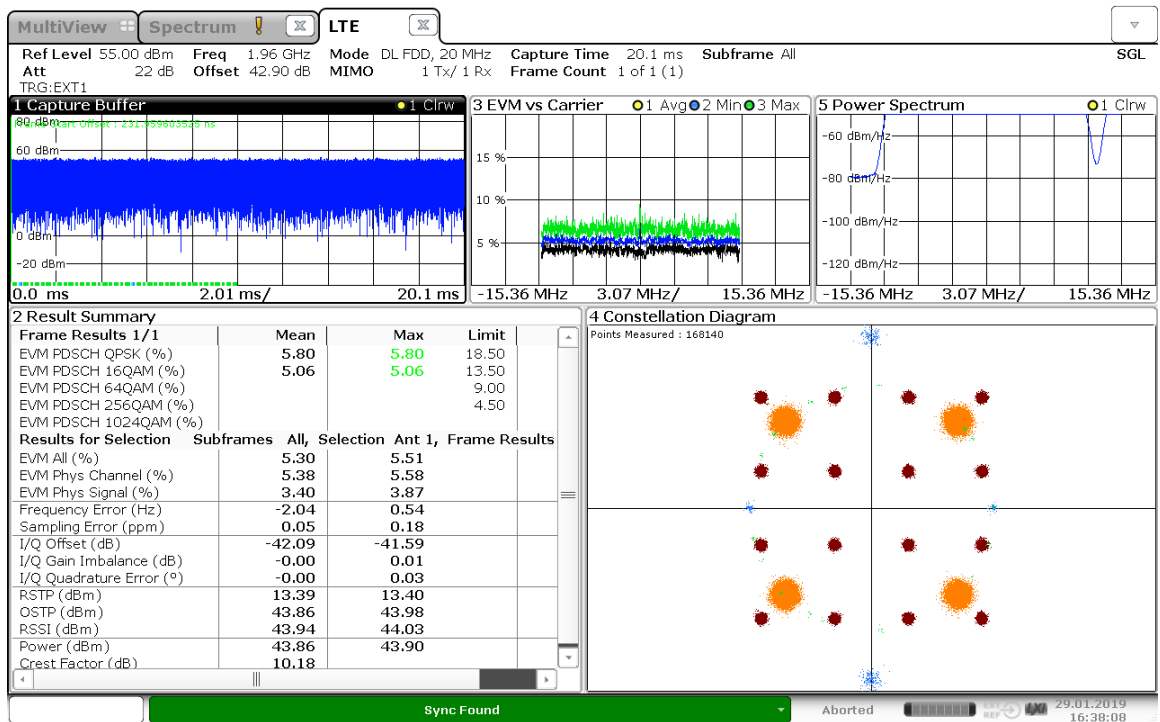
15:30:22 29.01.2019



15:52:58 29.01.2019



16:15:33 29.01.2019

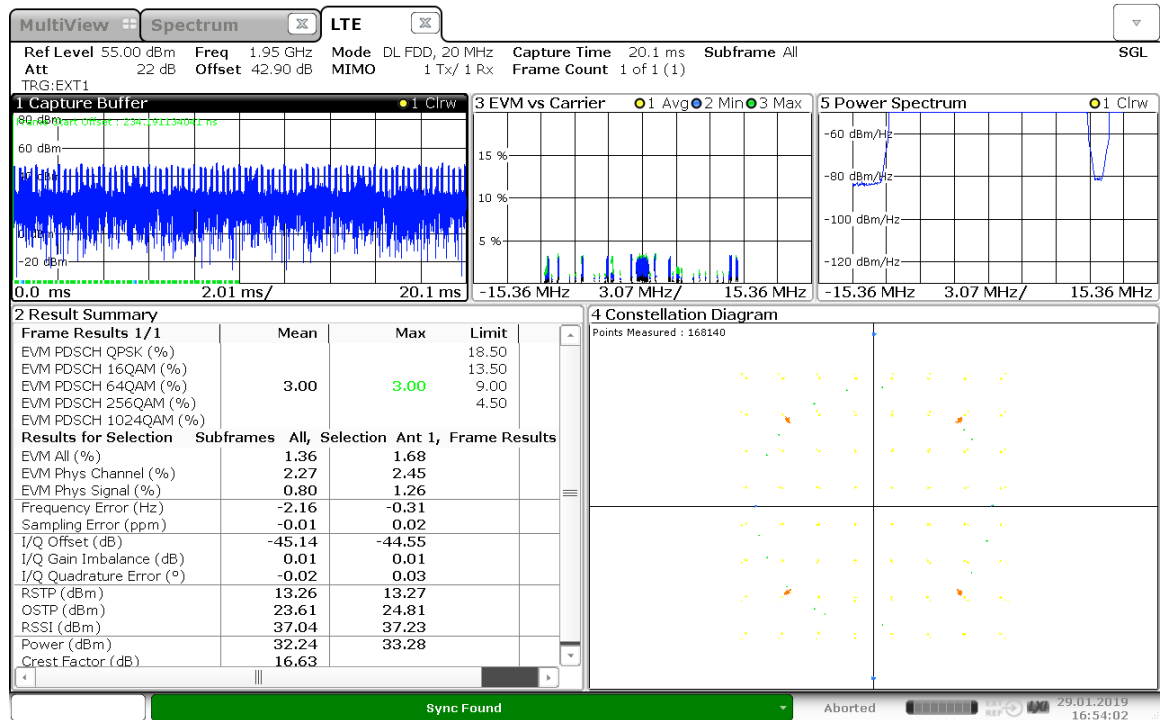


16:38:09 29.01.2019

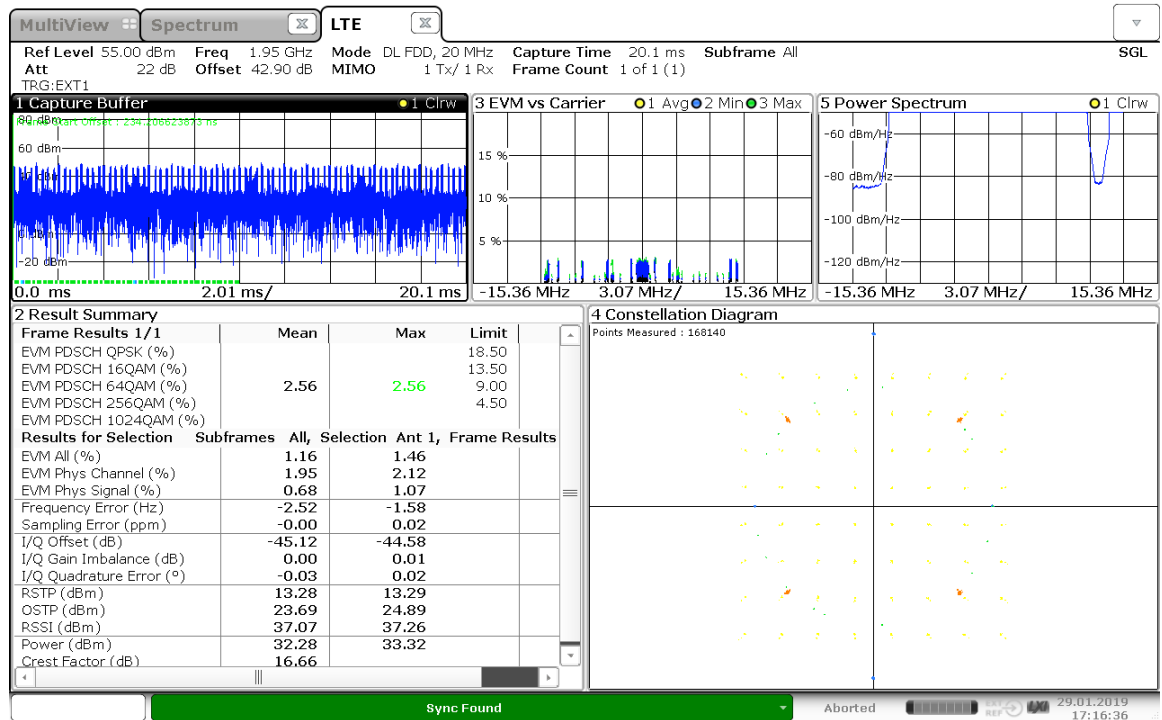
Channel Bandwidth :20M+20M(1950MHz+1970MHz)

Frequency (MHz)	Test mode	Port	EVM(%)
1950	TM2.0	1	3.00
		2	2.56
		3	2.67
		4	2.52
	TM3.1	1	3.82
		2	3.82
		3	3.83
		4	3.83
	TM3.2	1	4.42
		2	4.42
		3	4.41
		4	4.41
	TM3.3	1	5.80
		2	5.81
		3	5.80
		4	5.82
1970	TM2.0	1	3.08
		2	2.68
		3	2.63
		4	2.62
	TM3.1	1	3.83
		2	3.83
		3	3.84
		4	3.84
	TM3.2	1	4.41
		2	4.42
		3	4.39
		4	4.41
	TM3.3	1	5.81
		2	5.81
		3	5.81
		4	5.81

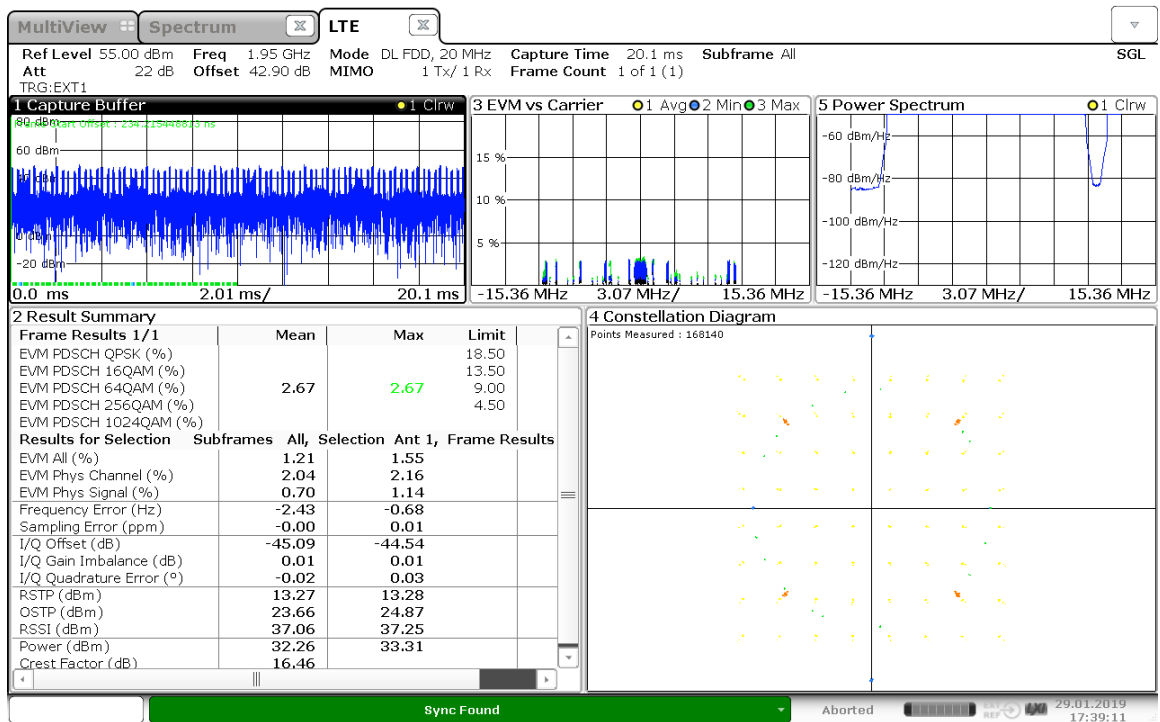
## 20M -1950MHz-TM2.0-Port 1 ~4:



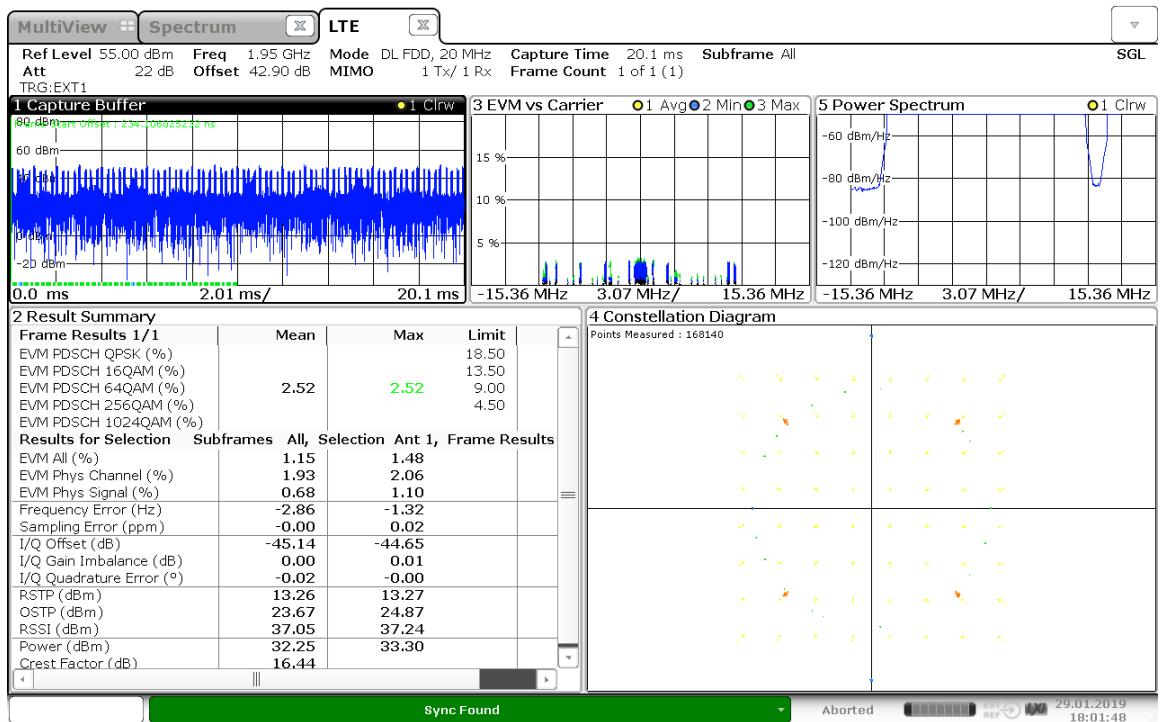
16:54:03 29.01.2019



17:16:37 29.01.2019



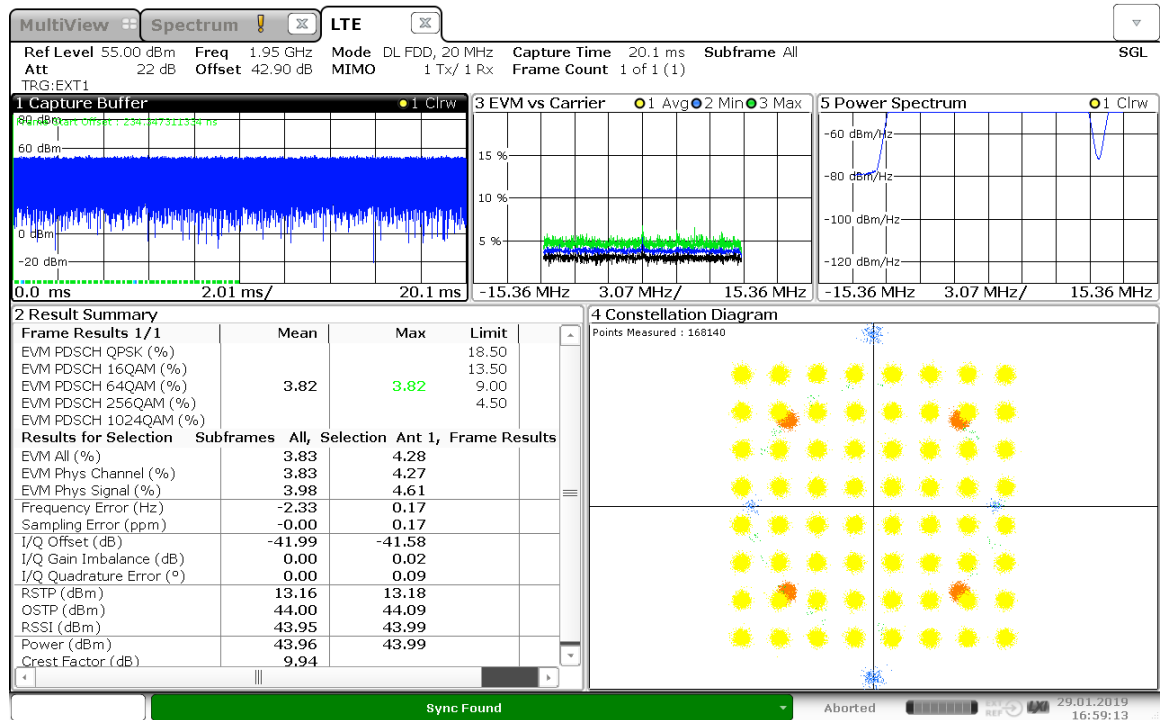
17:39:12 29.01.2019



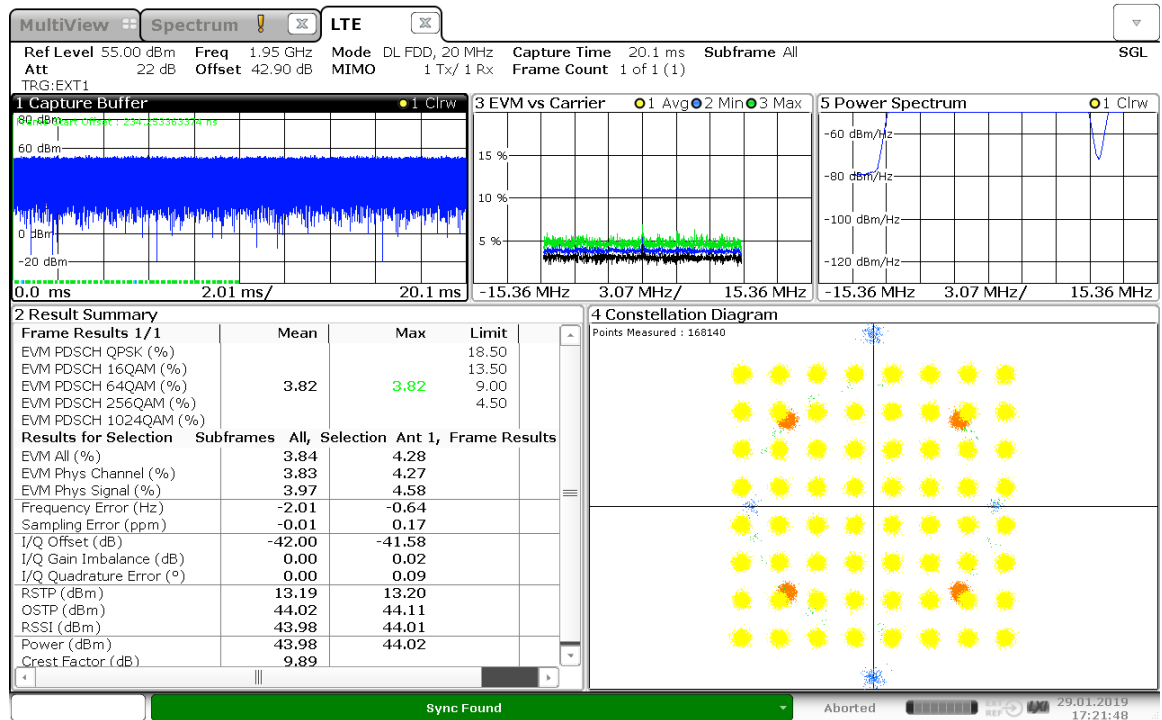
18:01:48 29.01.2019



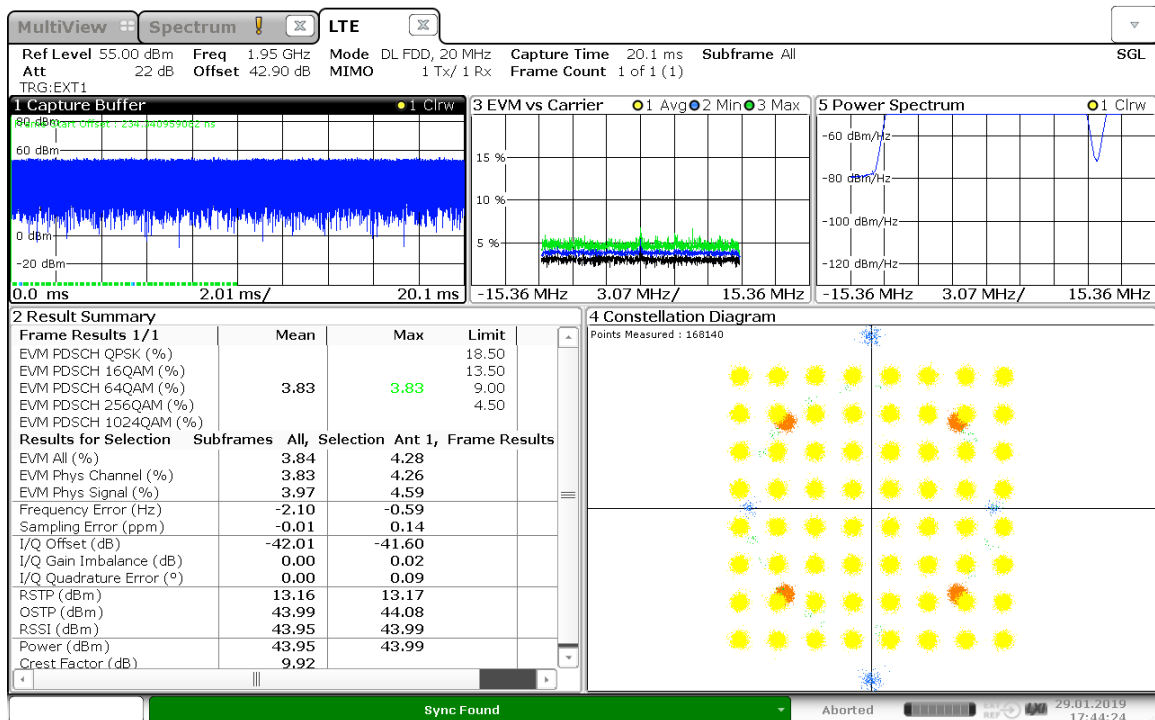
## 20M -1950MHz-TM3.1-Port 1 ~4:



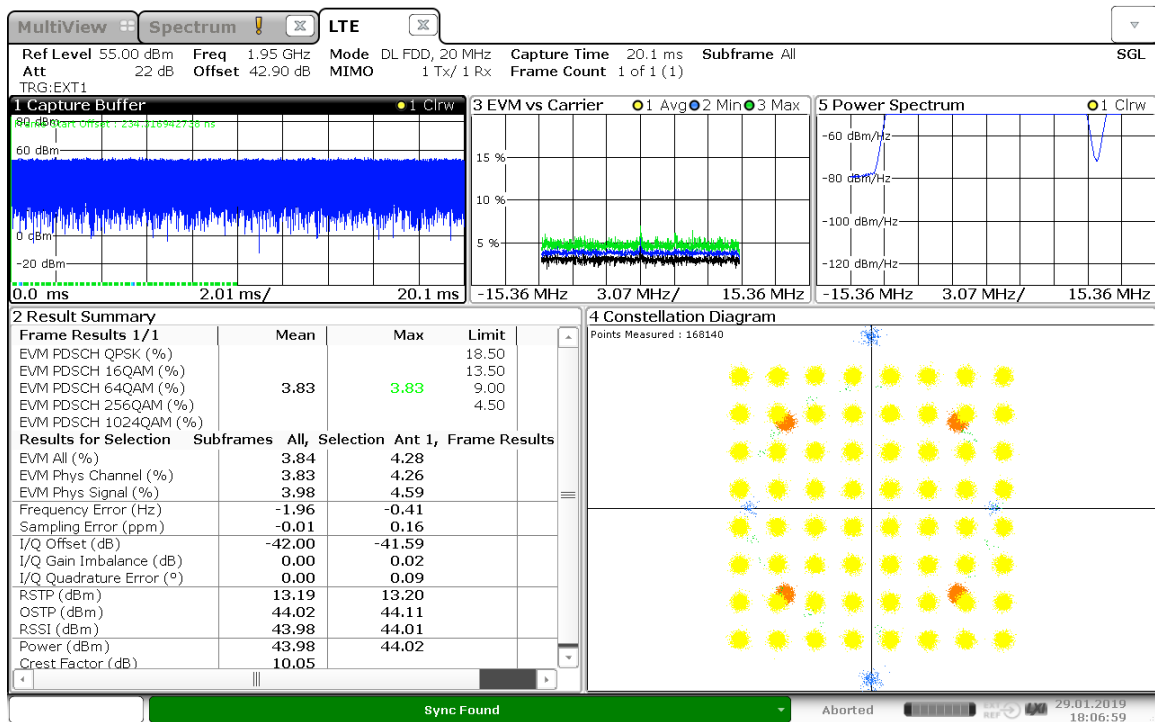
16:59:14 29.01.2019



17:21:48 29.01.2019

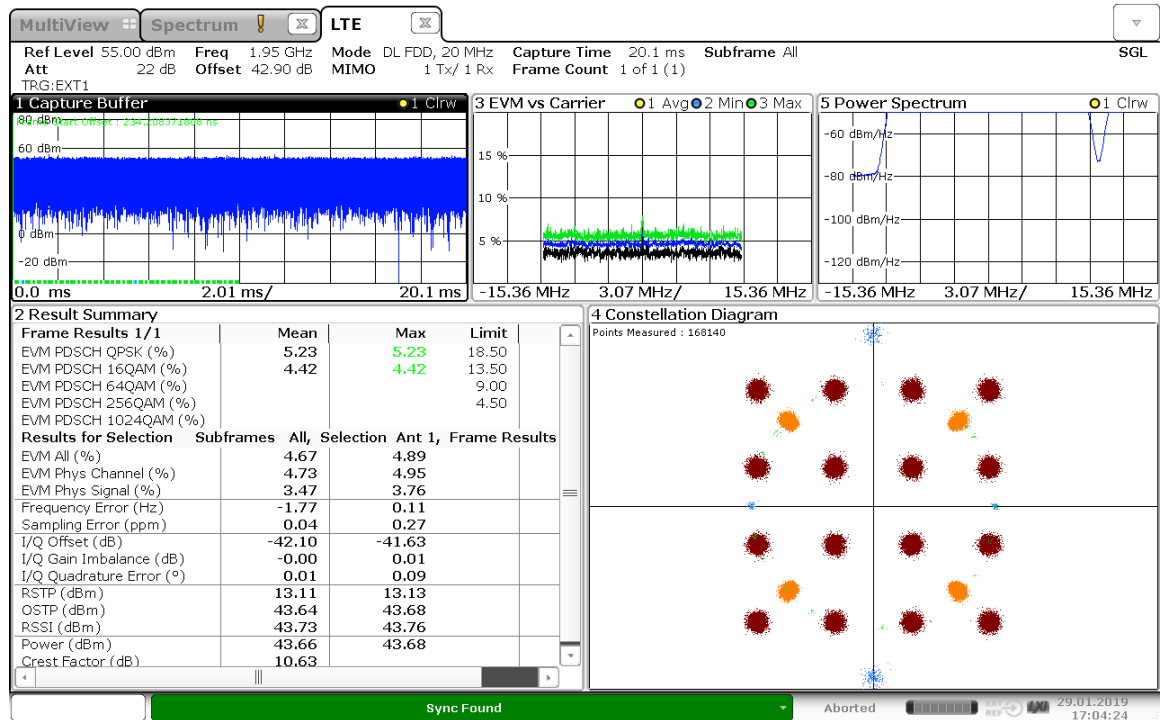


17:44:24 29.01.2019

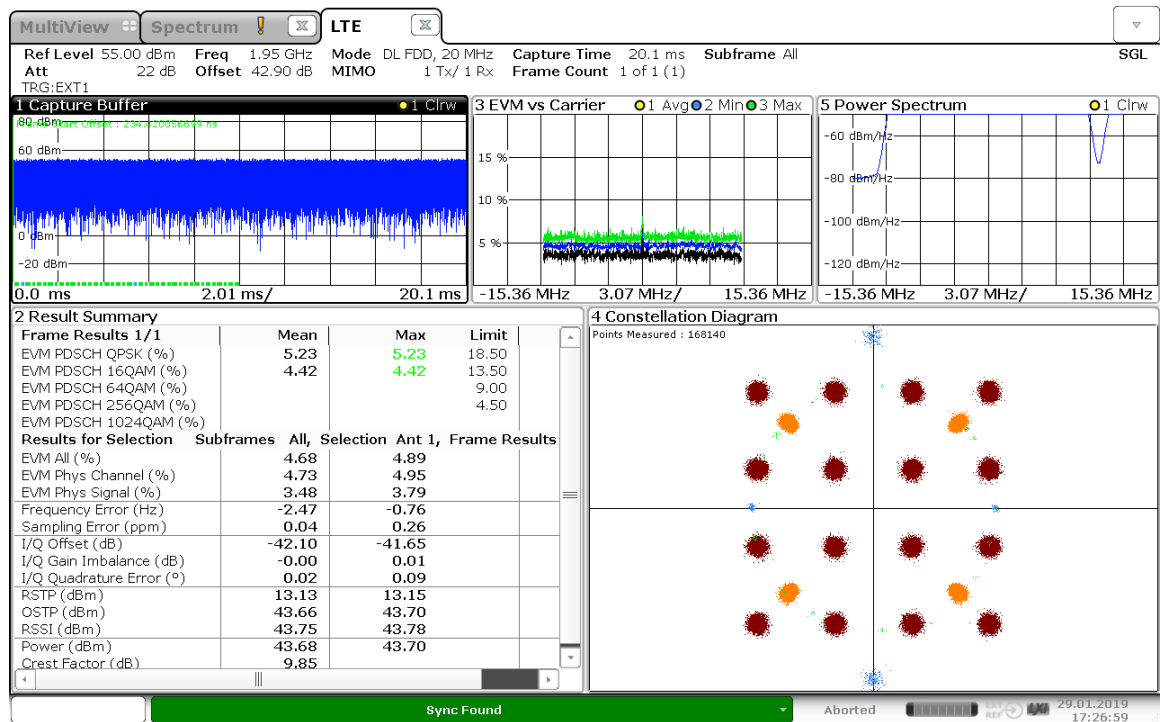


18:07:00 29.01.2019

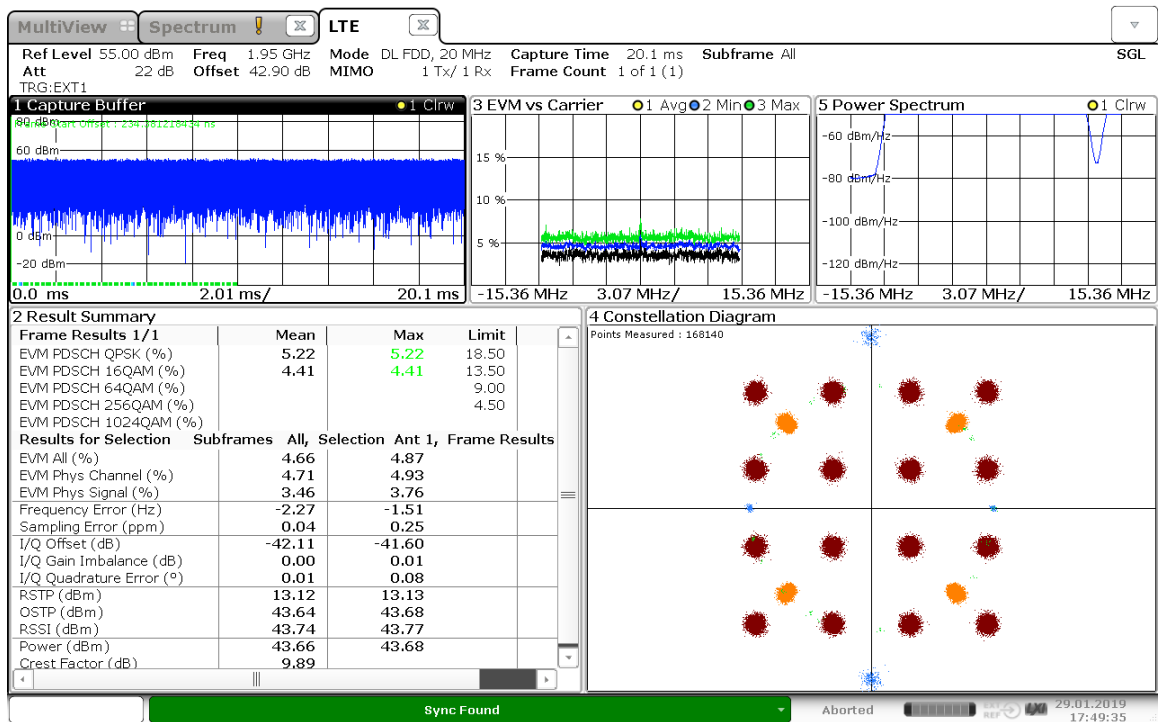
## 20M -1950MHz-TM3.2-Port 1 ~4:



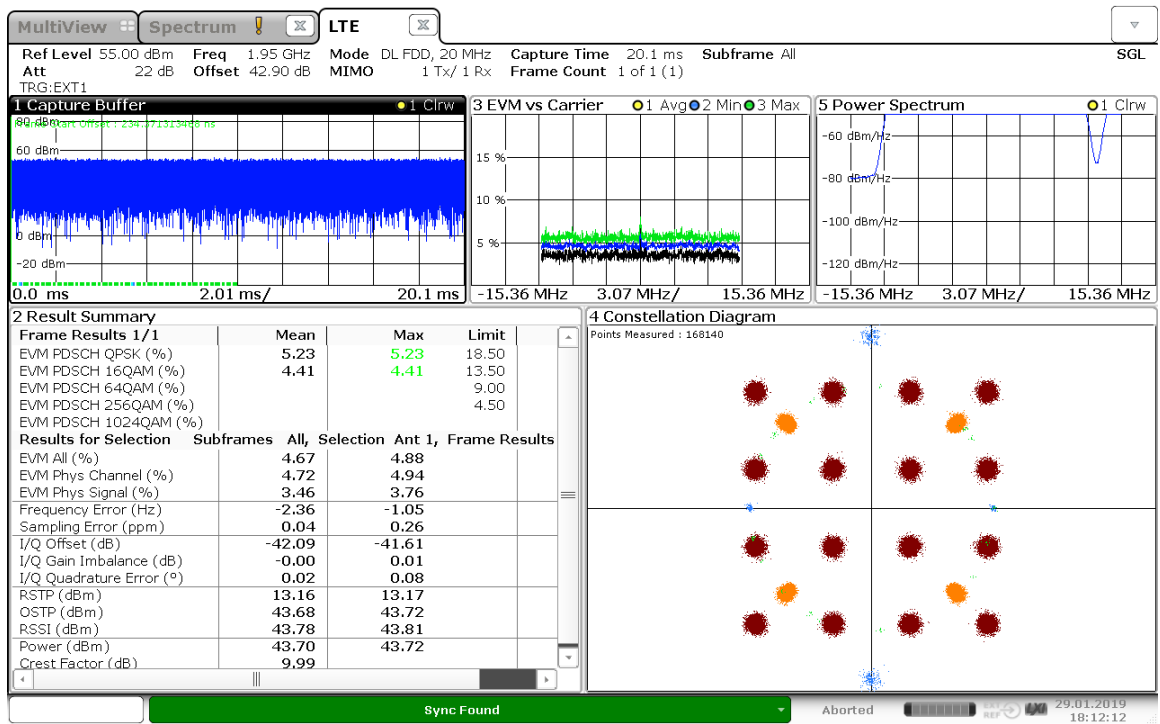
17:04:24 29.01.2019



17:26:59 29.01.2019

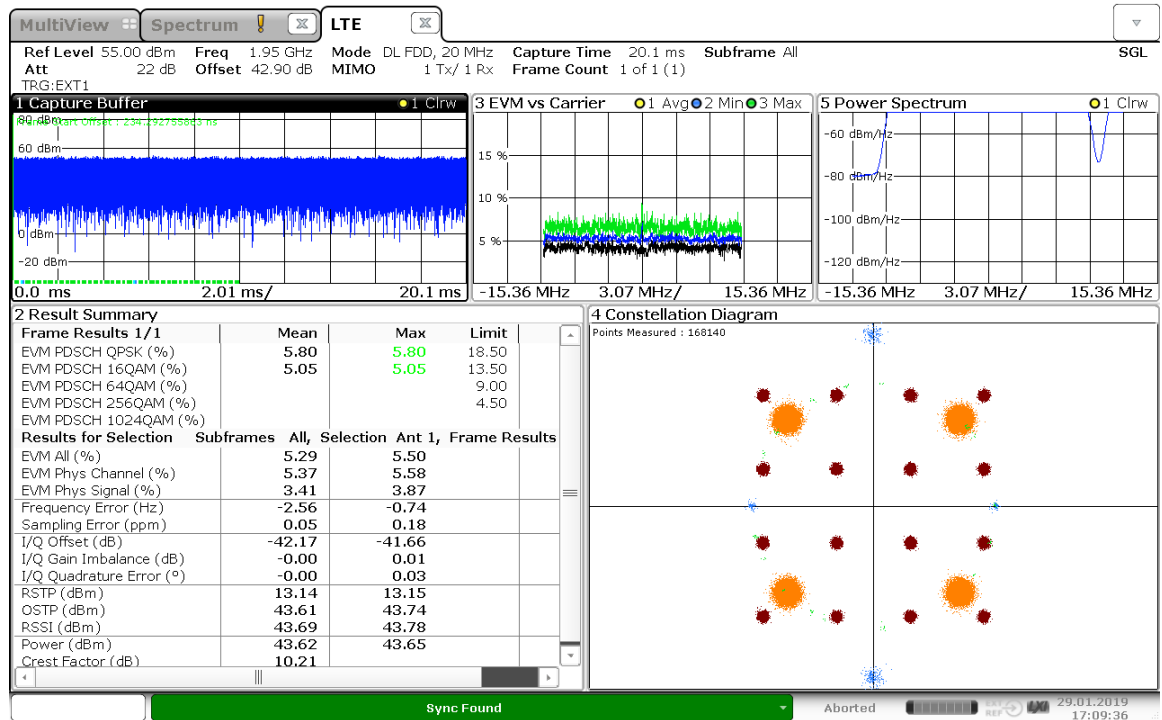


17:49:36 29.01.2019

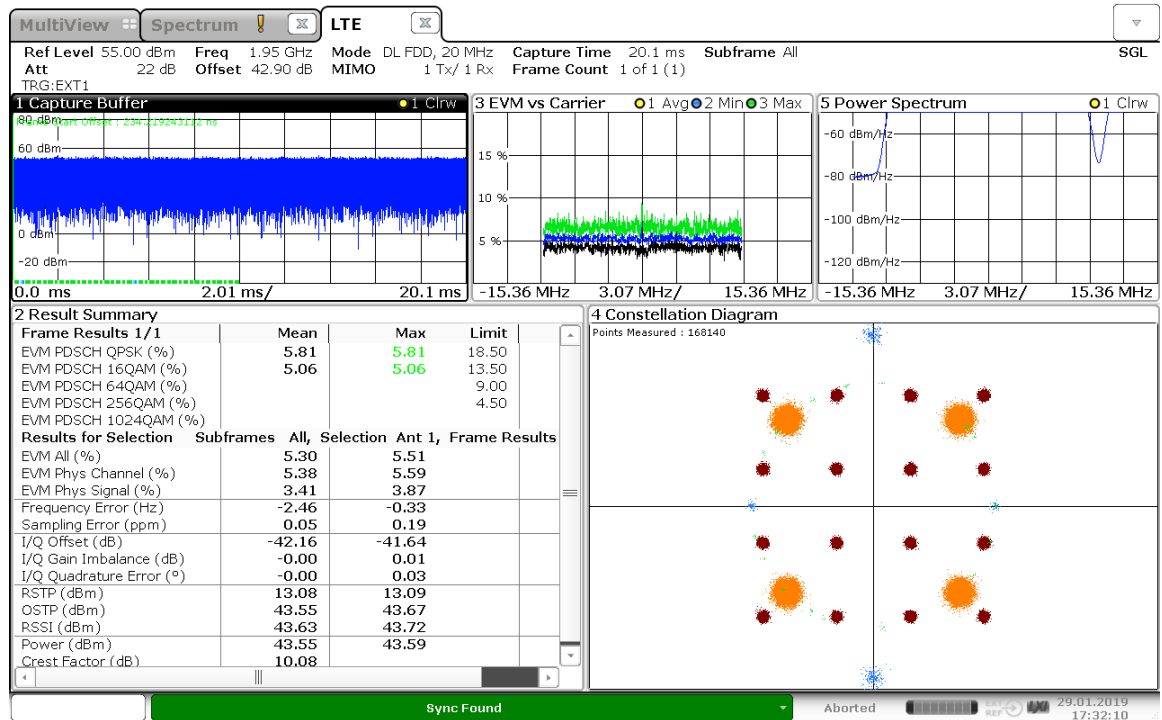


18:12:12 29.01.2019

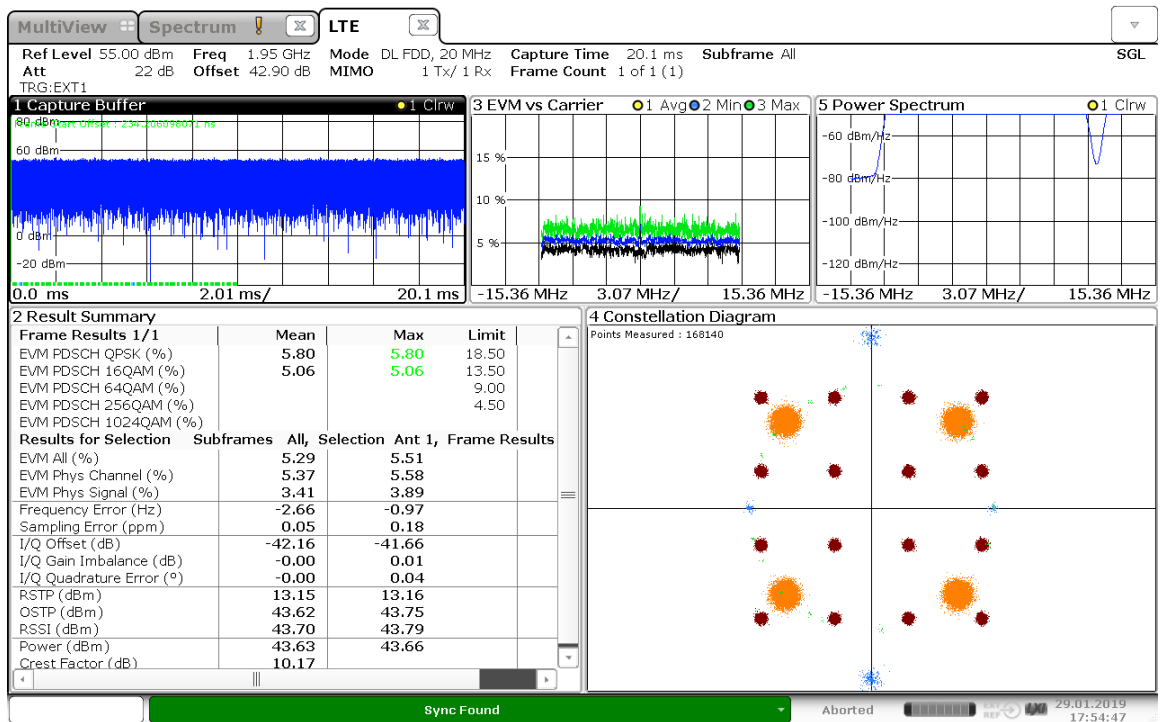
## 20M -1950MHz-TM3.3-Port 1 ~4:



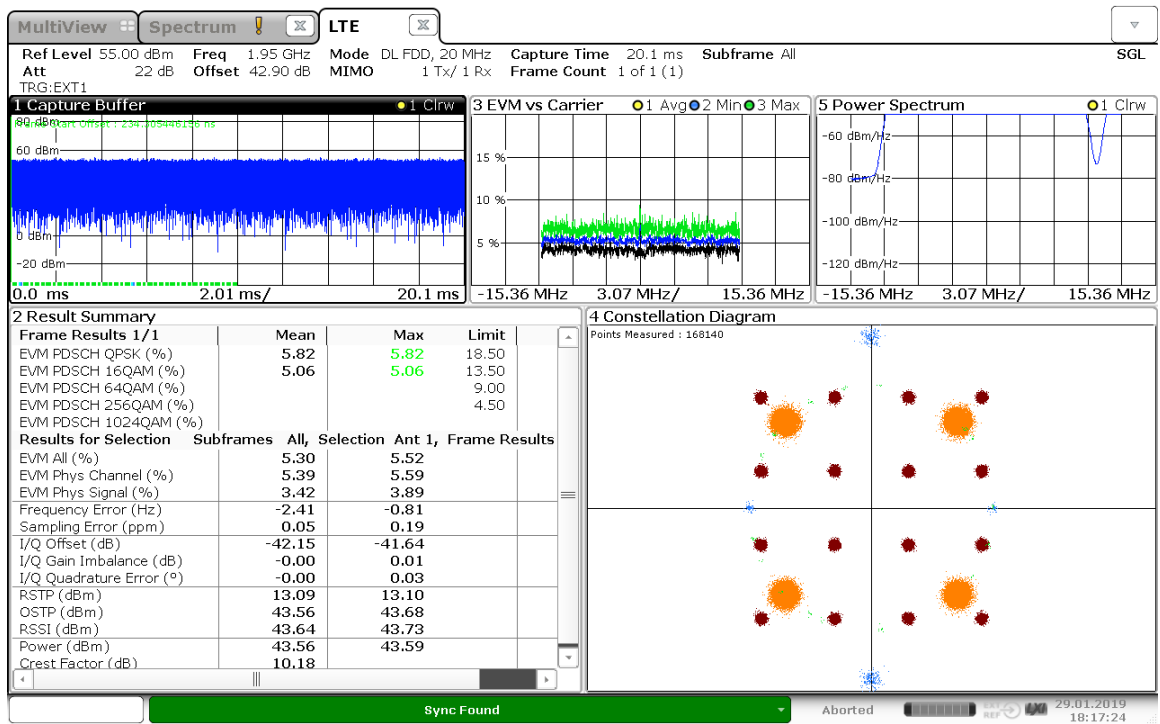
17:09:36 29.01.2019



17:32:10 29.01.2019

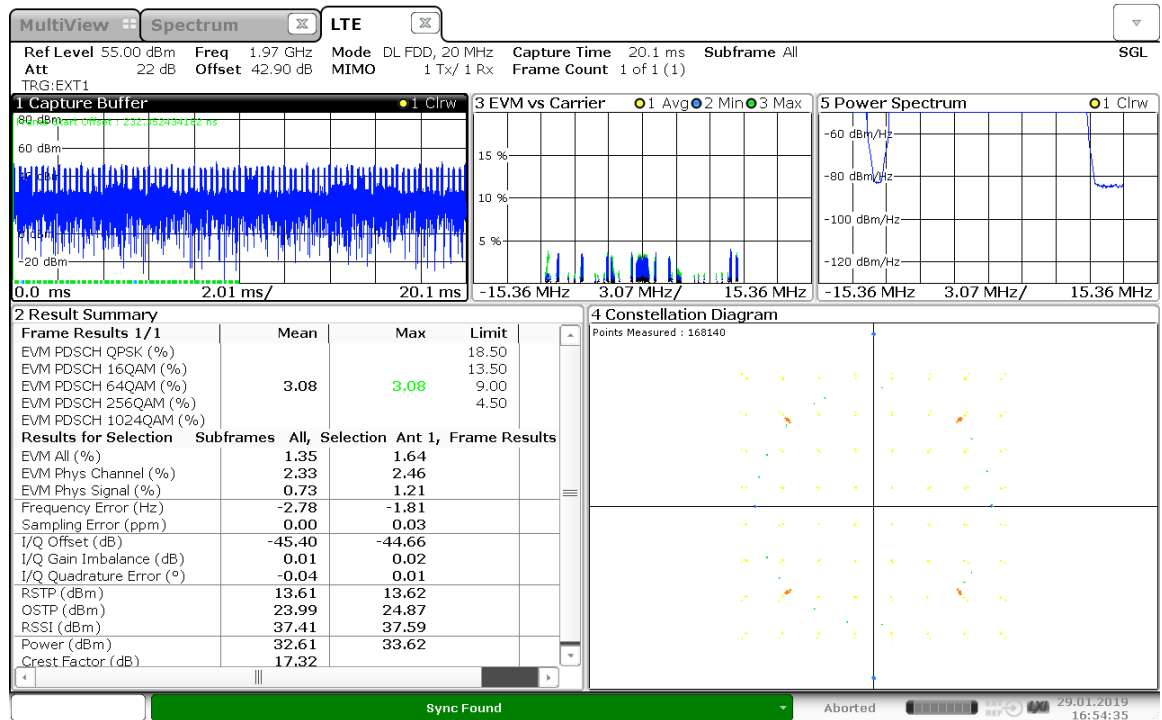


17:54:47 29.01.2019

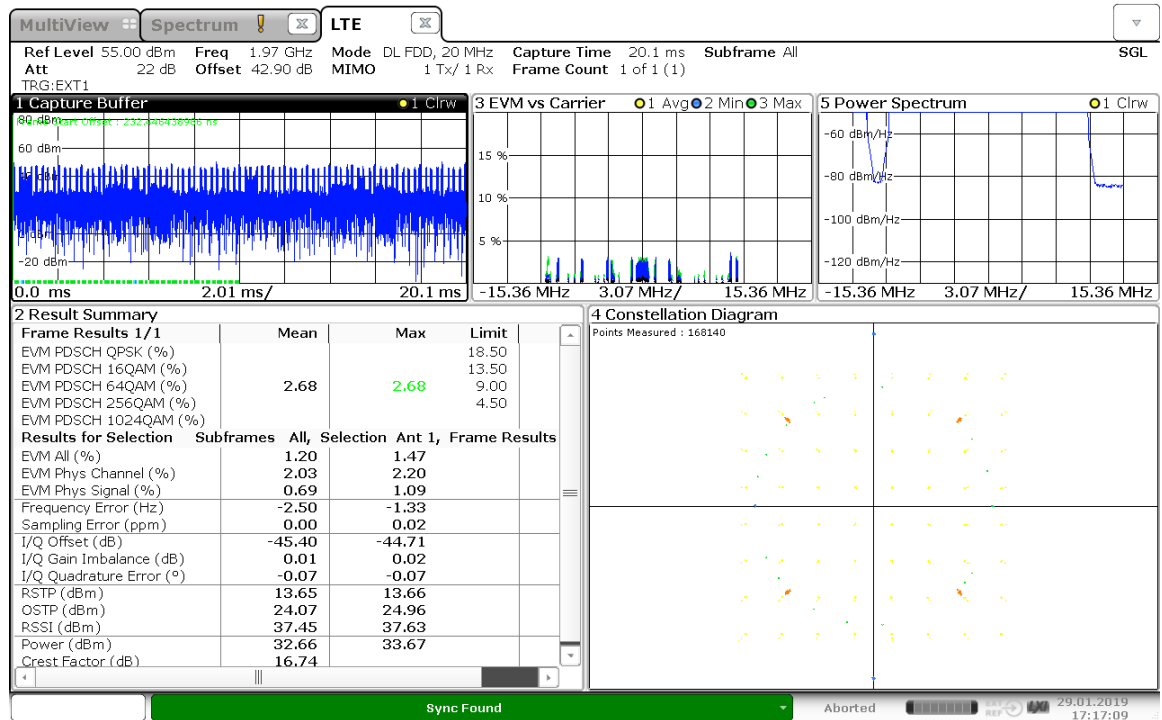


18:17:24 29.01.2019

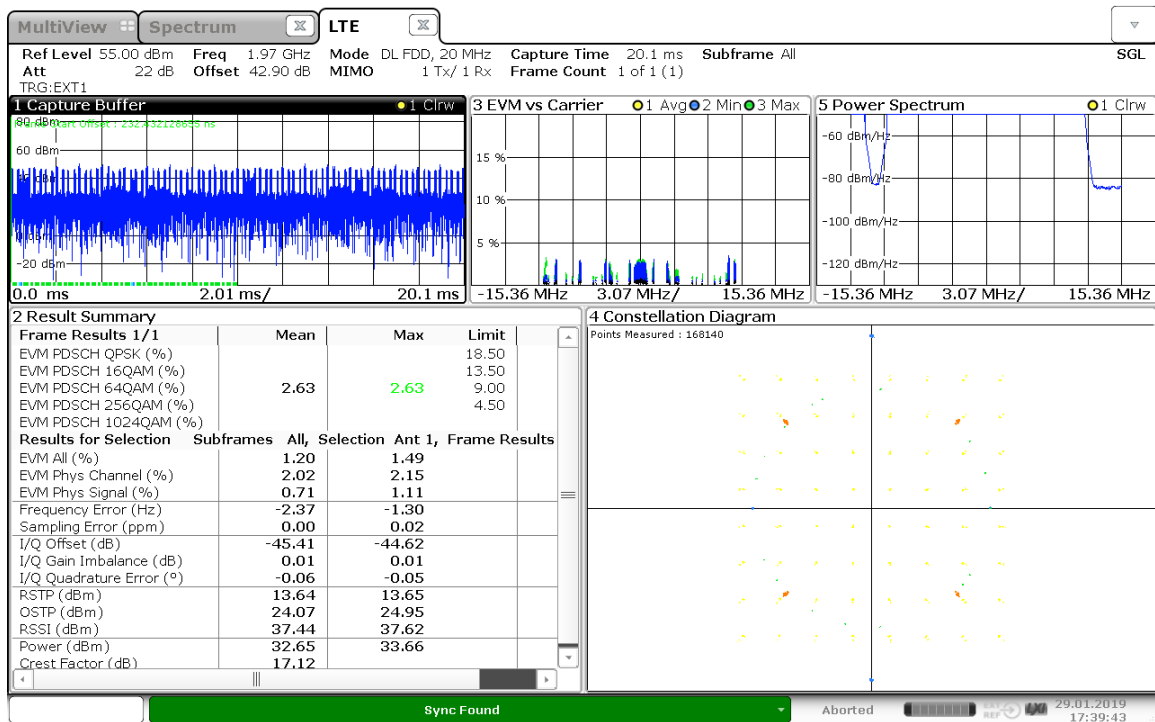
## 20M -1970MHz-TM2.0-Port 1 ~4:



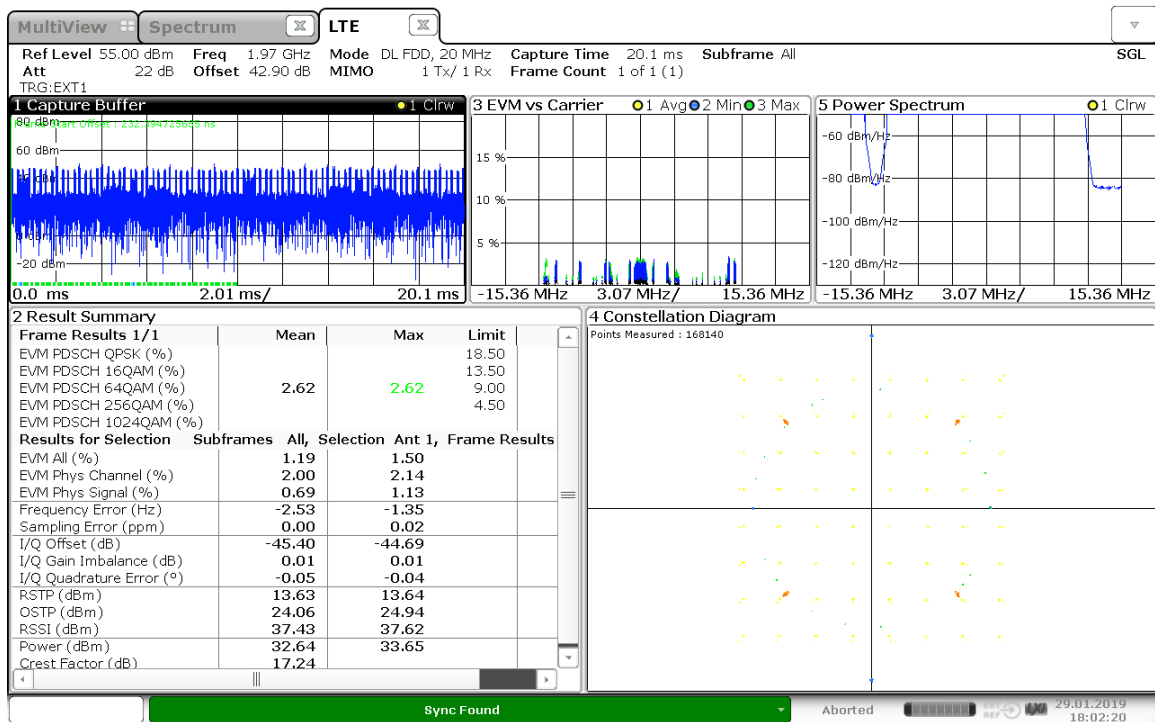
16:54:36 29.01.2019



17:17:10 29.01.2019



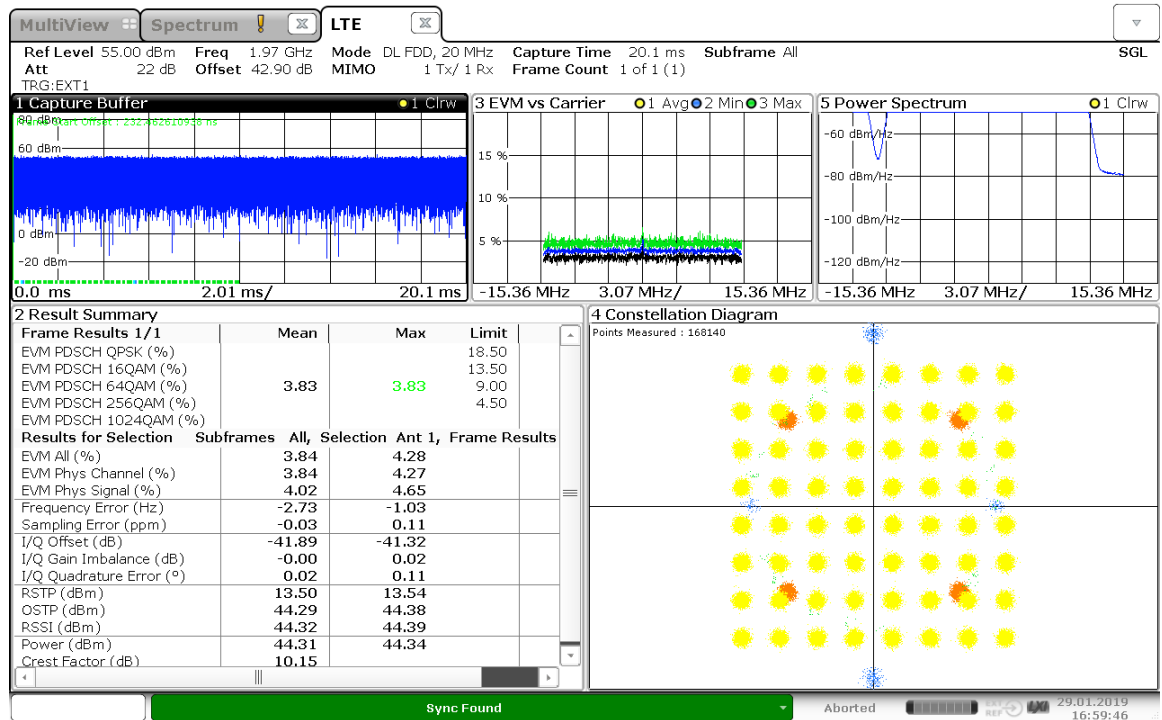
17:39:44 29.01.2019



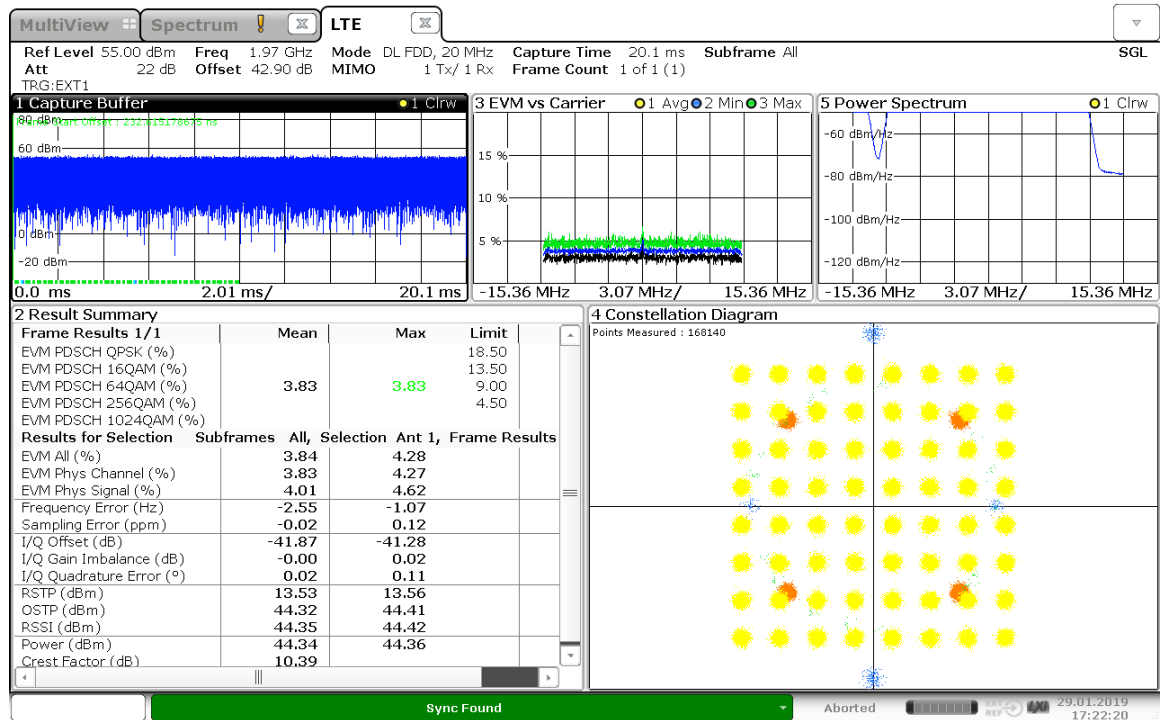
18:02:21 29.01.2019



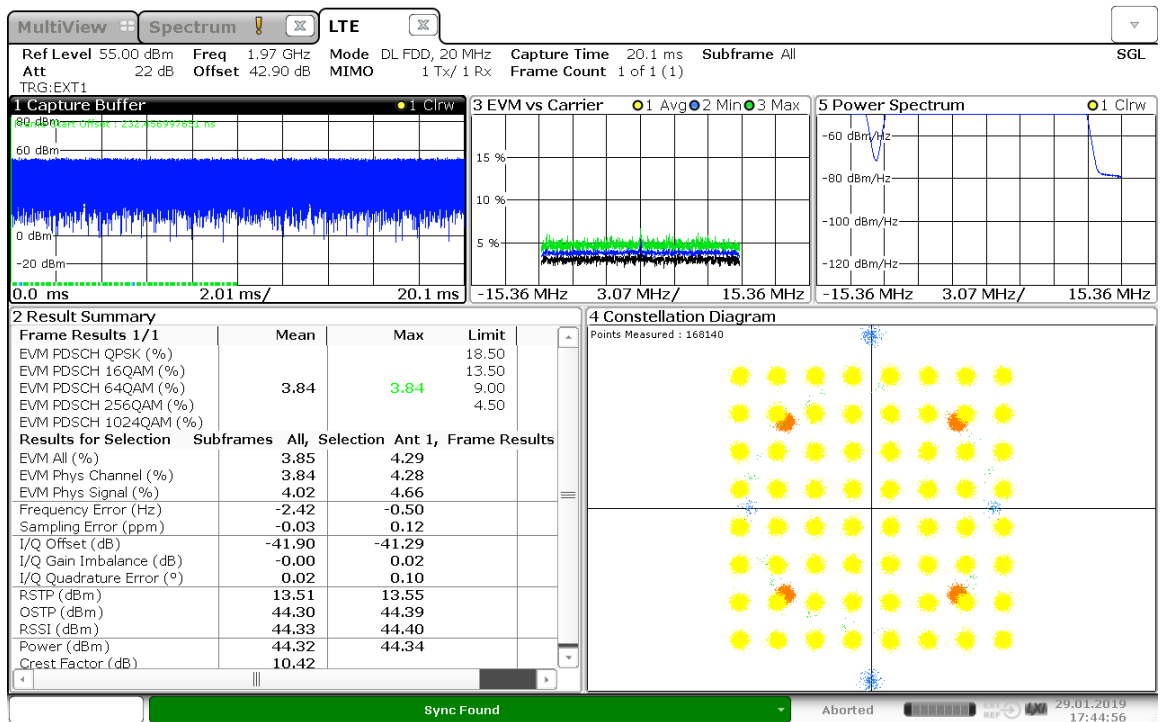
## 20M -1970MHz-TM3.1-Port 1 ~4:



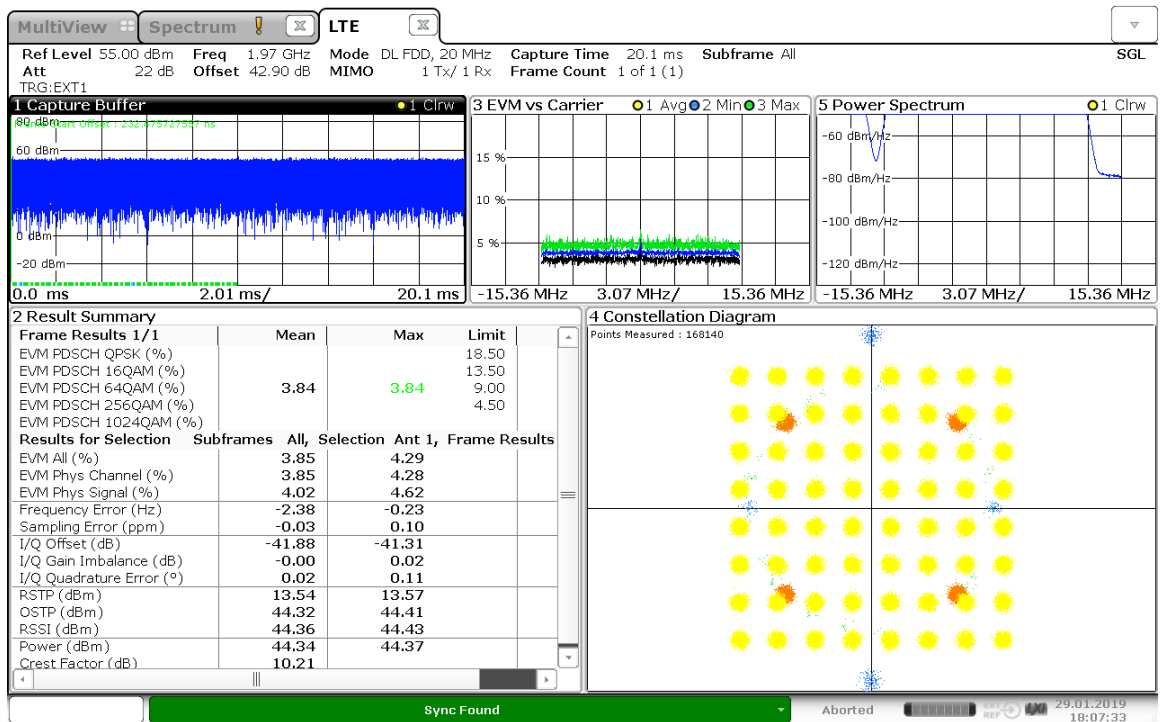
16:59:46 29.01.2019



17:22:21 29.01.2019

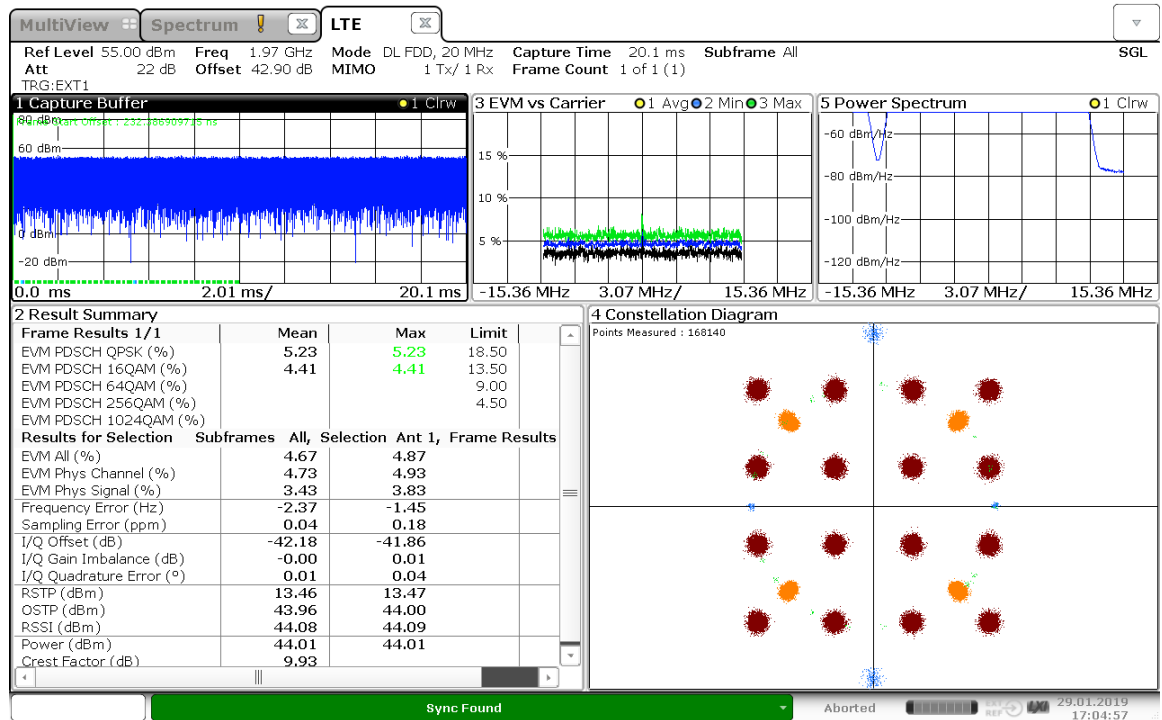


17:44:57 29.01.2019

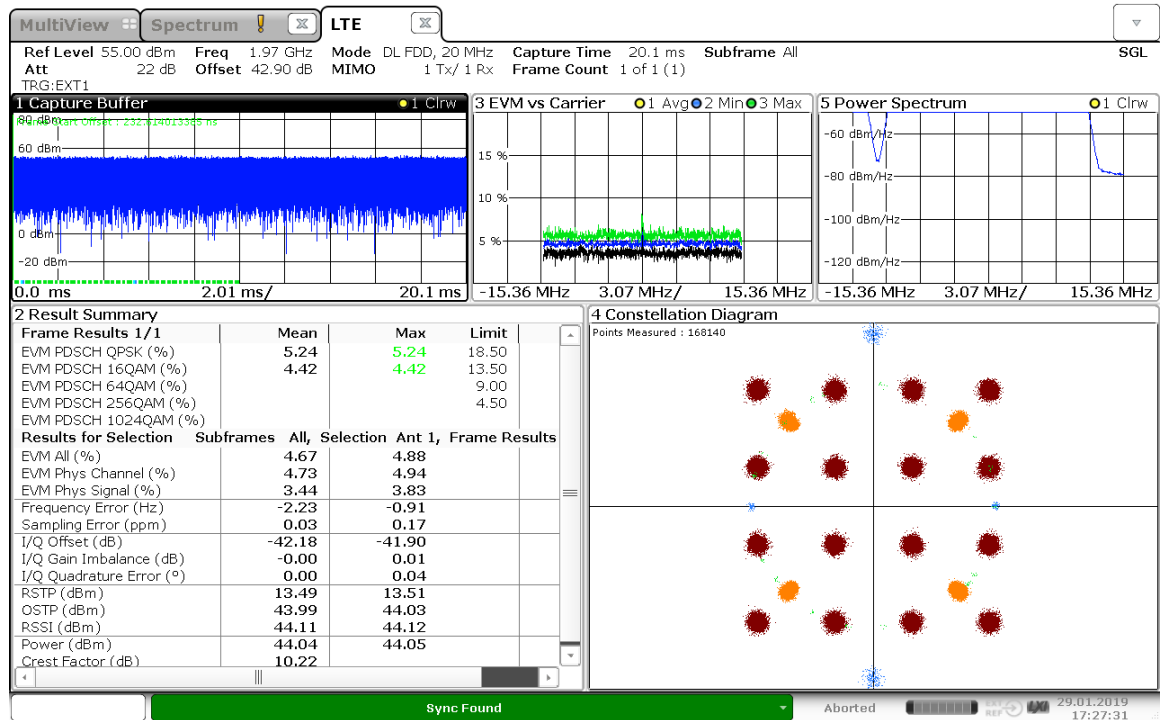


18:07:33 29.01.2019

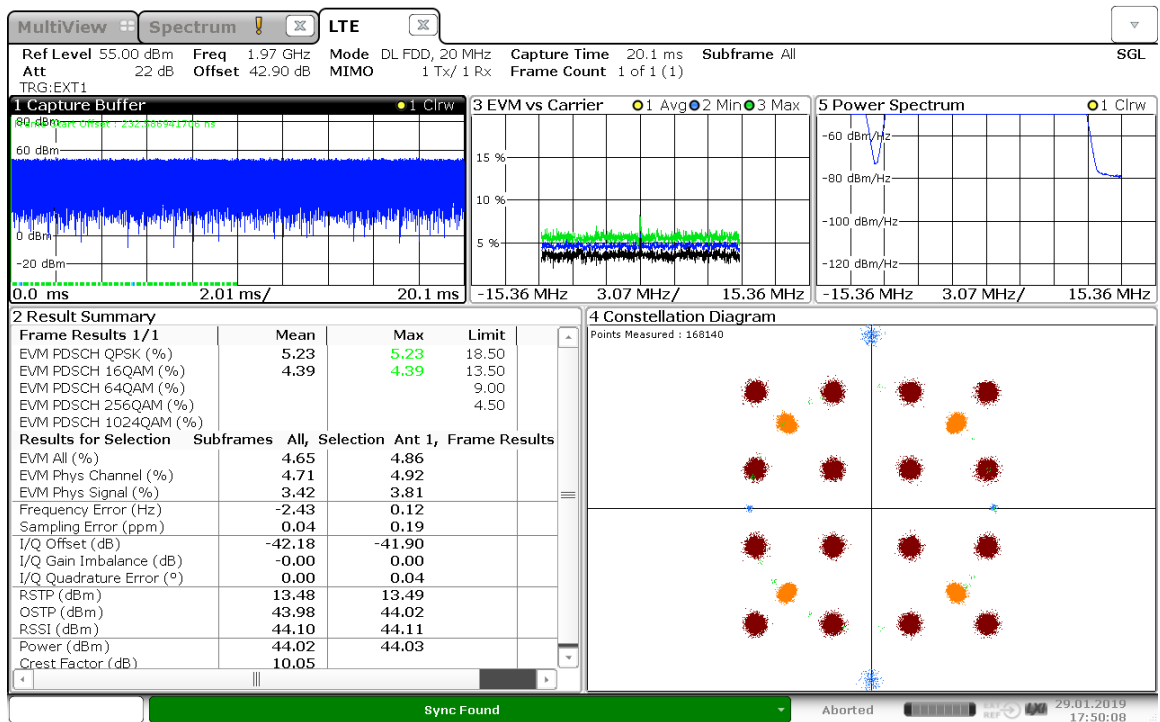
## 20M -1970MHz-TM3.2-Port 1 ~4:



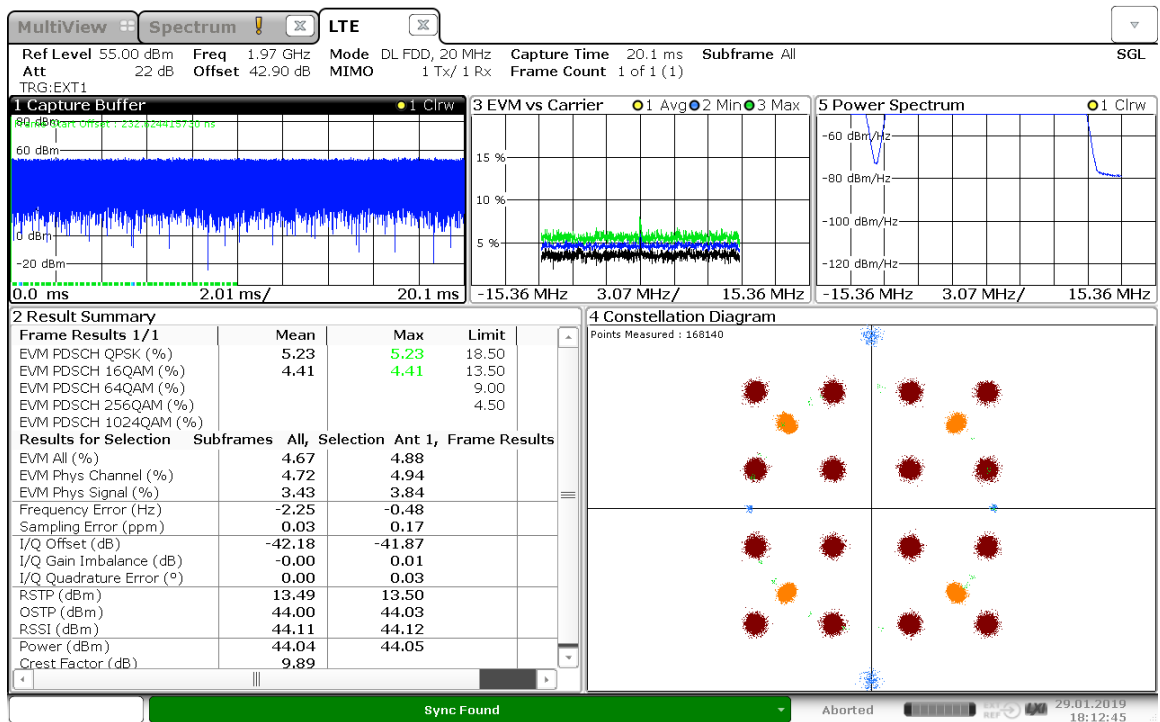
17:04:57 29.01.2019



17:27:32 29.01.2019

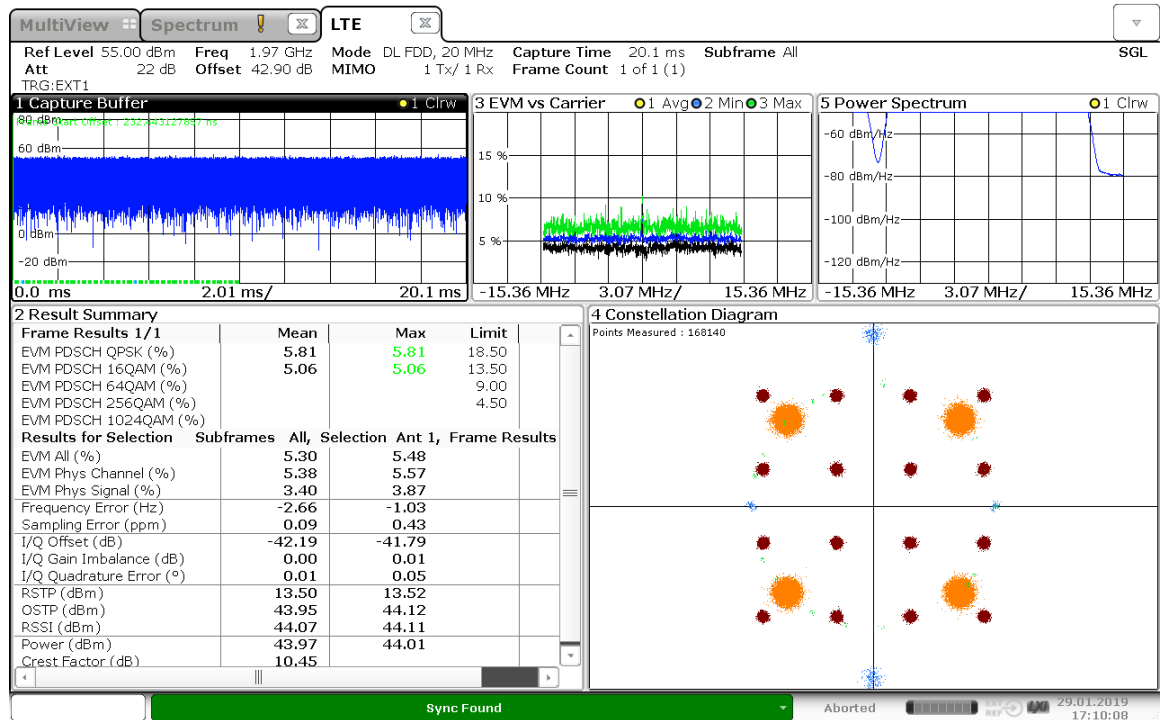


17:50:08 29.01.2019

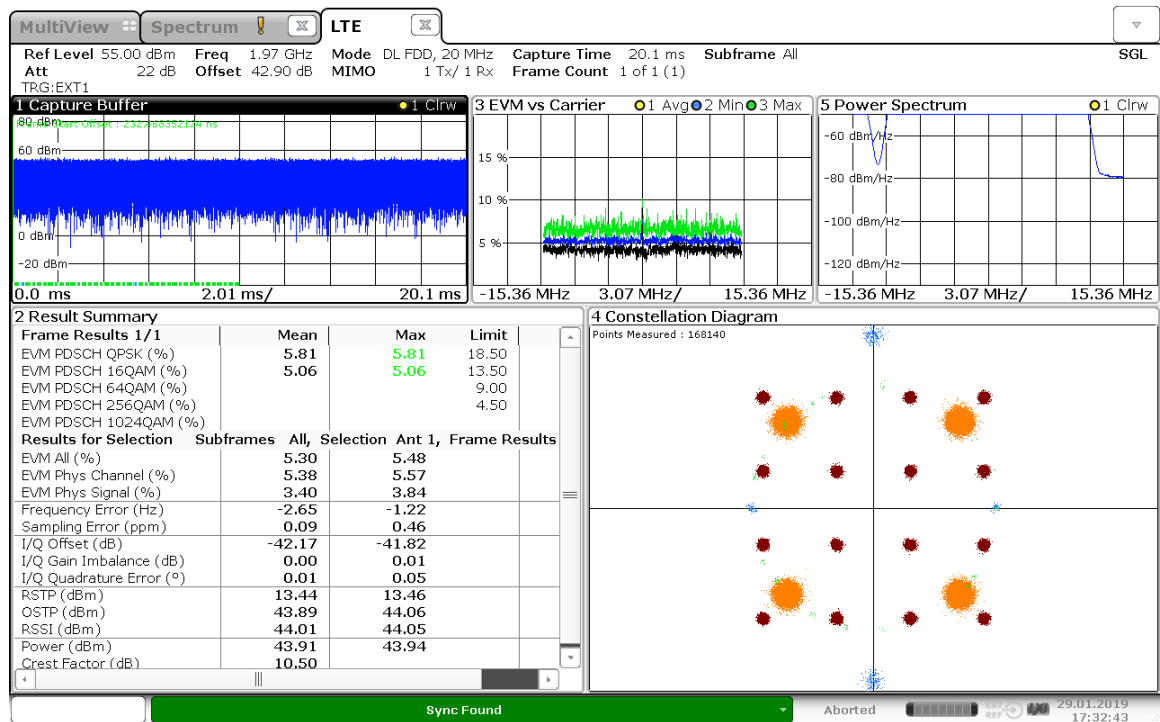


18:12:45 29.01.2019

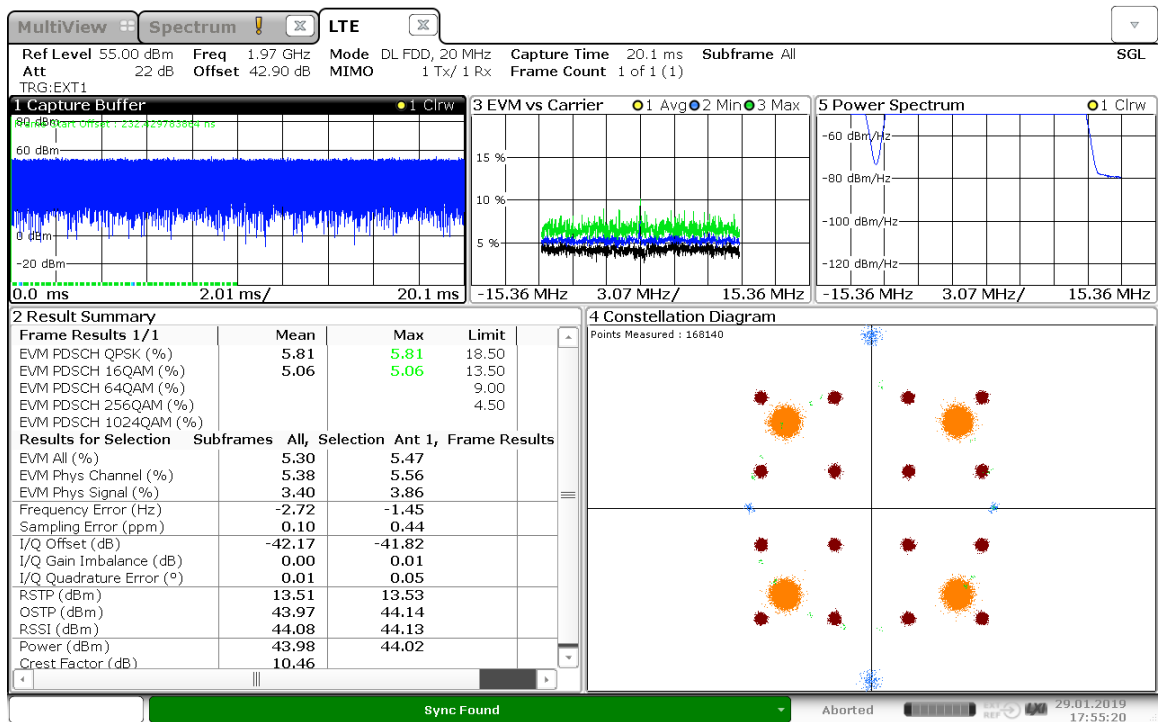
## 20M -1970MHz-TM3.3-Port 1 ~4:



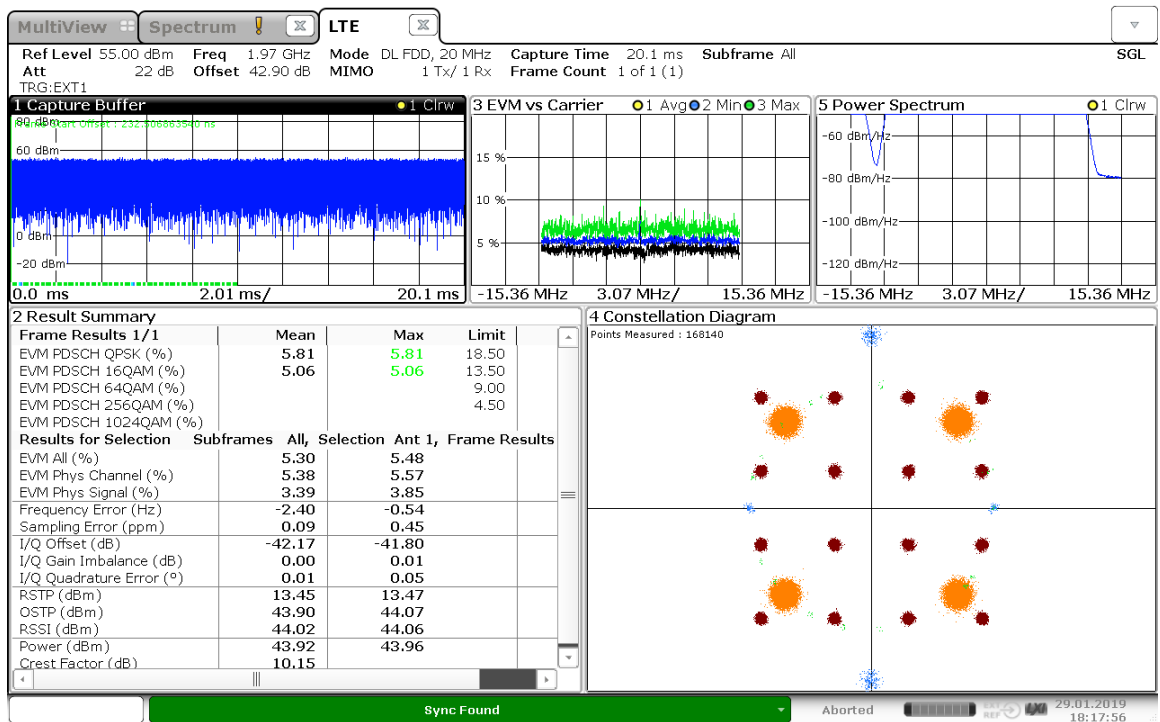
17:10:09 29.01.2019



17:32:43 29.01.2019



17:55:20 29.01.2019

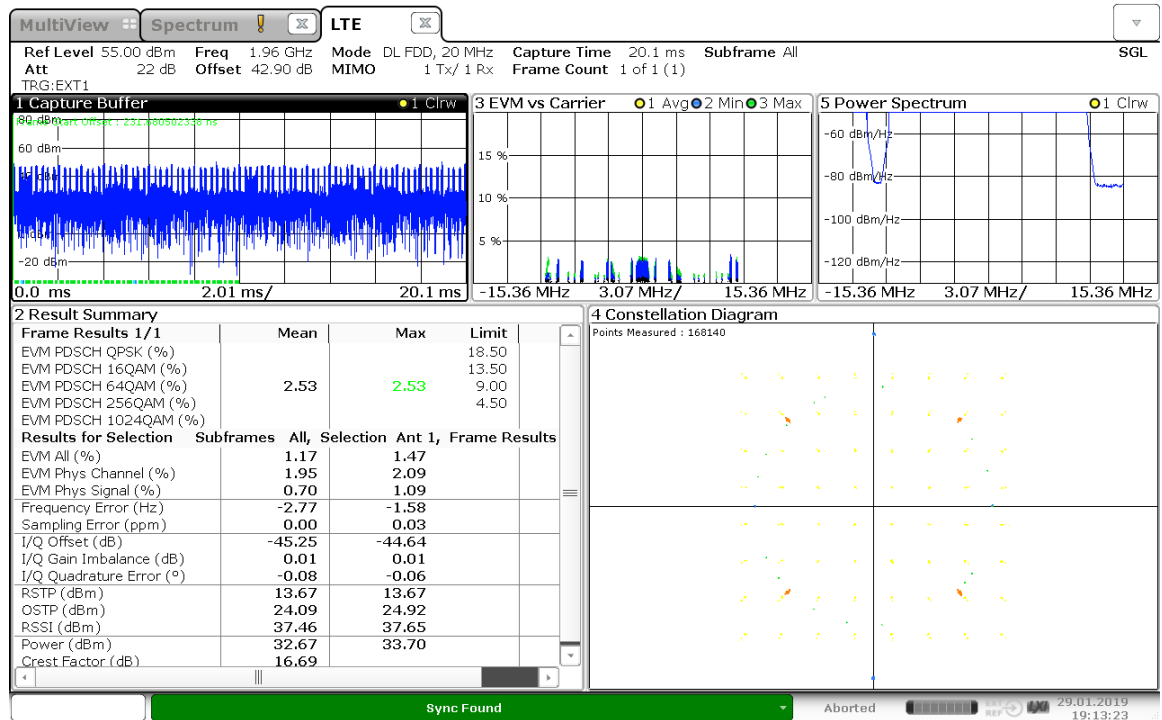


18:17:57 29.01.2019

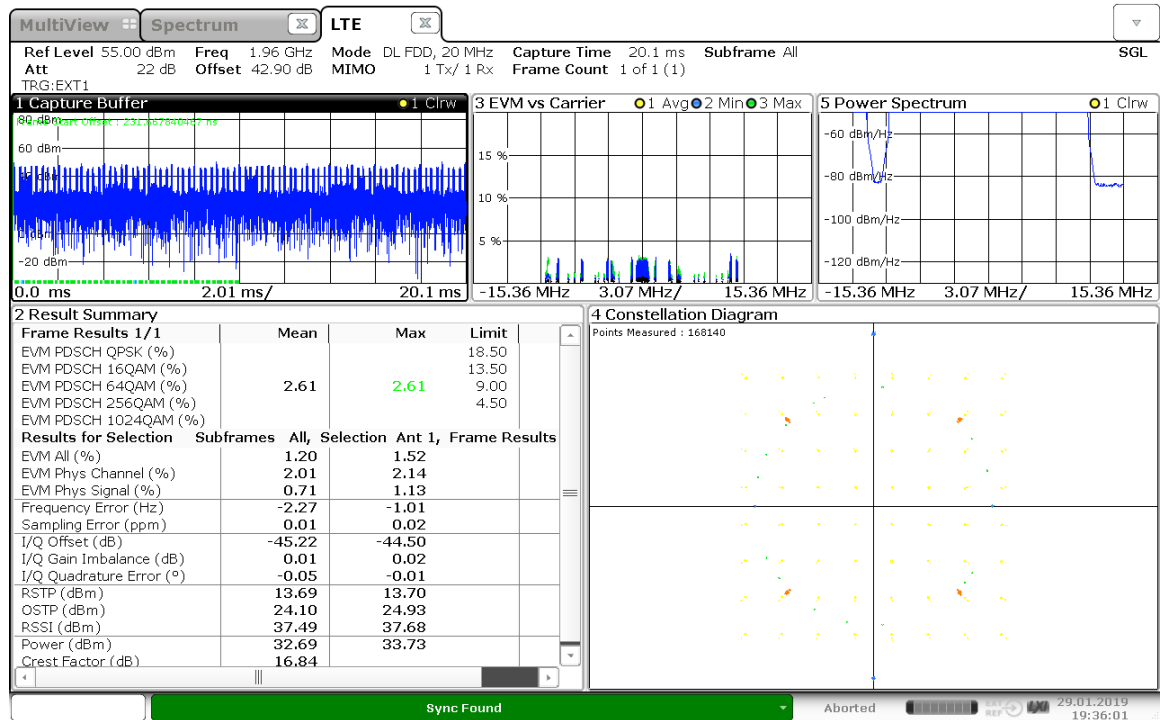
Channel Bandwidth :20M+20M(1960MHz+1980MHz)

Frequency (MHz)	Test mode	Port	EVM(%)
1960	TM2.0	1	2.53
		2	2.61
		3	2.32
		4	2.68
	TM3.1	1	3.83
		2	3.84
		3	3.83
		4	3.84
	TM3.2	1	4.42
		2	4.41
		3	4.43
		4	4.41
	TM3.3	1	5.83
		2	5.82
		3	5.82
		4	5.83
1980	TM2.0	1	3.07
		2	2.79
		3	2.72
		4	2.92
	TM3.1	1	3.84
		2	3.83
		3	3.84
		4	3.82
	TM3.2	1	4.42
		2	4.42
		3	4.42
		4	4.42
	TM3.3	1	5.82
		2	5.82
		3	5.83
		4	5.82

## 20M -1960MHz-TM2.0-Port 1 ~4:

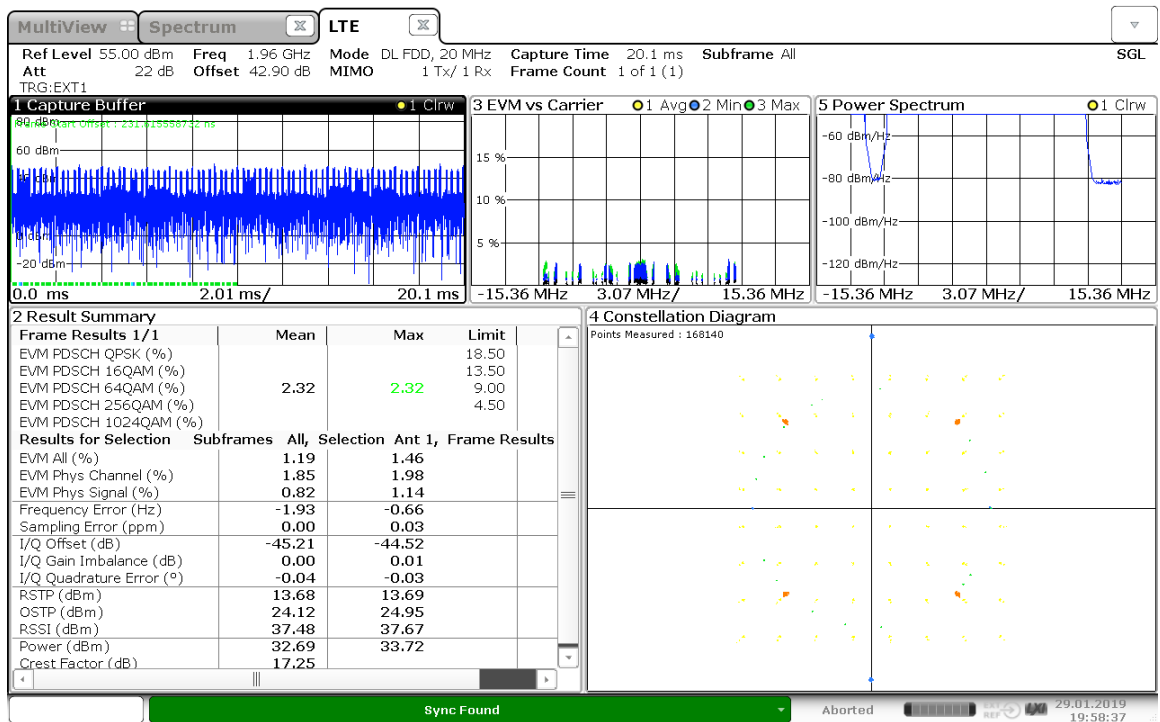


19:13:24 29.01.2019

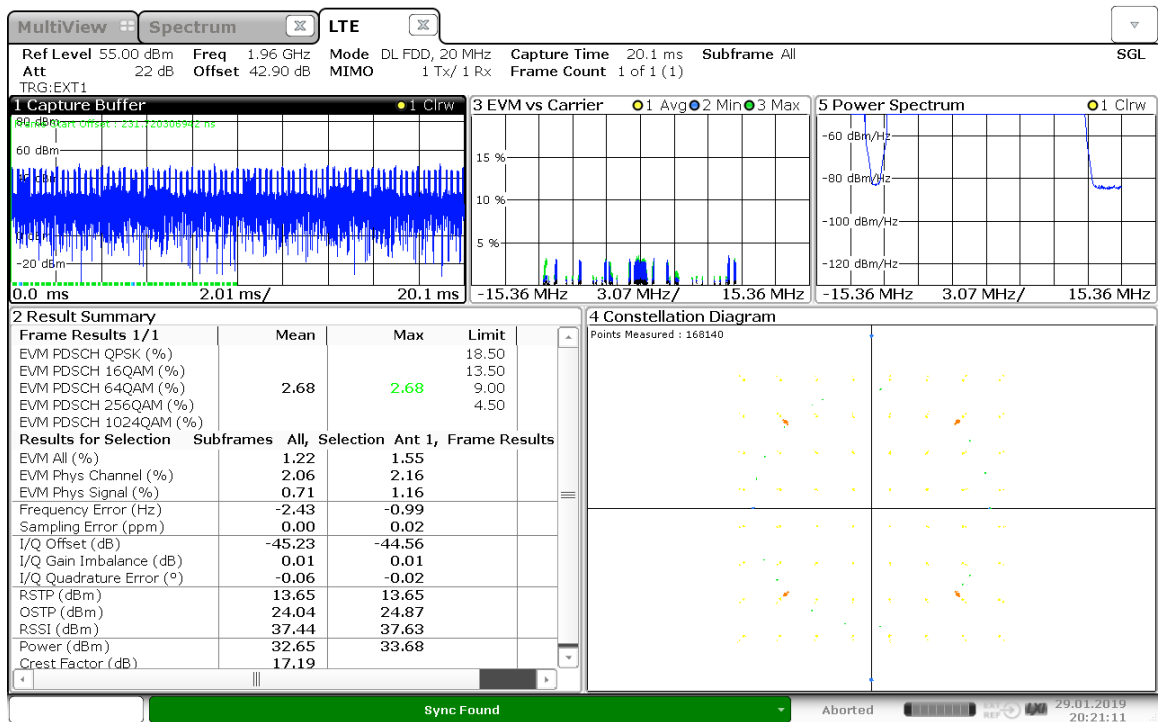


19:36:01 29.01.2019



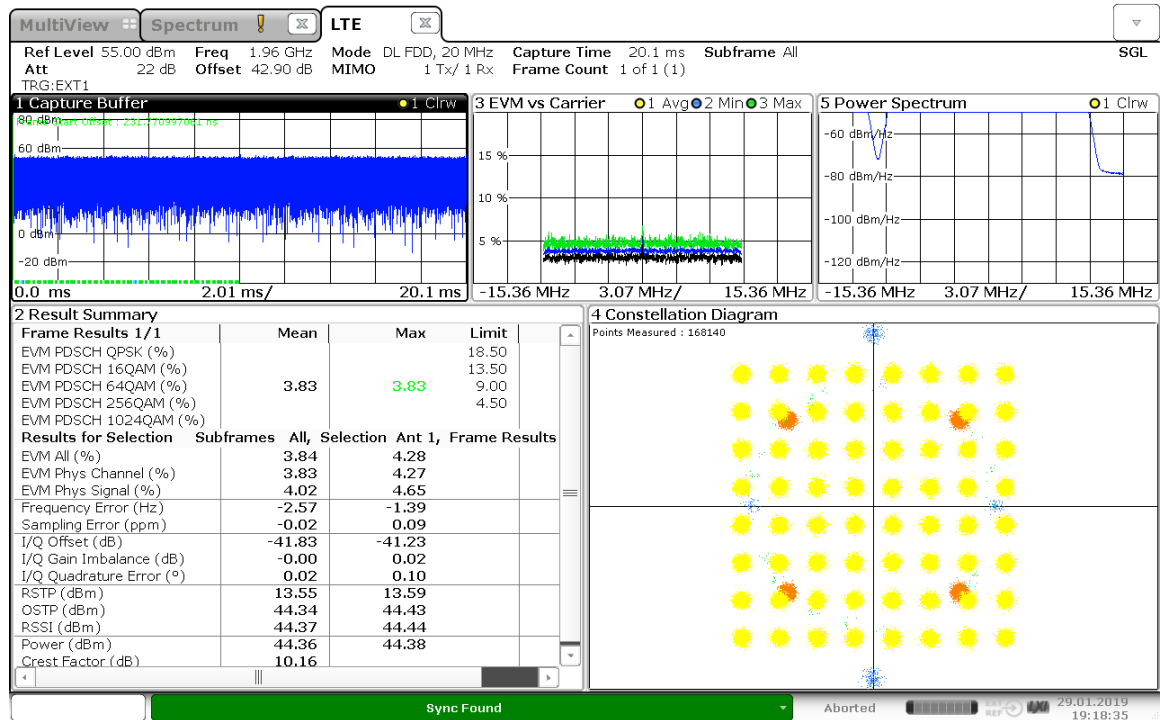


19:58:37 29.01.2019

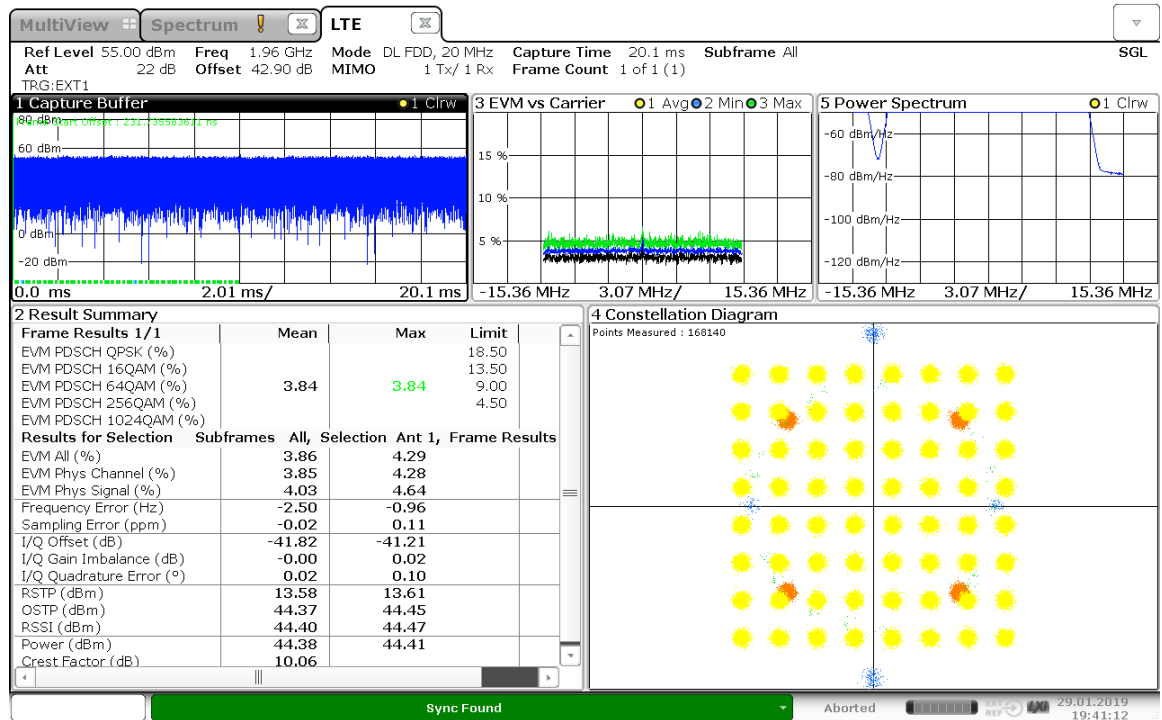


20:21:12 29.01.2019

## 20M -1960MHz-TM3.1-Port 1 ~4:



19:18:36 29.01.2019



19:41:13 29.01.2019

