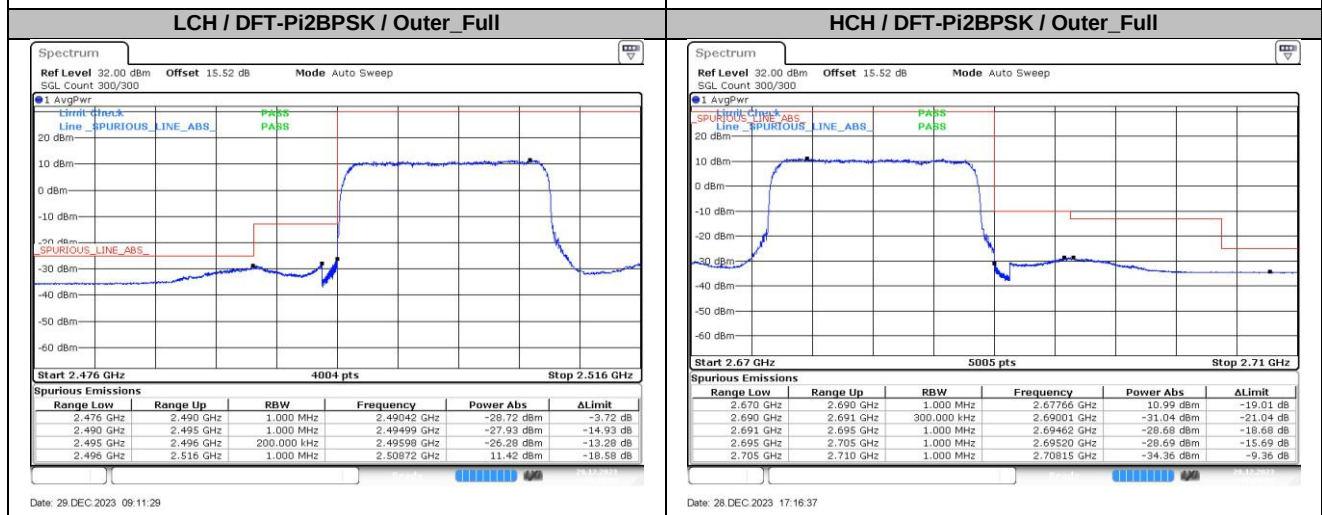
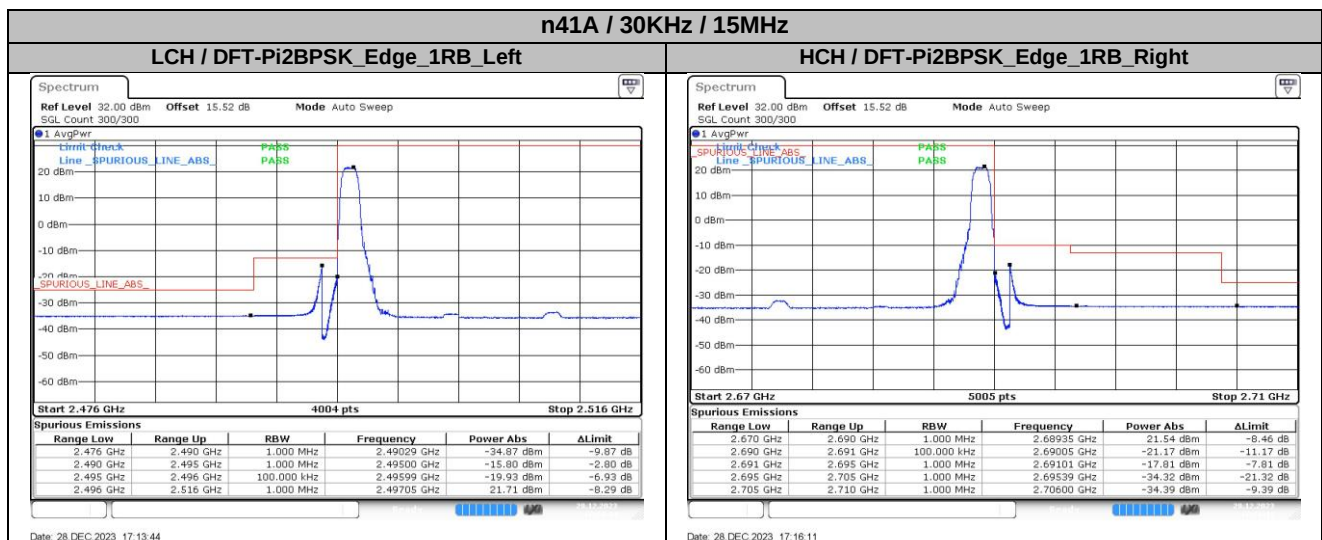
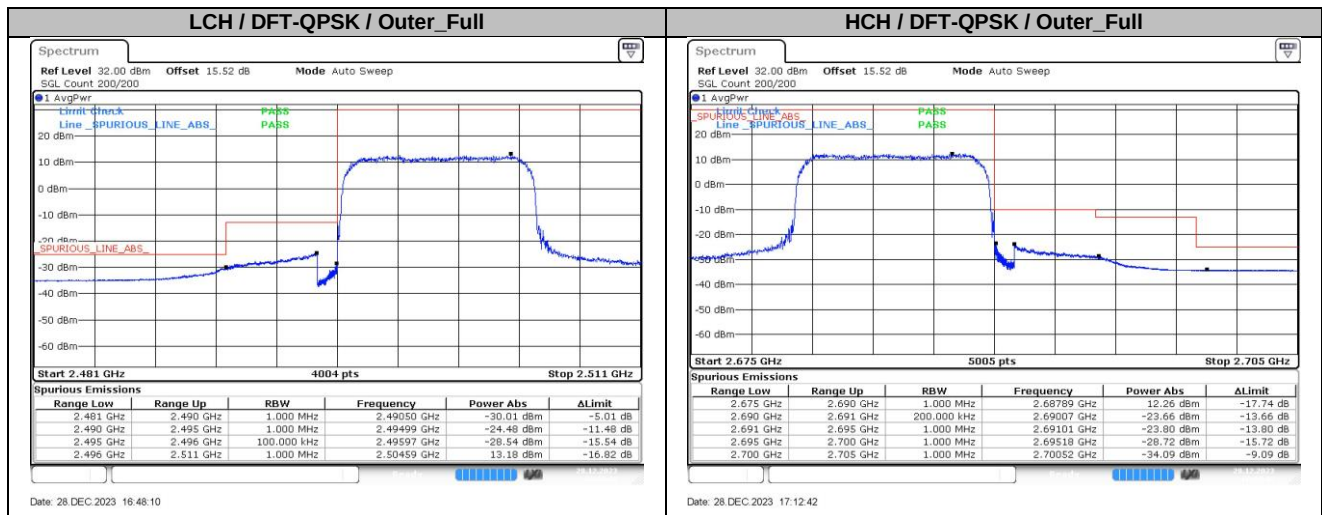
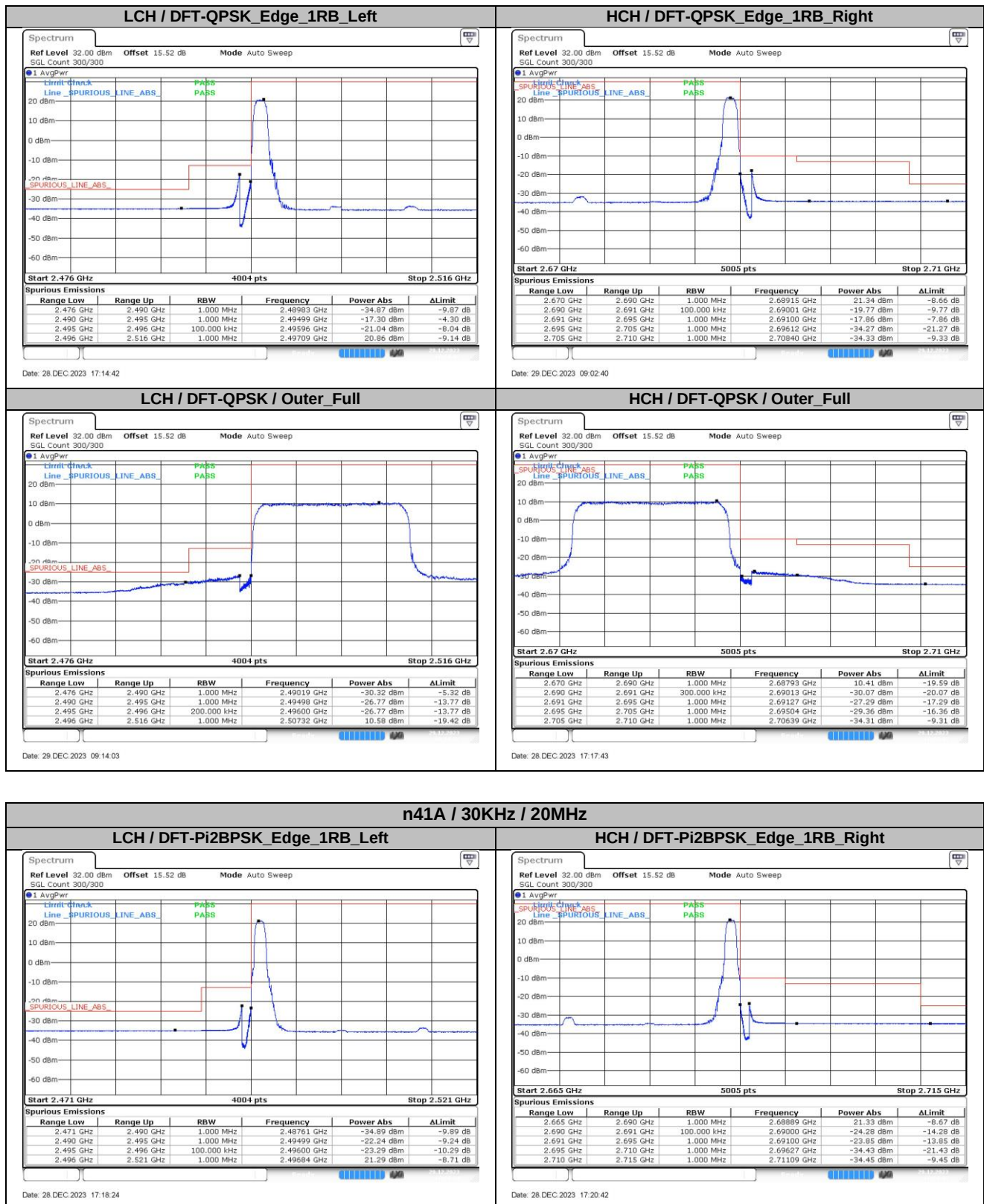
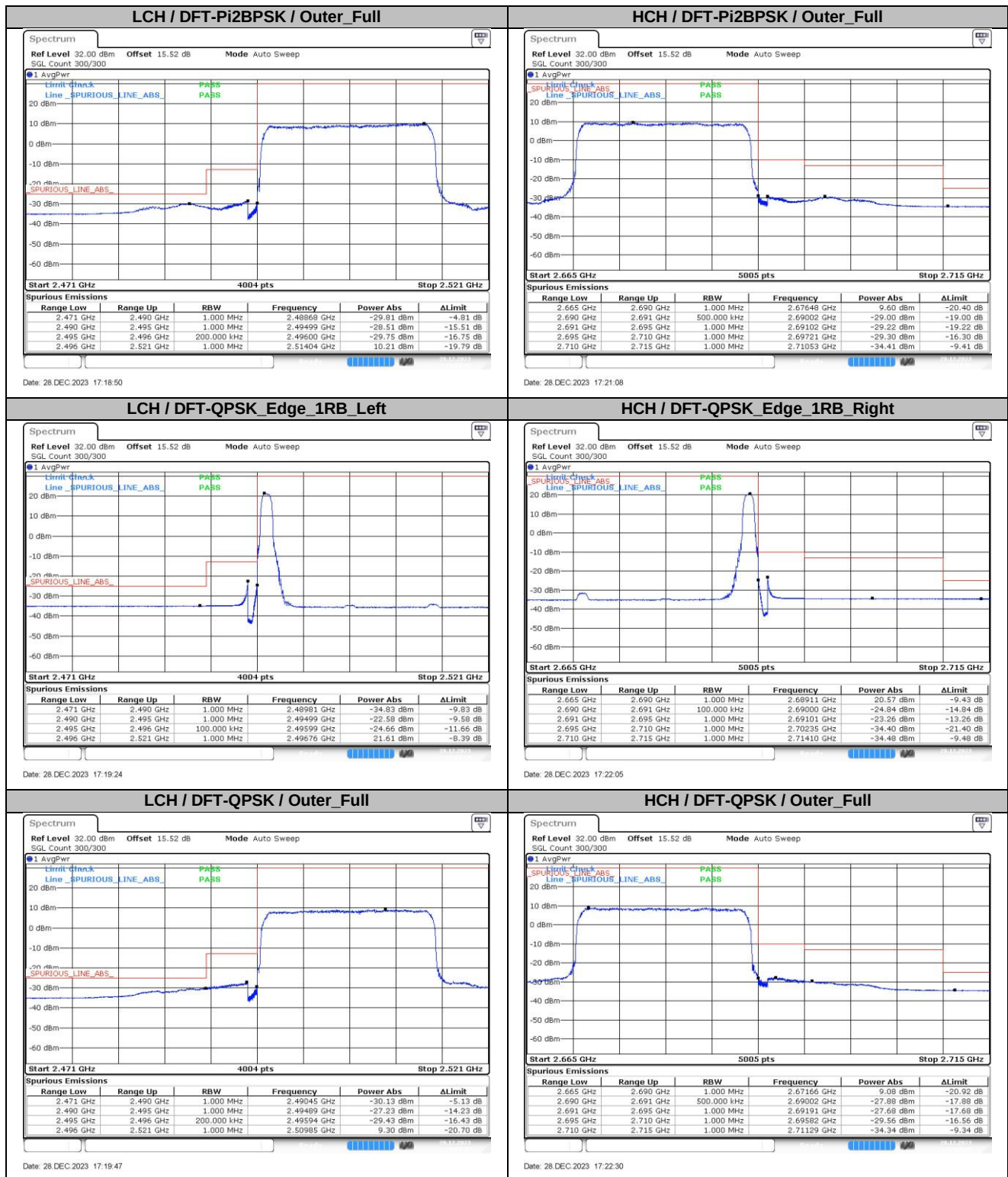








n41A / 30KHz / 30MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



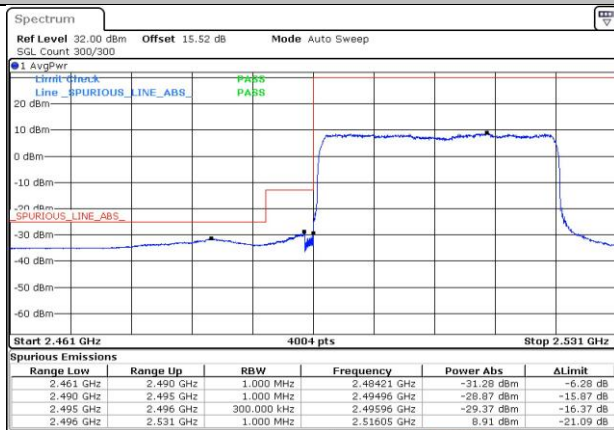
Date: 19 DEC 2023 02:27:32

HCH / DFT-Pi2BPSK_Edge_1RB_Right



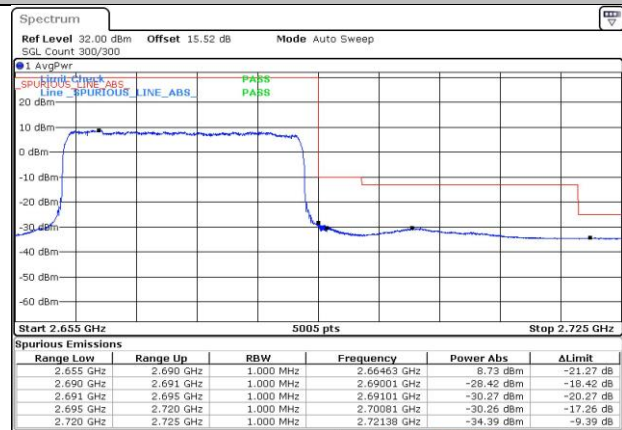
Date: 19 DEC 2023 02:30:08

LCH / DFT-Pi2BPSK / Outer_Full



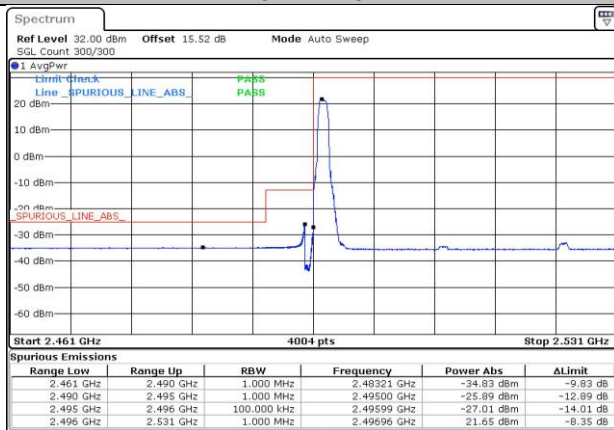
Date: 19 DEC 2023 02:28:15

HCH / DFT-Pi2BPSK / Outer_Full



Date: 19 DEC 2023 02:30:46

LCH / DFT-QPSK_Edge_1RB_Left

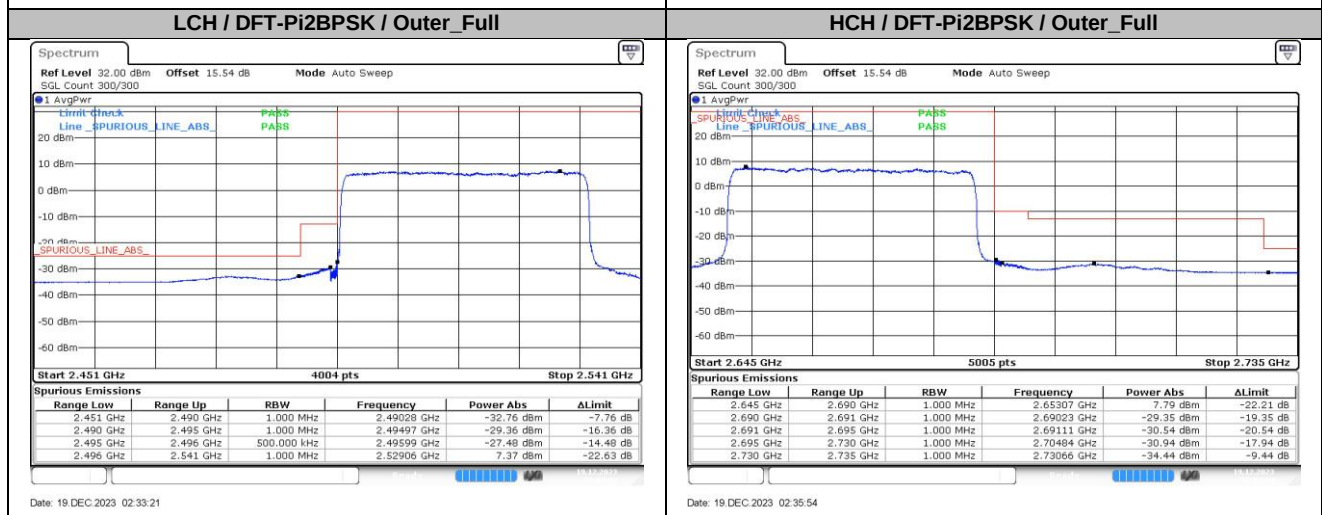
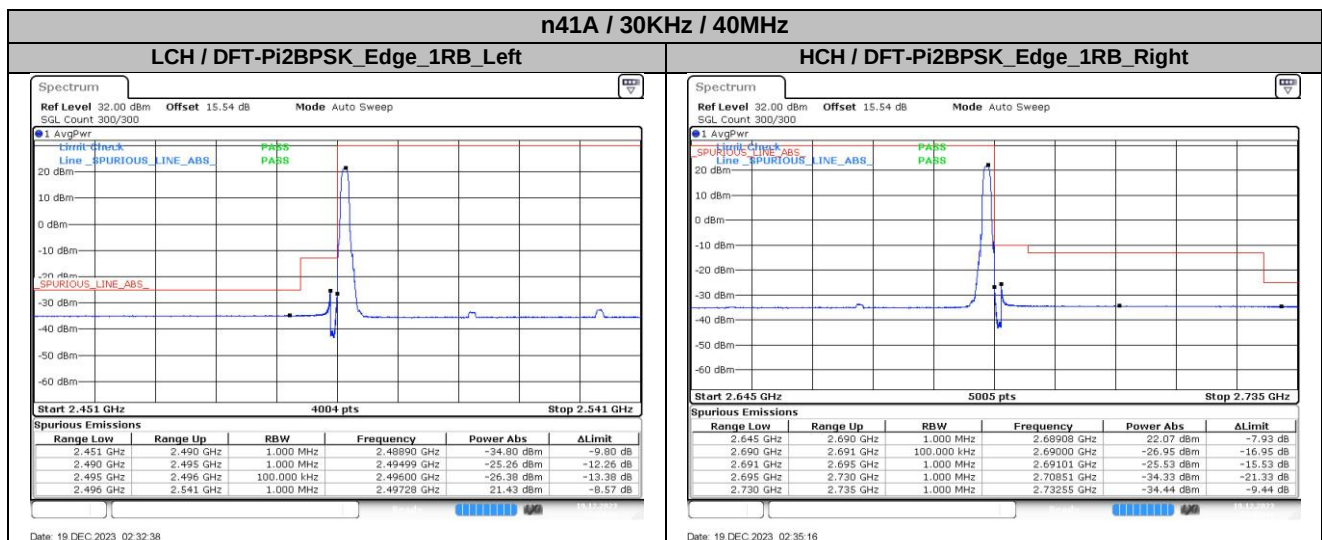
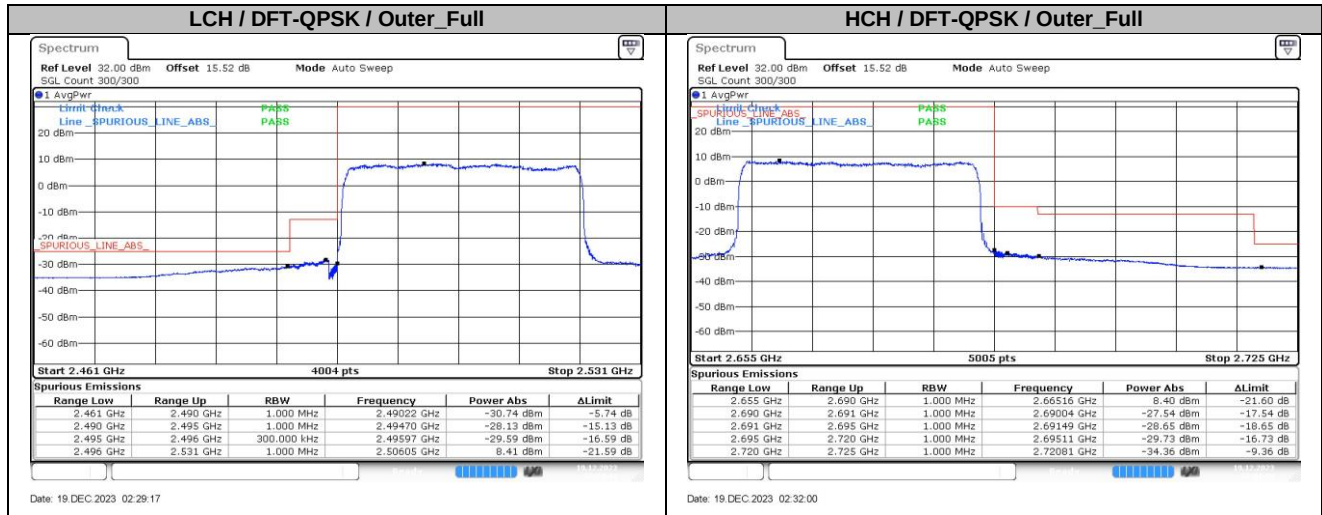


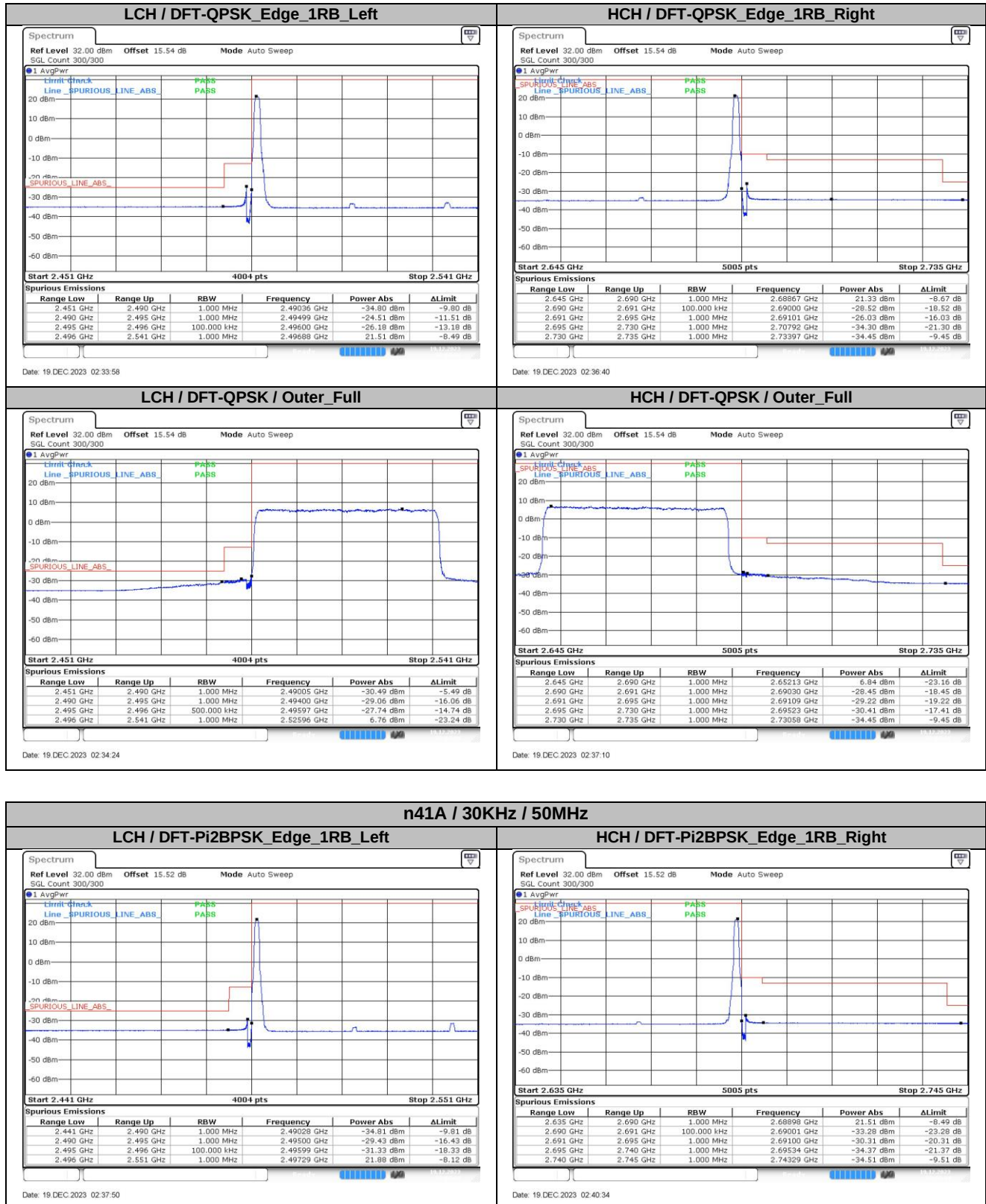
Date: 19 DEC 2023 02:28:52

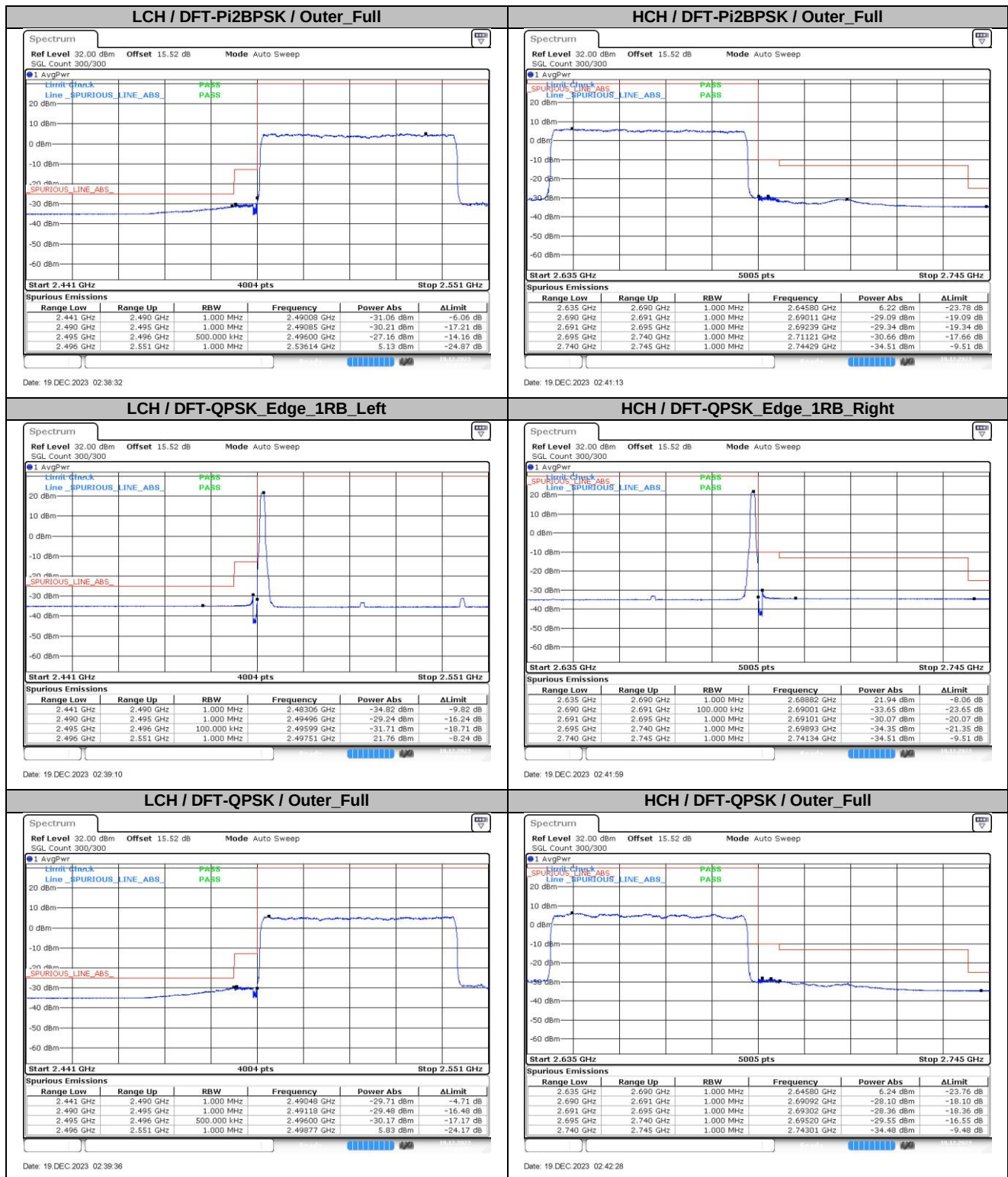
HCH / DFT-QPSK_Edge_1RB_Right



Date: 19 DEC 2023 02:31:31







n41A / 30KHz / 60MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



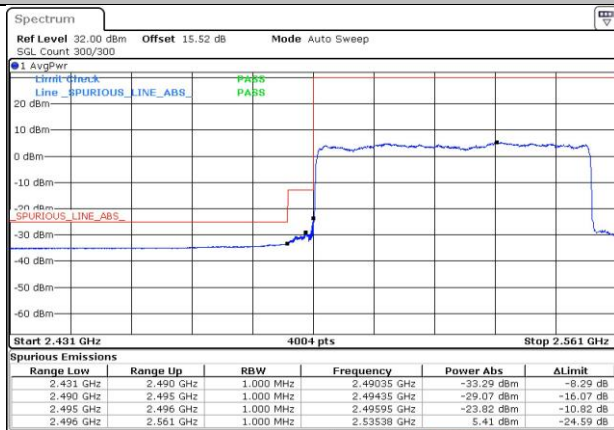
Date: 19 DEC 2023 02:43:09

HCH / DFT-Pi2BPSK_Edge_1RB_Right



Date: 19 DEC 2023 02:45:51

LCH / DFT-Pi2BPSK / Outer_Full



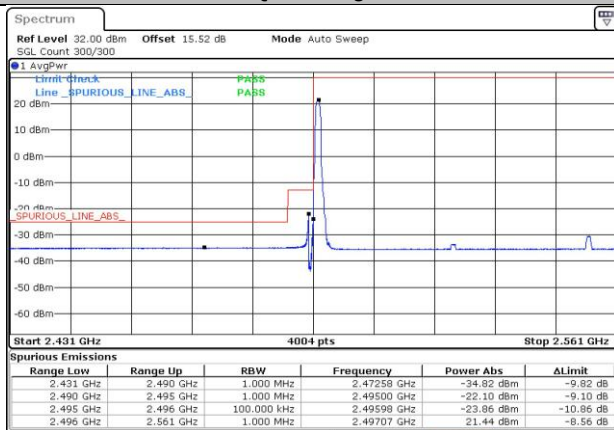
Date: 29 DEC 2023 09:05:11

HCH / DFT-Pi2BPSK / Outer_Full



Date: 29 DEC 2023 09:06:40

LCH / DFT-QPSK_Edge_1RB_Left

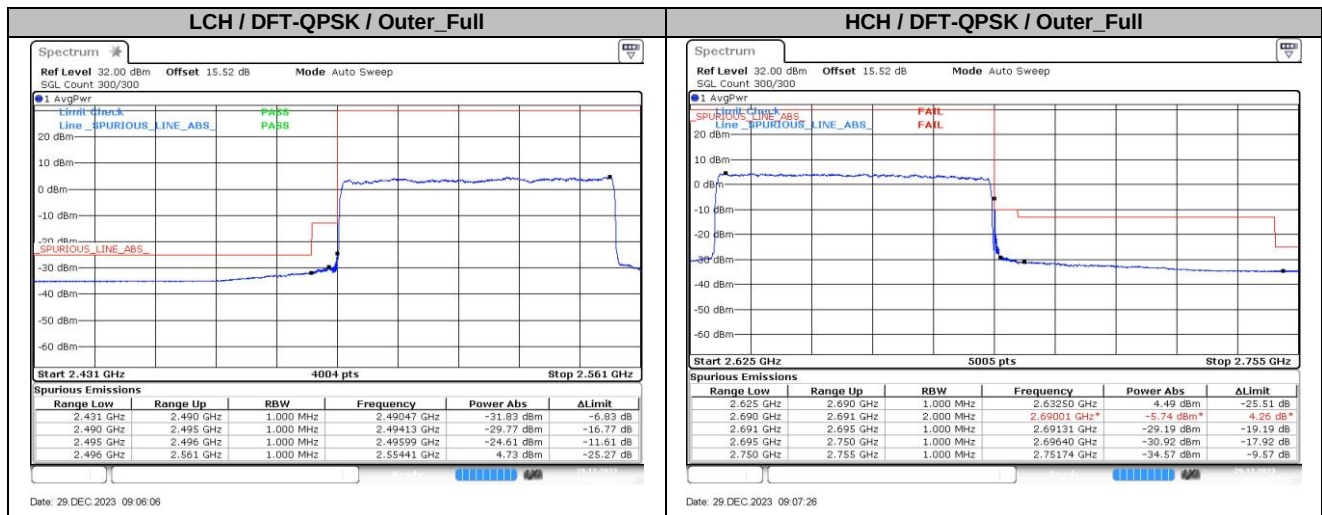


Date: 19 DEC 2023 02:44:31

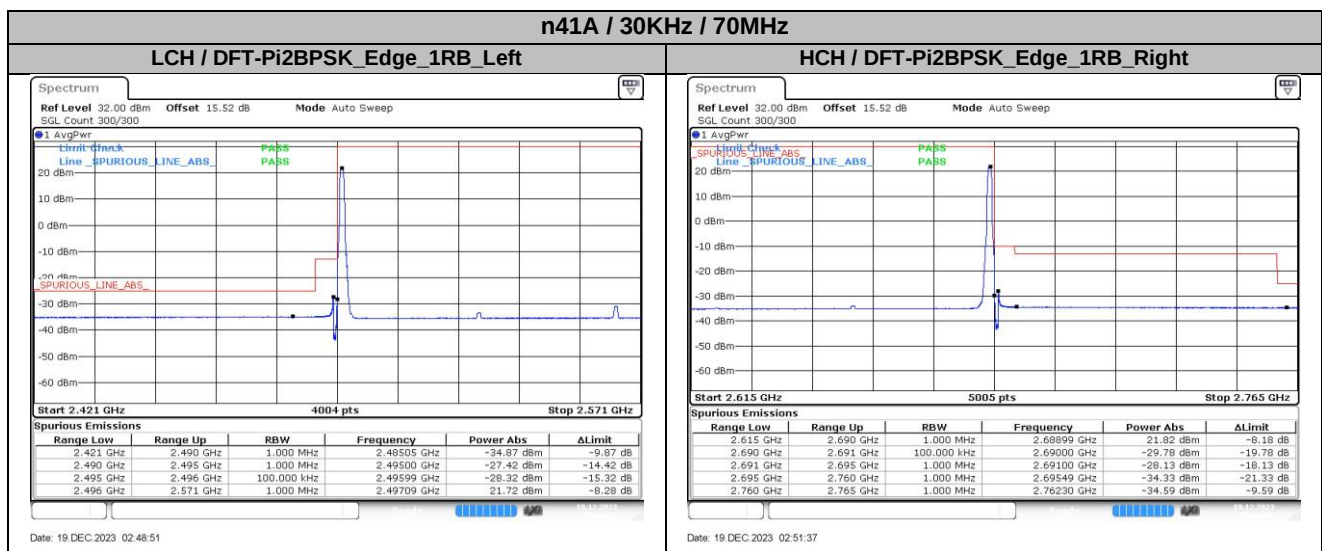
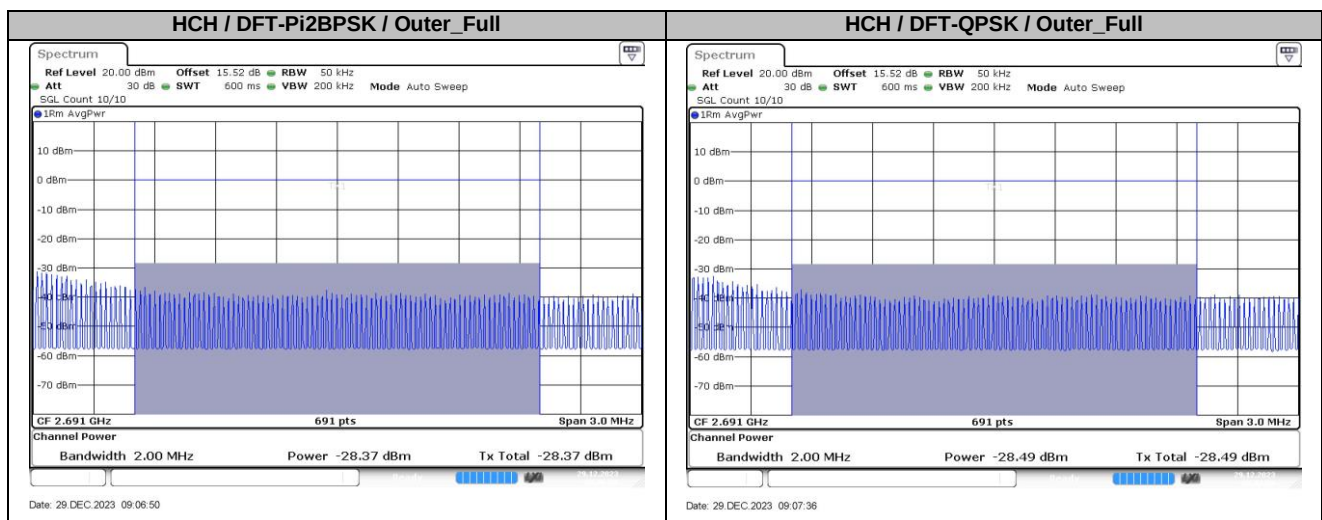
HCH / DFT-QPSK_Edge_1RB_Right

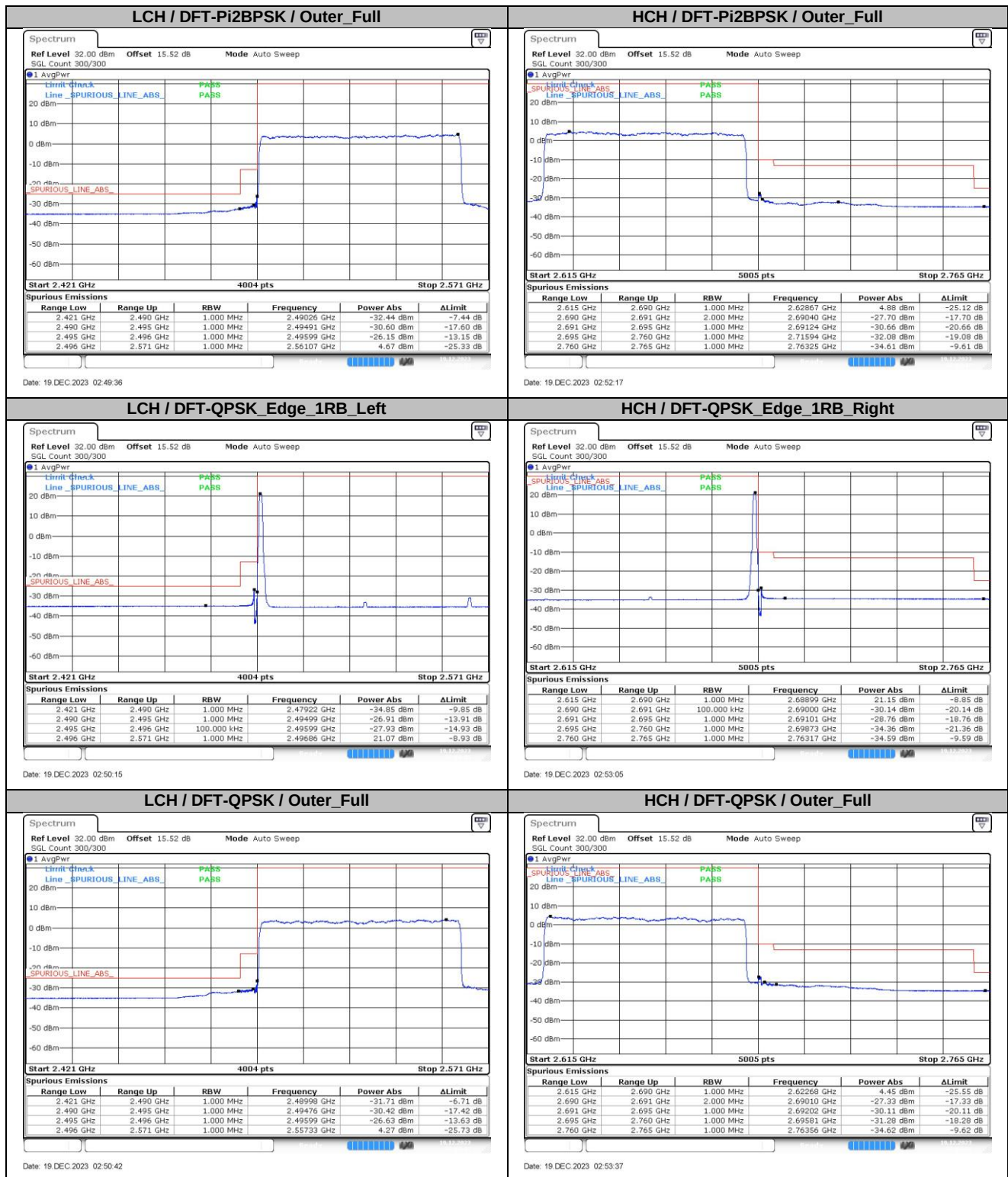


Date: 19 DEC 2023 02:47:30



Channel Power:





n41A / 30KHz / 80MHz

LCH / DFT-Pi2BPSK_Edge_1RB_Left



Date: 19 DEC 2023 02:54:18

HCH / DFT-Pi2BPSK_Edge_1RB_Right



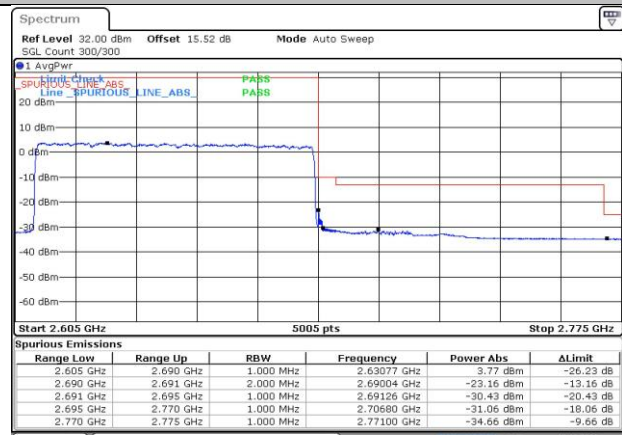
Date: 19 DEC 2023 02:57:07

LCH / DFT-Pi2BPSK / Outer_Full



Date: 19 DEC 2023 02:55:03

HCH / DFT-Pi2BPSK / Outer_Full



Date: 19 DEC 2023 02:57:48

LCH / DFT-QPSK_Edge_1RB_Left

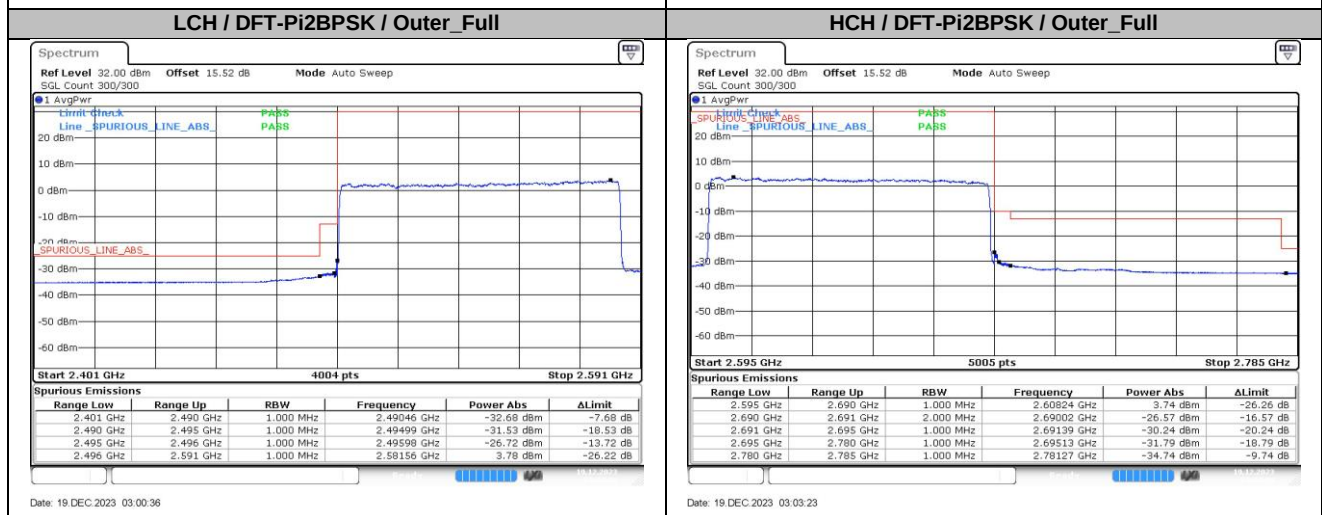
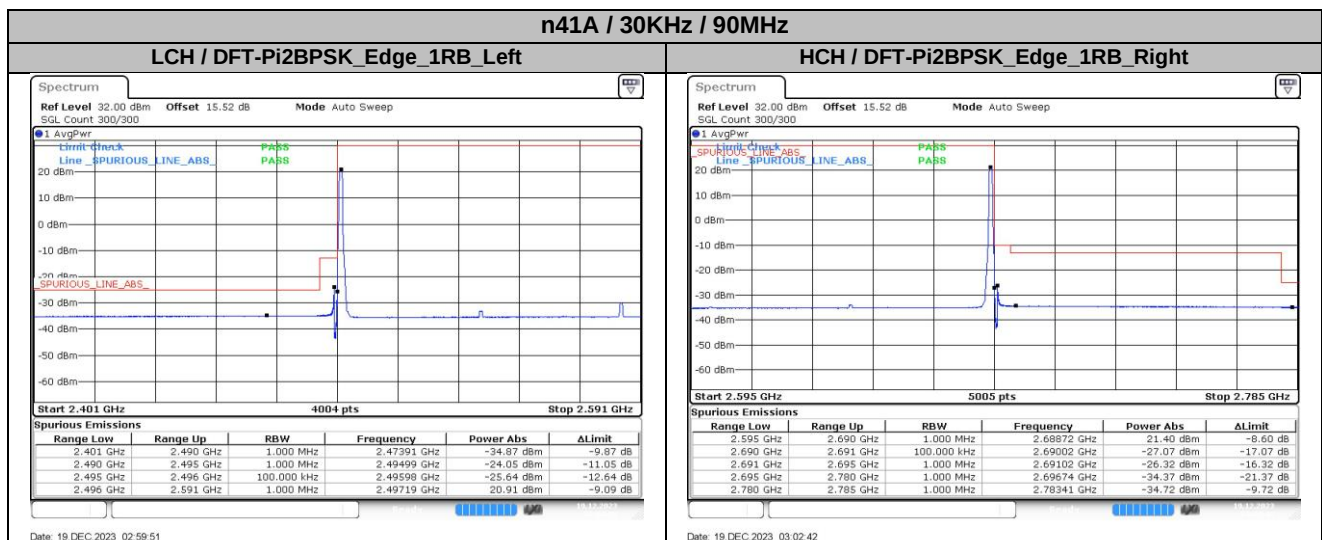
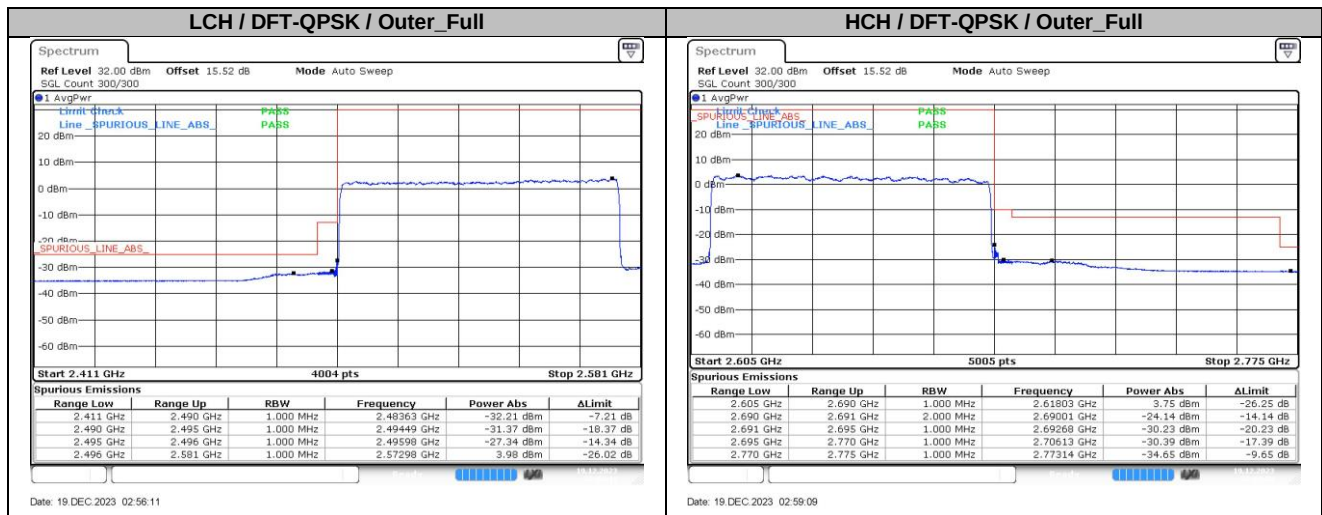


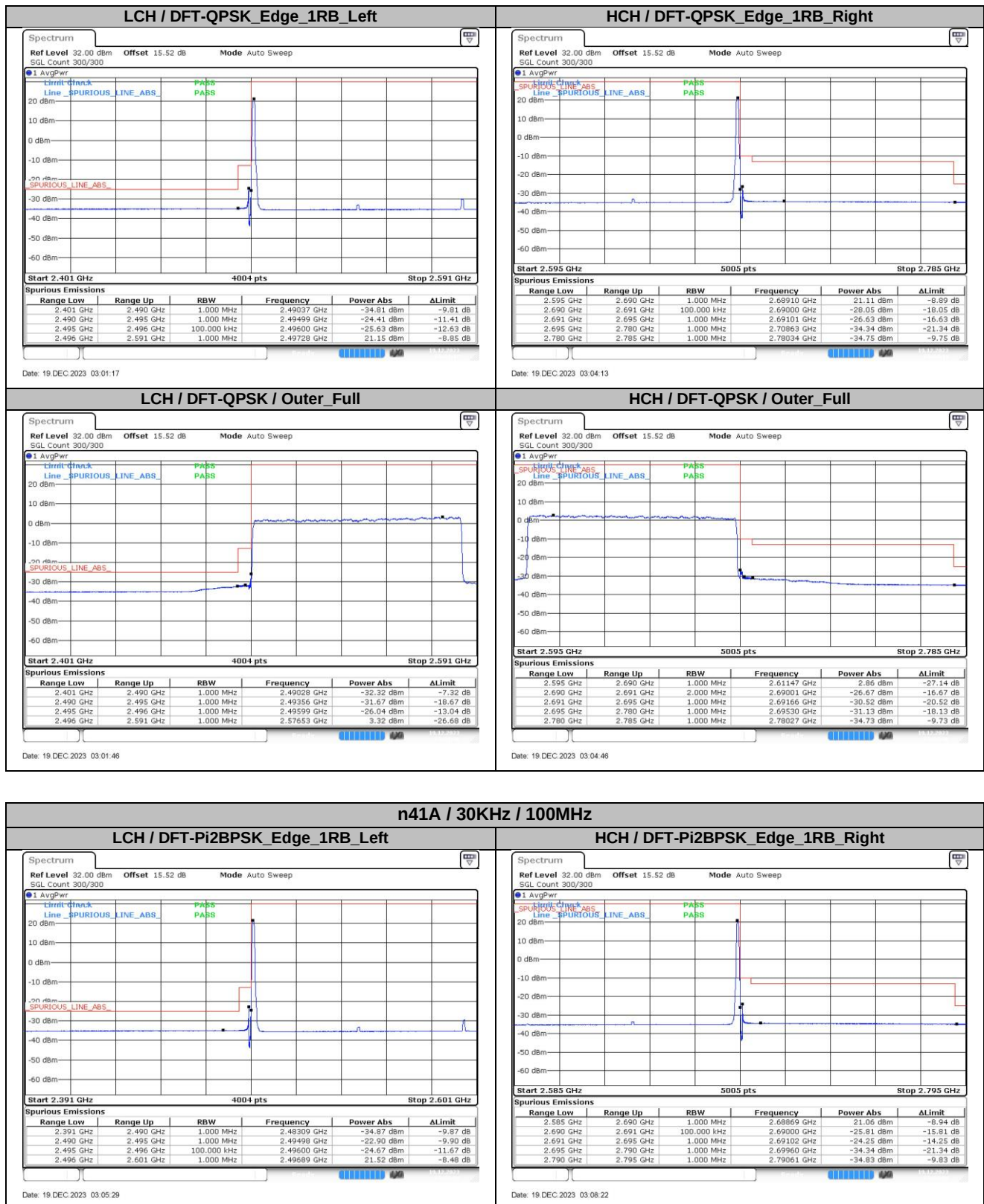
Date: 19 DEC 2023 02:55:43

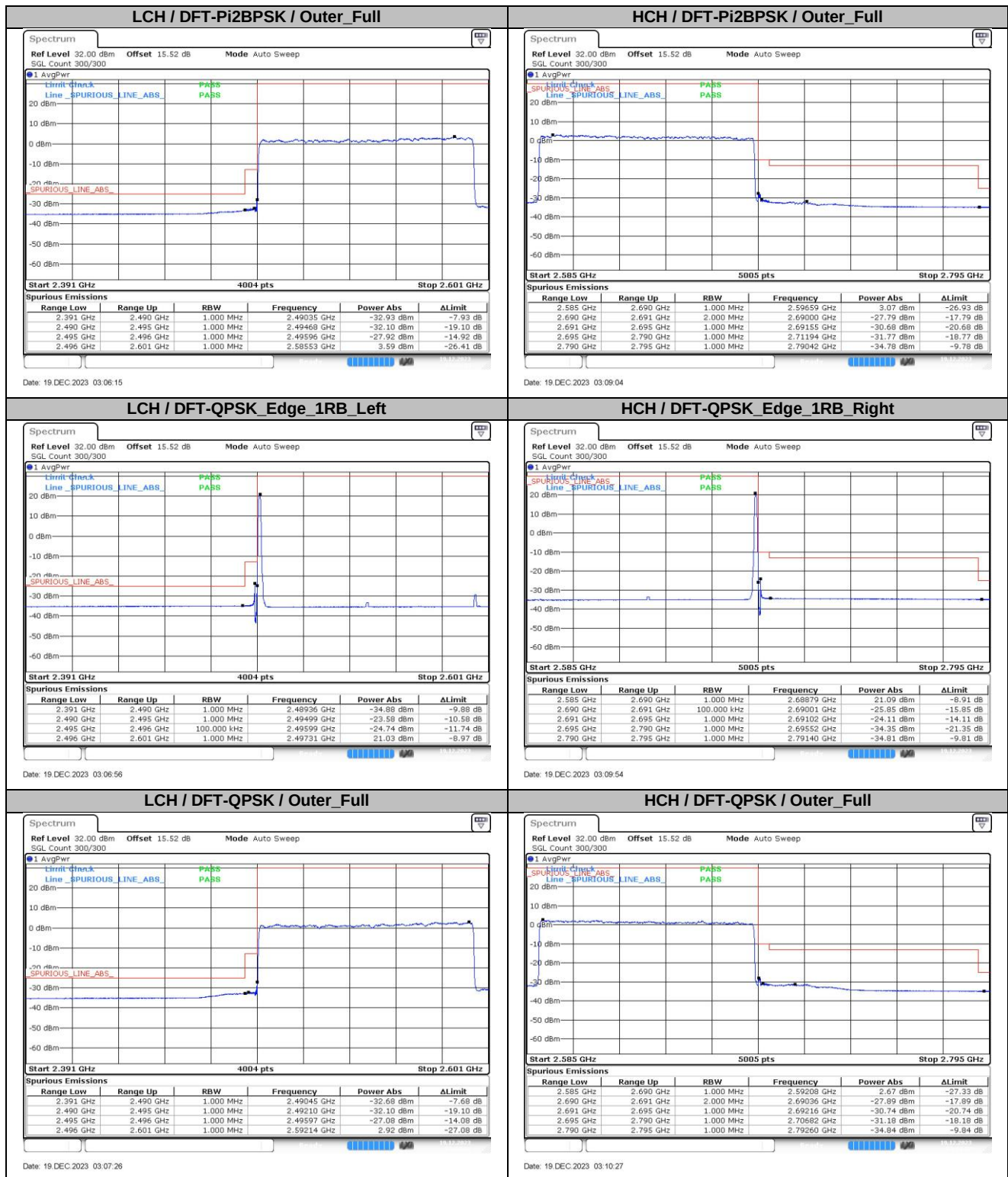
HCH / DFT-QPSK_Edge_1RB_Right



Date: 19 DEC 2023 02:58:37







6. Conducted Spurious Emission

6.1. Test Plots

