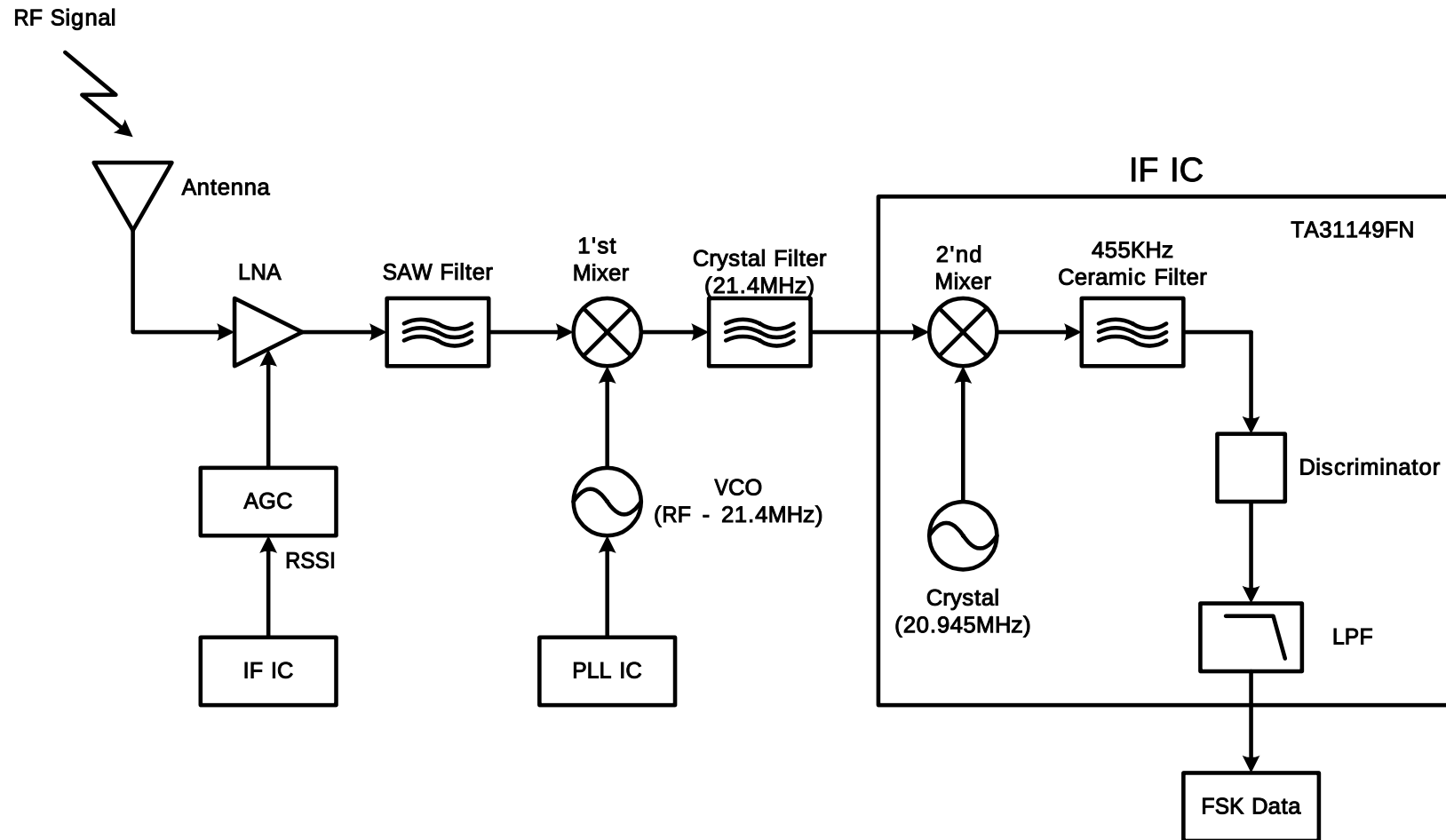


ATTACHMENT C.

- BLOCK DIAGRAM

RF Block Diagram



Logic Block Diagram

