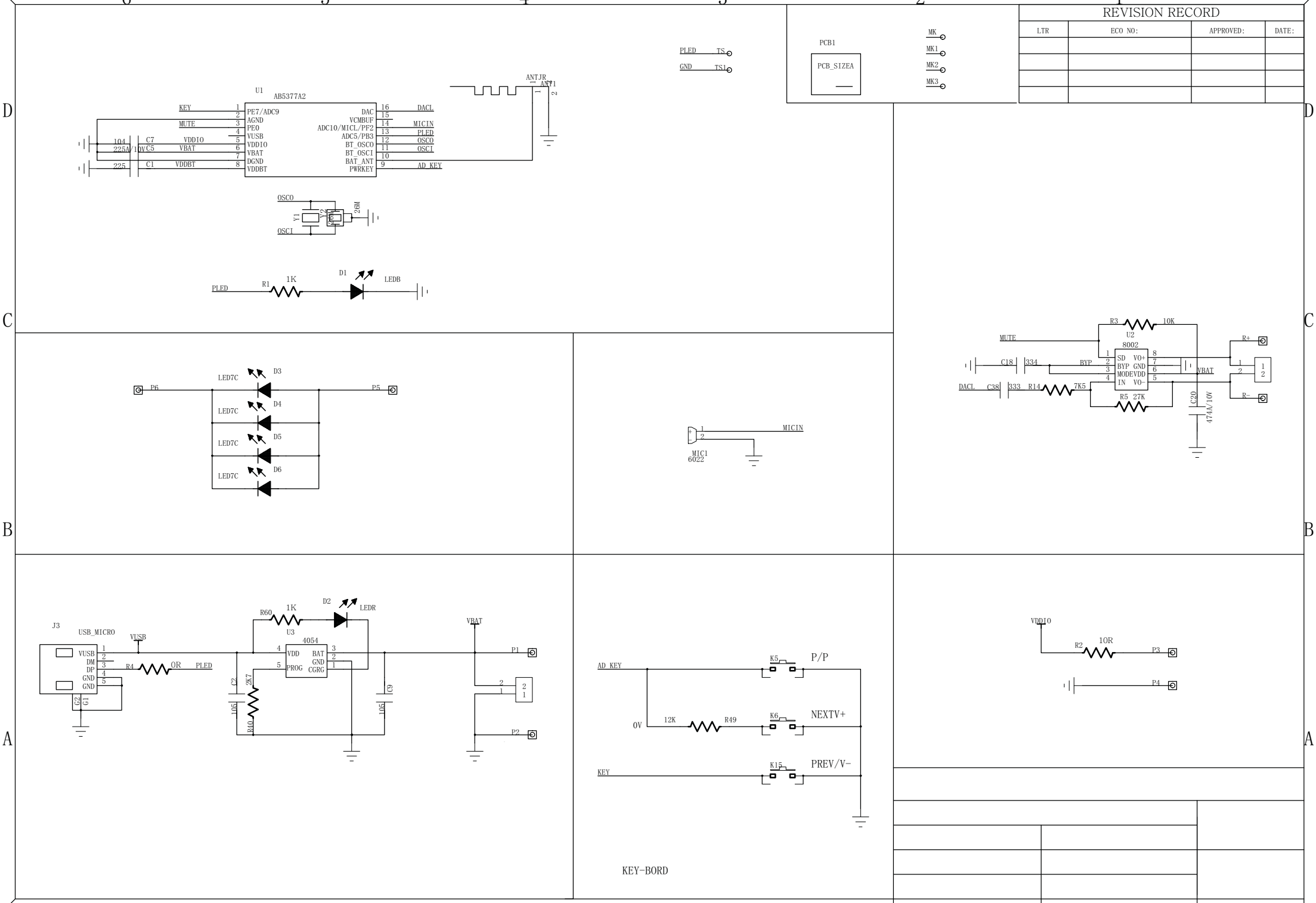
REVISION RECORD
LTR
ECO NO:
APPROVED:
DATE:
PCB1
PCB_SIZEA
MK
MK1
MK2
MK3
PLED
TS
GND
TS1
U1
AB5377A
KEY
MUTE
VDDIO
VBAT
VDDBT
PE7/ADC9
AGND
PE0
VUSB
VDDIO
VBAT
DGND
VDDBT
DAC
VCMBUF
ADC10/MICL/PF2
ADC5/PB3
BT_OSC0
BT_OSC1
BAT_ANT
PWRKEY
DACL
MICIN
PLED
OSC0
OSC1
AD_KEY
ANTJR
ANT1
C7
C5
C1
104
225A/10V
225
OSC0
OSC1
V1
V2
26M
PLED
R1
1K
D1
LEDB
P6
P5
LED7C
D3
D4
D5
D6
MIC1
6022
MICIN
R3
10K
U2
8002
MUTE
SD
VO+
BYP
GND
MODEVDD
IN
VO-
C18
334
BYP
VBAT
DACL
C38
333
R14
7K5
R5
27K
C20
474A/10W
R+
R-
J3
USB_MICRO
VUSB
DM
DP
GND
GND
R4
OR
PLED
G2
G1
R60
1K
D2
LEDR
U3
4054
VDD
PROG
BAT
GND
CGRG
C2
2K7
R40
105
C9
VBAT
P1
P2
AD_KEY
K5
P/P
0V
12K
R49
K6
NEXTV+
KEY
K15
PREV/V-
KEY-BORD
VDDIO
R2
10R
P3
P4