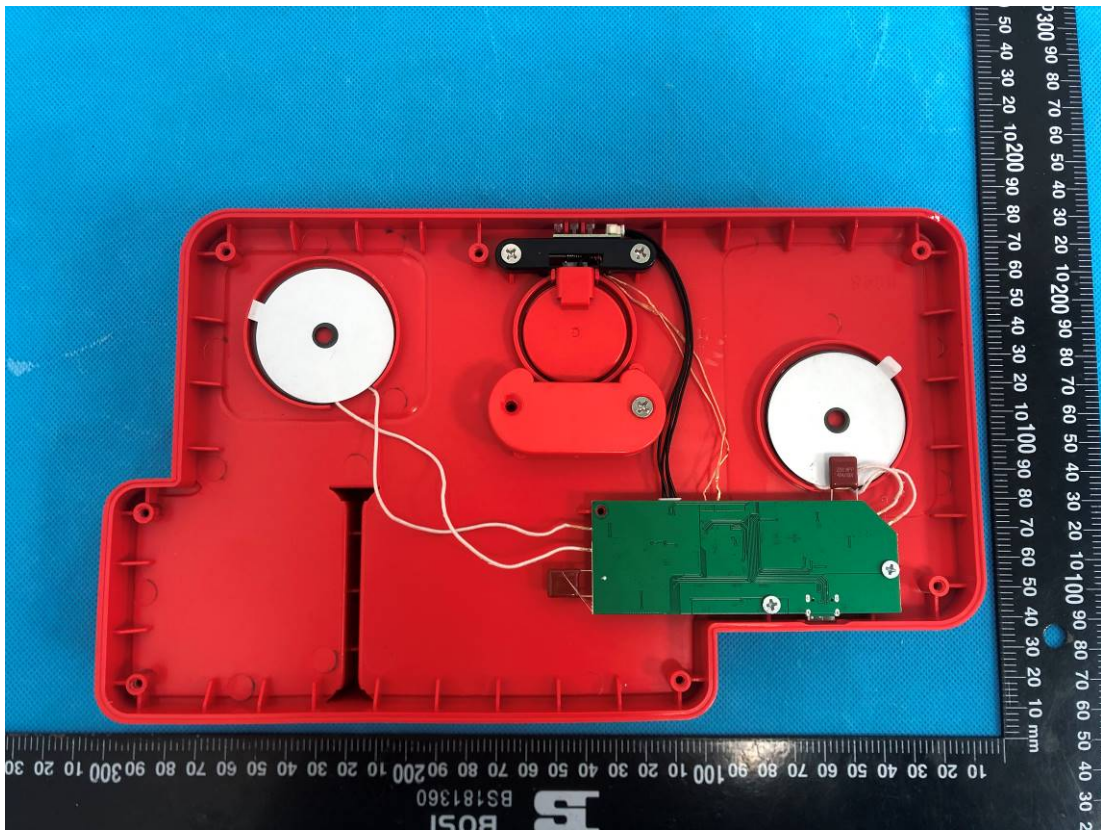
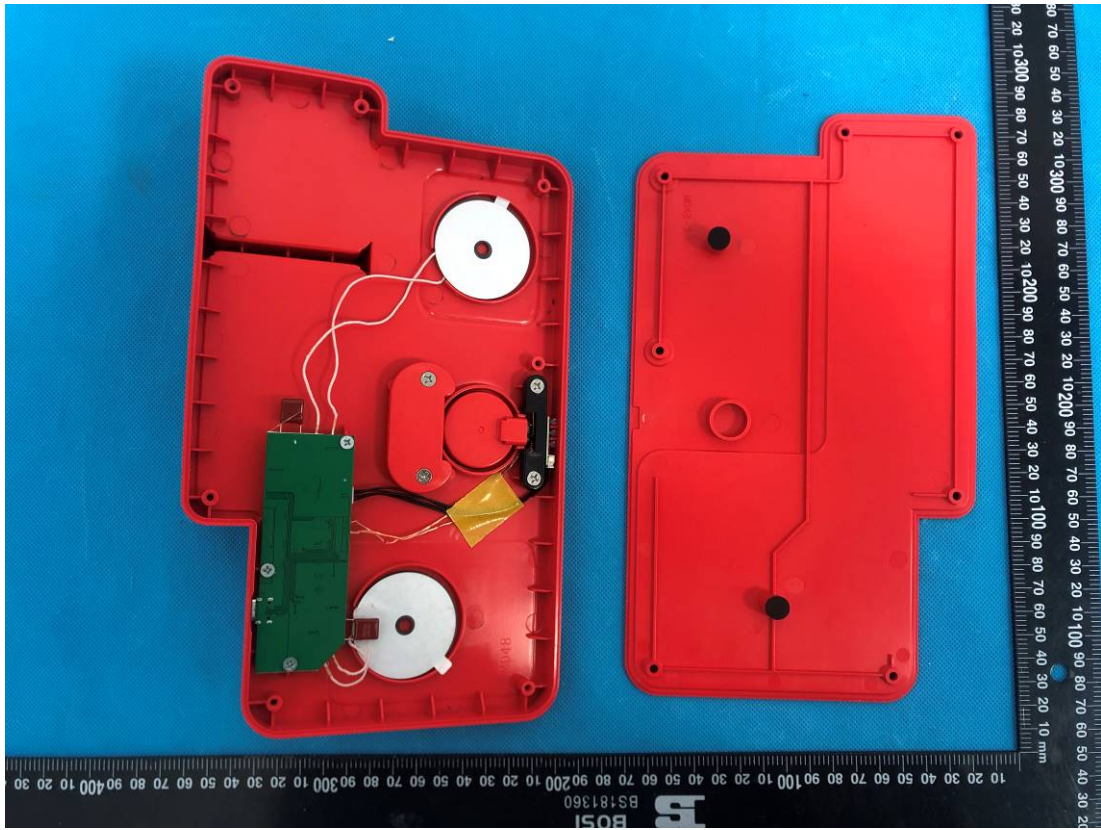
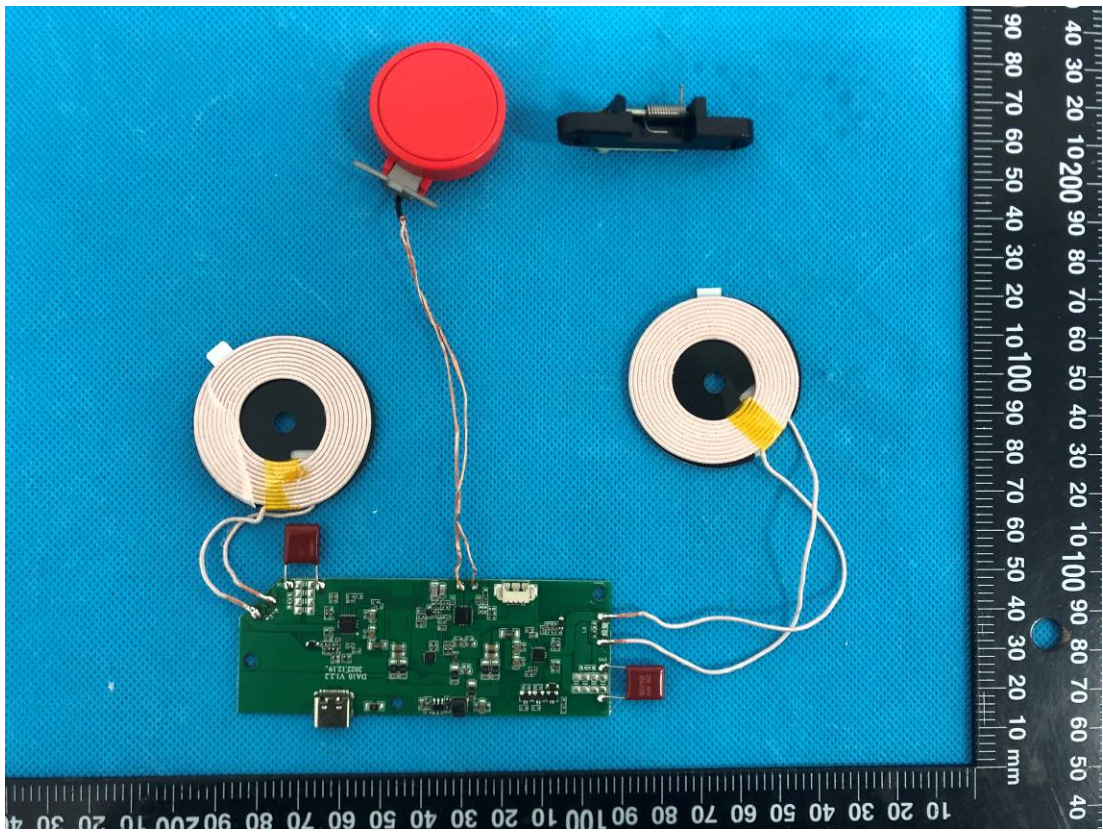
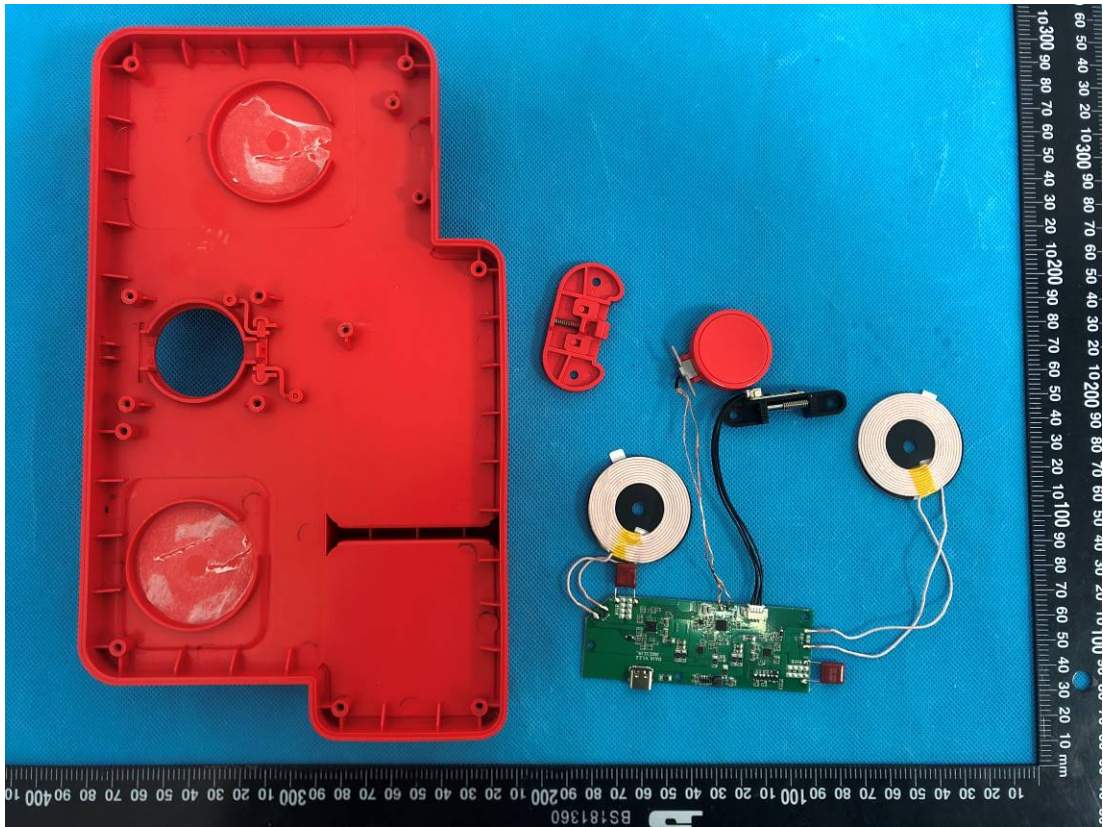
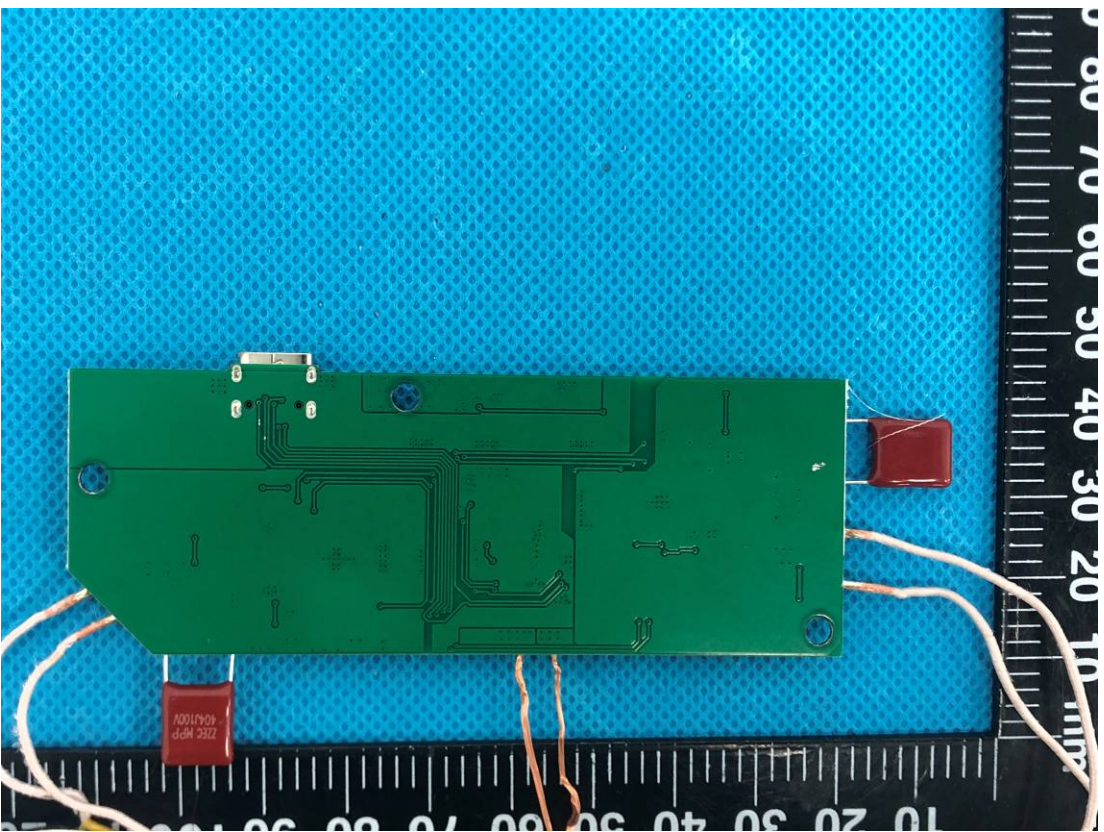
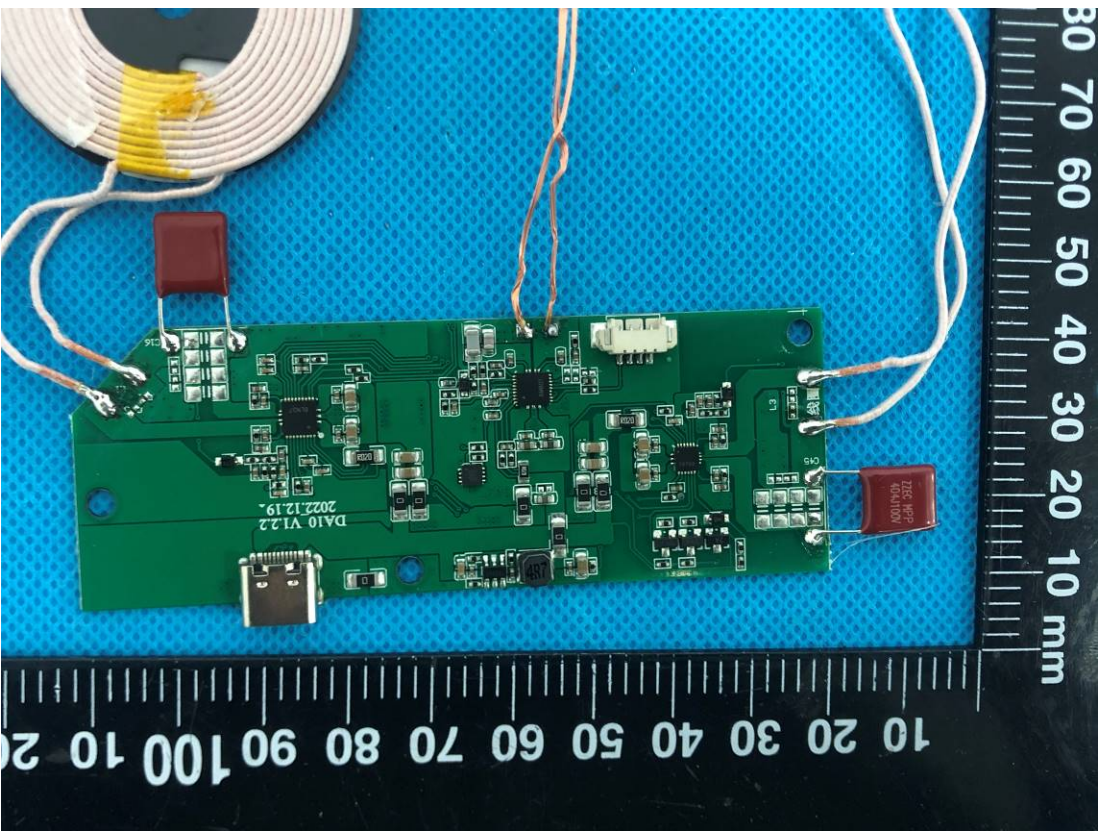


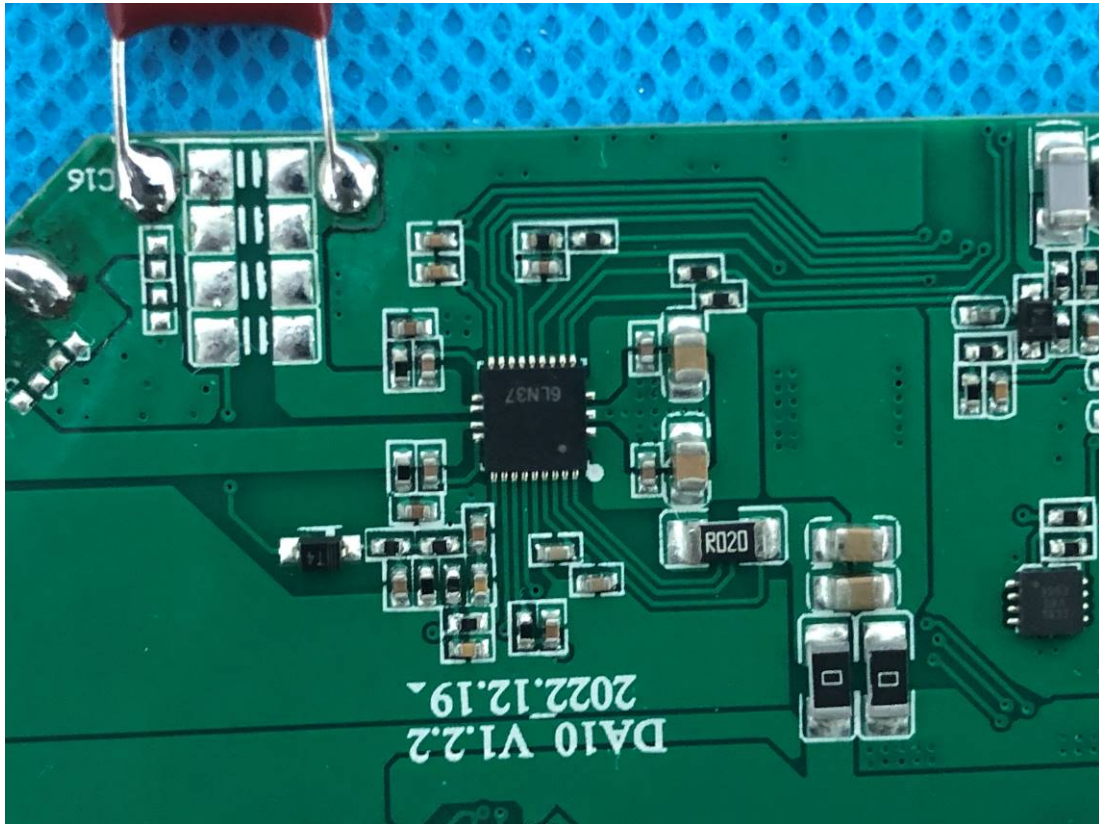
EXHIBIT C - EUT INTERNAL PHOTOGRAPHS



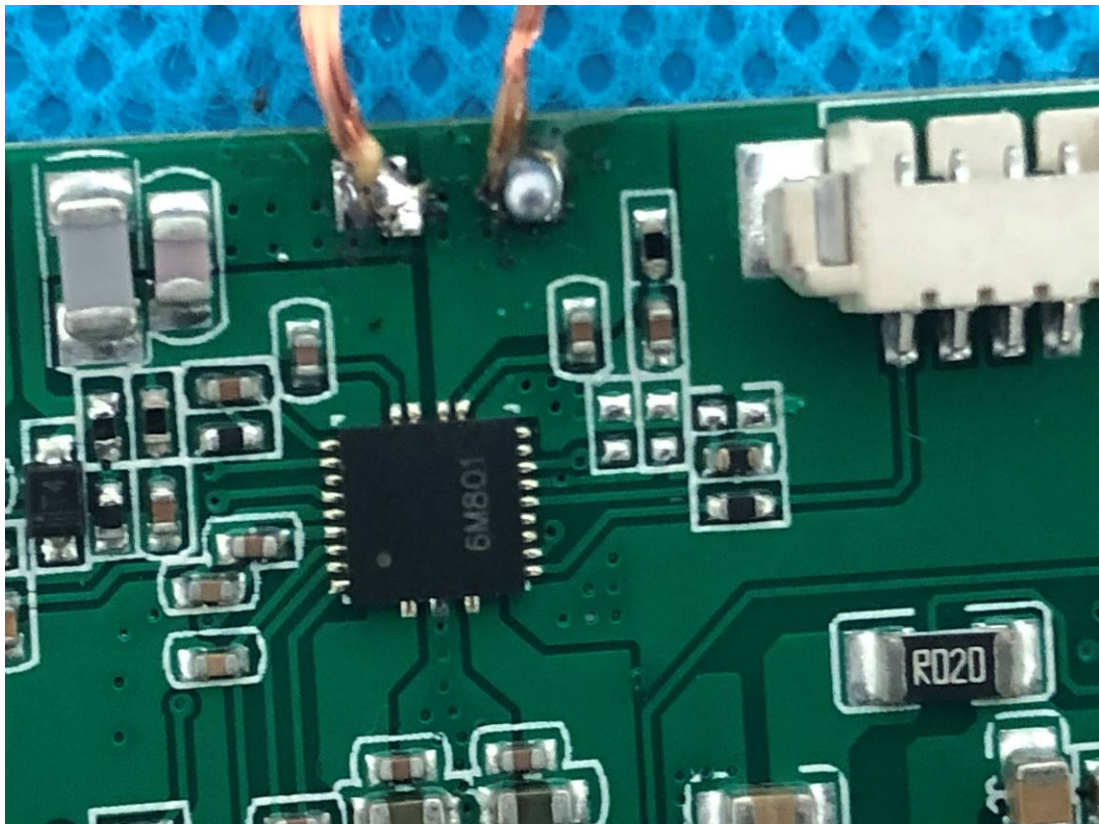




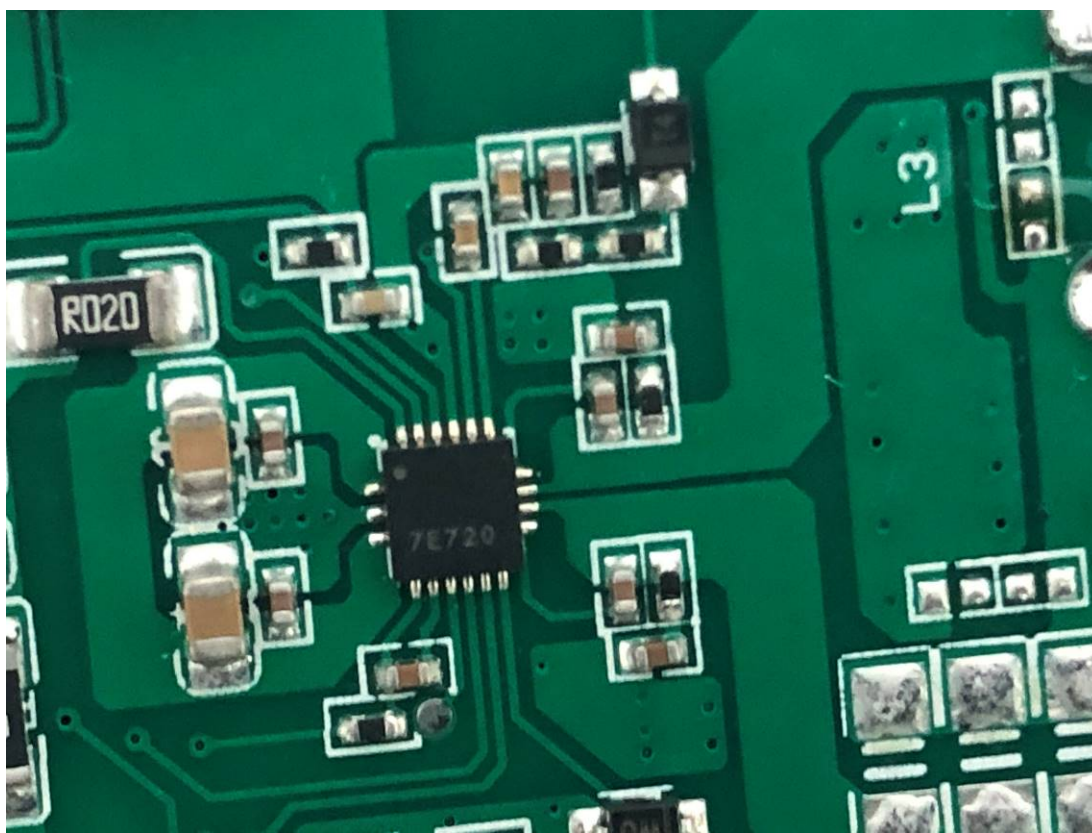
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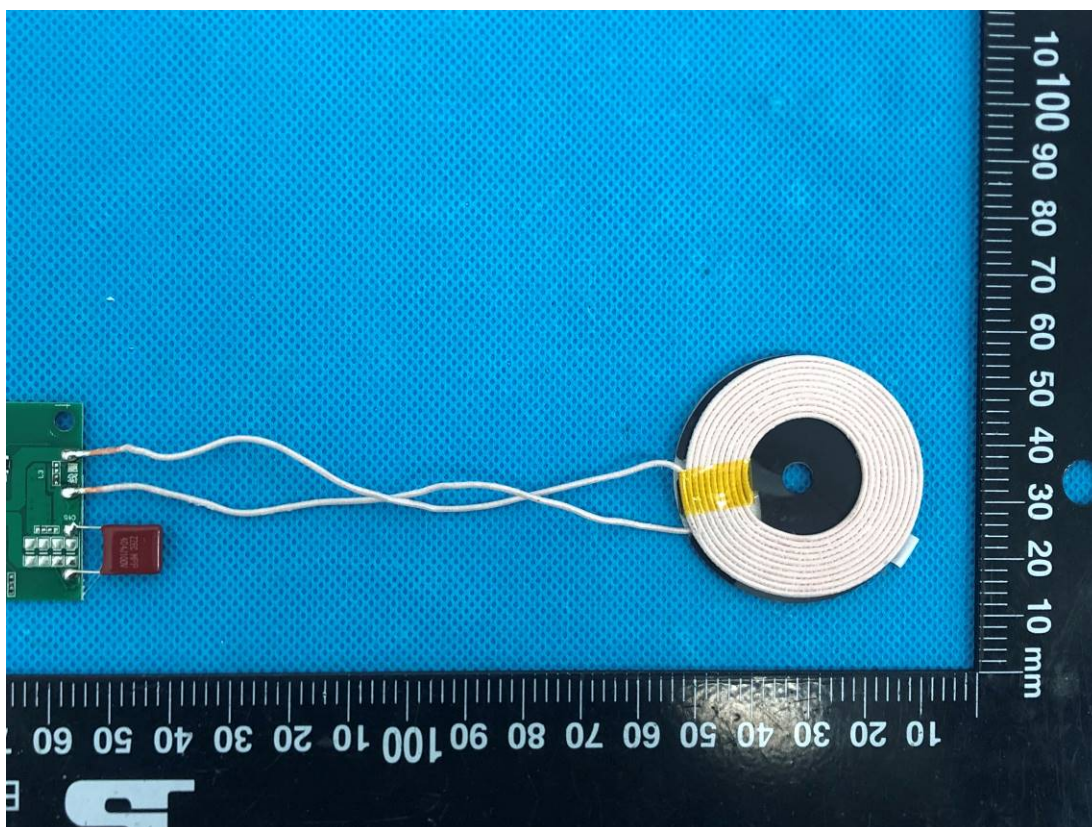
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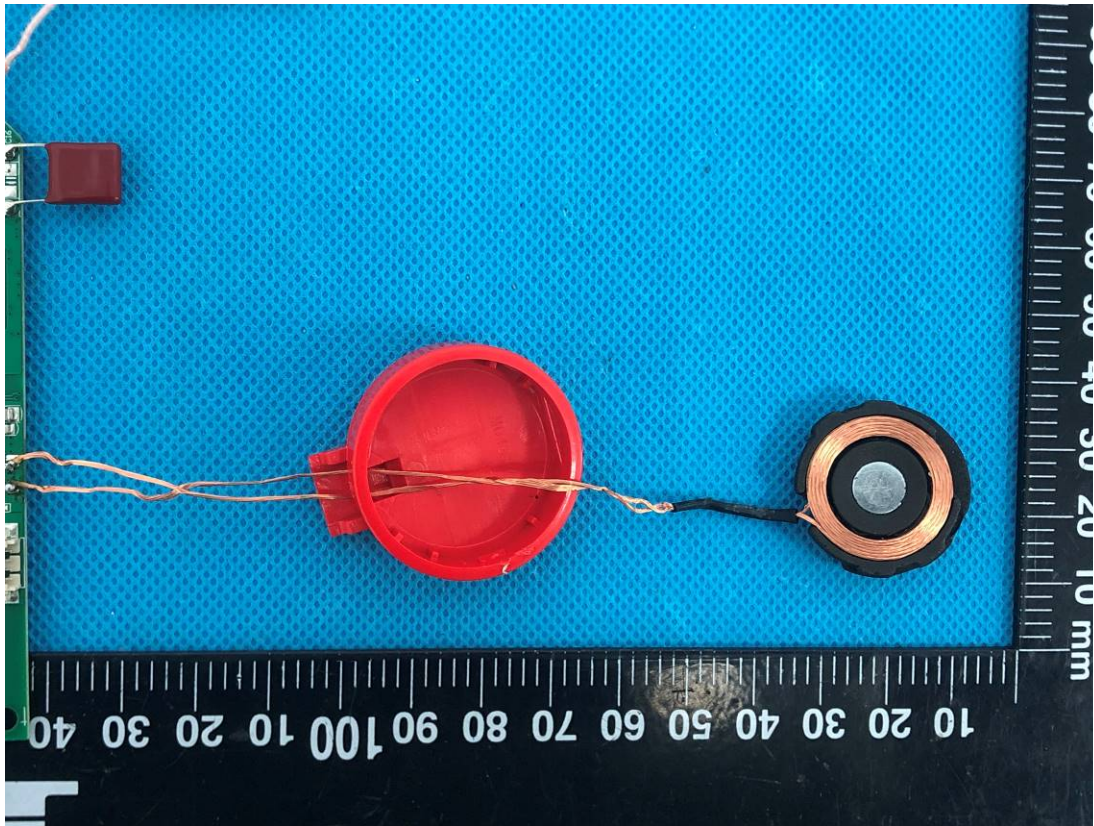
chip



Coil 1



Coil 0



Coil 2

