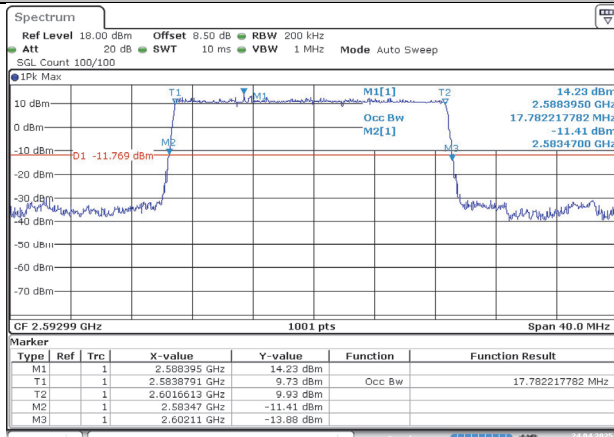


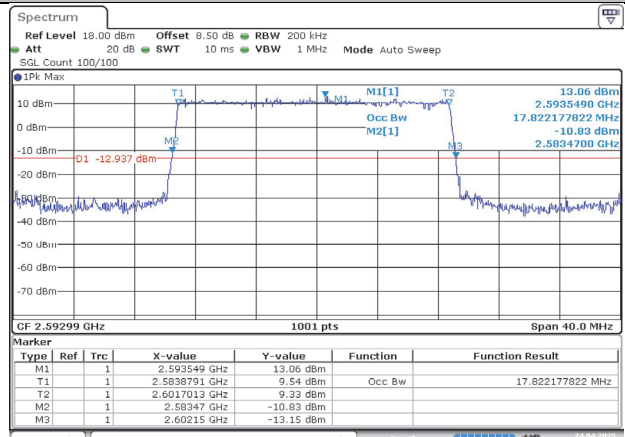
DC_66A_n41A / 30KHz / 20MHz

MCH / DFT-Pi2BPSK / Outer_Full



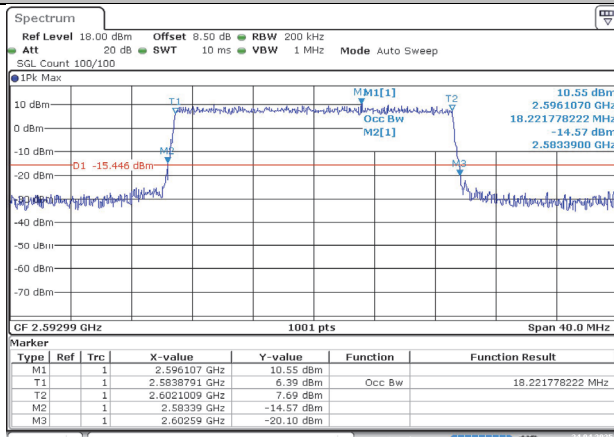
Date 24/FEB/2015 13:22:05

MCH / DFT-QPSK / Outer_Full



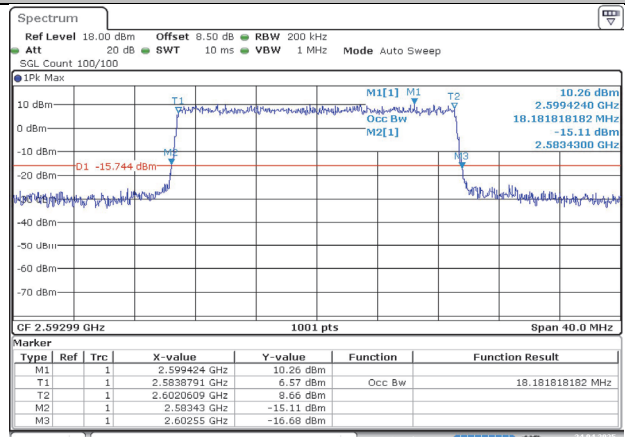
Date 24/FEB/2015 13:22:13

MCH / CP-QPSK / Outer_Full



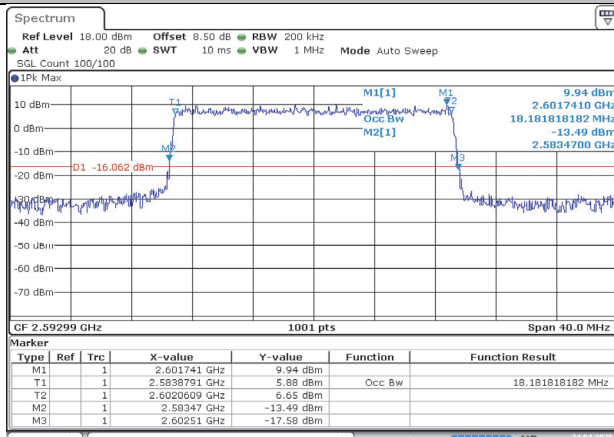
Date 24/FEB/2015 13:22:35

MCH / CP-16QAM / Outer_Full



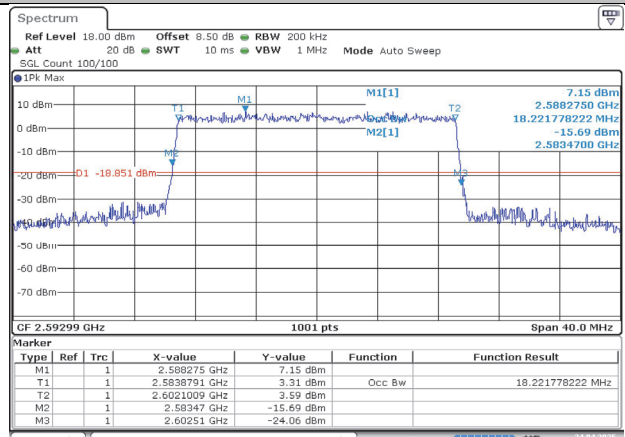
Date 24/FEB/2015 13:22:41

MCH / CP-64QAM / Outer_Full



Date 24/FEB/2015 13:22:45

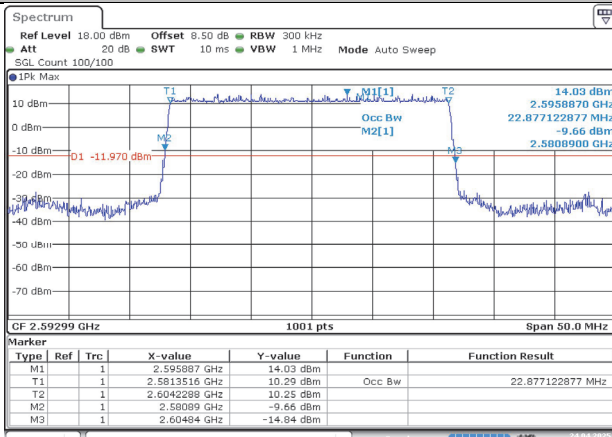
MCH / CP-256QAM / Outer_Full



Date 24/FEB/2015 13:22:51

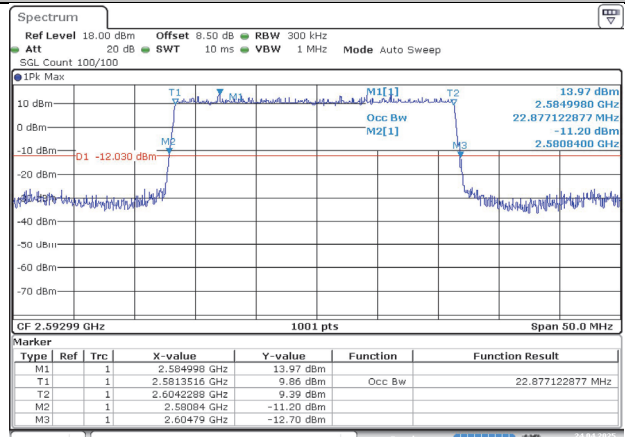
DC_66A_n41A / 30KHz / 25MHz

MCH / DFT-Pi2BPSK / Outer_Full



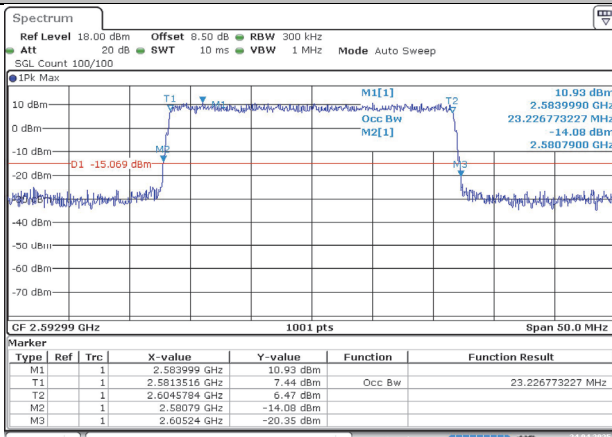
Date 24/FFZ05 132259

MCH / DFT-QPSK / Outer_Full



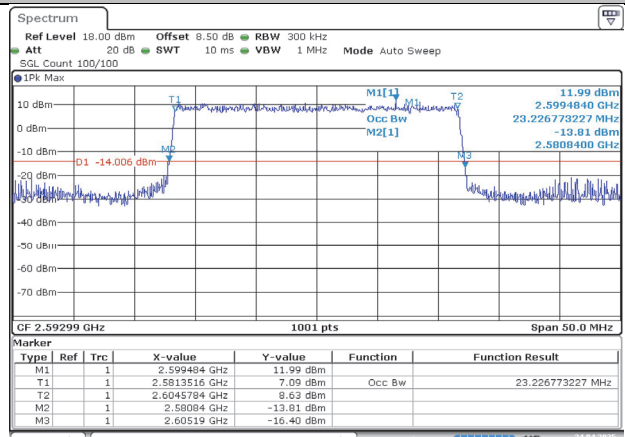
Date 24/FFZ05 132306

MCH / CP-QPSK / Outer_Full



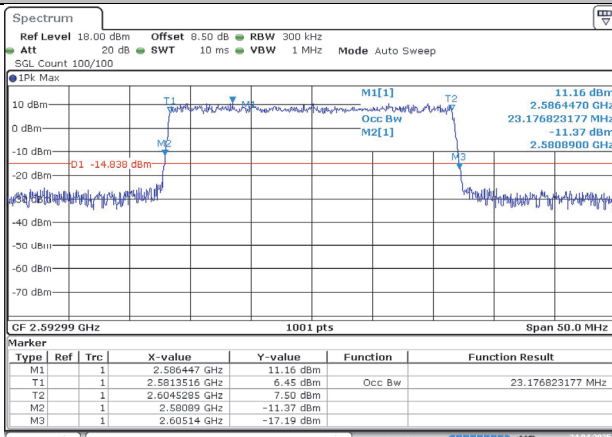
Date 24/FFZ05 132323

MCH / CP-16QAM / Outer_Full



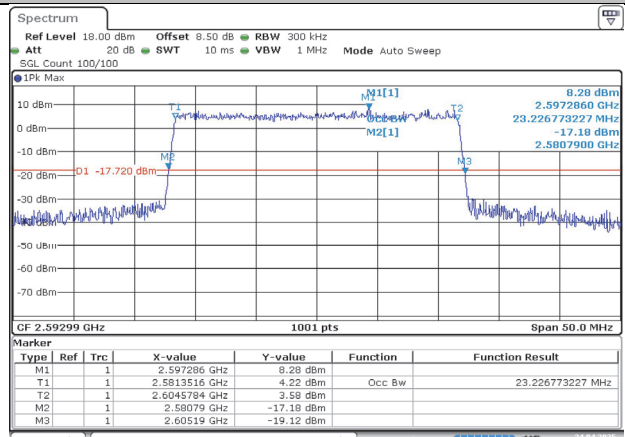
Date 24/FFZ05 132327

MCH / CP-64QAM / Outer_Full



Date 24/FFZ05 132331

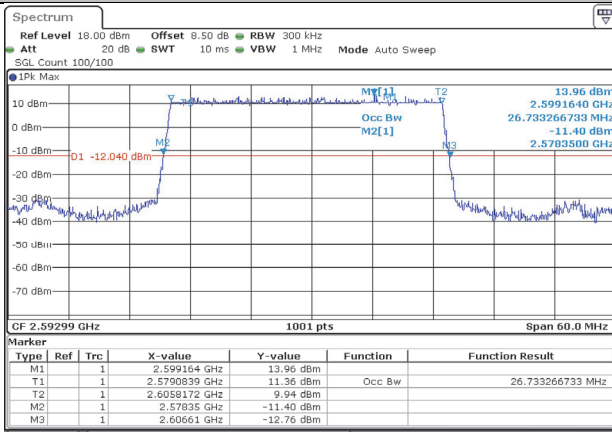
MCH / CP-256QAM / Outer_Full



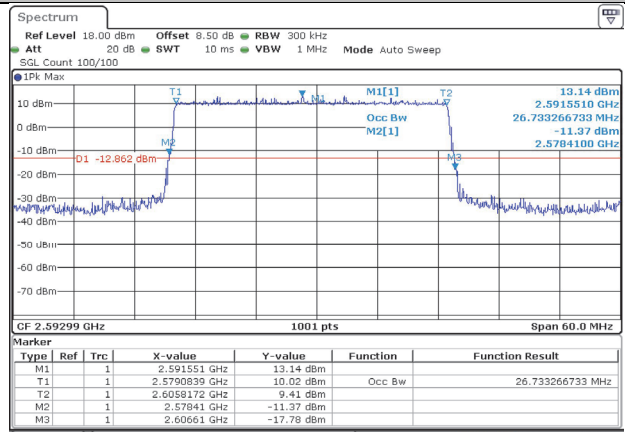
Date 24/FFZ05 132335

DC_66A_n41A / 30KHz / 30MHz

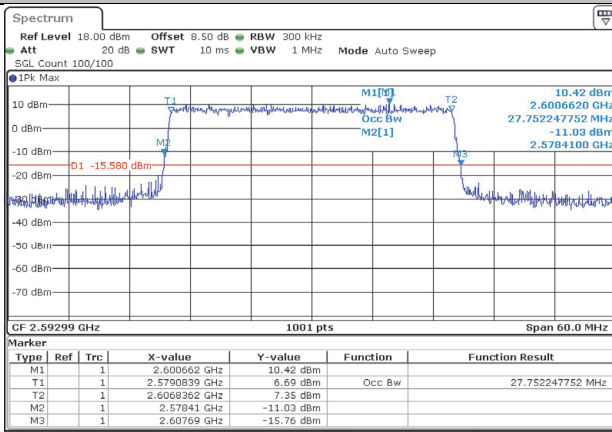
MCH / DFT-Pi2BPSK / Outer_Full



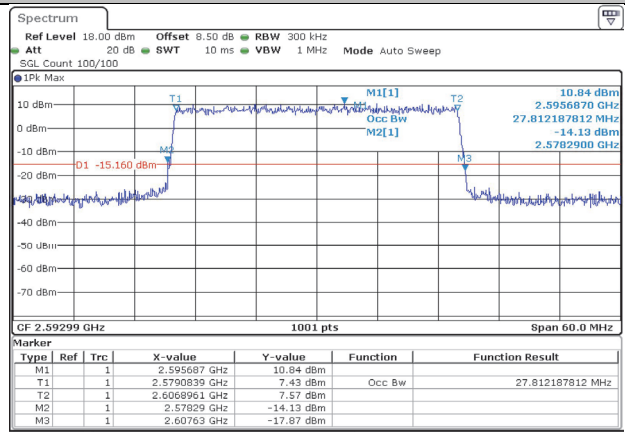
MCH / DFT-QPSK / Outer_Full



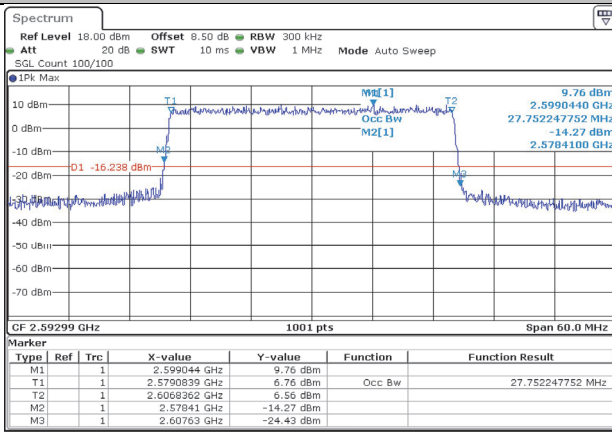
MCH / CP-QPSK / Outer_Full



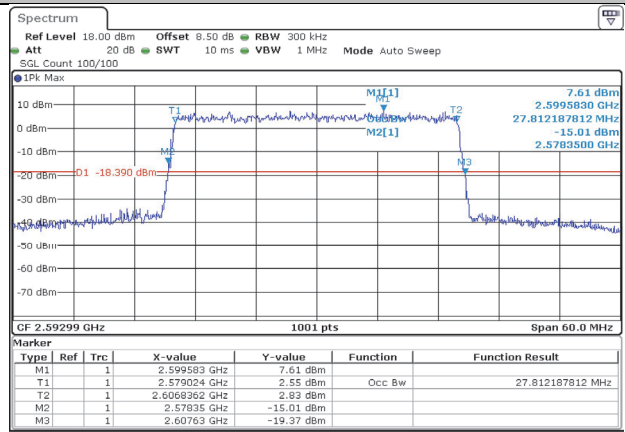
MCH / CP-16QAM / Outer_Full



MCH / CP-64QAM / Outer_Full

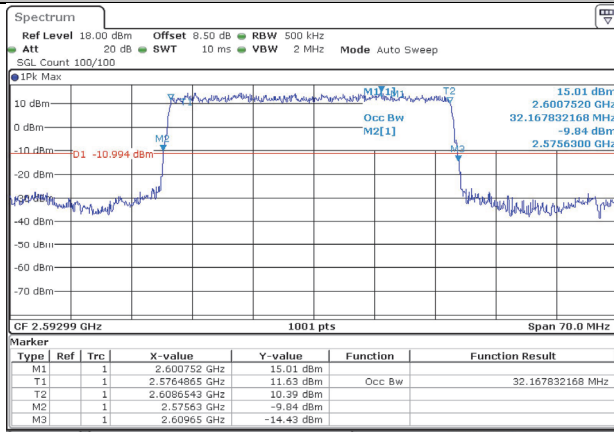


MCH / CP-256QAM / Outer_Full



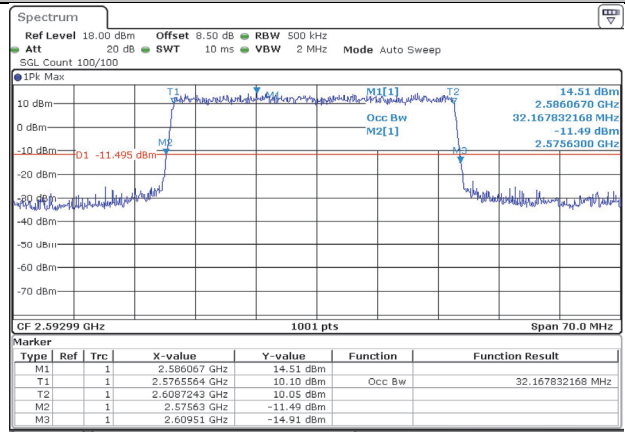
DC_66A_n41A / 30KHz / 35MHz

MCH / DFT-Pi2BPSK / Outer_Full



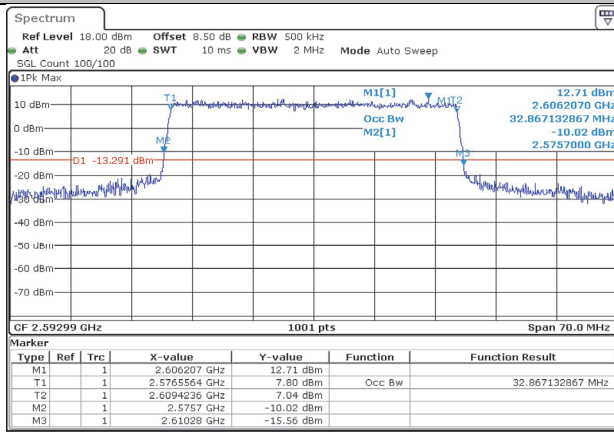
Date 24/FFZ05 132428

MCH / DFT-QPSK / Outer_Full



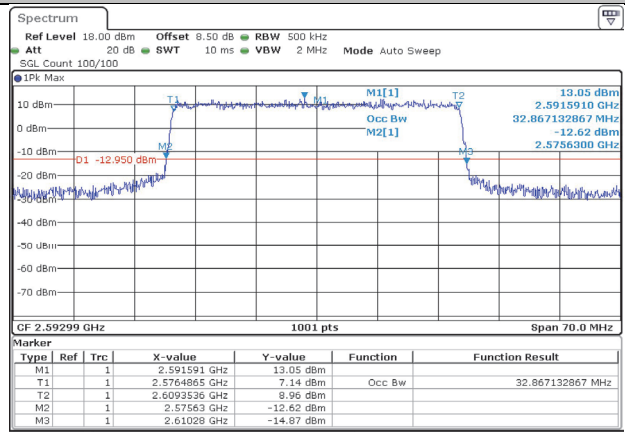
Date 24/FFZ05 132436

MCH / CP-QPSK / Outer_Full



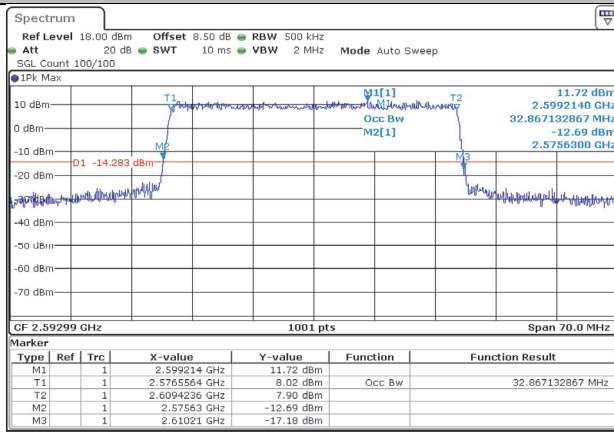
Date 24/FFZ05 132452

MCH / CP-16QAM / Outer_Full



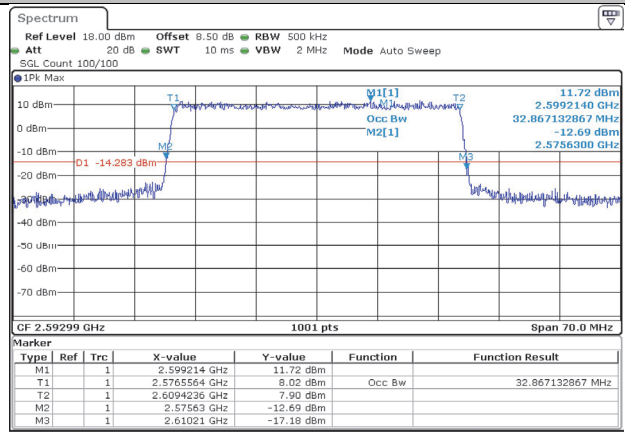
Date 24/FFZ05 132456

MCH / CP-64QAM / Outer_Full



Date 24/FFZ05 132500

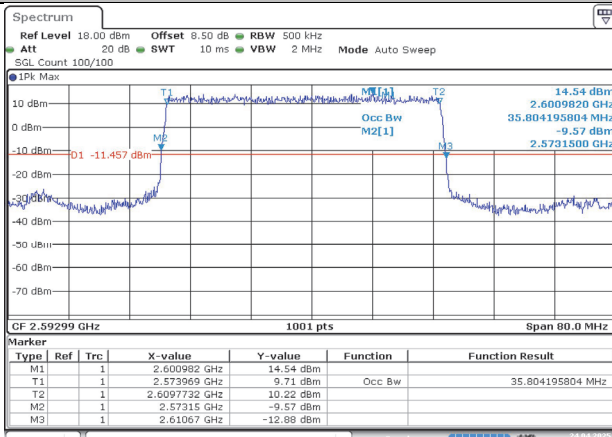
MCH / CP-256QAM / Outer_Full



Date 24/FFZ05 132500

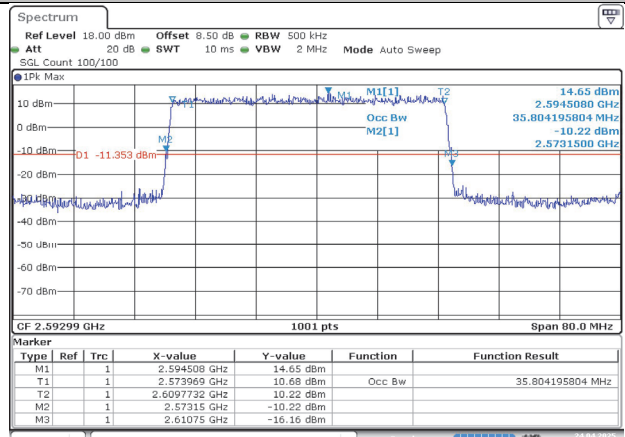
DC_66A_n41A / 30KHz / 40MHz

MCH / DFT-Pi2BPSK / Outer_Full



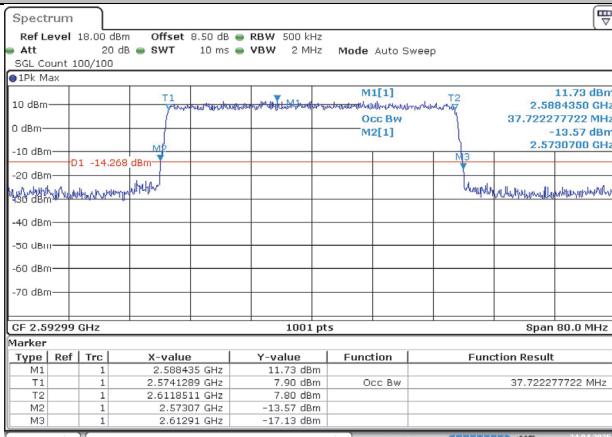
Date 24/Feb/2015 13:25:13

MCH / DFT-QPSK / Outer_Full



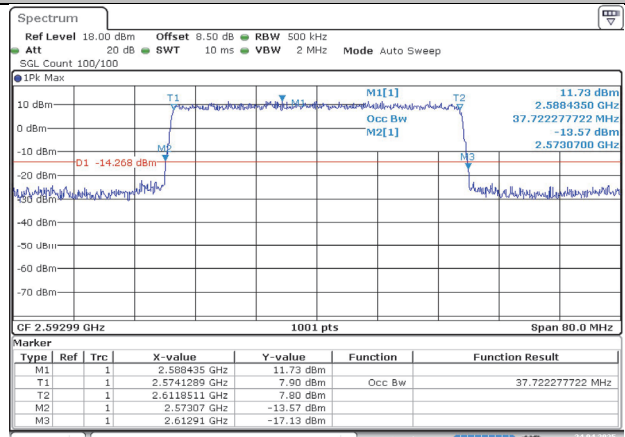
Date 24/Feb/2015 13:25:20

MCH / CP-QPSK / Outer_Full



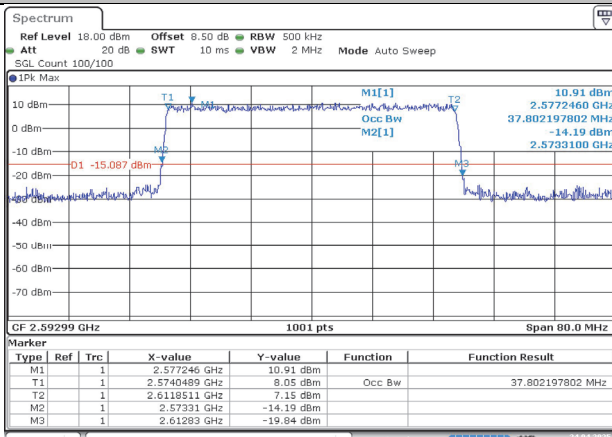
Date 24/Feb/2015 13:25:42

MCH / CP-16QAM / Outer_Full



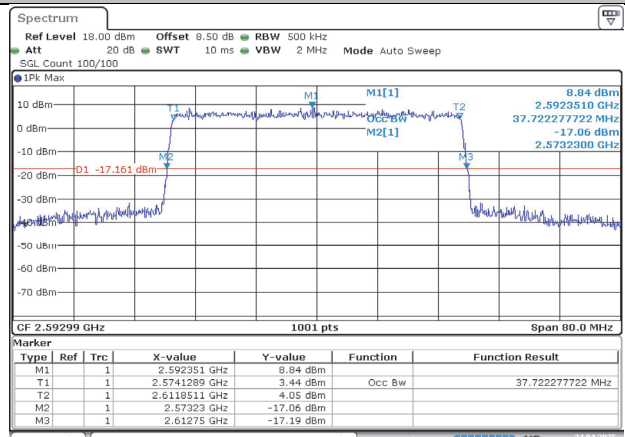
Date 24/Feb/2015 13:25:42

MCH / CP-64QAM / Outer_Full



Date 24/Feb/2015 13:25:46

MCH / CP-256QAM / Outer_Full



Date 24/Feb/2015 13:25:50