Hi Helen,

Here are the answers you have been waiting for from Alvarion.

best regards
Tom

> -----Original Message-----
> From: Thomas Cokenias [mailto:tom@tncokenias.org]
> Sent: Tuesday, February 28, 2006 9:54 PM
>>Subject: Re: Alvarion Ltd., FCC ID: LKT-VL-53 , Assessment NO.:
> AN06T5528, Notice#1
>
>
> >Question #1: The test report shows the fundamental frequency allocation is
>> changed, from 5260-5320MHz to 5265-5335MHz. Based upon FCC2.1043
>> (a), "changes to the basic frequency determining and stabilizing
>> circuitry (including clock or data rates), frequency multiplication
>> stages, bacis modulator circuit or maximum power or field strength
>> ratings shall not be performed without application for and
>> authorization of a new grant of certification. "
>
>> ANS 1: The changes made do not involve changes to basic frequency
>> determining and stabilizing circuitry - both versions of the
>> Atheros chipset produce OFDM modulation ion that meet 802.11a data
>> rates, bandwidths, etc., furthermore, the differences between the
>> two versions of the chips are minimal, the newer set is an
>> enhancement of the older design. The expansion of the frequency
>> coverage to 5265-5335 MHzis achieved via software selection only.
>
>> Question #2: RevB and RevC change table says "final stage of RF path
>> has been changed". Please explain what and how "final stage of RF path"
>> has been changed, does it affect any one of "basic frequency determining
>> and stabilizing circuitry (including clock or data rates), frequency
>> multiplication stages, bacis modulator circuit"?

> ANS 2: “Final stage of RF path has been changed” means:
   - the power amplifier was changed (it was HMC406, it is LX5506);
   - the T/R switch was changed (it was a PIN diode circuitry, it is a MMIC - HWS432).

   Basic frequency determining and stabilizing circuitry (including clock or data rates), frequency multiplication stages, basic modulator circuit, up- and down-converting circuits have not been changed.
>
> Question #3: The power output in the class II report (page 24 & 26) decreased
>> a lot from the original filing (page 36 & 48), more than 0.5dBm
>> which is allowed by FCC for unlicensed device Class II change filing.
>
> ANS 3: Please refer to revised test report for question #3. The maximum power at 14.27 dBm is within +/- 0.5 dB of maximum power shown on original grant, 27 mW = 14.31 dBm

> Question #4: The class II change cover letter as well as the operational
>> description it refers indicate RF chipsets have been changed from
>> AR5111+AR5211 to AR5112+AR5212. Does it affect any one of "basic frequency
> determining and stabilizing circuitry (including clock or data rates), frequency multiplication stages, basic modulator circuit"? The change may not be granted under Class II permissive change scheme, it may be granted by a new certification. Please justify why this is a qualified Class II permissive change application.

> ANS 4: Attestation statement is attached.

Question #5: Please submit setup photos.

ANS 5: Set-up photos are attached.

>> Best Regards,
>> Helen Zhao