

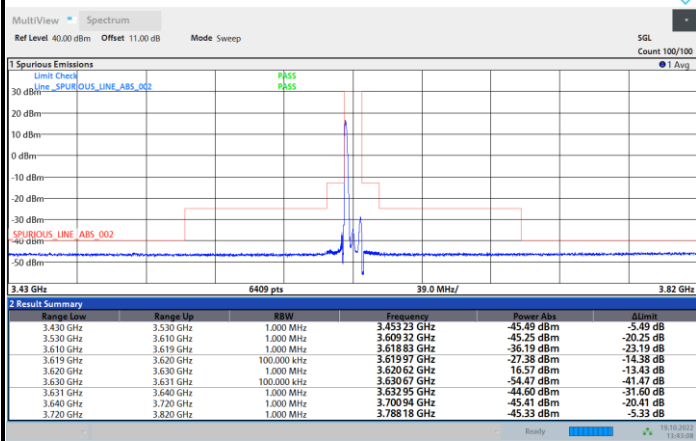


FR1 n48 / 10MHz / DFT-S OFDM / 256QAM

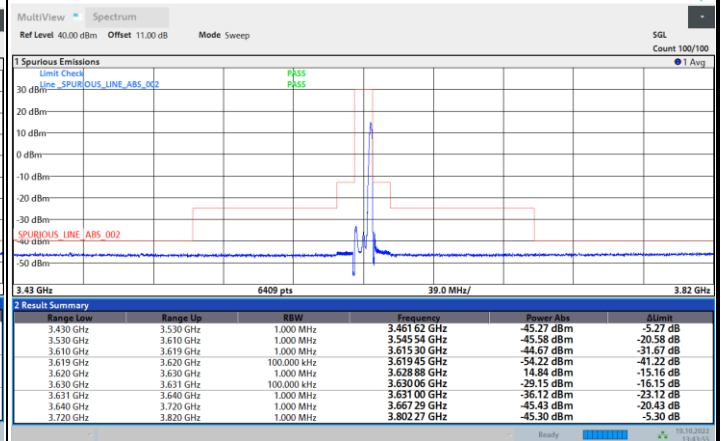
Middle Channel

1RB0

1RBmax

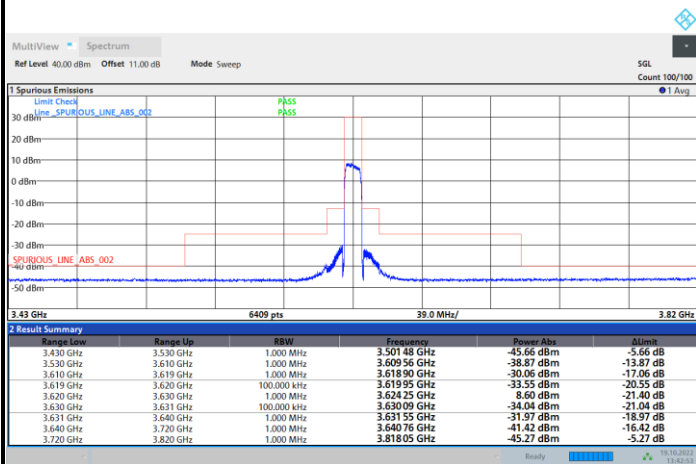


13:43:09 19.10.2022



13:43:55 19.10.2022

Full RB



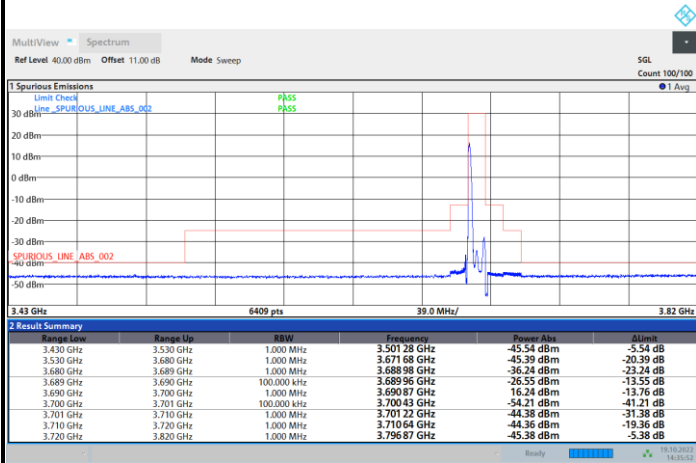
13:42:54 19.10.2022



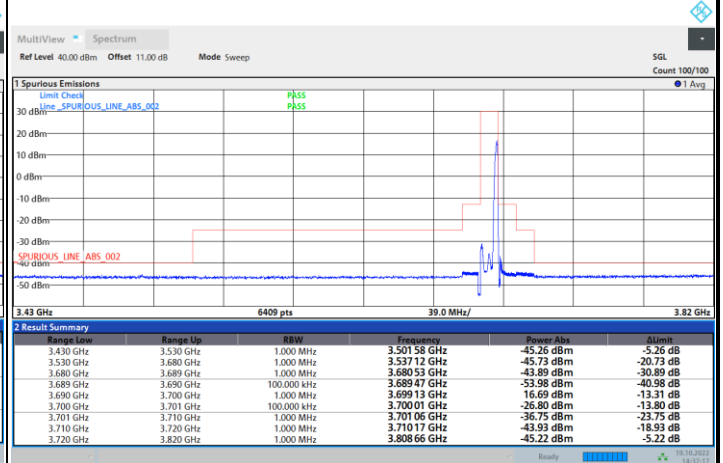
FR1 n48 / 10MHz / DFT-S OFDM / 256QAM

Highest Channel

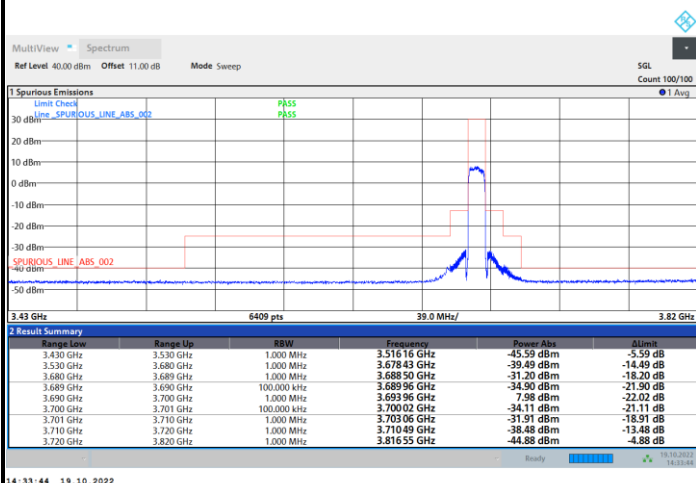
1RB0



1RBmax



Full RB



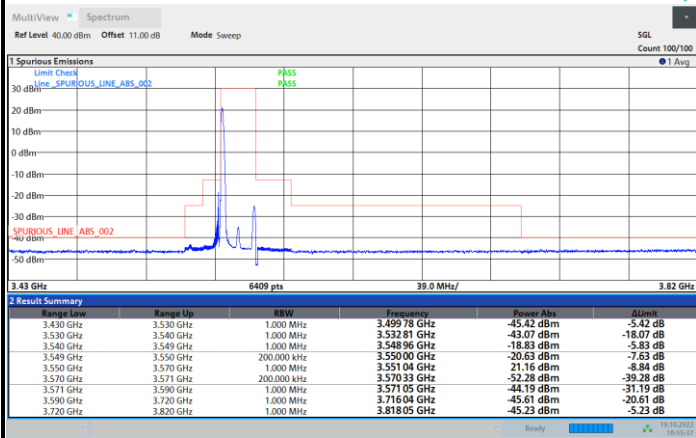


FR1 n48 / 20MHz / DFT-S OFDM / PI/2 BPSK

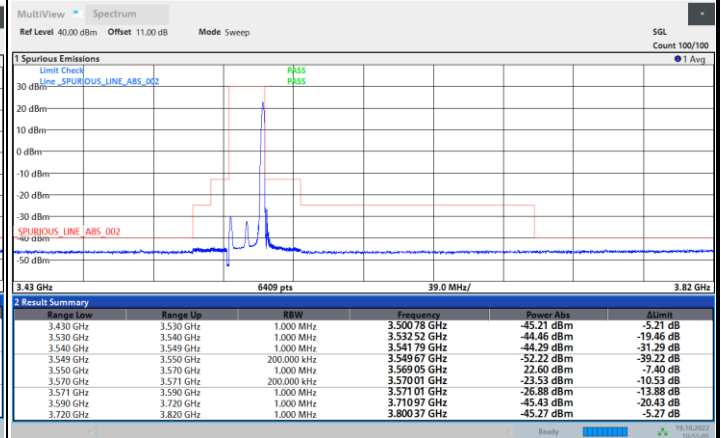
Lowest Channel

1RB0

1RBmax

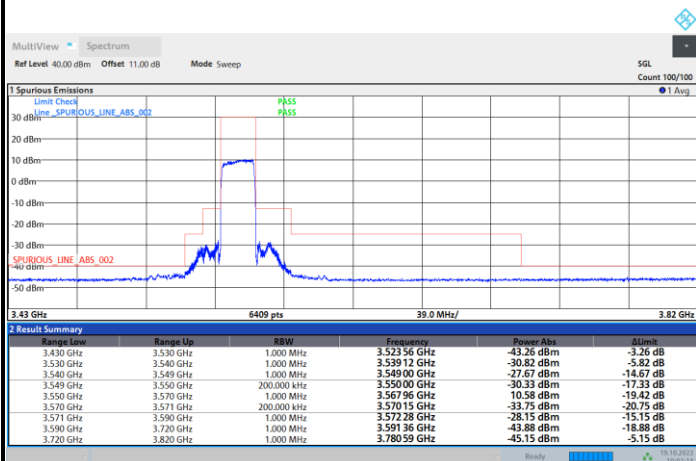


10:55:38 19.10.2022



10:55:05 19.10.2022

Full RB



10:02:15 19.10.2022

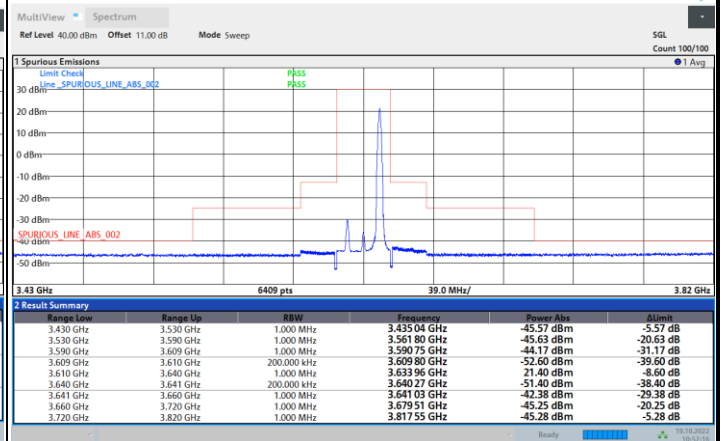
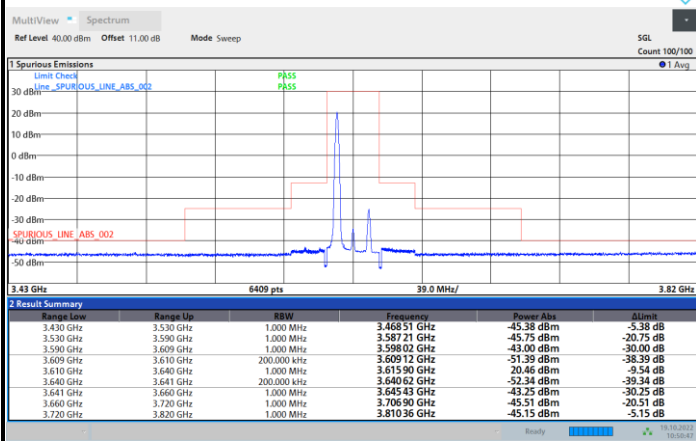


FR1 n48 / 20MHz / DFT-S OFDM / PI/2 BPSK

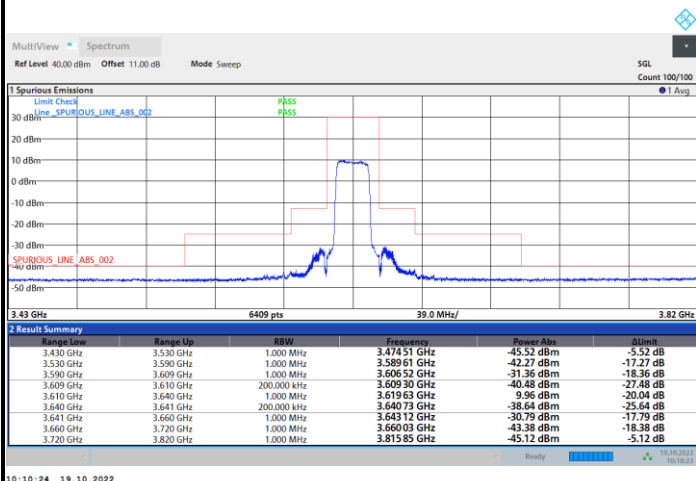
Middle Channel

1RB0

1RBmax



Full RB

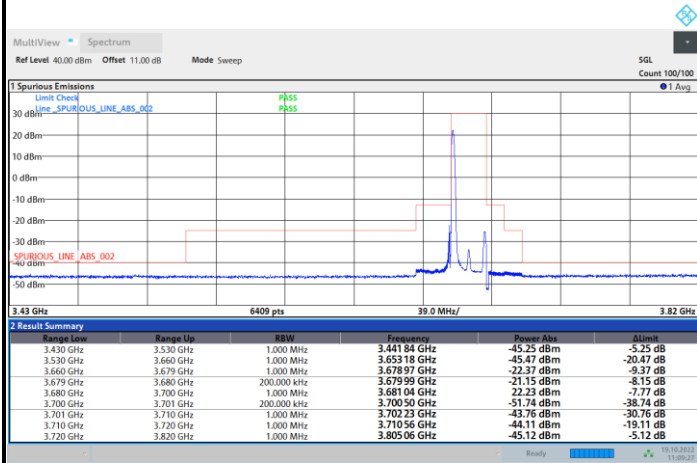




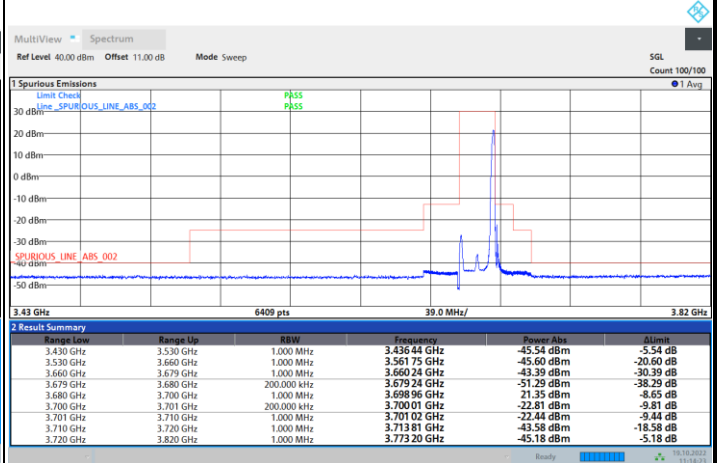
FR1 n48 / 20MHz / DFT-S OFDM / PI/2 BPSK

Highest Channel

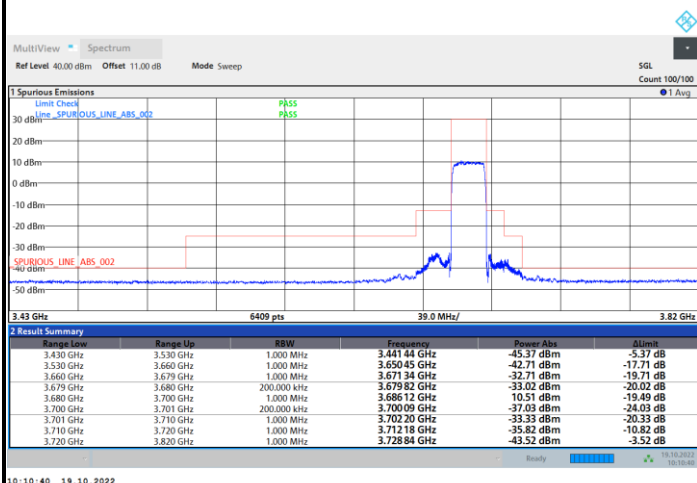
1RB0



1RBmax



Full RB



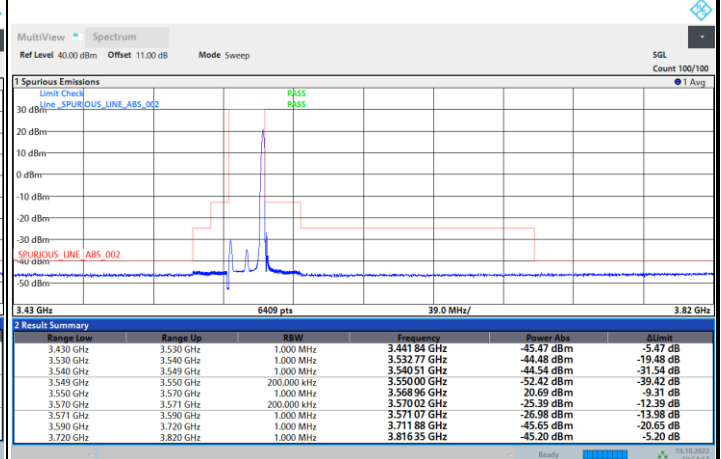
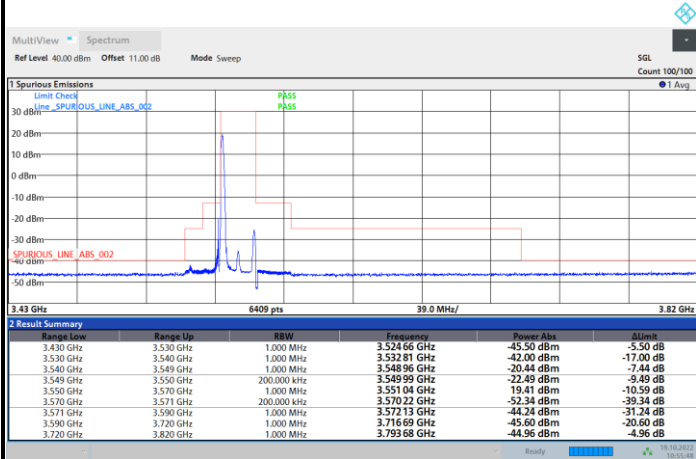


FR1 n48 / 20MHz / DFT-S OFDM / QPSK

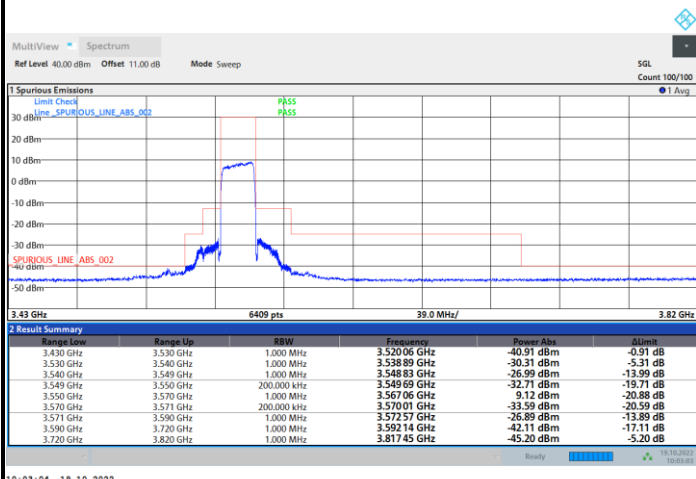
Lowest Channel

1RB0

1RBmax



Full RB



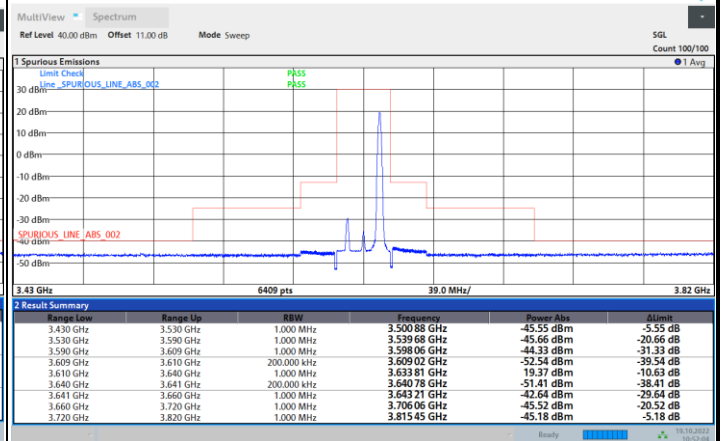
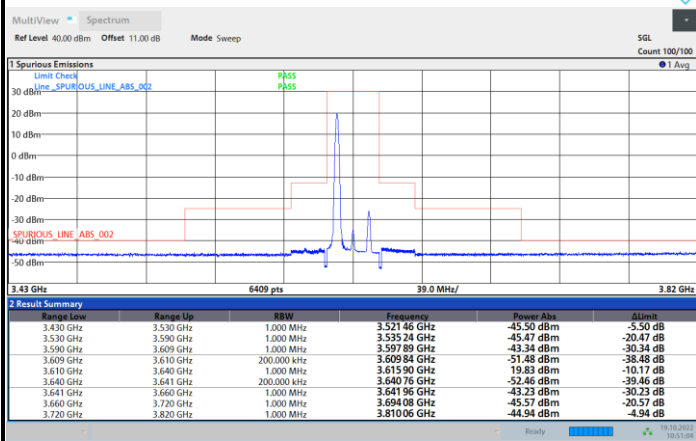


FR1 n48 / 20MHz / DFT-S OFDM / QPSK

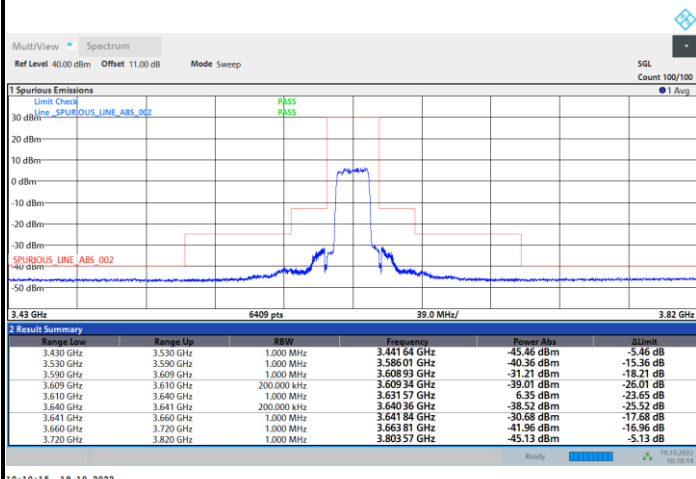
Middle Channel

1RB0

1RBmax



Full RB



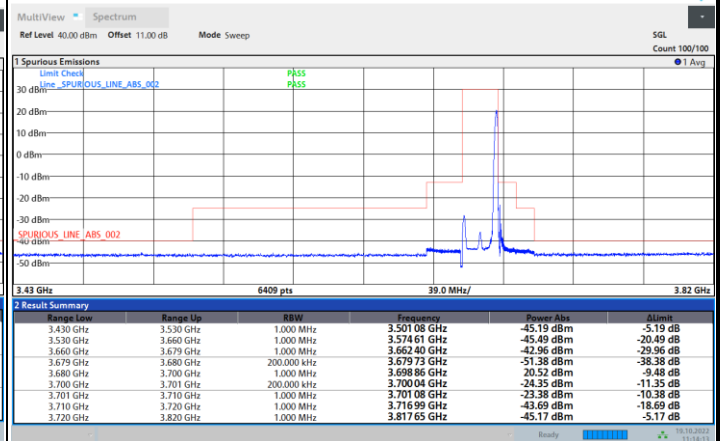
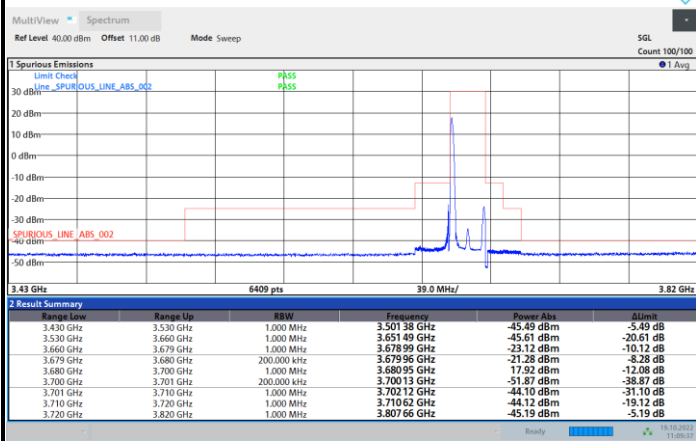


FR1 n48 / 20MHz / DFT-S OFDM / QPSK

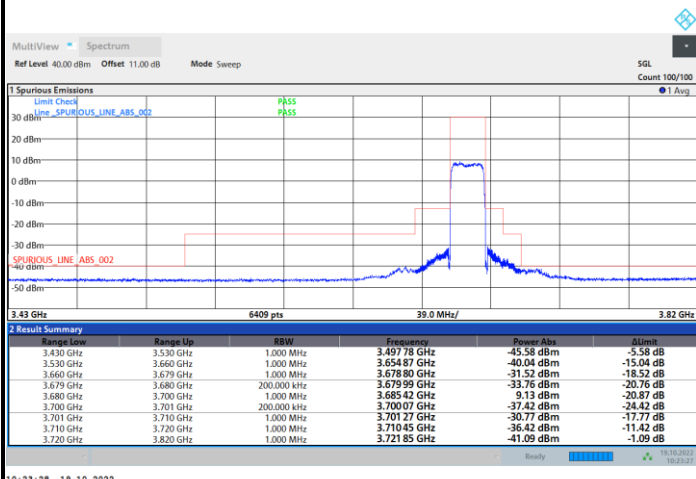
Highest Channel

1RB0

1RBmax



Full RB



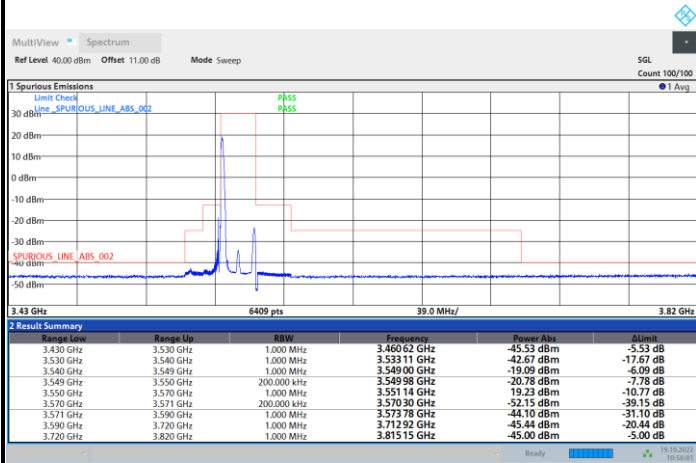


FR1 n48 / 20MHz / DFT-S OFDM / 16QAM

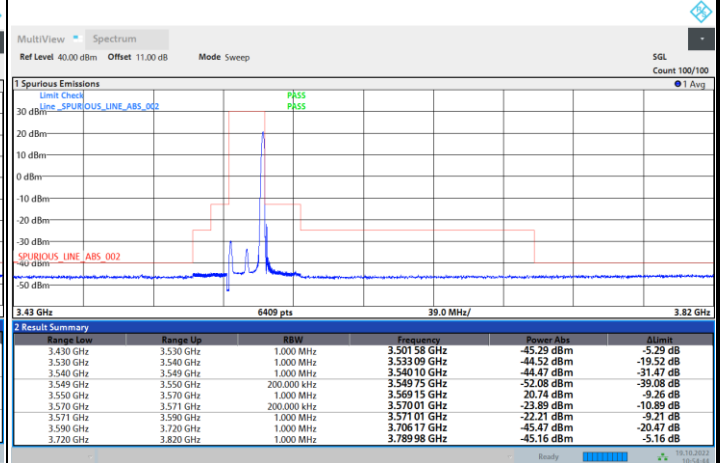
Lowest Channel

1RB0

1RBmax

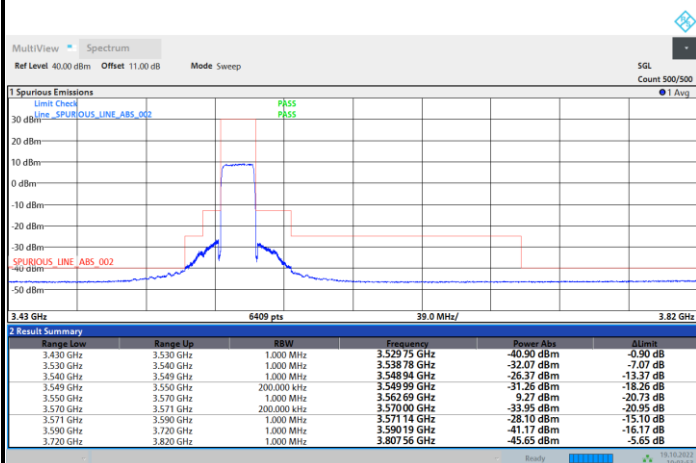


10:56:02 19.10.2022



10:54:44 19.10.2022

Full RB



10:03:53 19.10.2022

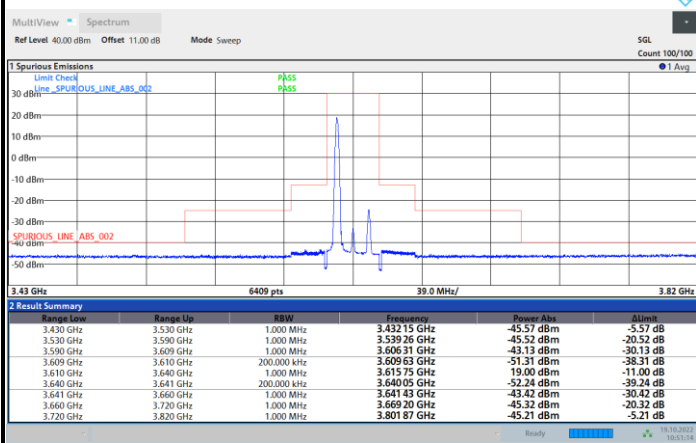


FR1 n48 / 20MHz / DFT-S OFDM / 16QAM

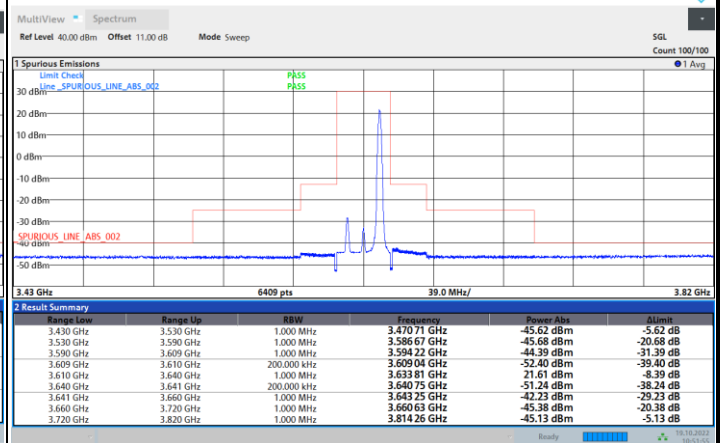
Middle Channel

1RB0

1RBmax

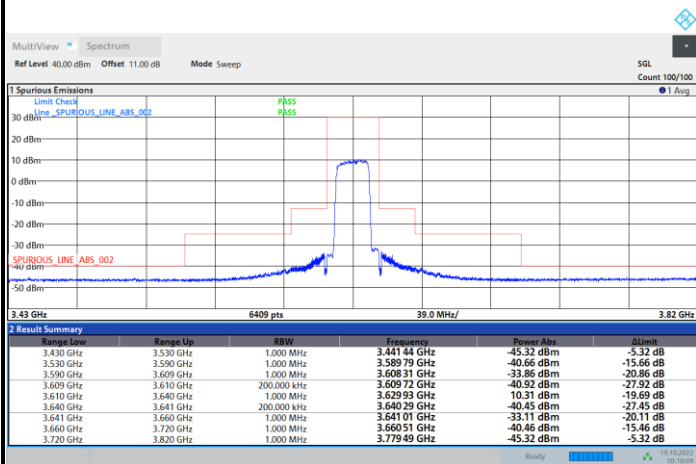


10:51:15 19.10.2022



10:51:55 19.10.2022

Full RB



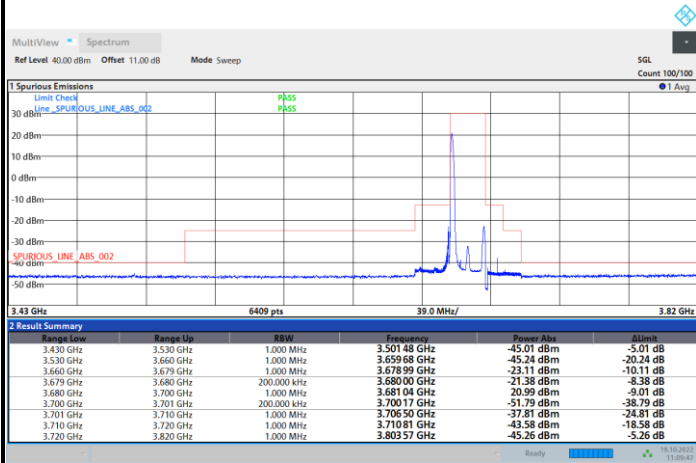
10:10:04 19.10.2022



FR1 n48 / 20MHz / DFT-S OFDM / 16QAM

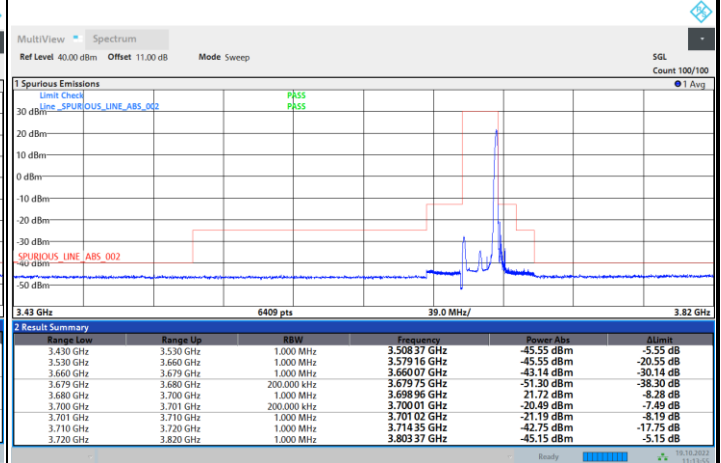
Highest Channel

1RB0



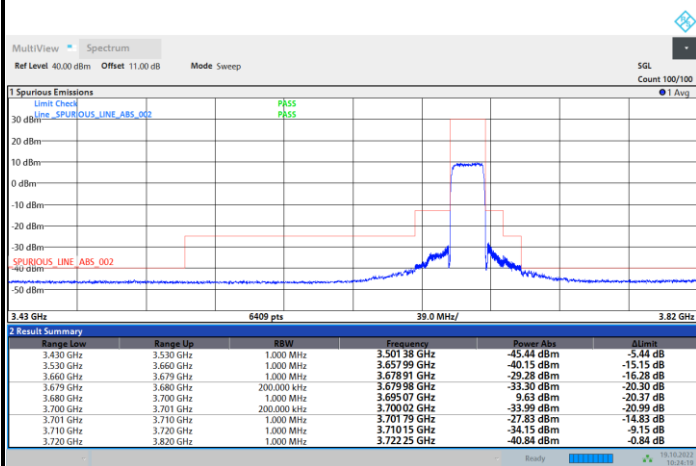
11:09:47 19.10.2022

1RBmax



11:13:56 19.10.2022

Full RB



10:24:19 19.10.2022

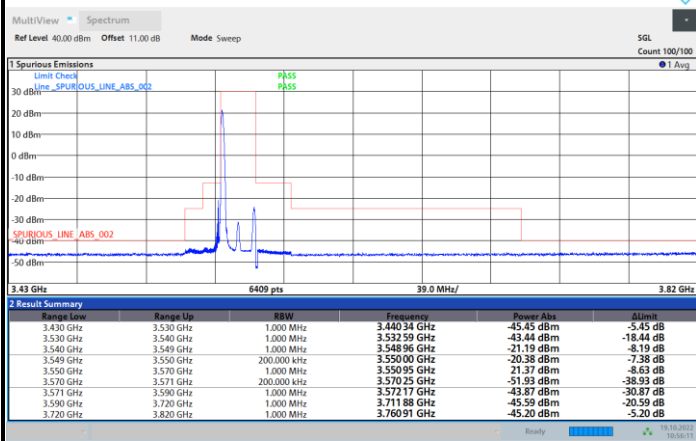


FR1 n48 / 20MHz / DFT-S OFDM / 64QAM

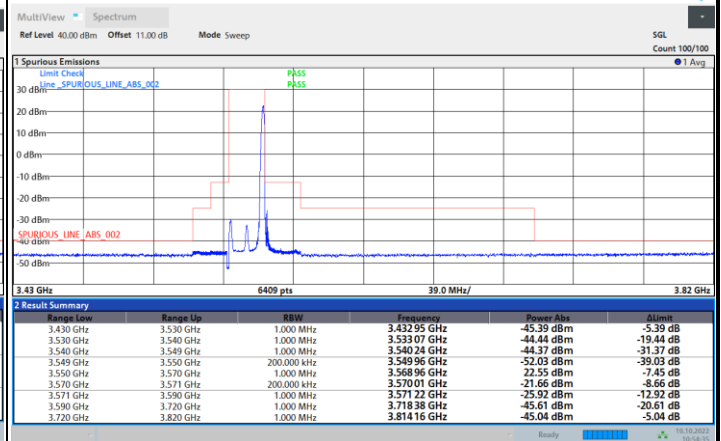
Lowest Channel

1RB0

1RBmax

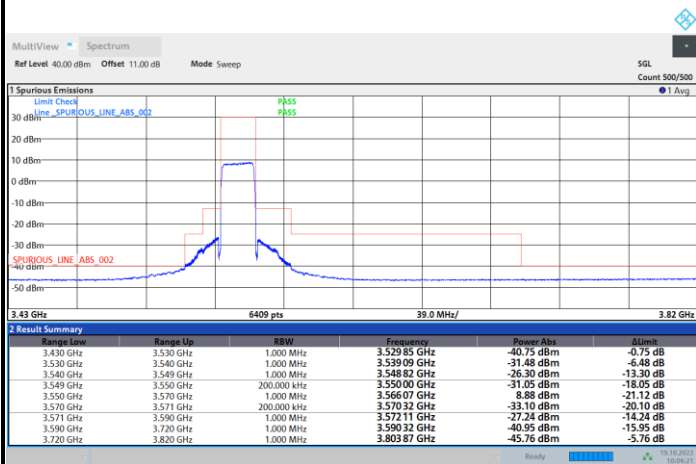


10:56:12 19.10.2022



10:56:35 19.10.2022

Full RB



10:04:22 19.10.2022

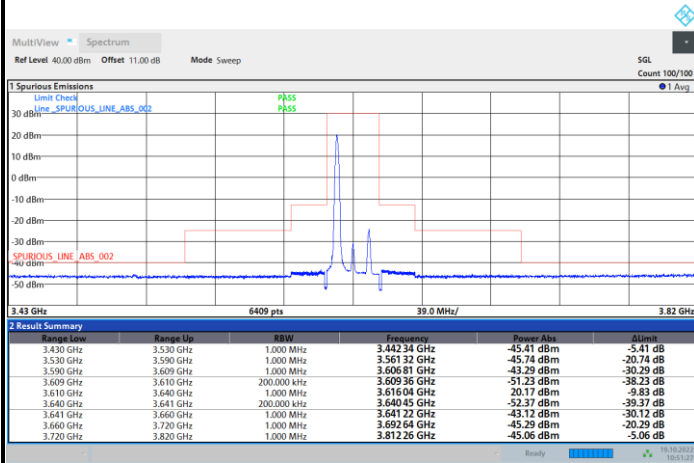


FR1 n48 / 20MHz / DFT-S OFDM / 64QAM

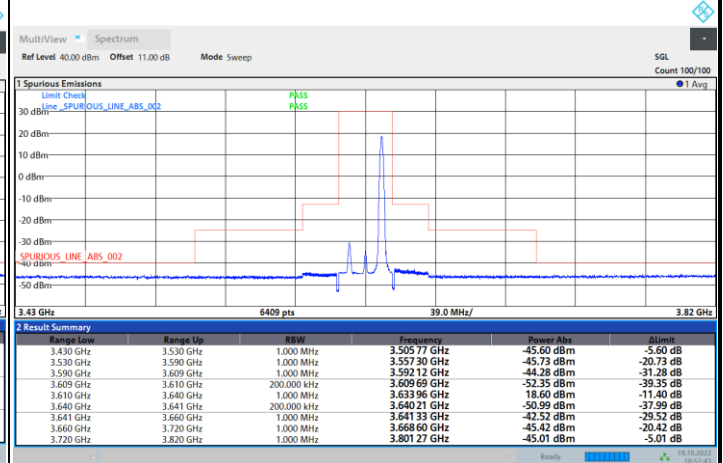
Middle Channel

1RB0

1RBmax

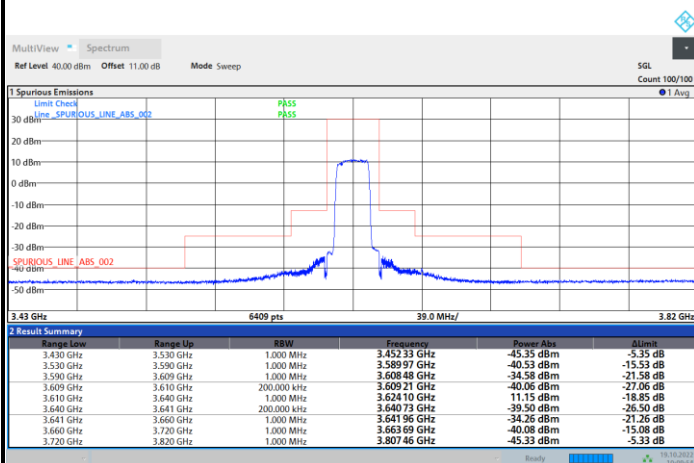


10:51:27 19.10.2022



10:51:43 19.10.2022

Full RB



10:09:55 19.10.2022

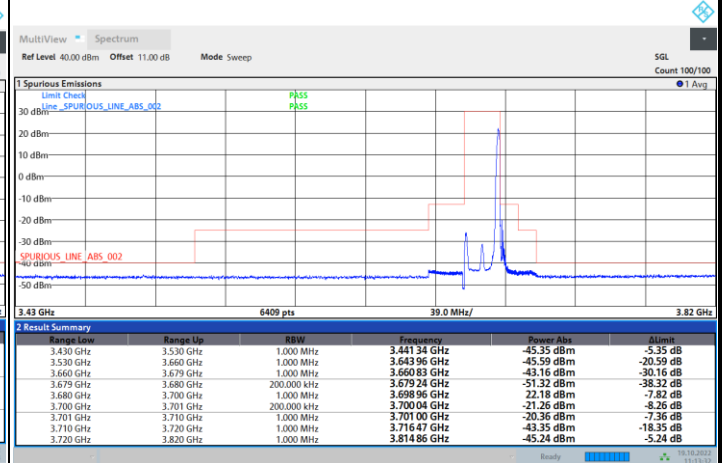
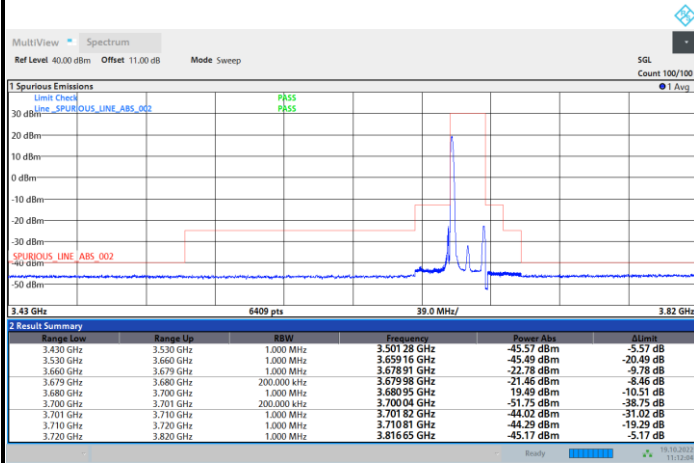


FR1 n48 / 20MHz / DFT-S OFDM / 64QAM

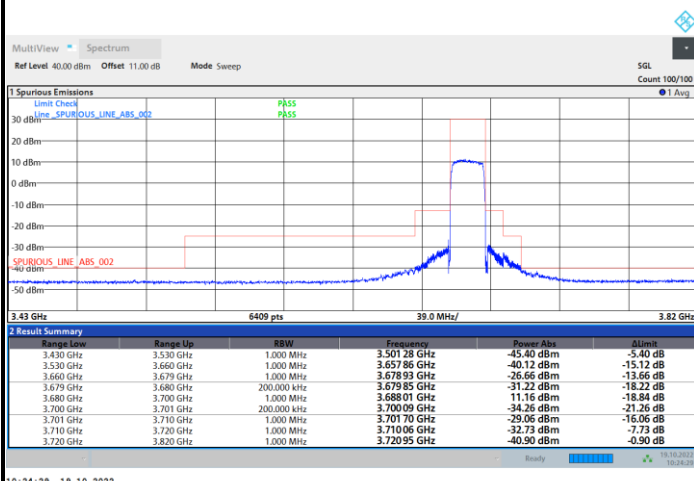
Highest Channel

1RB0

1RBmax



Full RB



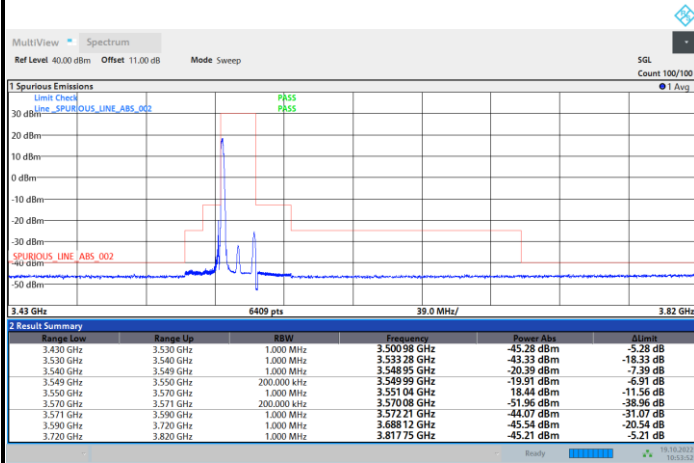


FR1 n48 / 20MHz / DFT-S OFDM / 256QAM

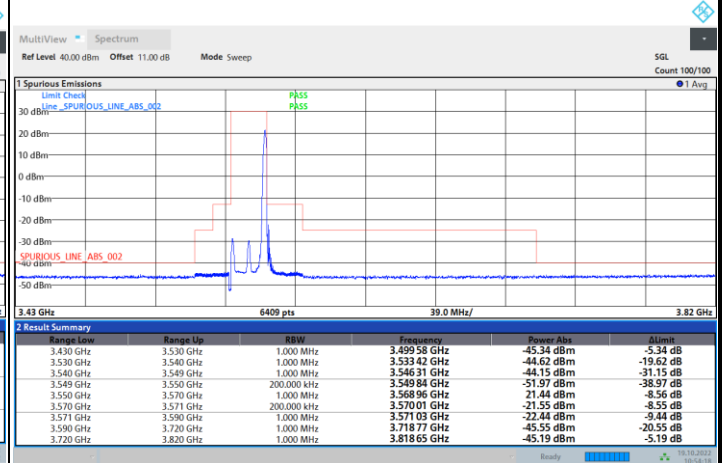
Lowest Channel

1RB0

1RBmax

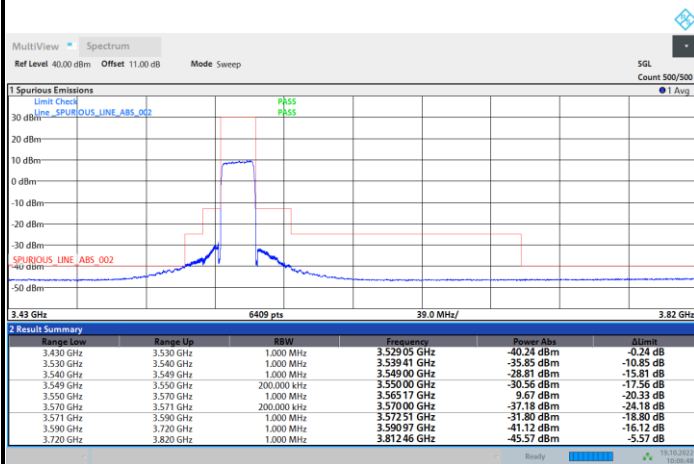


10:53:53 19.10.2022



10:54:18 19.10.2022

Full RB



10:06:49 19.10.2022

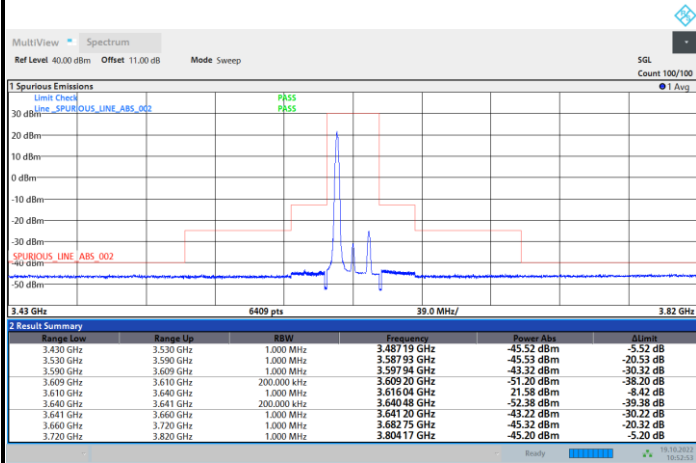


FR1 n48 / 20MHz / DFT-S OFDM / 256QAM

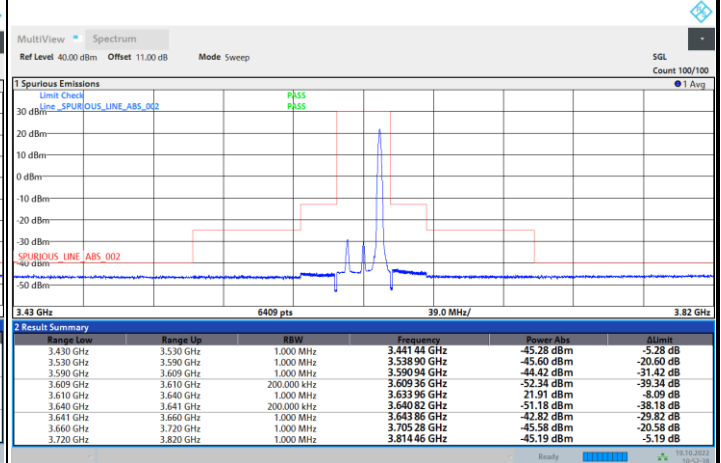
Middle Channel

1RB0

1RBmax

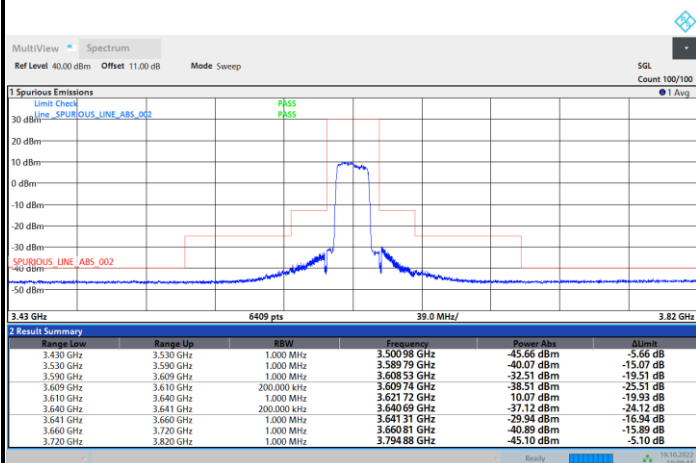


10:52:14 19.10.2022



10:52:38 19.10.2022

Full RB



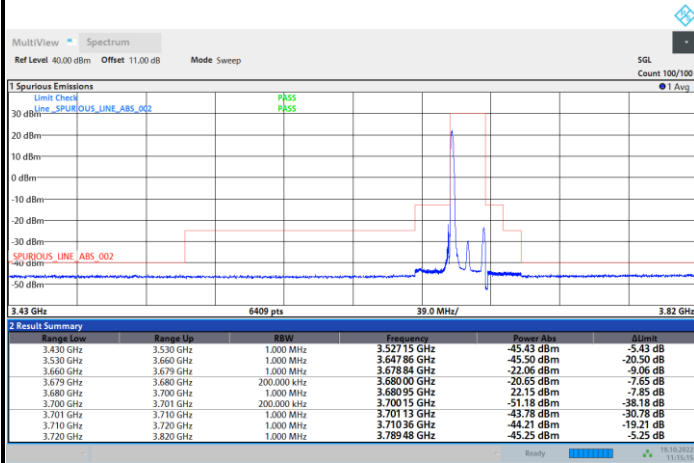
10:09:45 19.10.2022



FR1 n48 / 20MHz / DFT-S OFDM / 256QAM

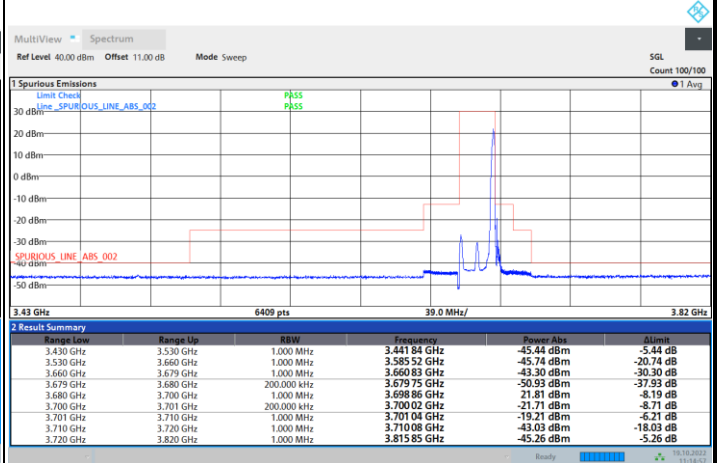
Highest Channel

1RB0



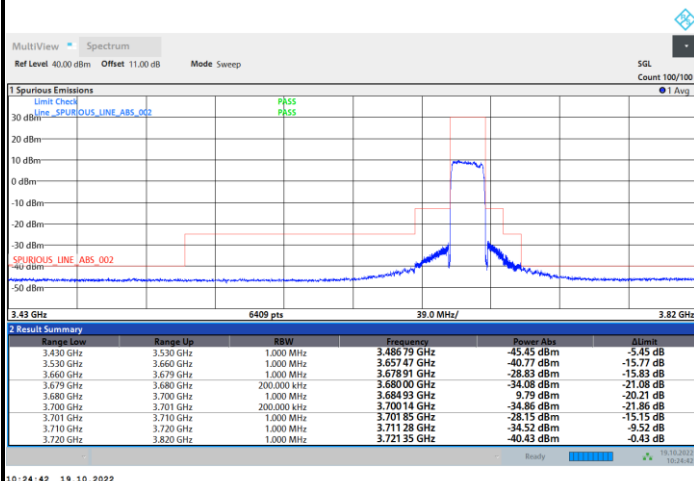
11:15:16 19.10.2022

1RBmax



11:14:58 19.10.2022

Full RB

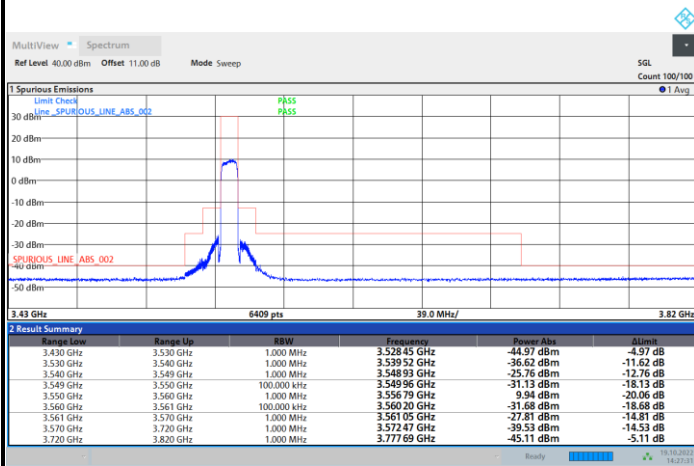


10:24:42 19.10.2022

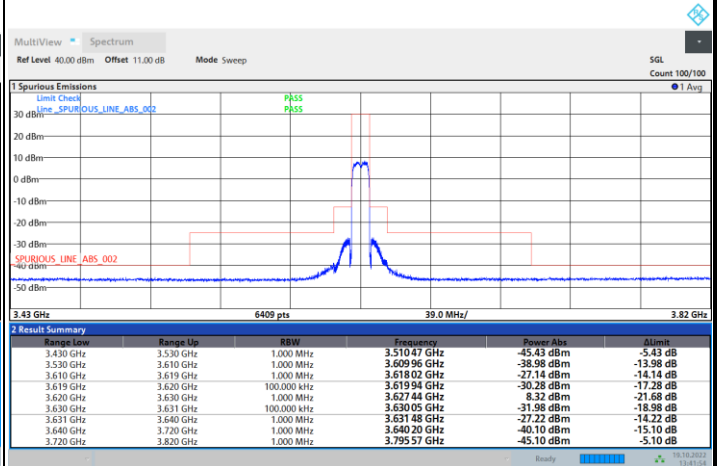


FR1 n48 / 10MHz / CP OFDM / QPSK / Full RB

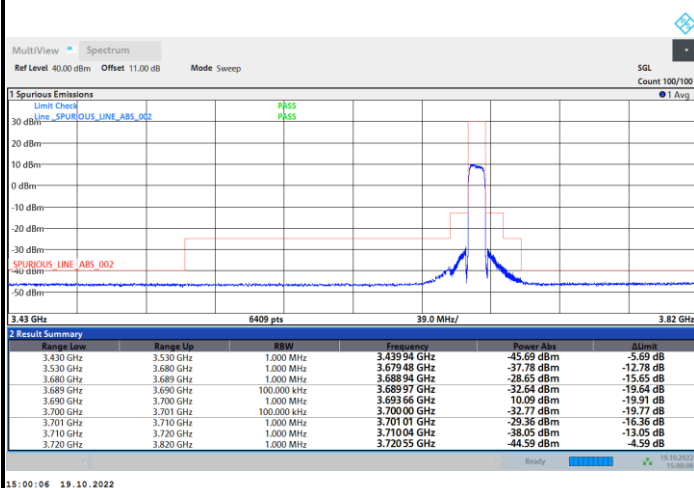
Lowest Channel



Middle Channel



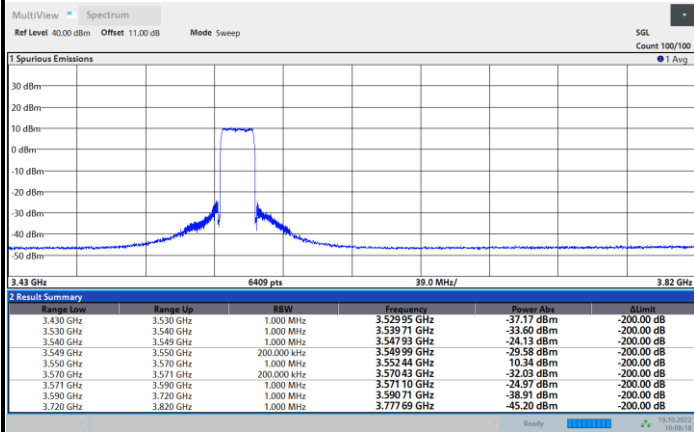
Highest Channel



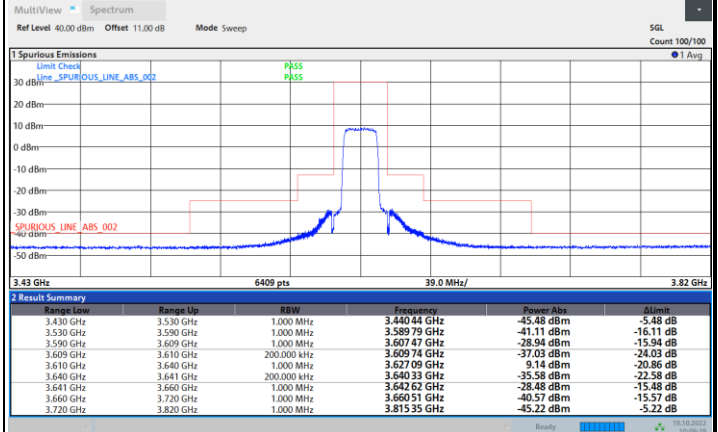


FR1 n48 / 20MHz / CP OFDM / QPSK / Full RB

Lowest Channel

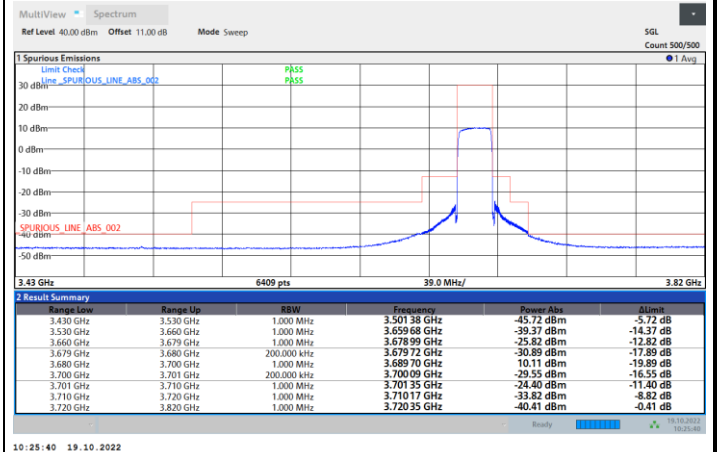
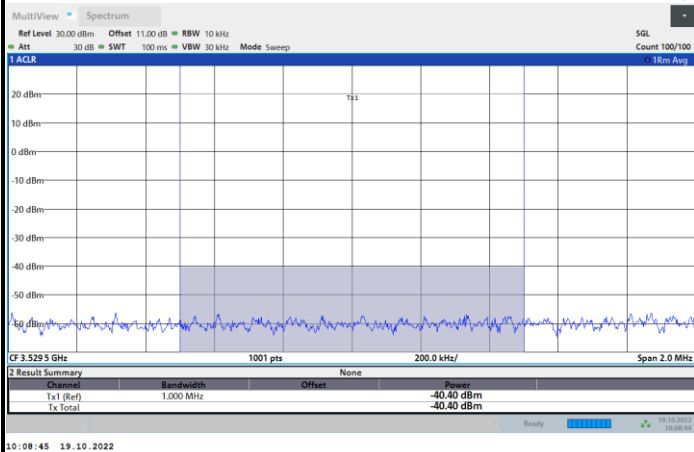


Middle Channel



Adjacent to the block edge can pass the limit
(shown below is the 3530MHz block edge)

Highest Channel



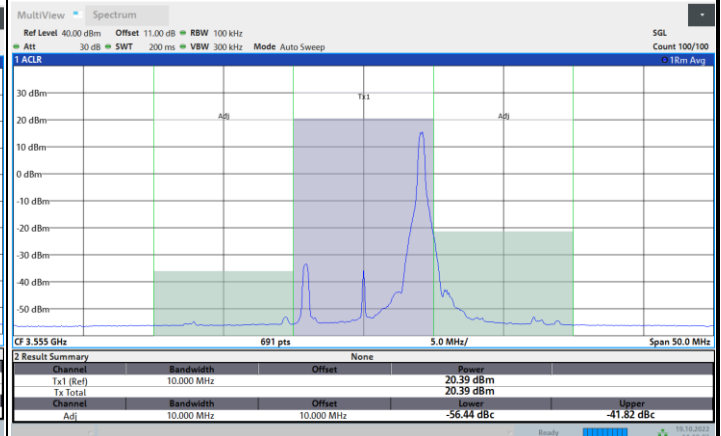
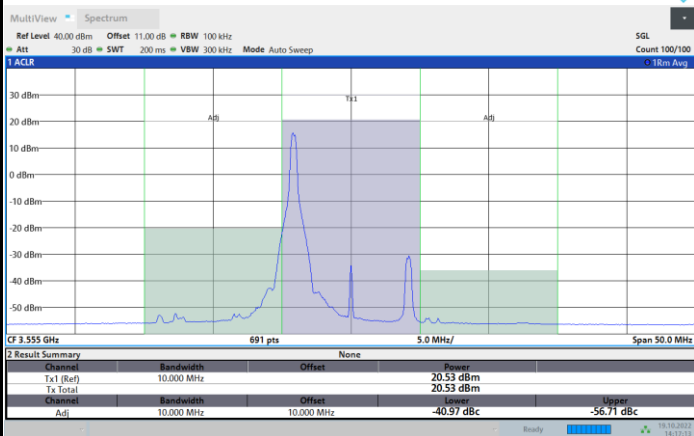
**Adjacent Channel Leakage Ratio (ACLR)**

FR1 n48 / 10MHz / DFT-S OFDM / PI/2 BPSK

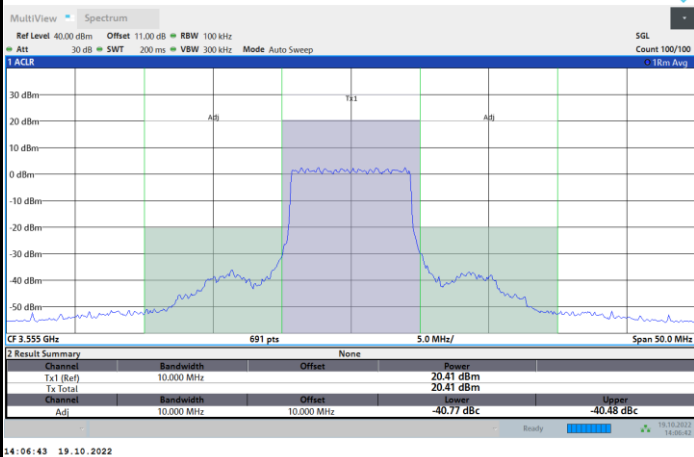
Lowest Channel

1RB0

1RBmax



Full RB

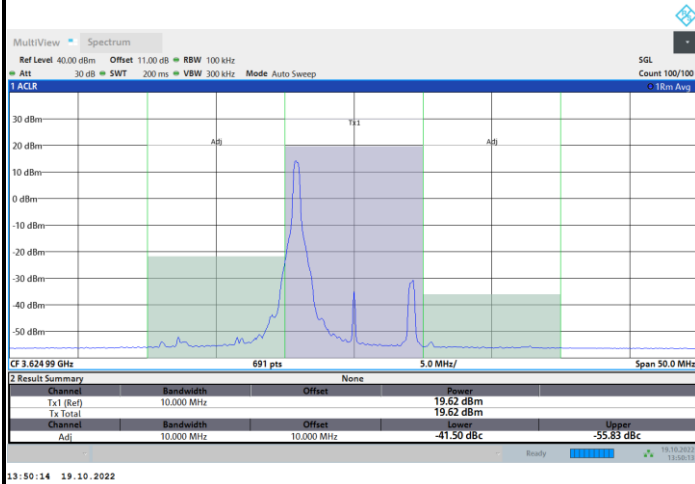




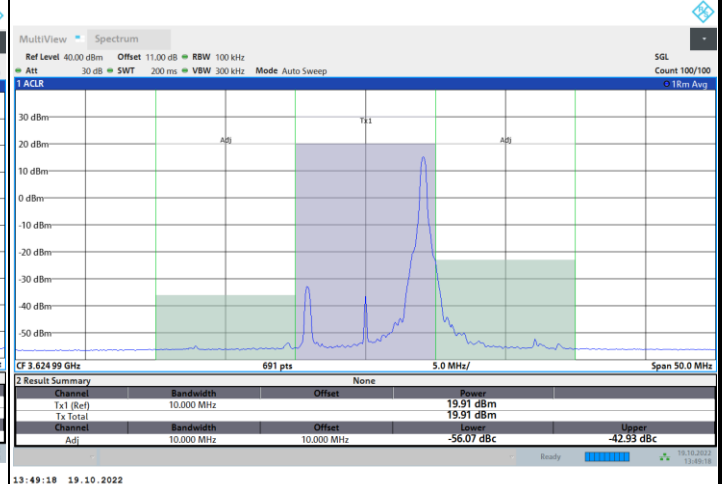
FR1 n48 / 10MHz / DFT-S OFDM / PI/2 BPSK

Middle Channel

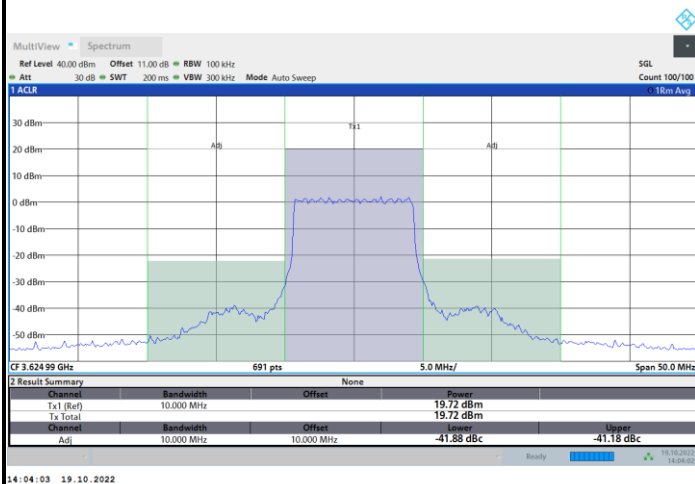
1RB0



1RBmax



Full RB

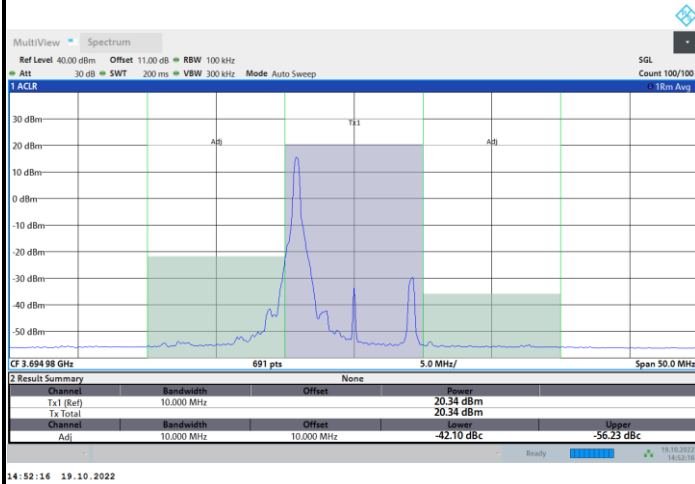




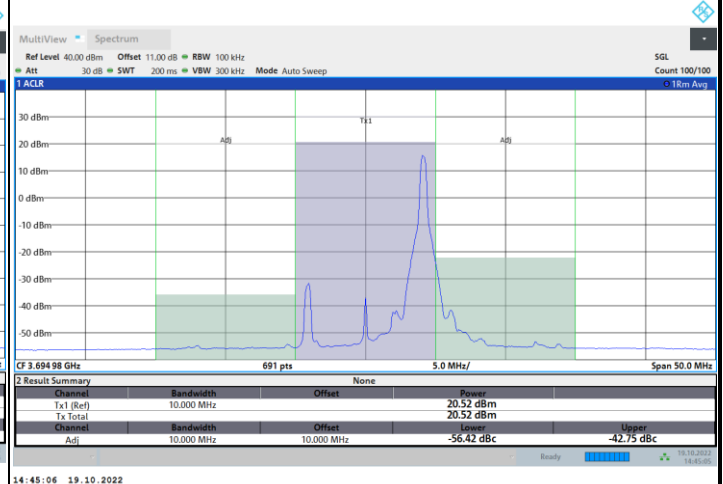
FR1 n48 / 10MHz / DFT-S OFDM / PI/2 BPSK

Highest Channel

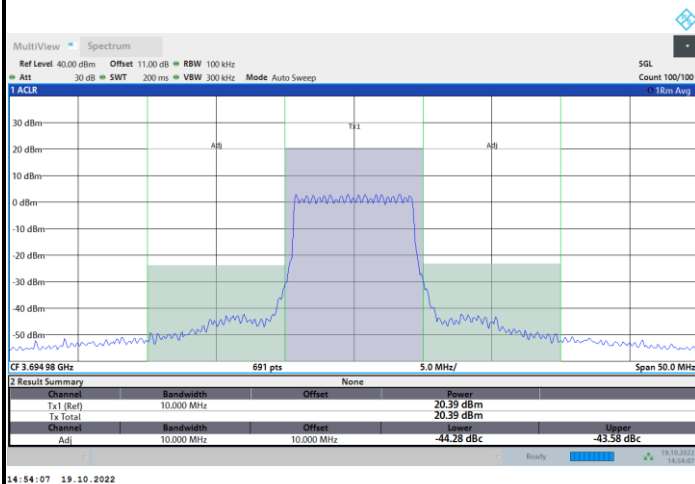
1RB0



1RBmax



Full RB

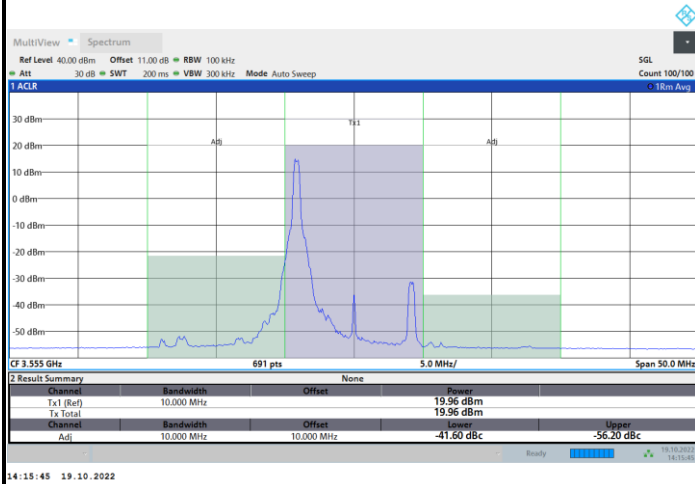




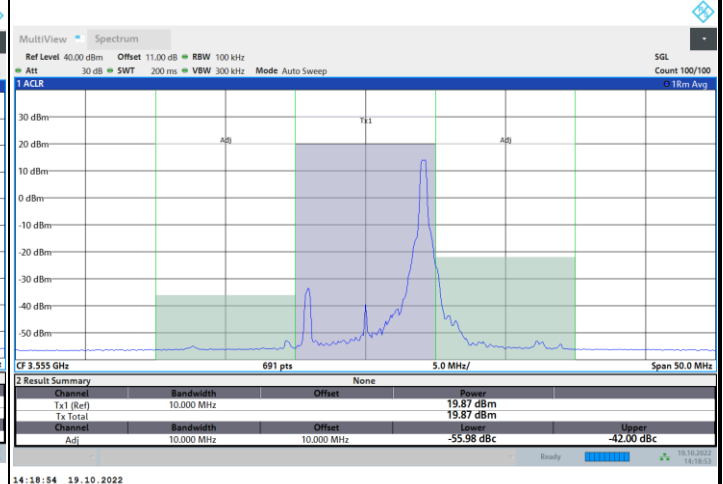
FR1 n48 / 10MHz / DFT-S OFDM / QPSK

Lowest Channel

1RB0



1RBmax



Full RB

