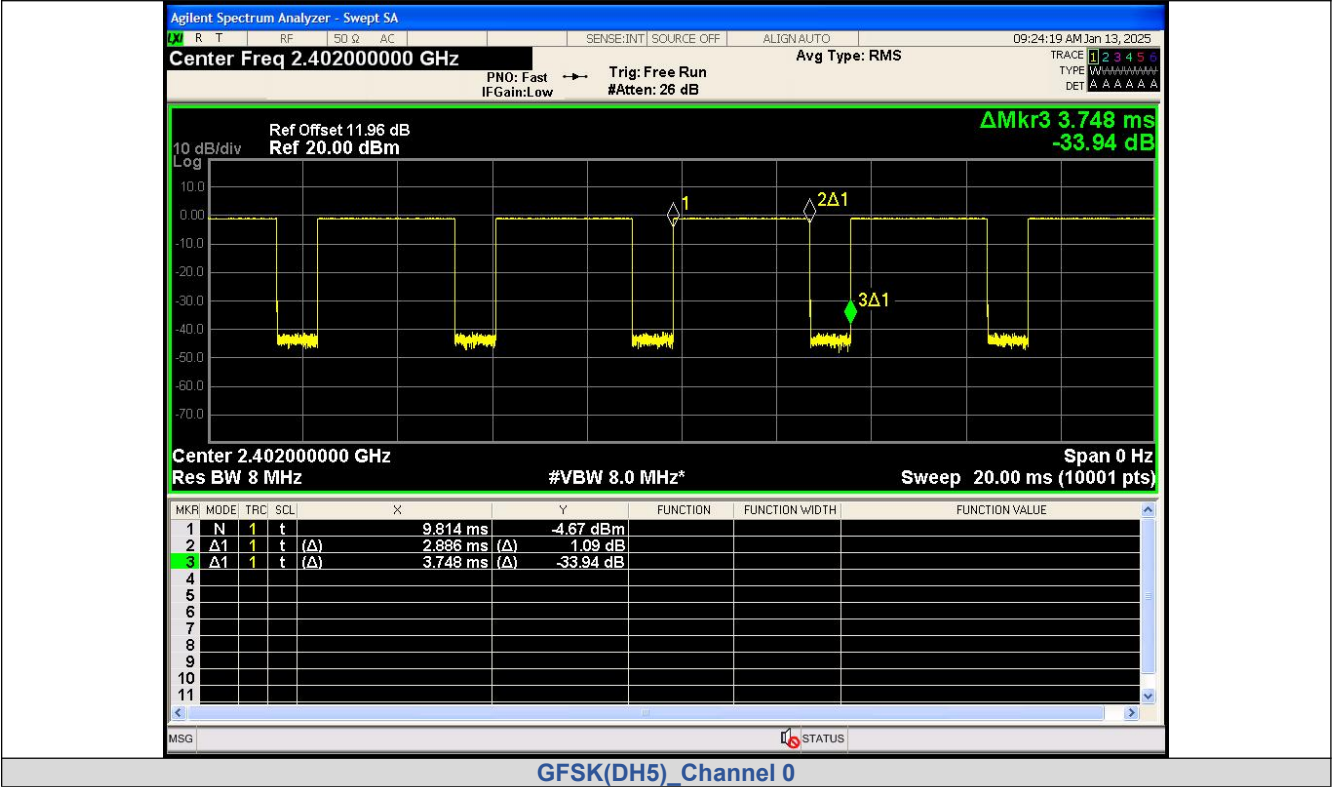


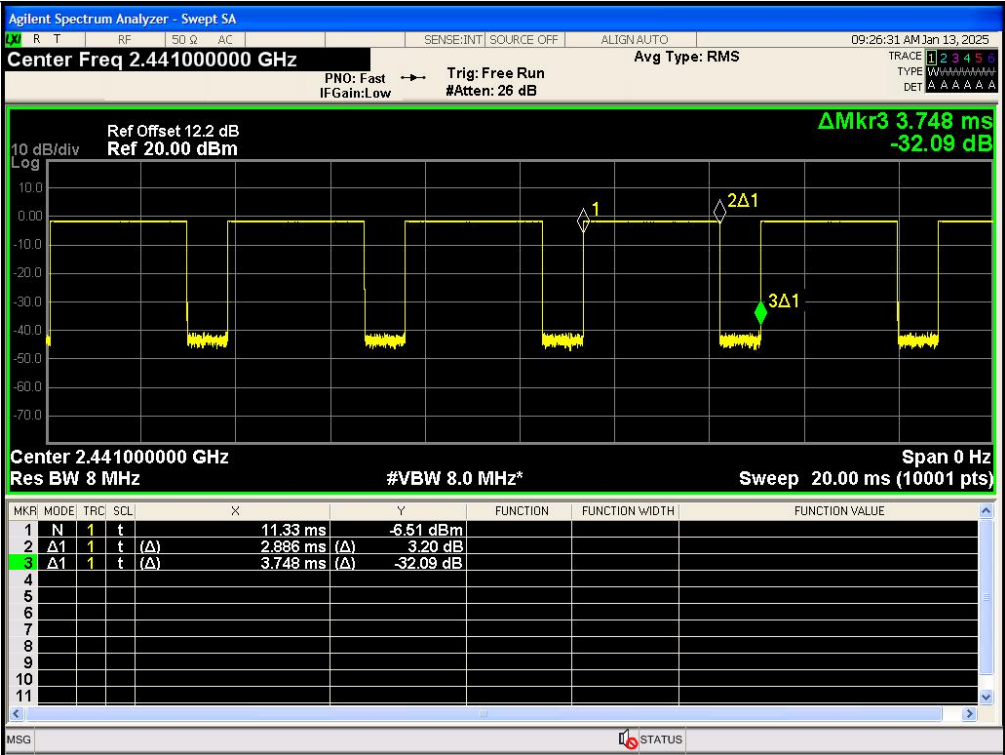
APPENDIX VI. Duty Cycle

Test Result

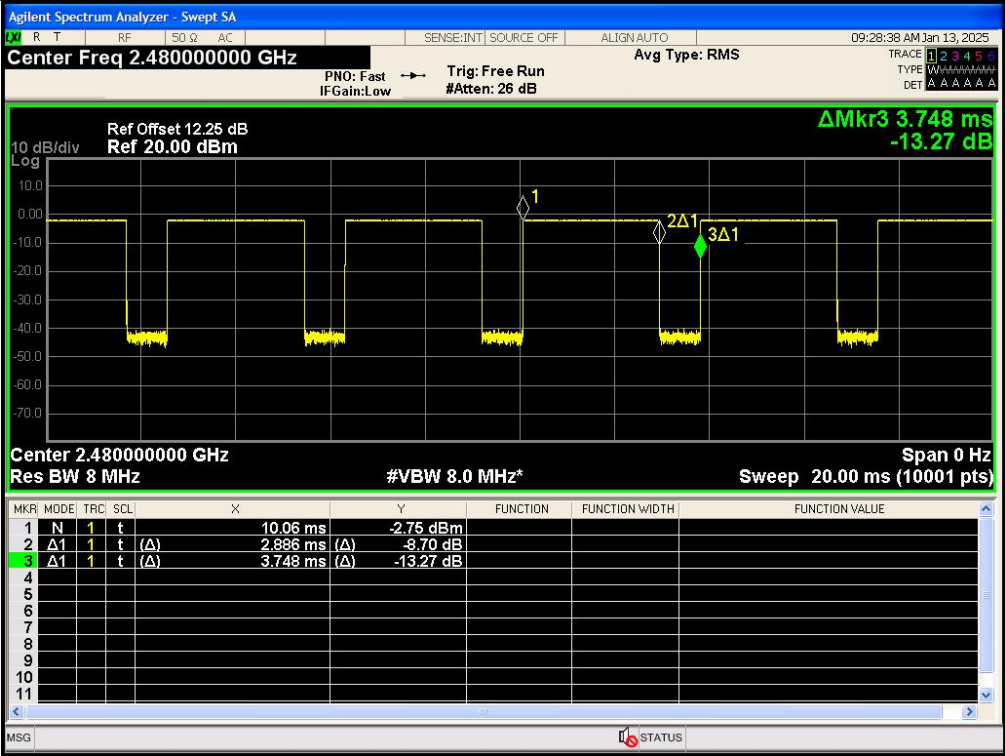
Modulation	Packets	Channel	On Time (ms)	Period (ms)	Duty Cycle (%)	Duty Cycle (linear)	Duty Cycle Factor (dB)
GFSK	DH5	0	2.886	3.748	77.00	0.7700	1.1351
		39	2.886	3.748	77.00	0.7700	1.1351
		78	2.886	3.748	77.00	0.7700	1.1351
π /4DQPSK	2-DH5	0	2.892	3.748	77.16	0.7716	1.1261
		39	2.892	3.748	77.16	0.7716	1.1261
		78	2.890	3.748	77.11	0.7711	1.1289
8DPSK	3-DH5	0	2.892	3.748	77.16	0.7716	1.1261
		39	2.892	3.748	77.16	0.7716	1.1261
		78	2.894	3.748	77.21	0.7721	1.1233

Test Graphs





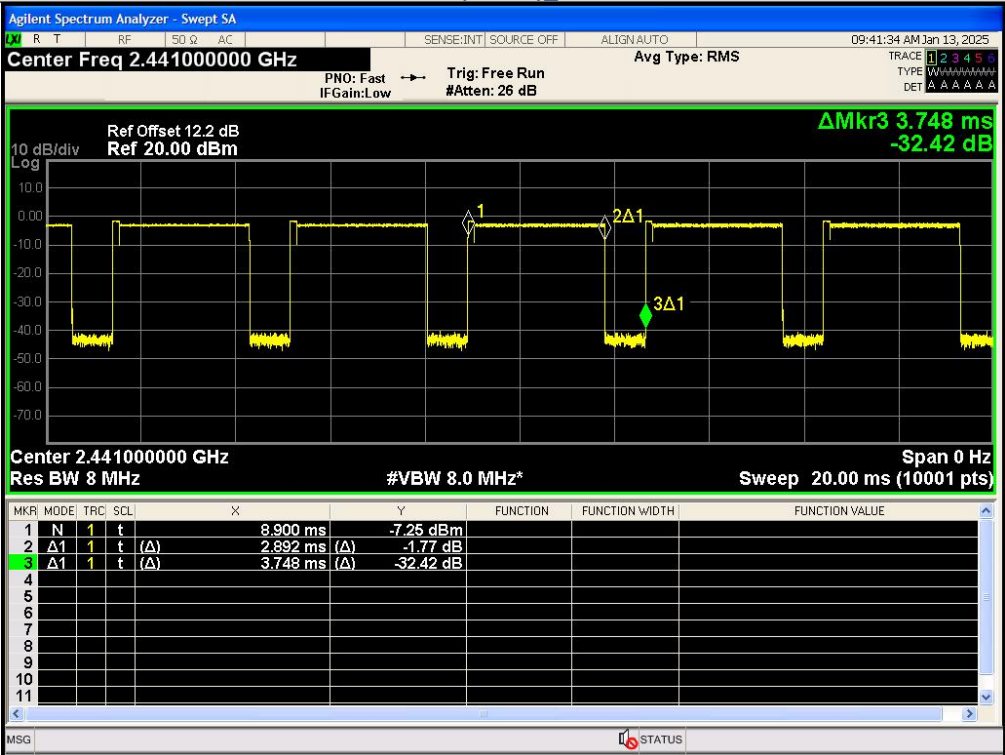
GFSK(DH5)_Channel 39



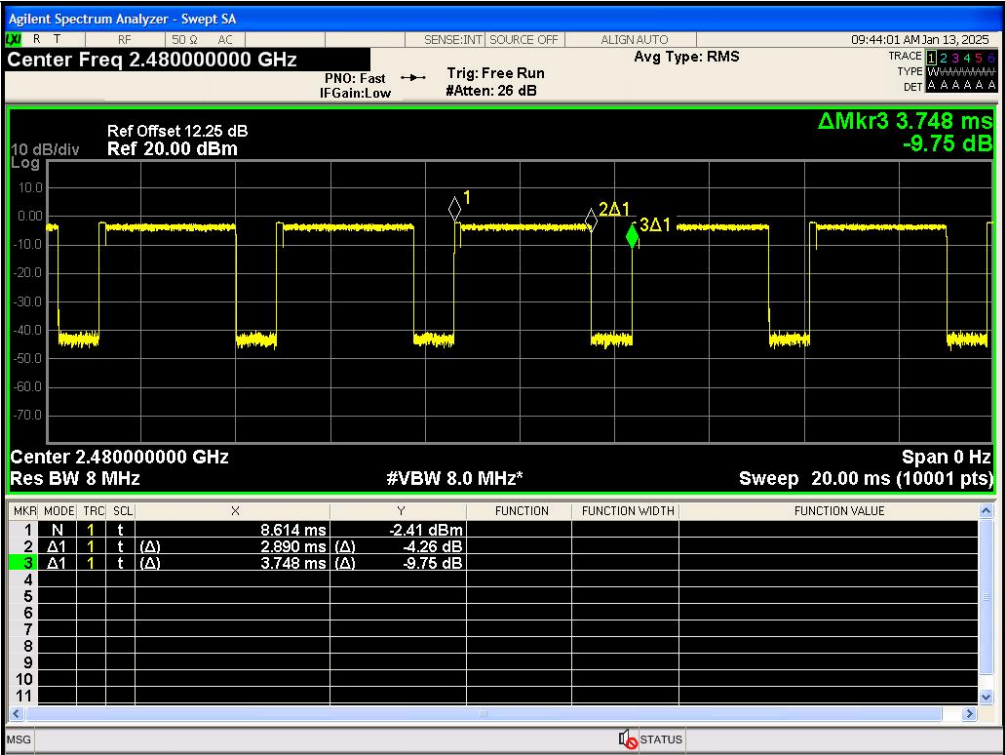
GFSK(DH5)_Channel 78



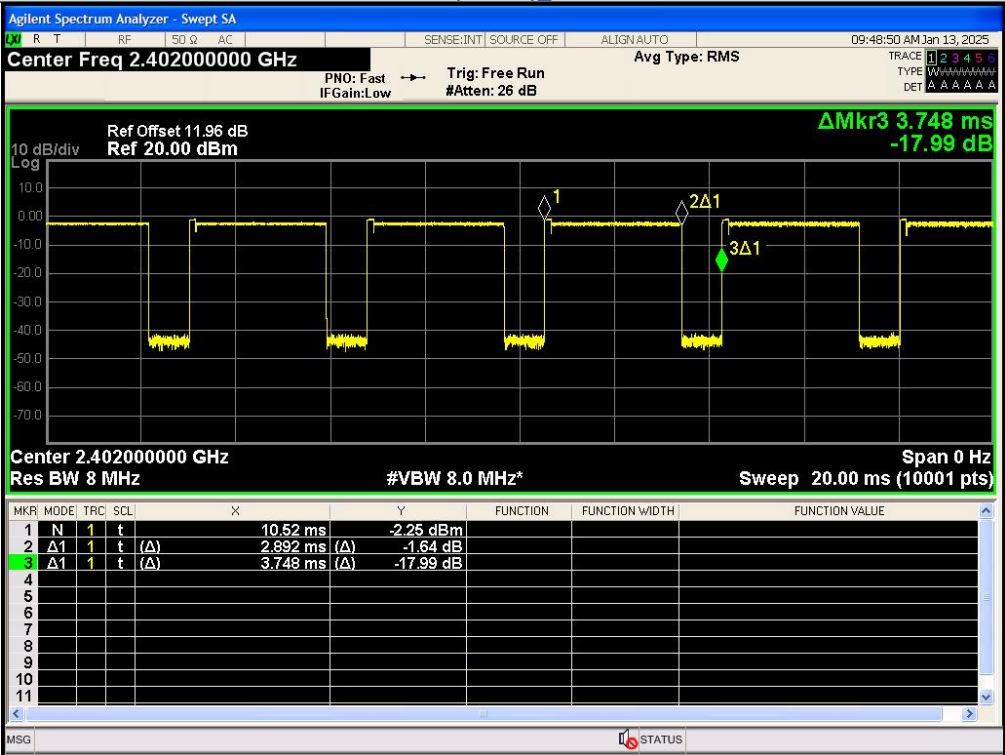
π/4DQPSK(2-DH5)_Channel 0



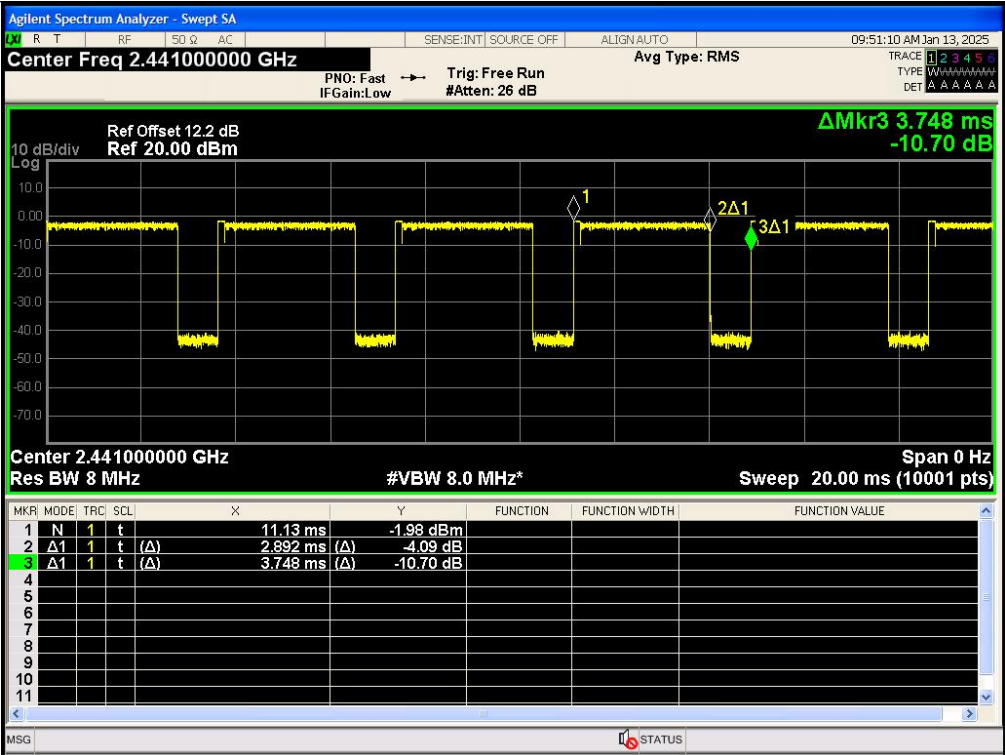
π/4DQPSK(2-DH5)_Channel 39



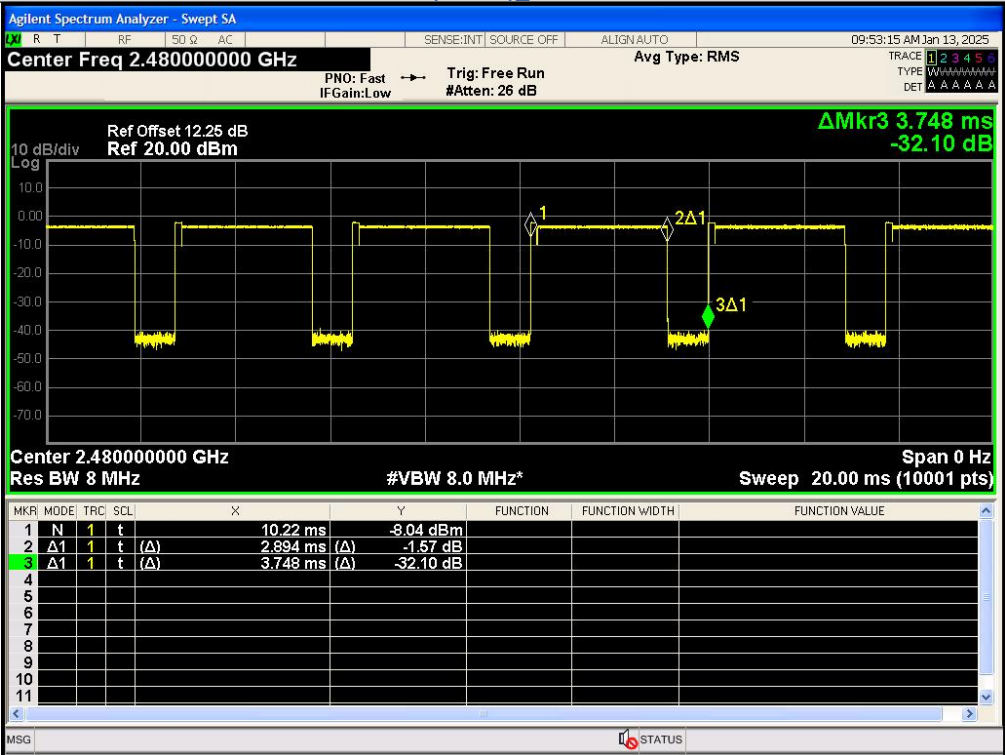
π/4DQPSK(2-DH5)_Channel 78



8DPSK(3-DH5)_Channel 0



8DPSK(3-DH5)_Channel 39



8DPSK(3-DH5)_Channel 78

APPENDIX VII. Dwell Time Test Result

Modulation	Packet	Channel	Pulse Width (ms)	Number of Pulses in 31.6 seconds	Dwell Time (ms)	Limit (ms)	Result
GFSK	DH1	CH39 (2441MHz)	0.3816	318	121.35	< 400	PASS
	DH3		1.632	156	254.59		PASS
	DH5		2.896	91	263.54		PASS
$\pi/4$ DQPSK	2-DH1		0.3900	318	124.02		PASS
	2-DH3		1.632	164	267.65		PASS
	2-DH5		2.896	109	315.66		PASS
8DPSK	3-DH1		0.3912	318	124.4		PASS
	3-DH3		1.656	151	250.06		PASS
	3-DH5		2.896	111	321.46		PASS

Test Graphs

