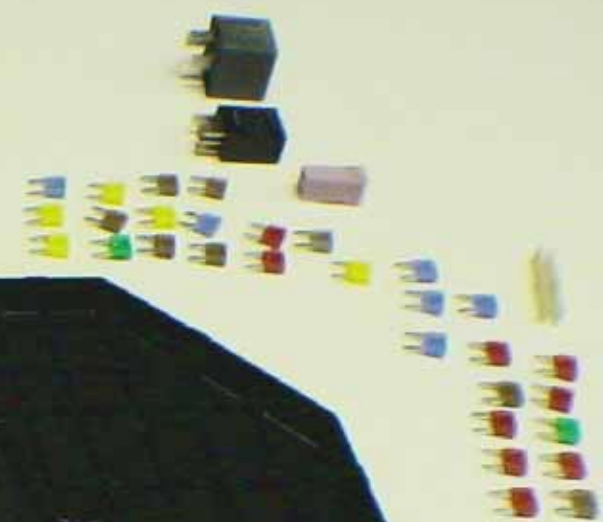
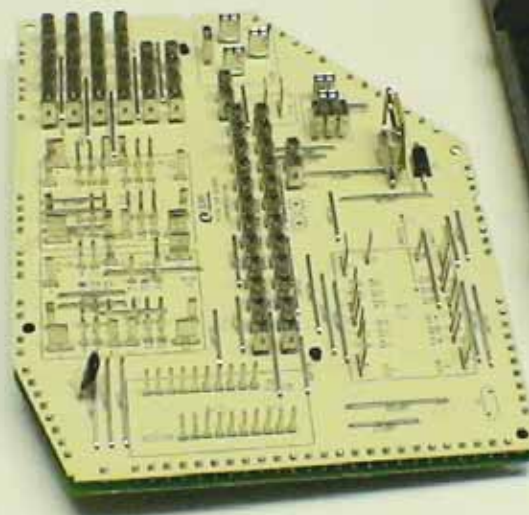
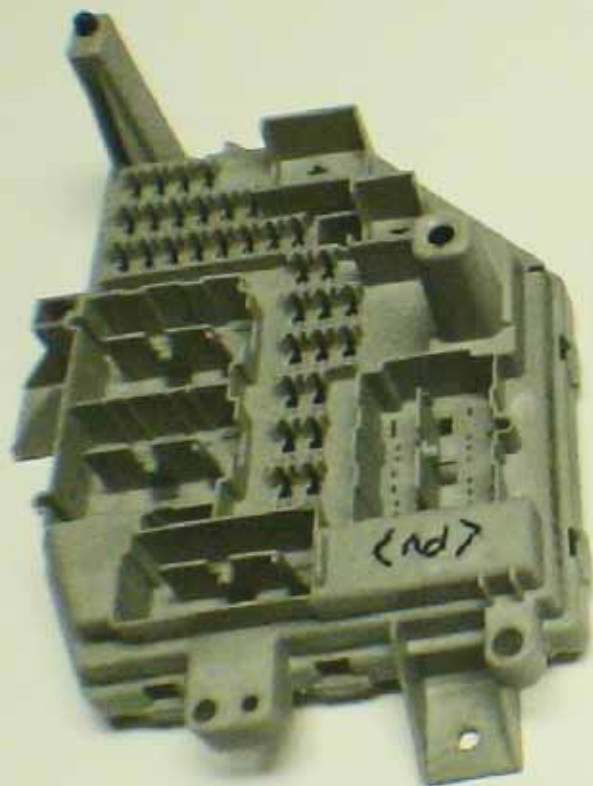


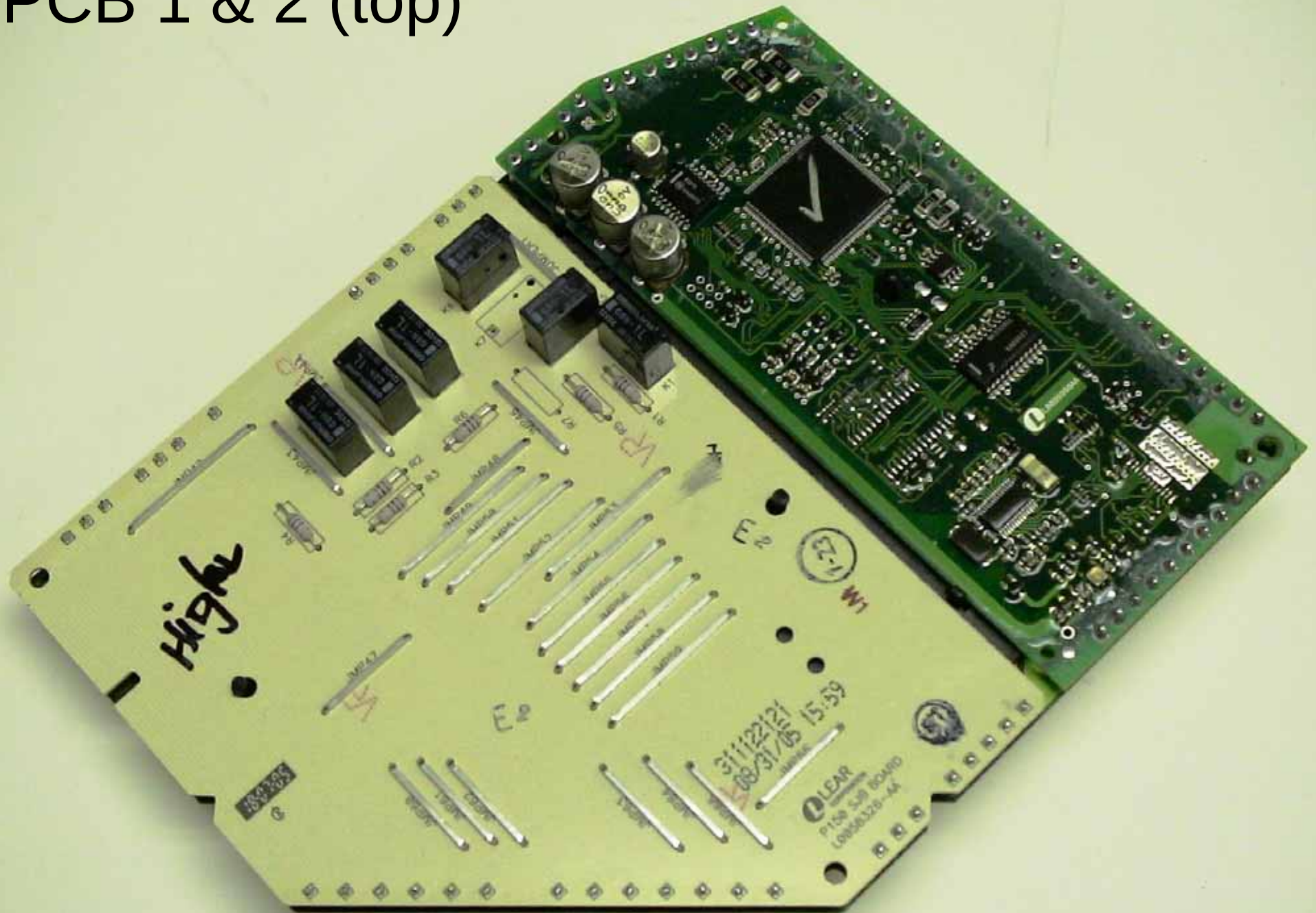
DUT Apart (top)



DUT Apart (bottom)

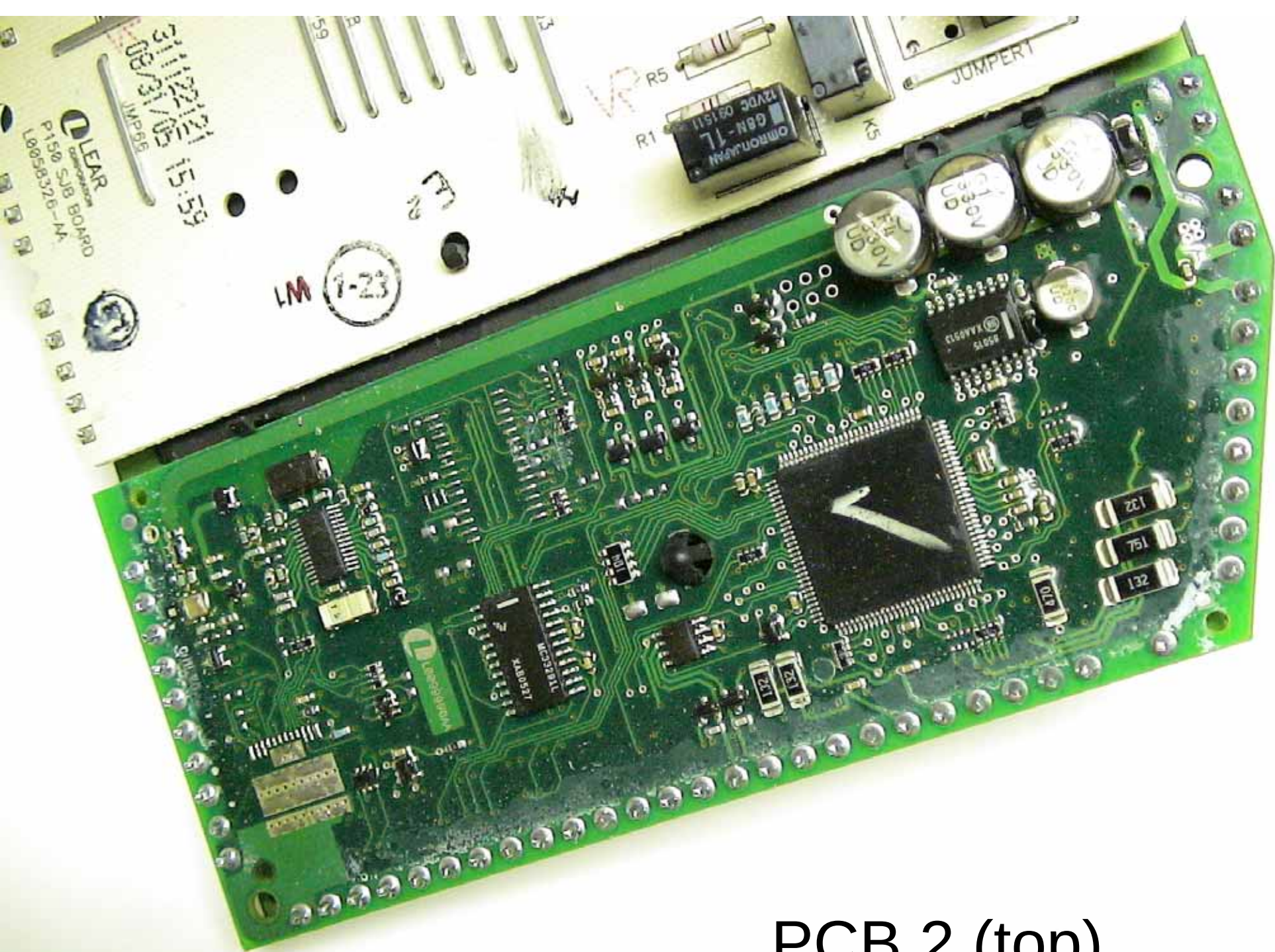


PCB 1 & 2 (top)





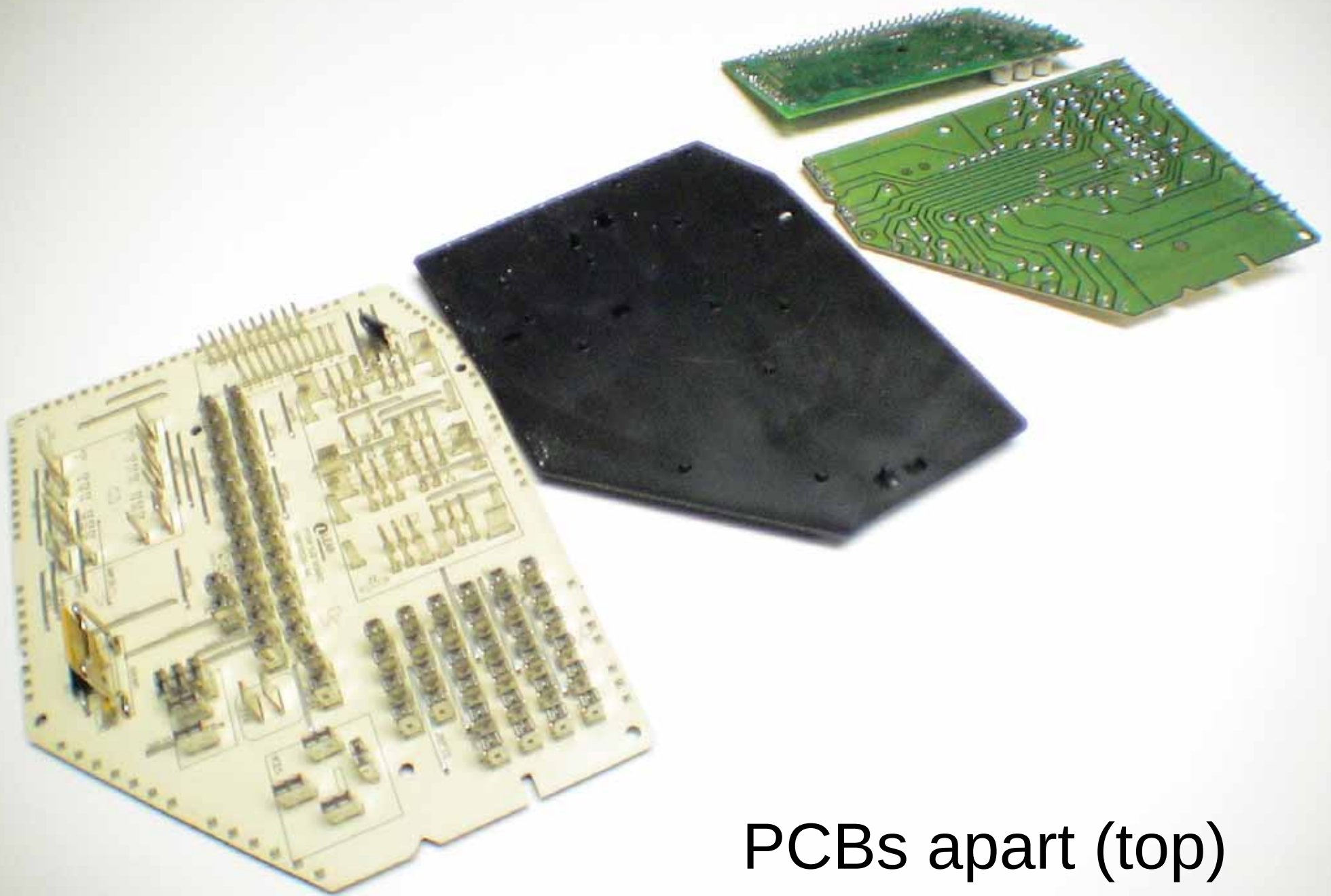
PCB 1 (top)



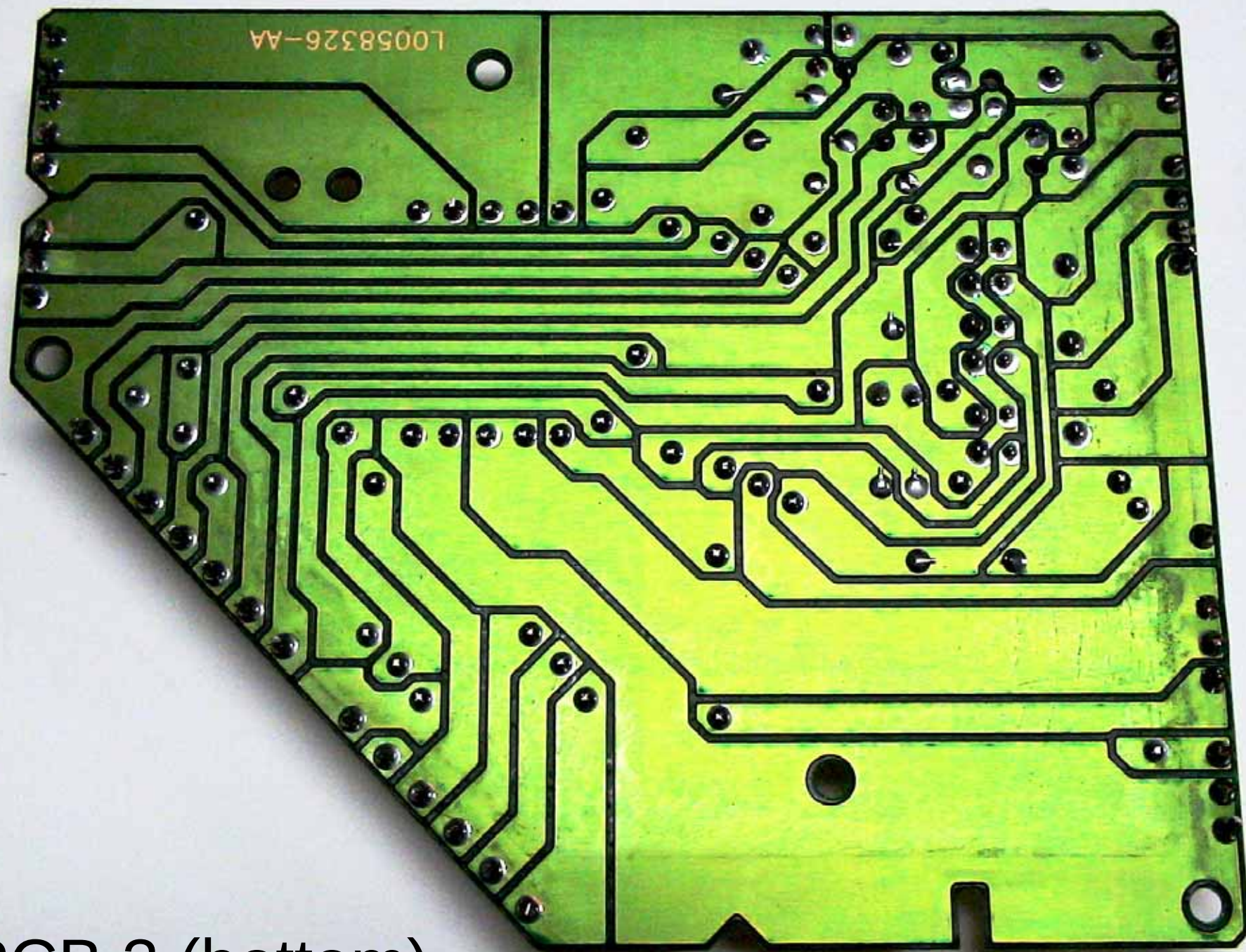
PCB 2 (top)



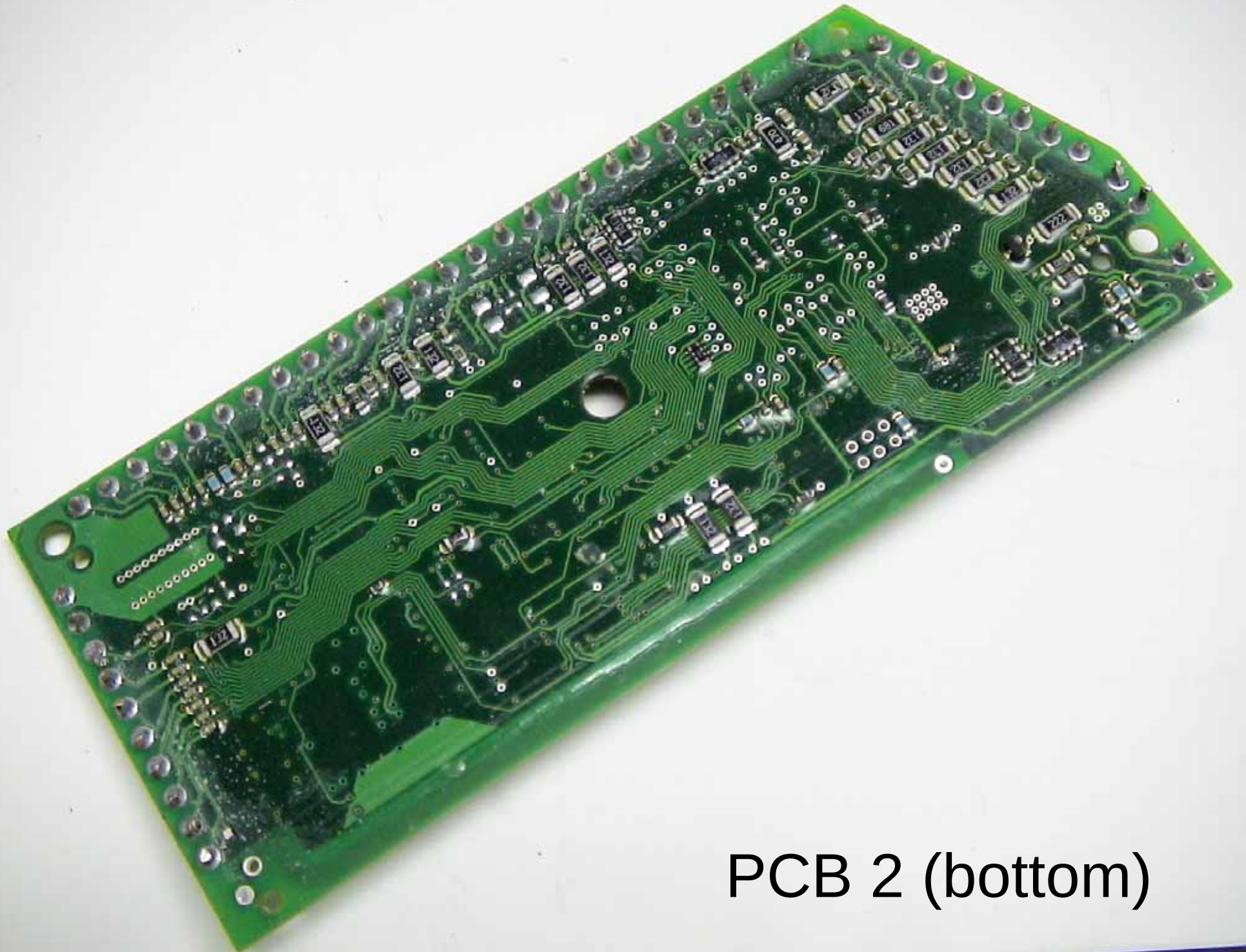
PCB 3 (top)

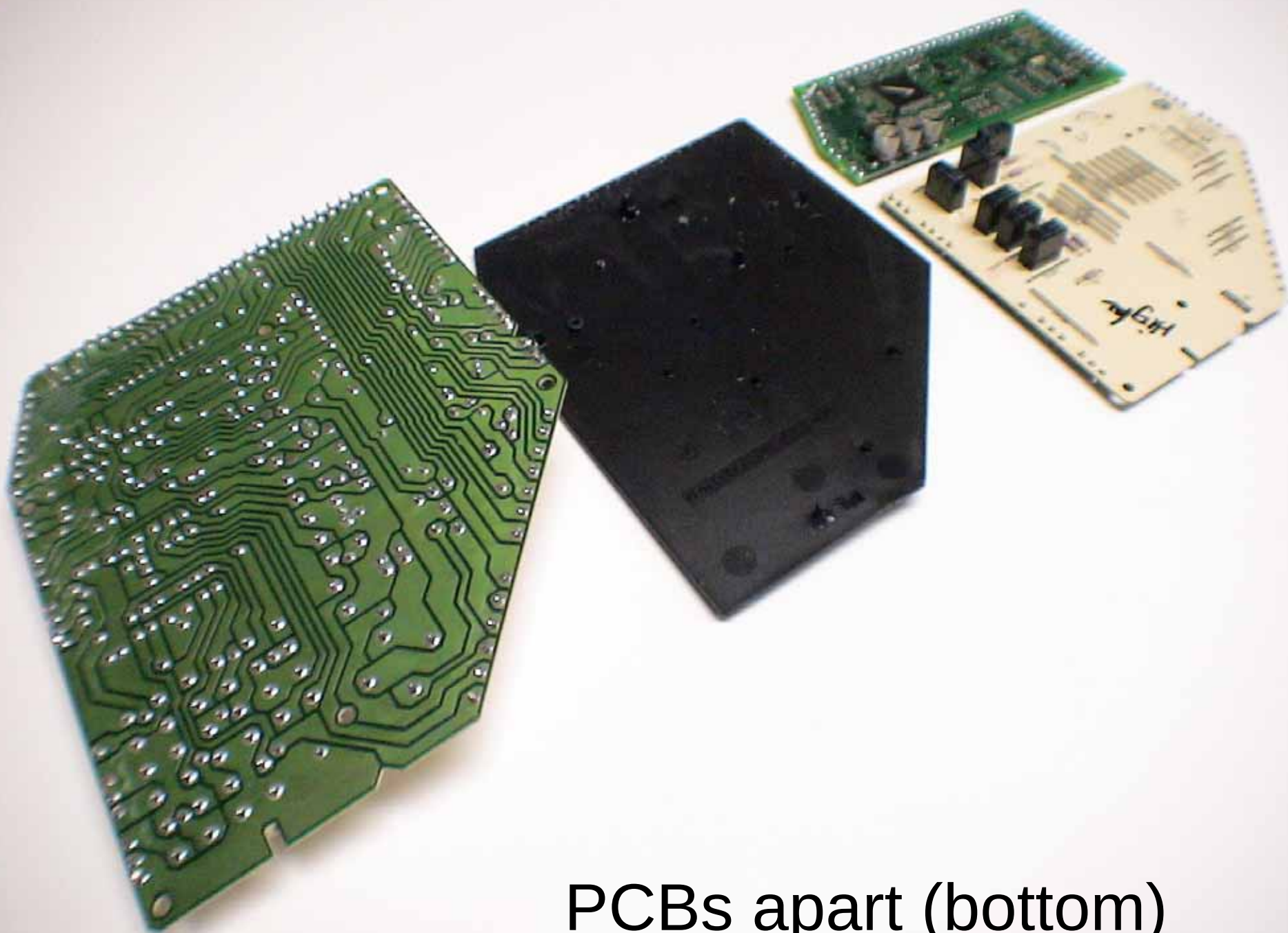


PCBs apart (top)

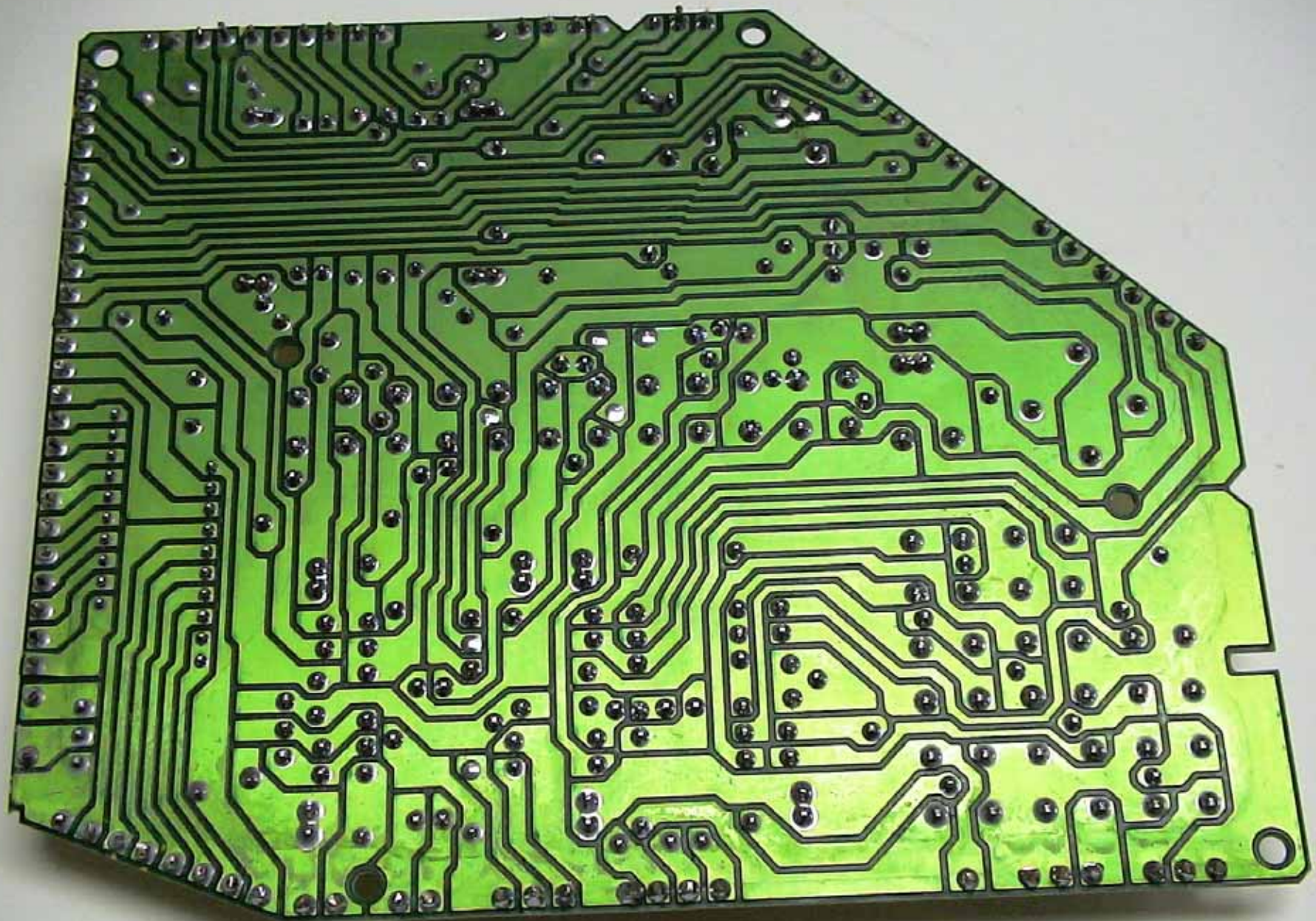


PCB 3 (bottom)



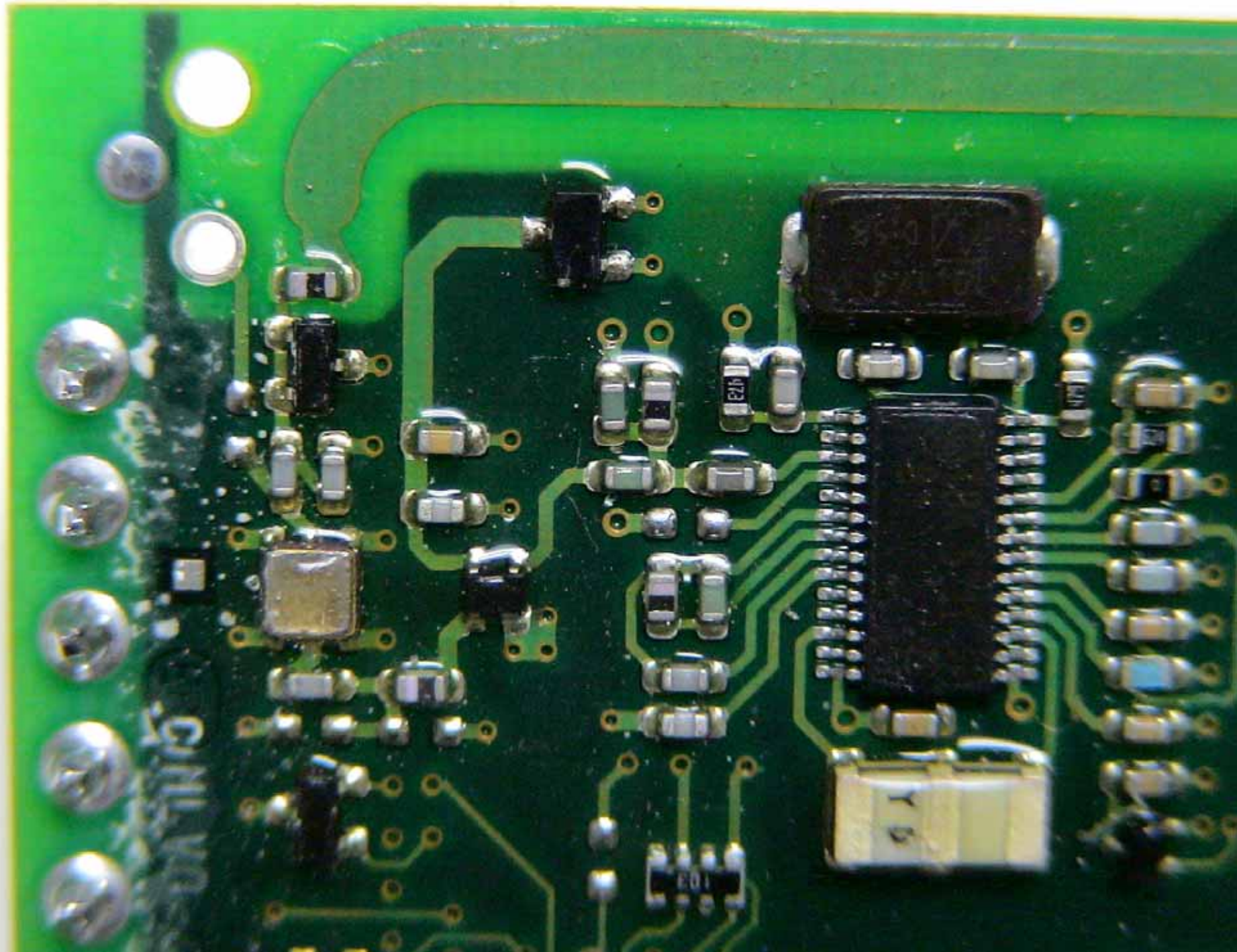


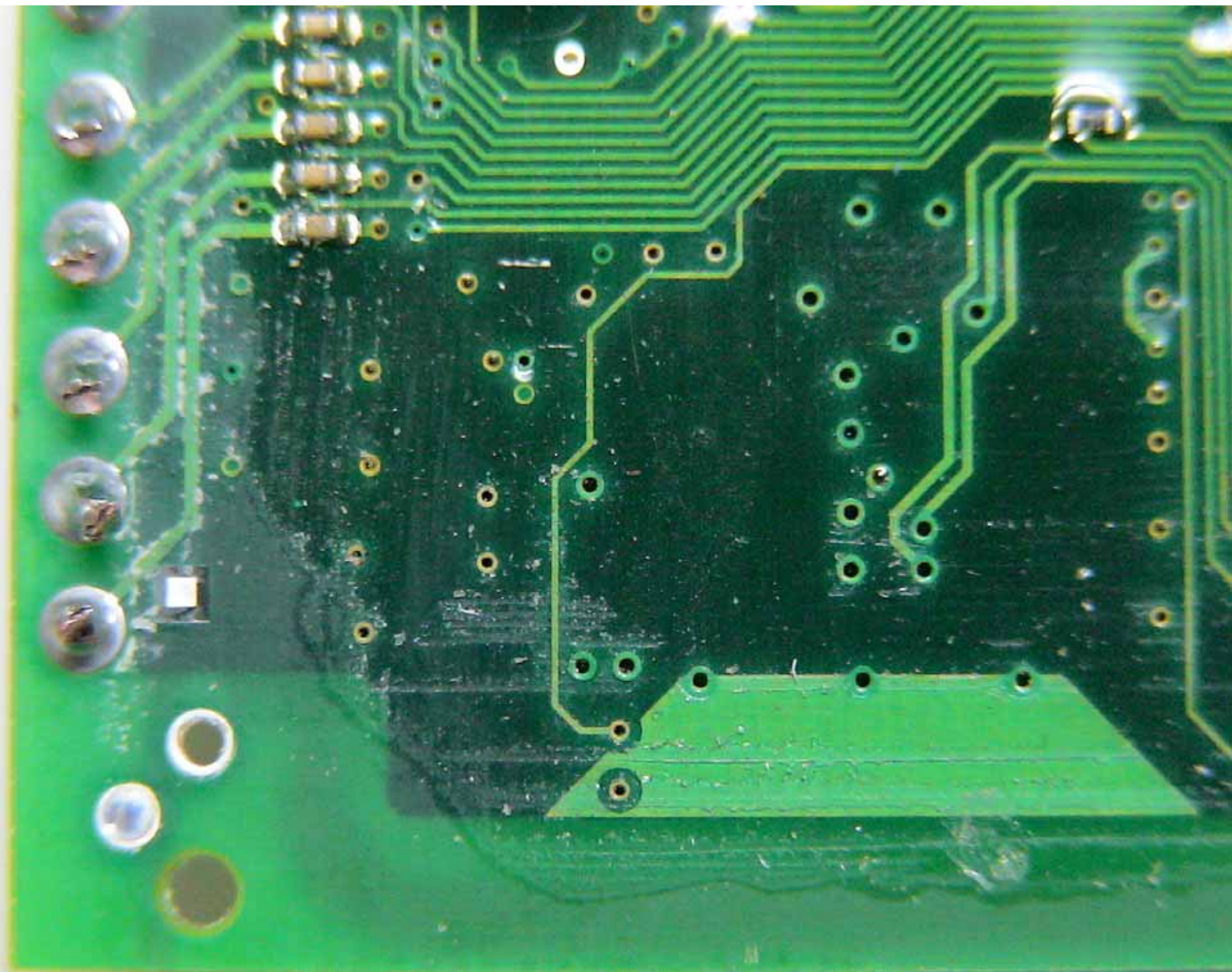
PCBs apart (bottom)



PCB 1 (bottom)

RF section, PCB 2 (top)





RF section, PCB 2 (bottom)