

CIRCUIT DESCRIPTION

The 345 Mhz RF signal enters the receiver at the diversity antennas ANT1 & ANT2. CR1 & CR2 switch ANT1 & ANT2 under the control of MICROPROCESSOR U8. The LNA (Q3,L10,L5,etc.) improves the noise figure. RECEIVER CHIP U1 provides all the basic receiver functions. FL1 & FL2 provide IF band pass. The L.O. is made up of Q1, L14, etc., the FREQUENCY DETERMINING ELEMENT is: Y1 (59.28333 Mhz), the 6th. harmonic of Y1 is: 355.7 Mhz this is high side injection for the 345.0 Mhz input. SIGNAL PROCESSING is accomplished via U2 & U3 and their associated components. Y2 (10.073 Mhz) is the source of the microprocessors clock, and is not part of the RF path. Other peripheral components include: VOLTAGE REGULATION / CONTROL: U5, U7, U11, etc.