



December 20, 1998

Federal Communications Commission
Equipment Approval Services
P.O. Box 358315
Pittsburgh, PA 15251-5315

Reference: Correspondence Reference Number:6252
FCC Confirmation Number: EA92496
FCC ID: I2OMCPS2000

Attention: Mr. Frank Coperich

Dear Frank :

In reply to your e-mail dated Feb. 24,1999. We have revised our request for confidentiality. Please refer to the following statement:

In applying for Grant of Certification for the MCPS2000 Multi-channel power amplifier system, Spectrian requests that certain items be withheld from public inspection upon such equipment authorization. This request is made in accordance with FCC section 0.459 (a) and (b).

The listed materials, submitted for review of the request for Grant of Certification, include trade secrets, which, if made available for public inspection, would seriously damage Spectrian's competitive advantage. Spectrian's competition could gain advantage by studying these documents and deduce the unique aspects of our system/amplifier architecture. These trade secrets allow Spectrian to produce an amplifier with lower labor cost, more production performance margin, and better performance over temperature, time, frequency, power level and a number of carriers. In particular, those documents are:

Description

- 1) " RF Interconnect ", Document no:WD-020593P1, Total 4 pages
- 2) " DC Interconnect ", Document no:WD-020593P1, Total 1 page.
- 3) " MCPS2000 MCPA Module Block Diagram". This Block Diagram is submitted to FCC under the file name : MCP2000 Tech Desc V1, Page 6 under section title 2.983(d)(7) Complete Circuit Diagram.
- 4) Internal Photographs: Submitted total of 19 internal photos via FCC web site.

Request special consideration from the Commission: Spectrian has not completed filing patents for all of the unique design aspects of this product. In some countries, any publicly available disbursement of information constitutes "disclosure" and therefore voids any patent application. The U.S. allows a one year grace period. I believe that Japan is one such country that does not allow this grace period, and we have active competitors in this country. As the result, we are requesting the Commission to withhold internal photos from public disclosure until the patents received from U.S. government.

5) Pre Amp Block Diagram

Spectrian owned patents and/or patent filings covers several technologies designed into this amplifier. The following is a summary of some of those technologies:

Approved Patents

Planer Microstrip Baluns are used in the RF amplifier stages. This technology is covered by Patent # 5,304,959 issued 4/19/94.

The **RF Power Transistor Package** used in the RF devices throughout the amplifier is covered by Patent # 5,338,974 issued 8/16/94.

The Method of Thermal Balancing RF Power Transistor Array is covered by Patent # 5,023,189 issued 6/11/94.

Patents Pending

A patent application has been filed for the **Power Stack Basic Power Module (BPM) Architecture** used in the main amplification path to maximize power and reliability.

Sincerely,

John Pelose
General Manager, Multi-Carrier