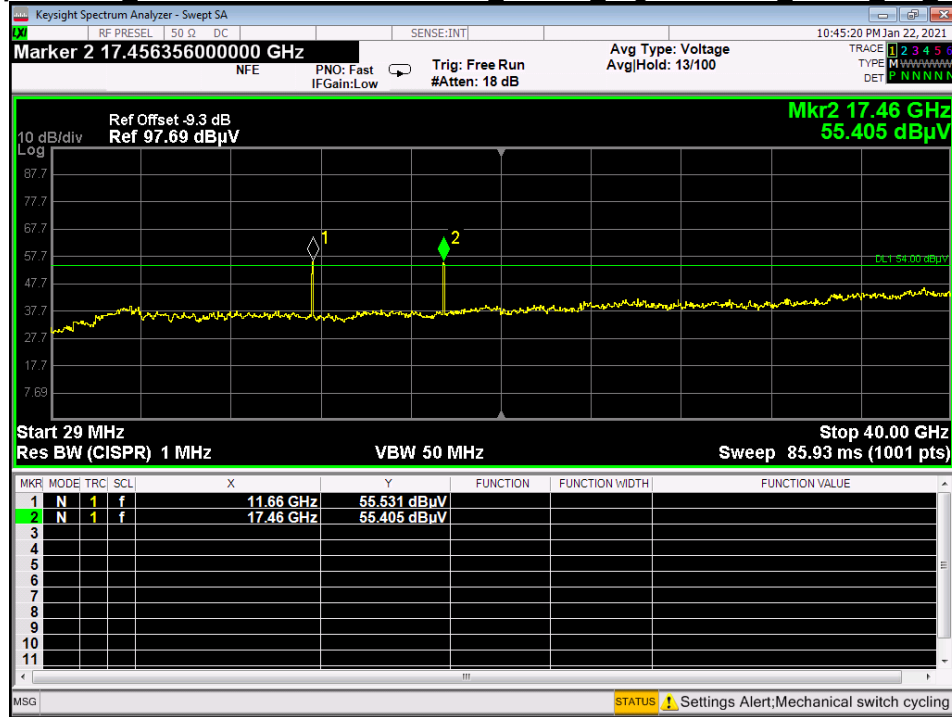
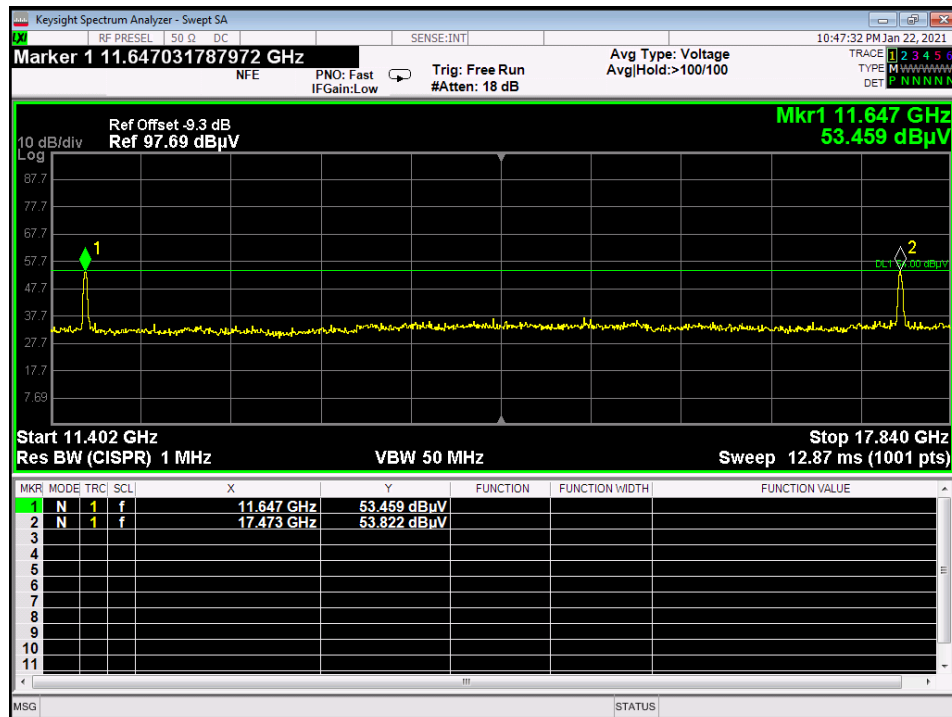


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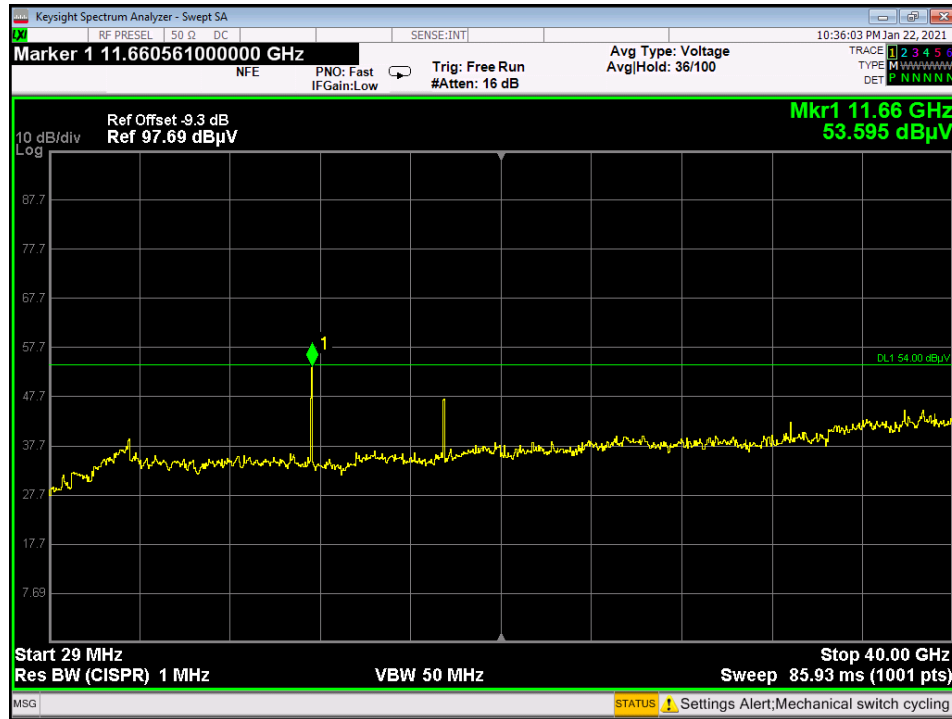
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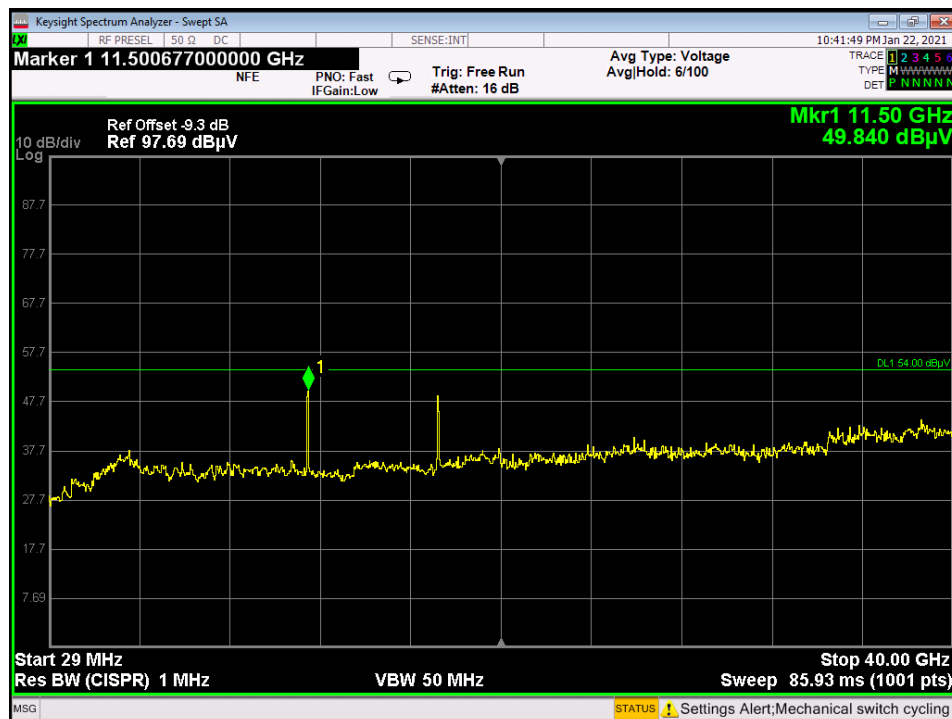
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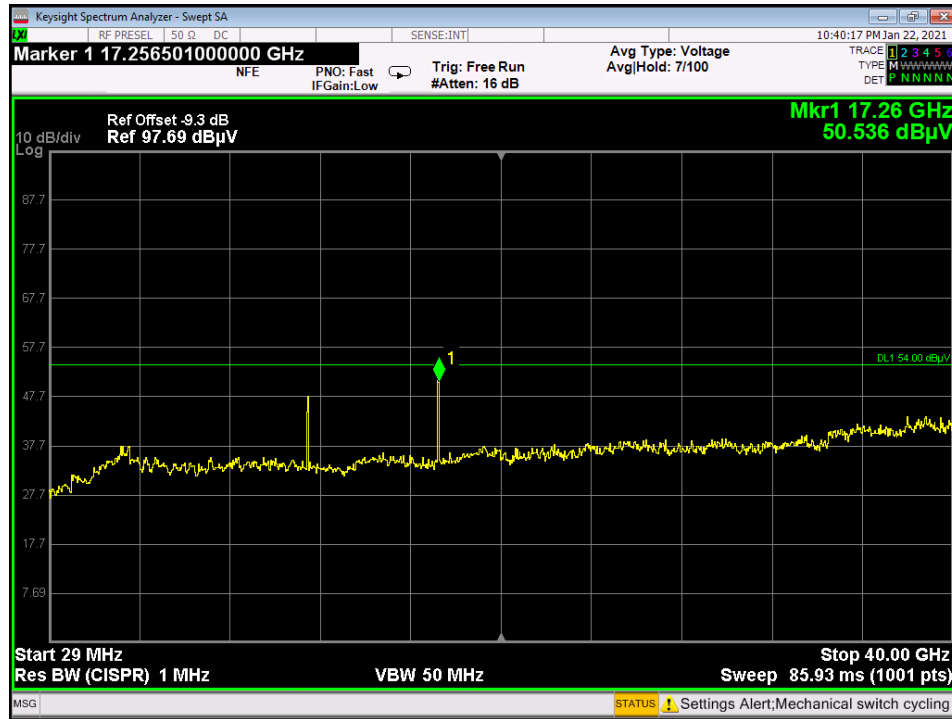
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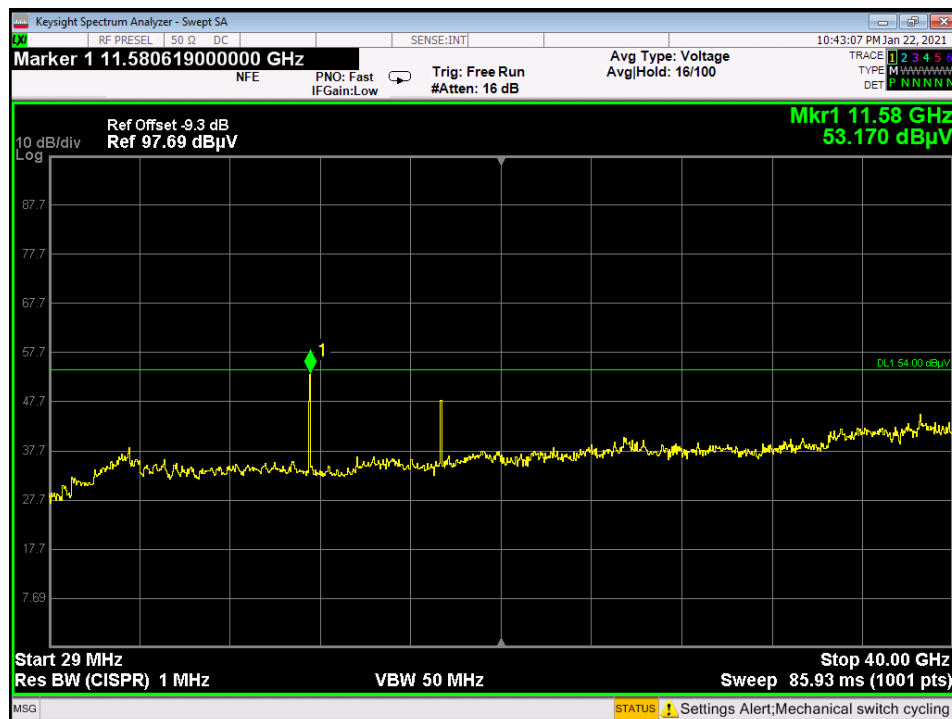
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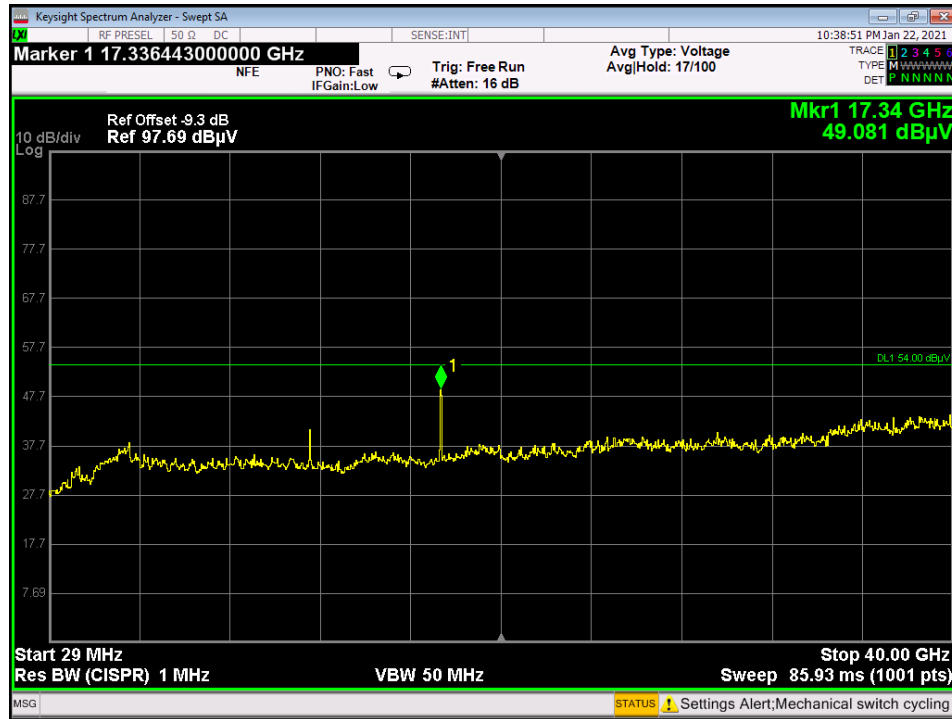
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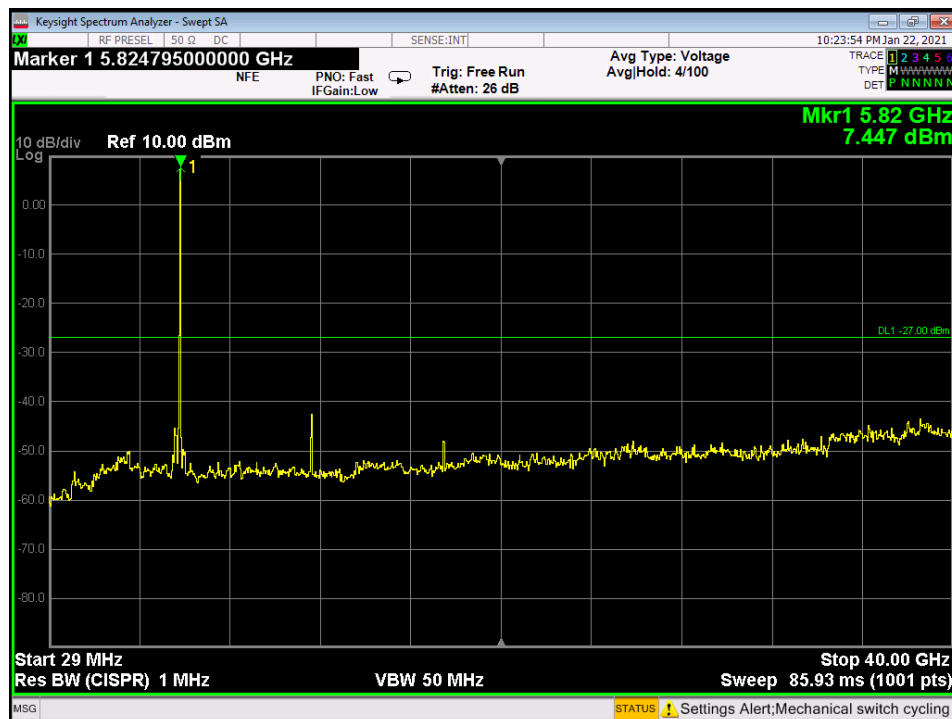
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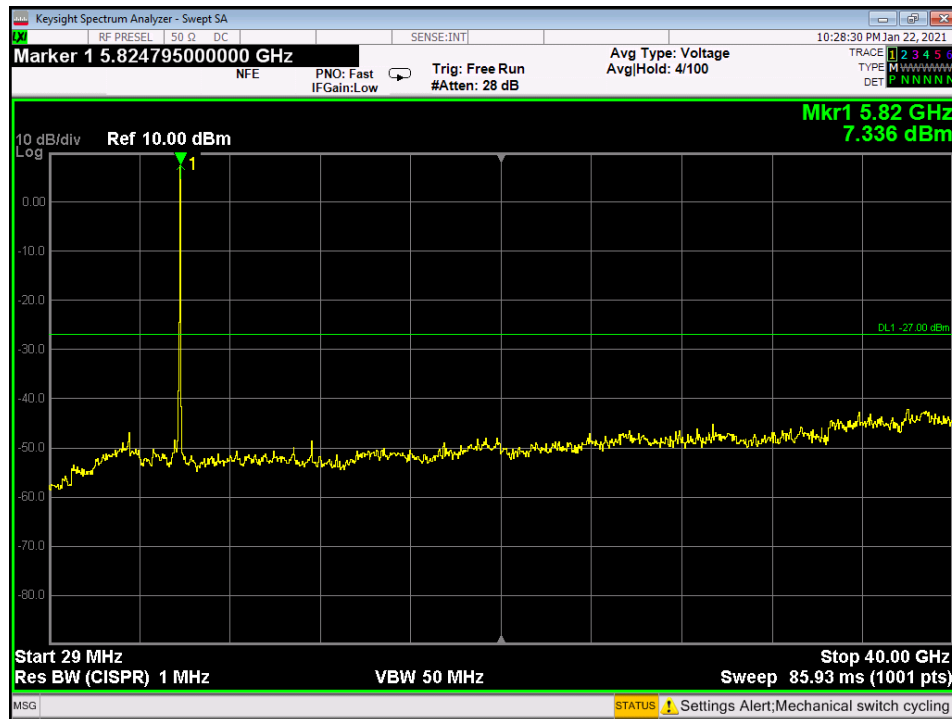
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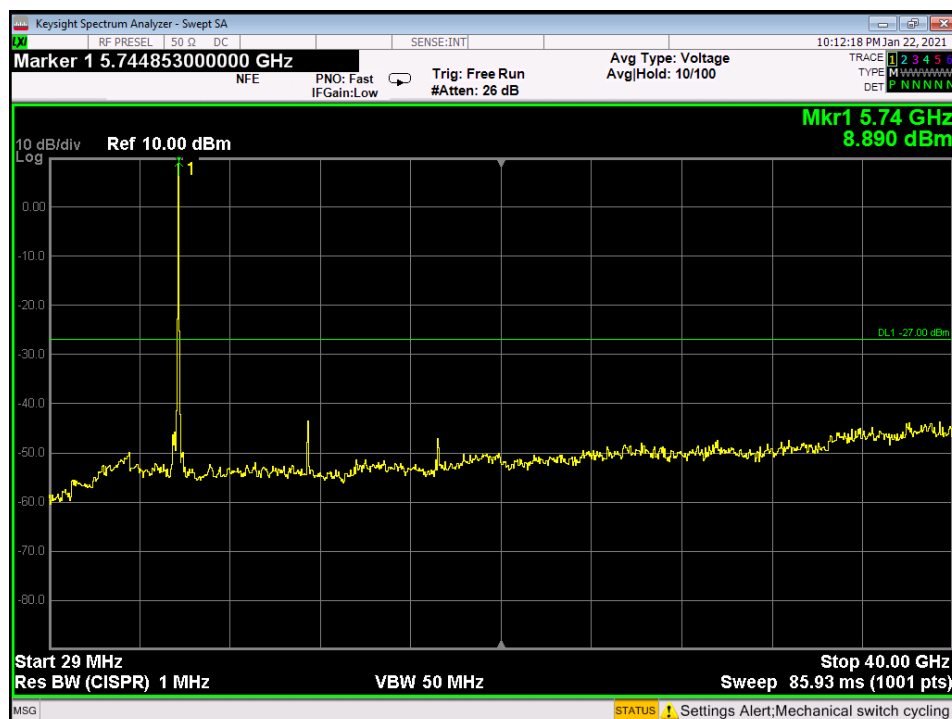
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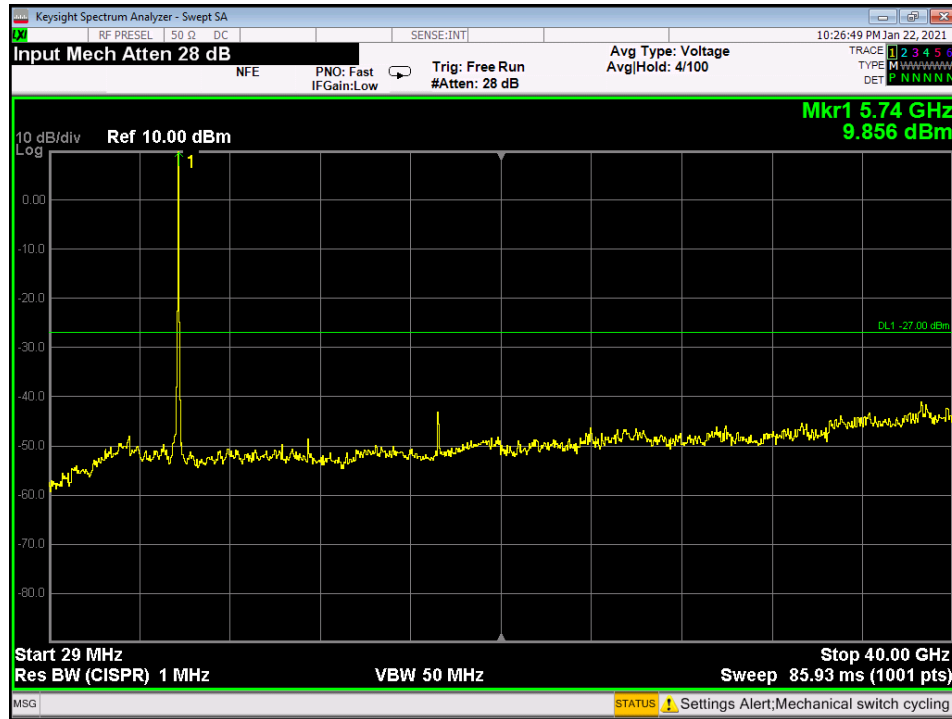
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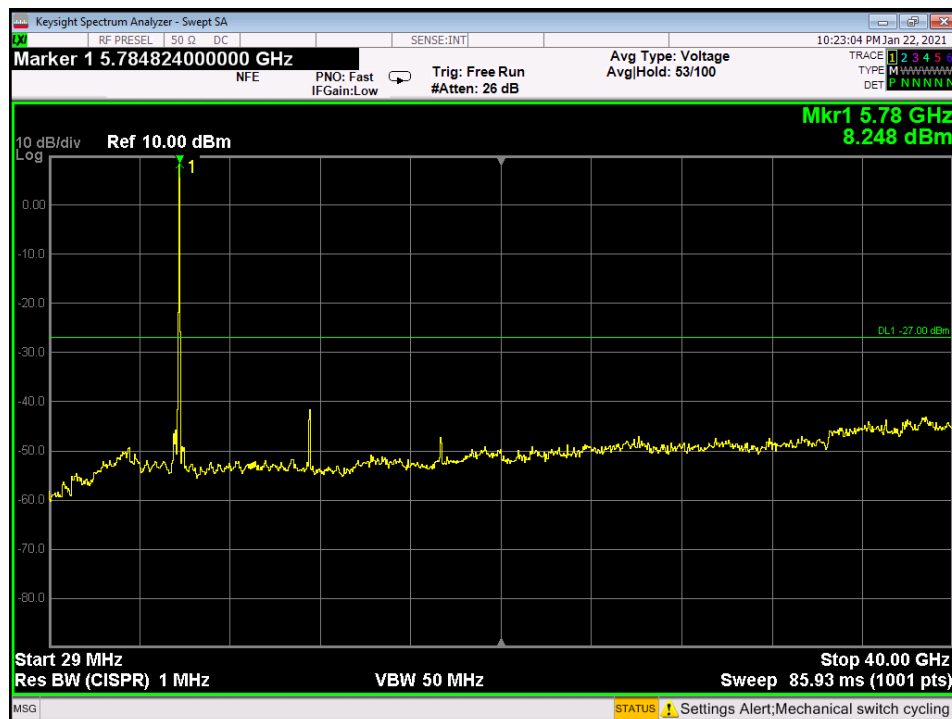
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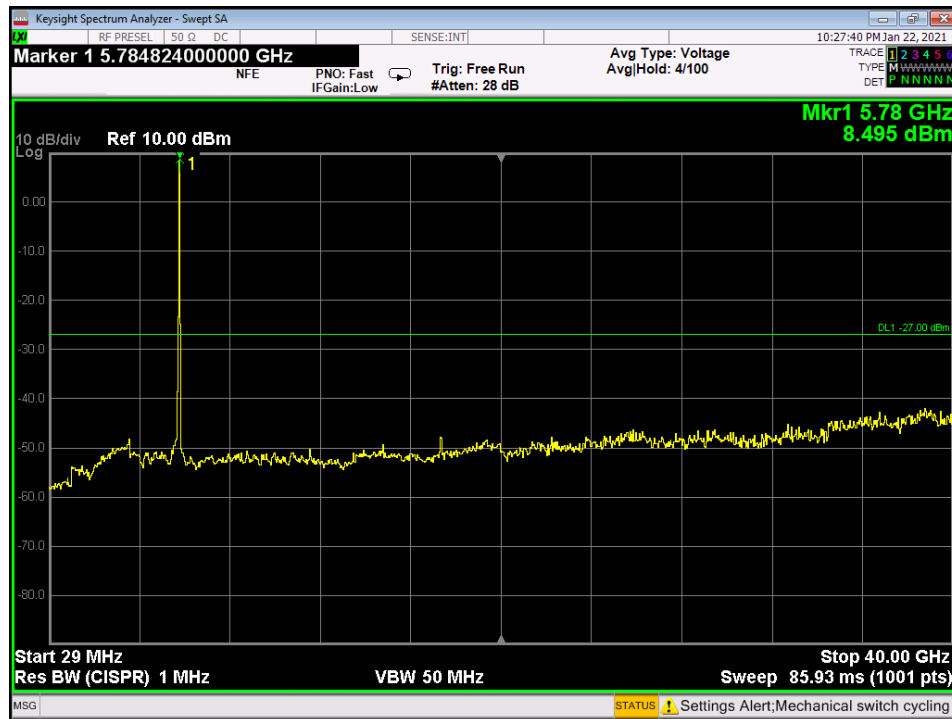
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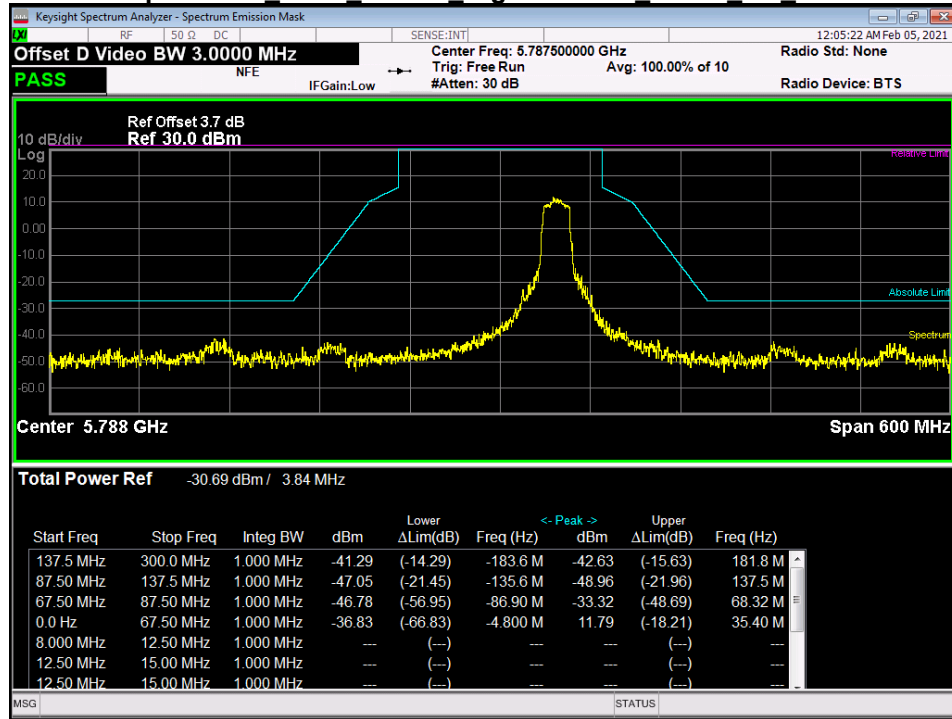
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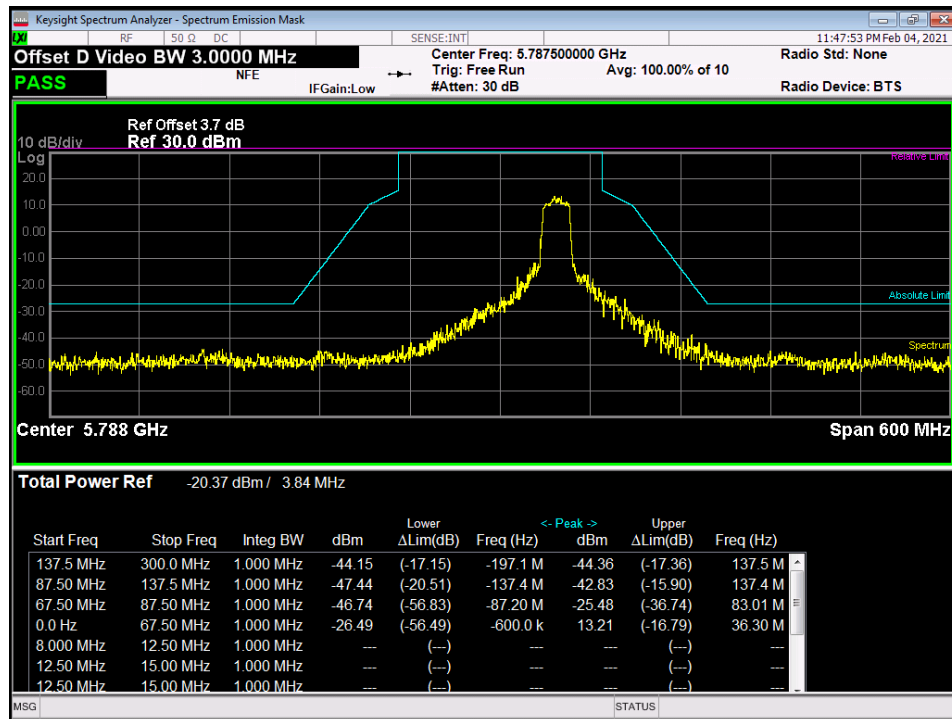
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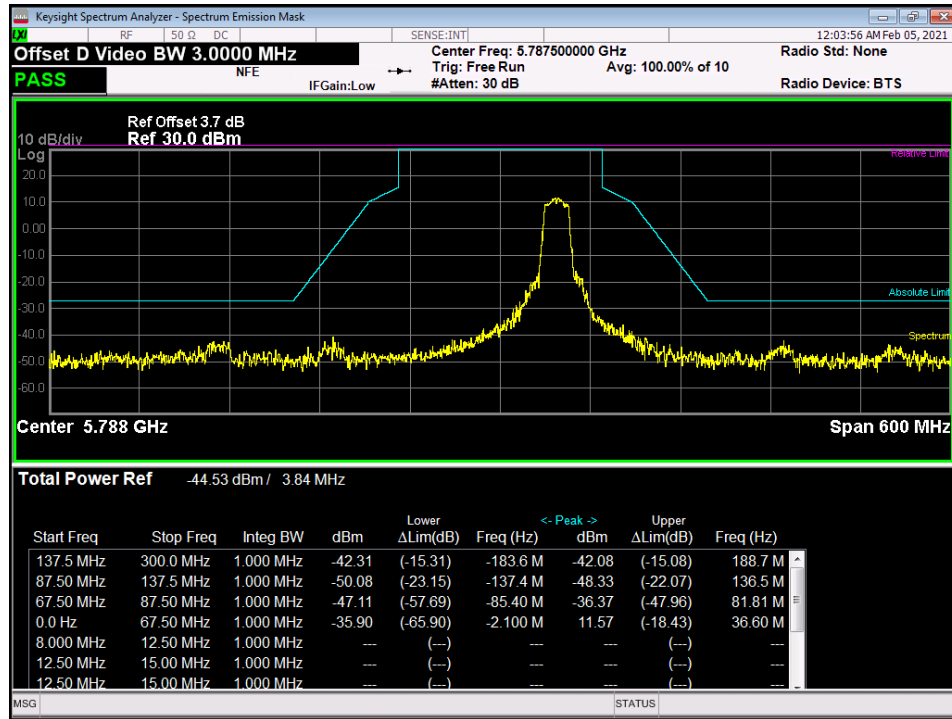
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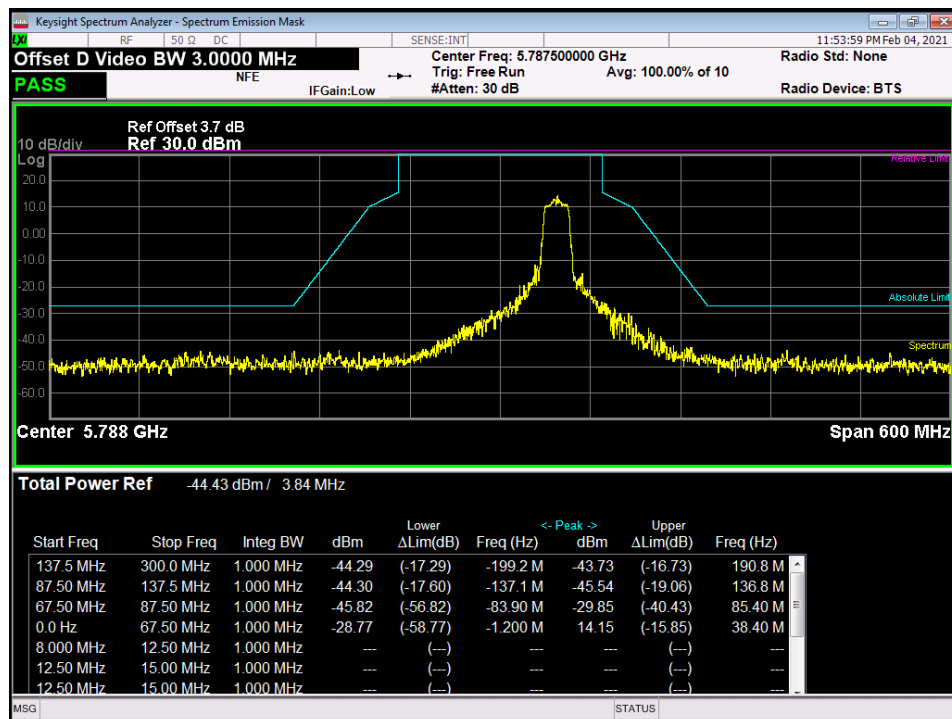
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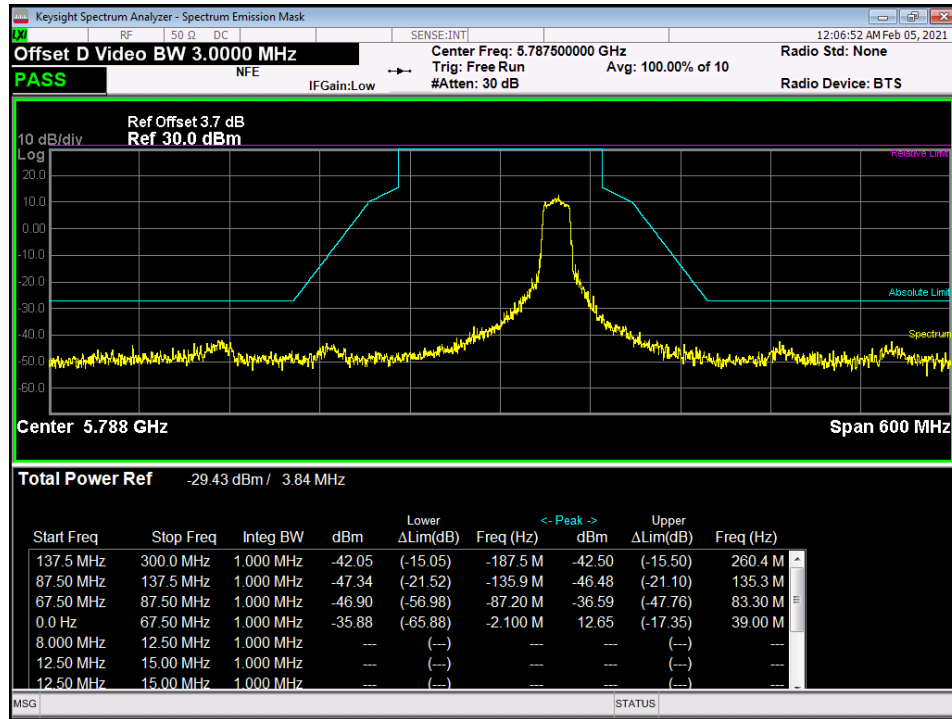
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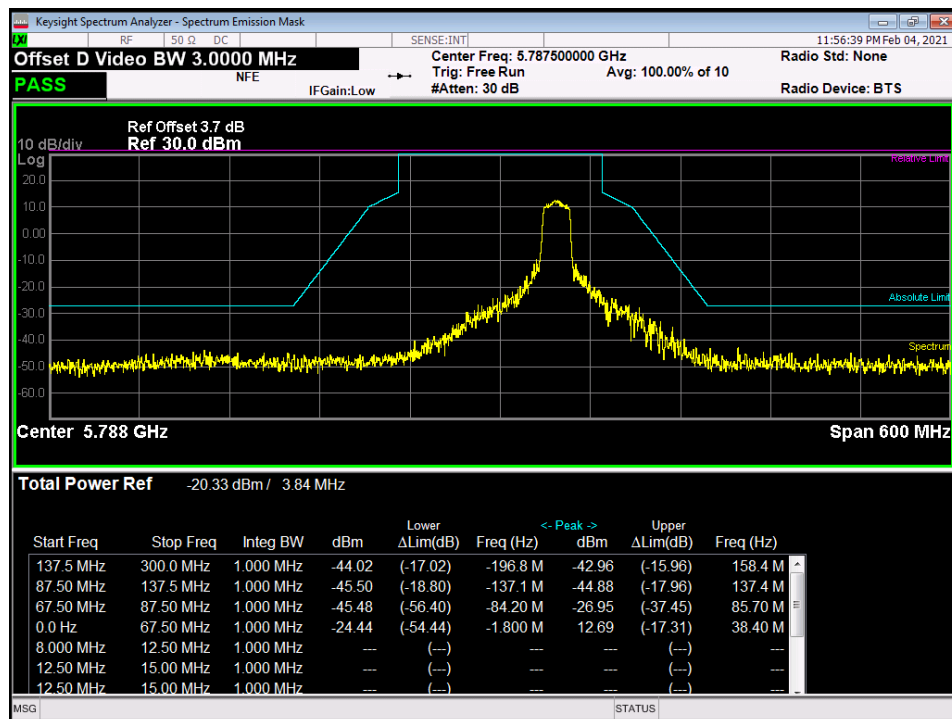
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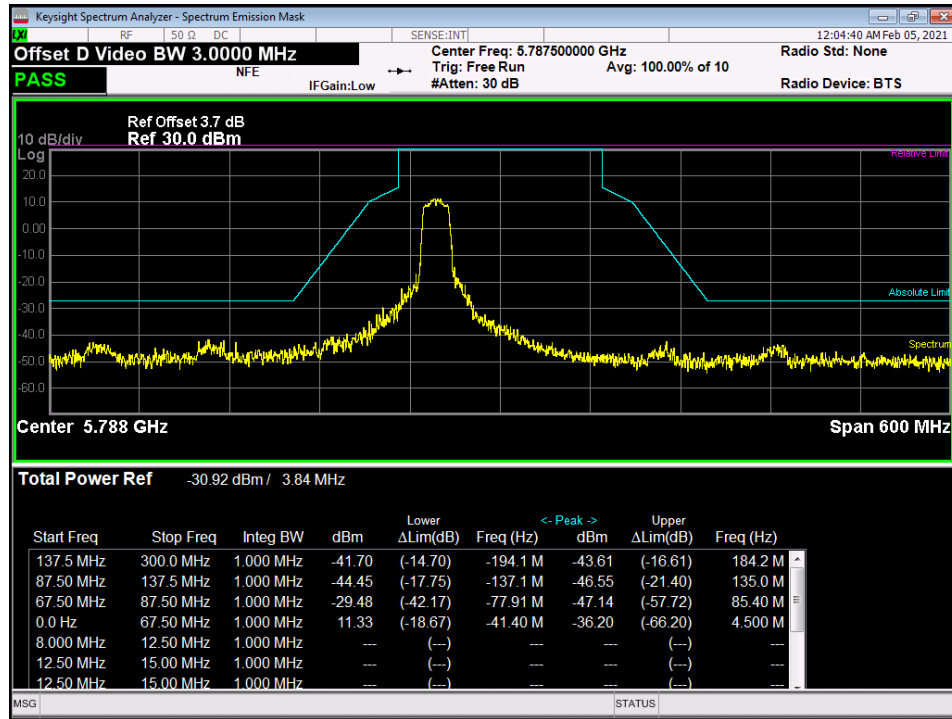
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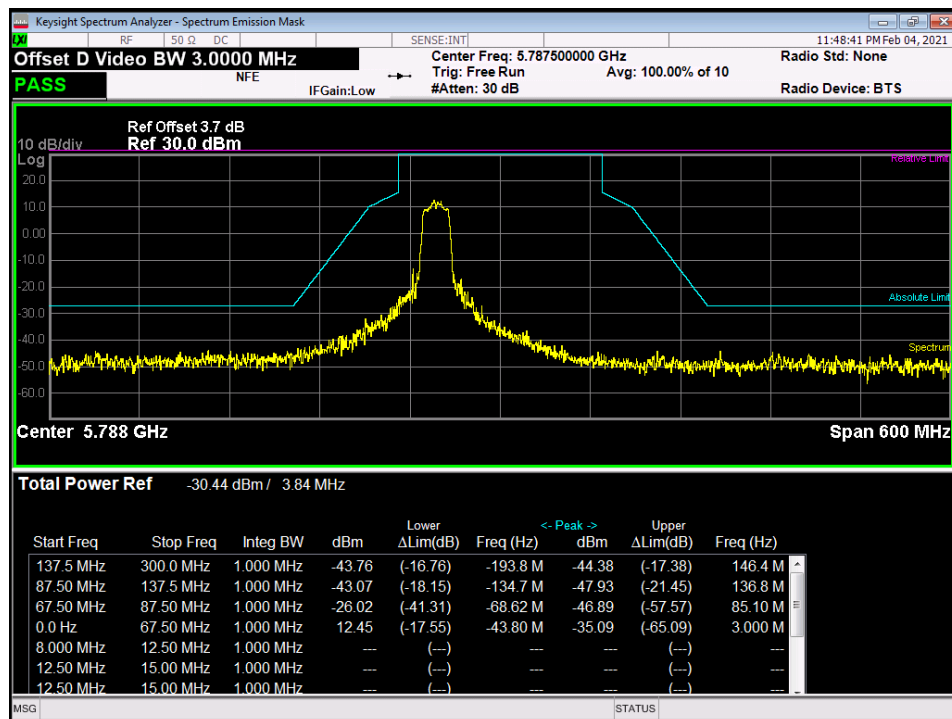
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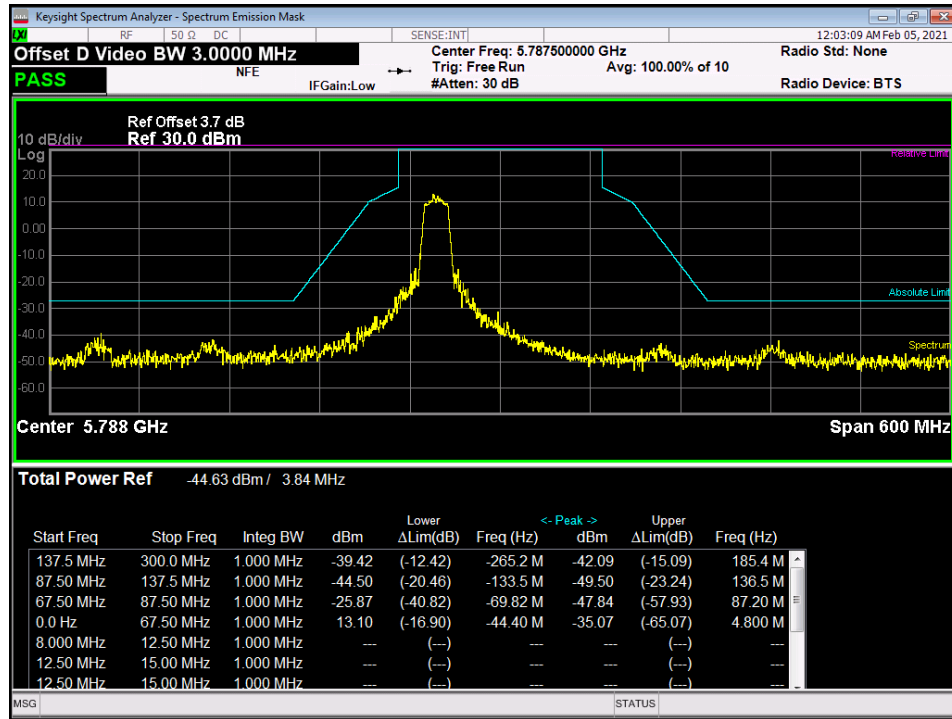
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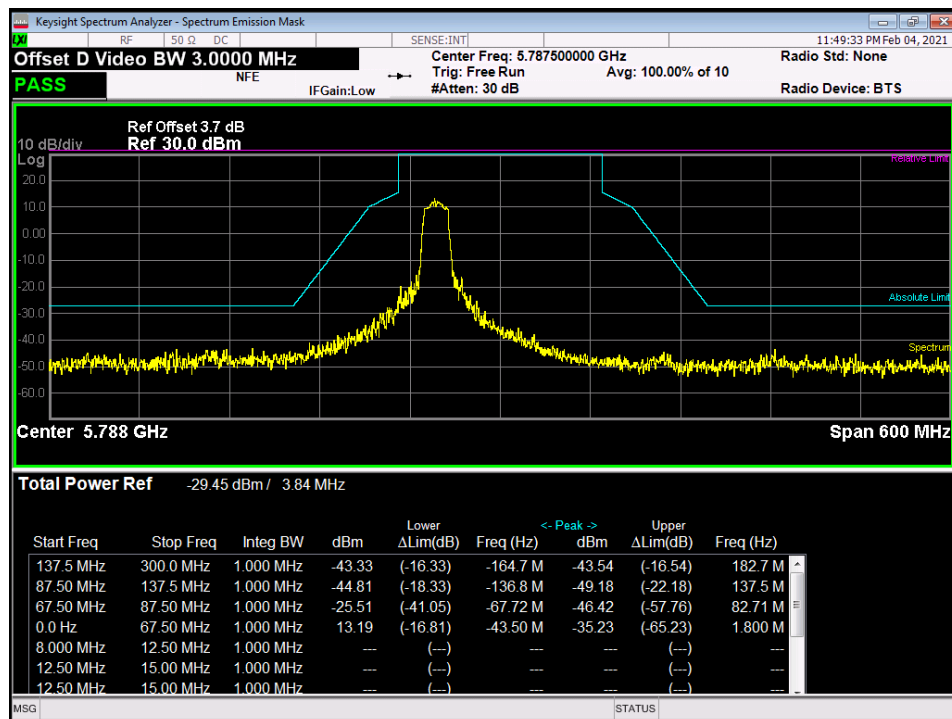
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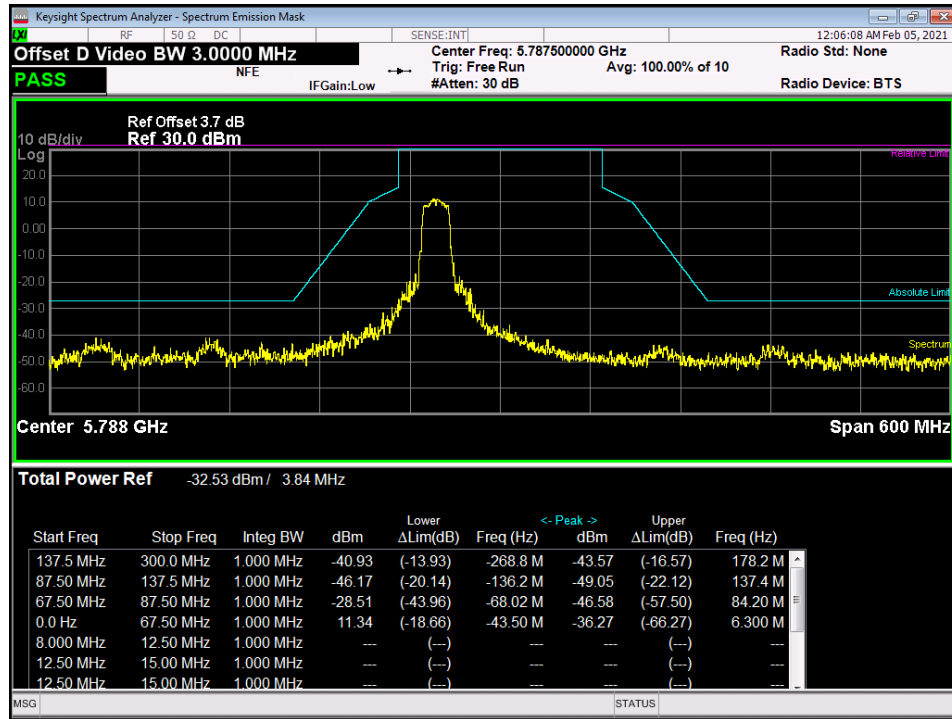
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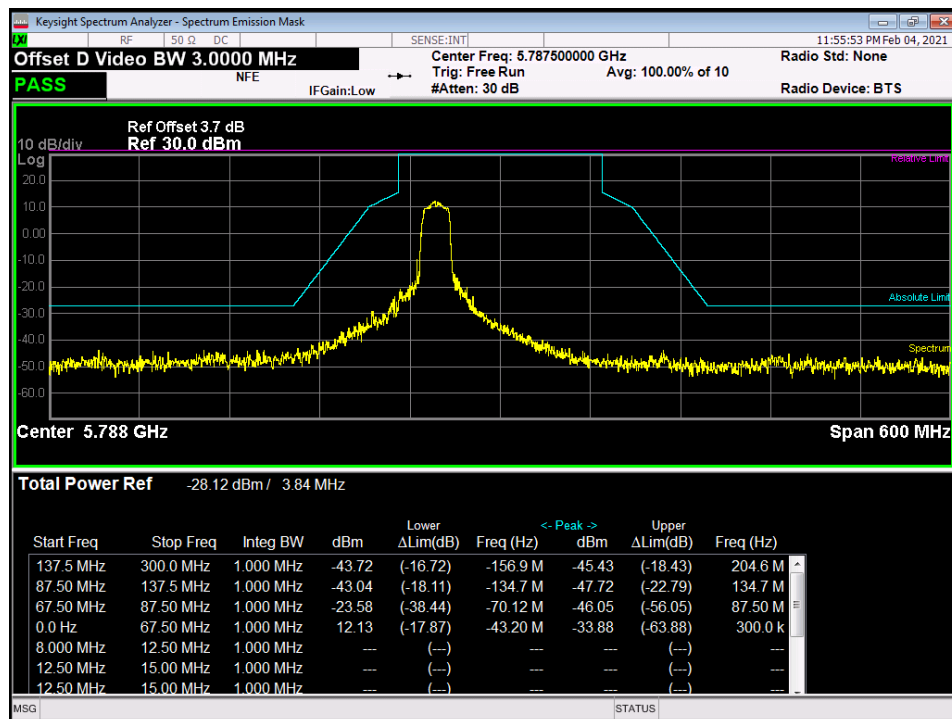
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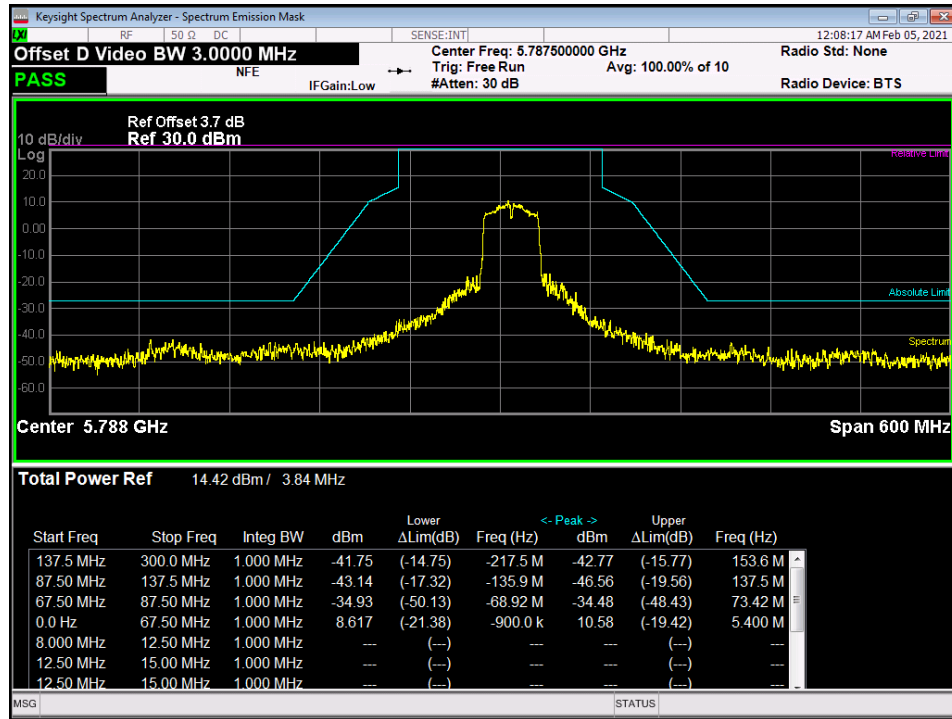
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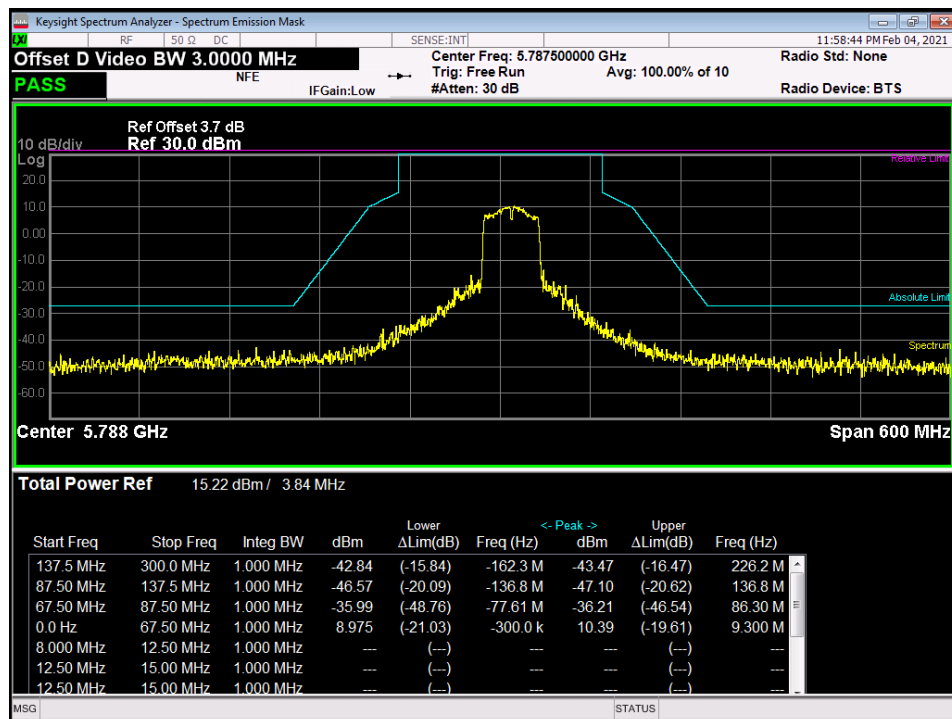
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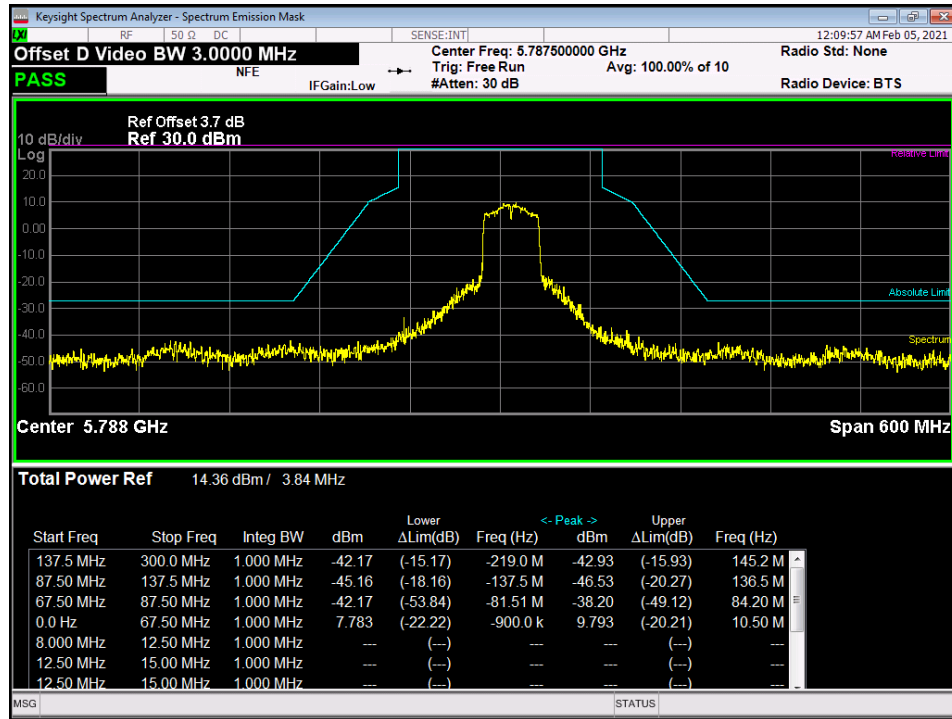
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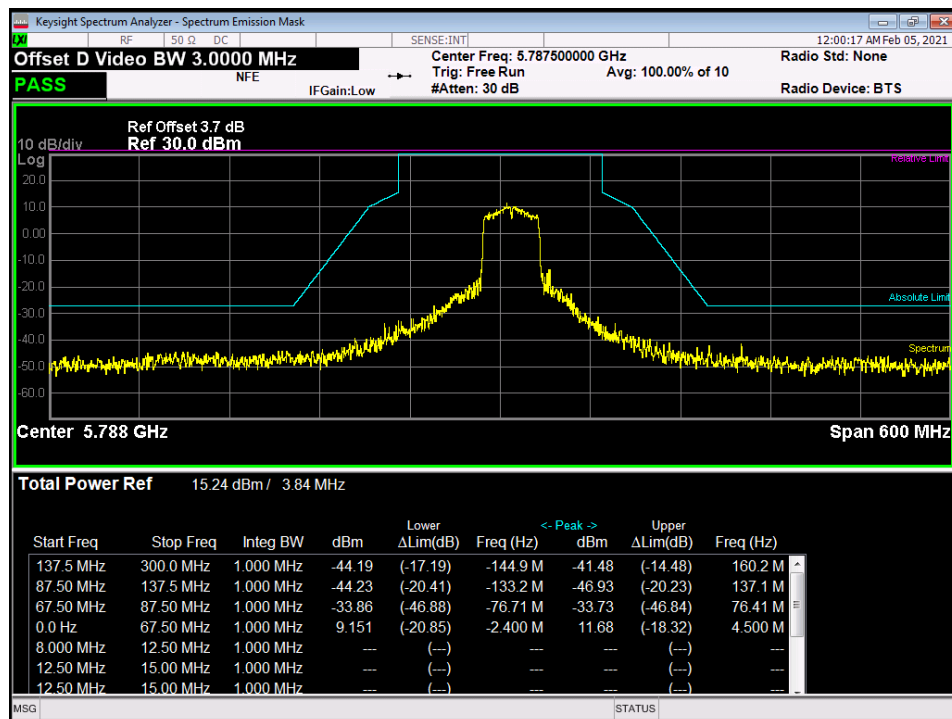
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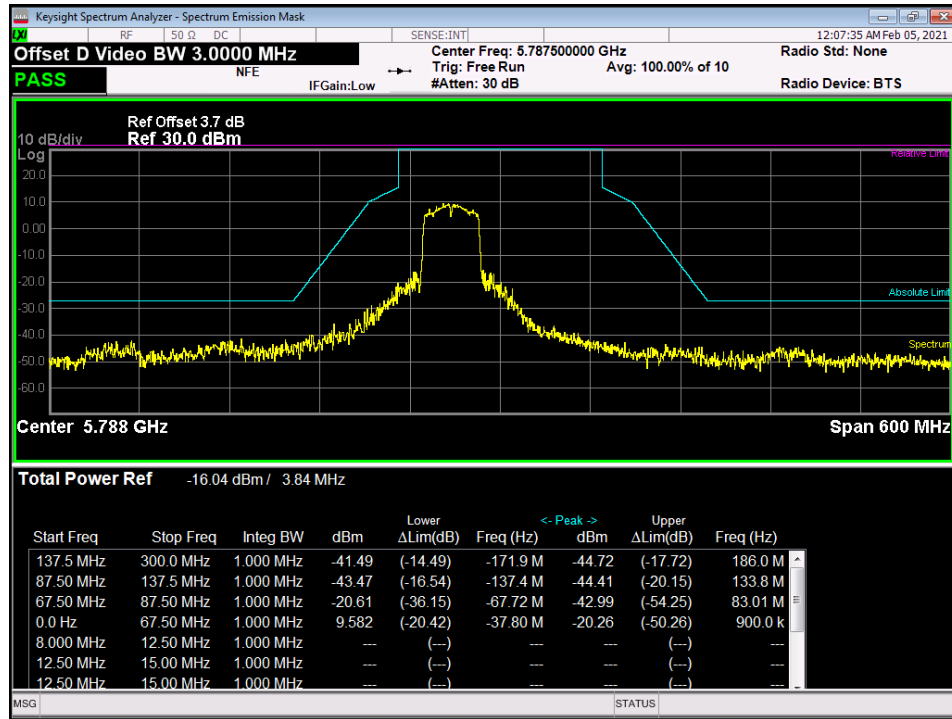
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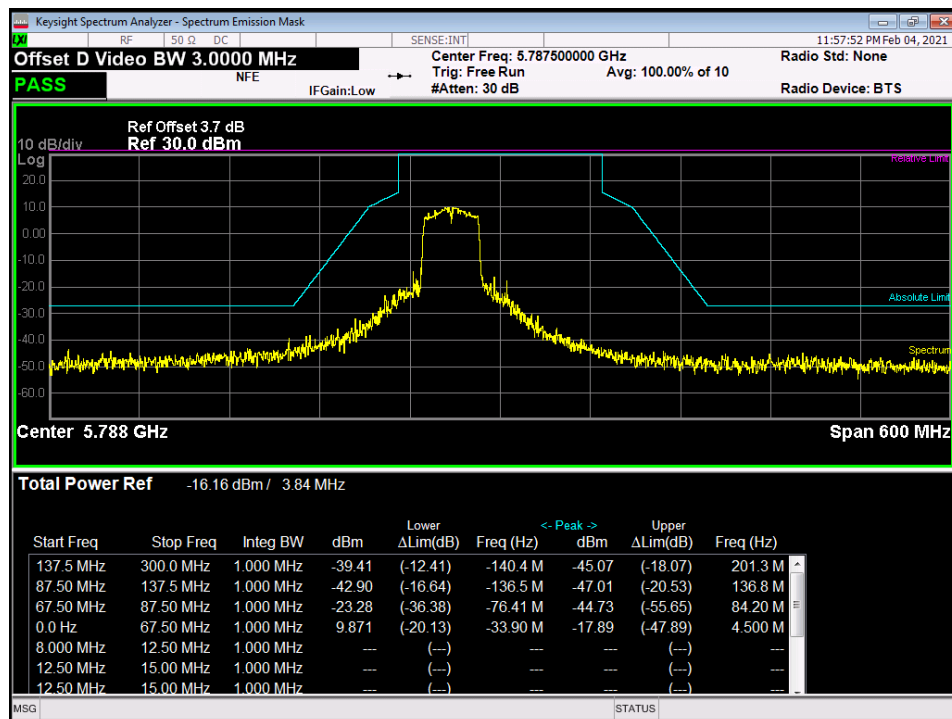
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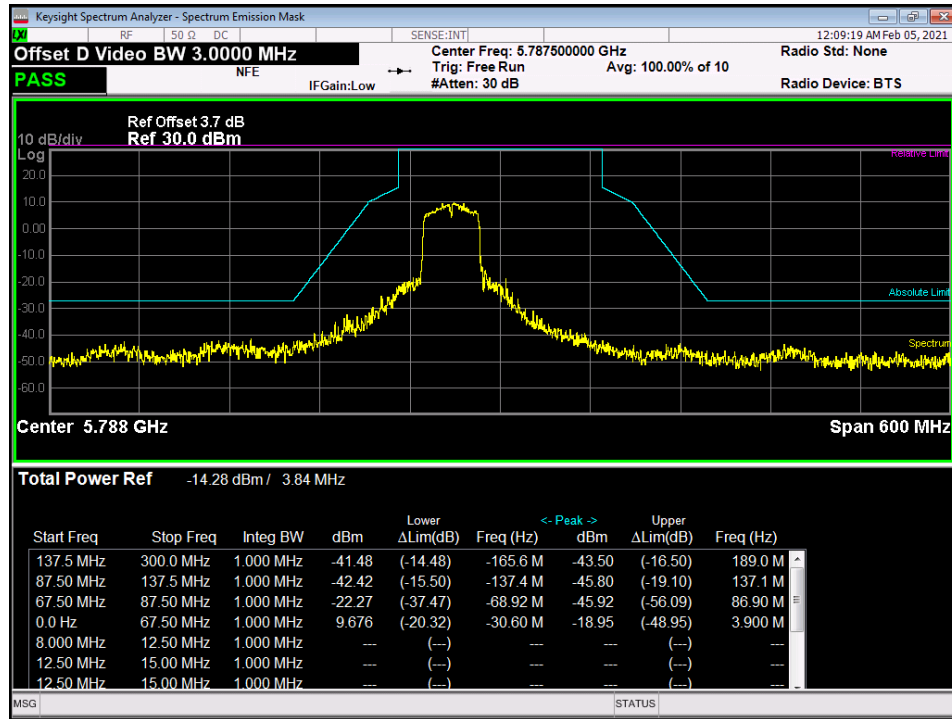
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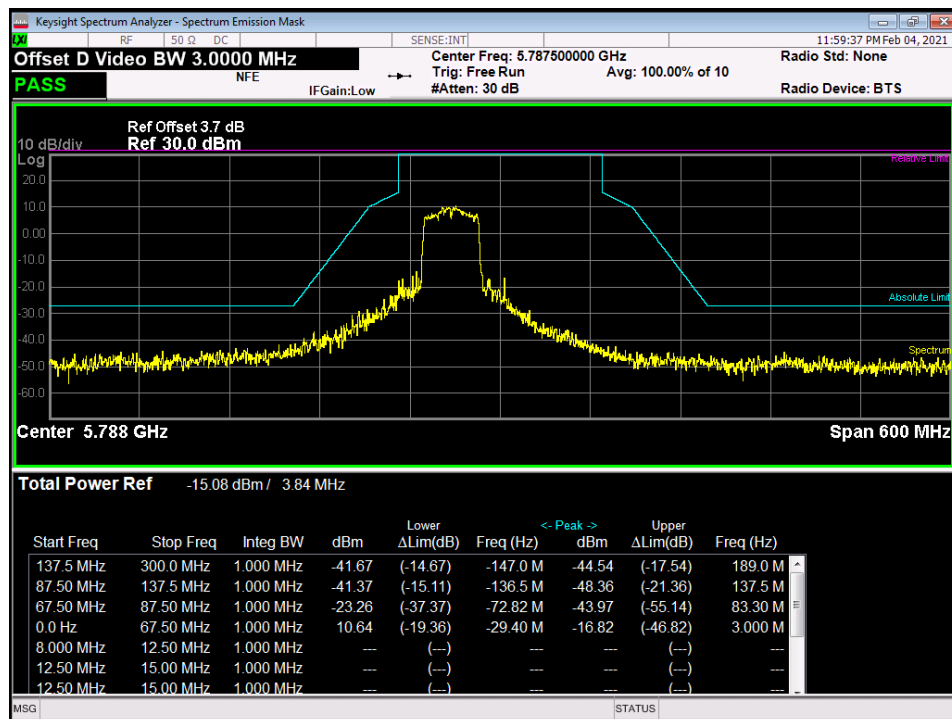
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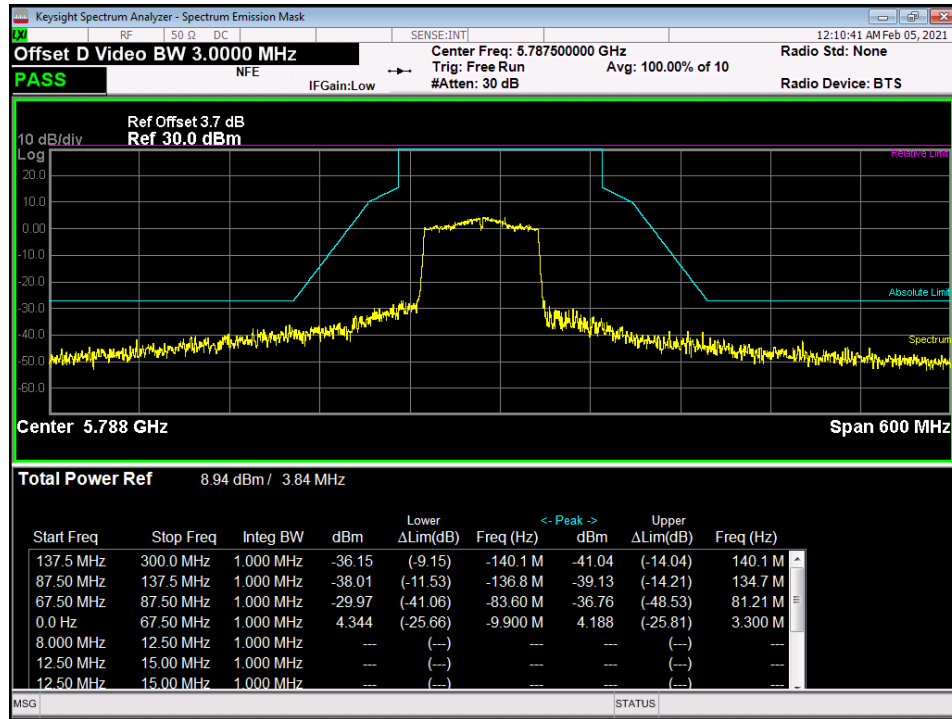
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p20c0005_Mask_40MHz_Low Channel_UNII-3_N_P1



p20c0005_Mask_80MHZ_UNII-3_AC_P0



p20c0005_Mask_80MHZ_UNII-3_AC_P1

