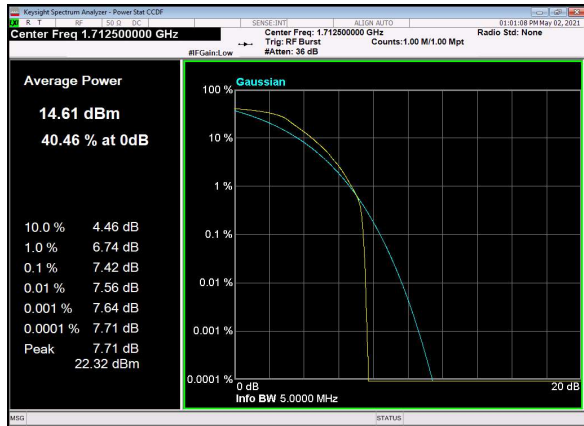
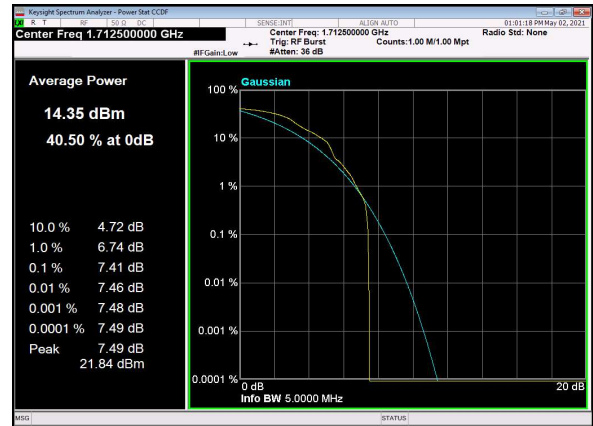
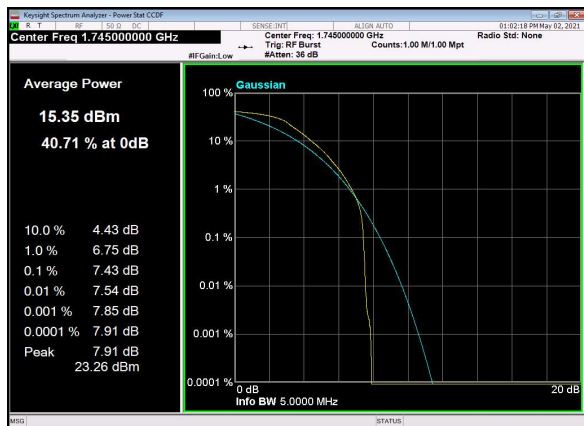
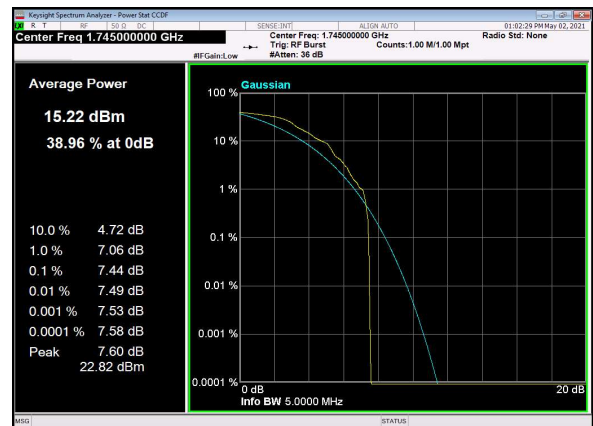
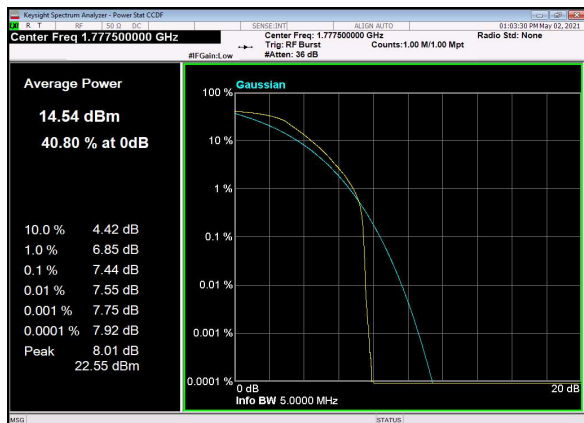
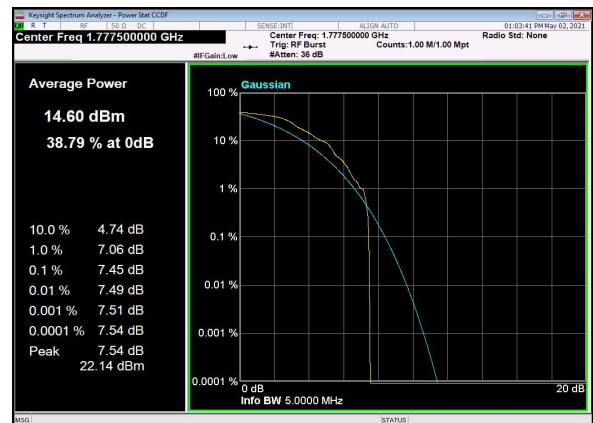
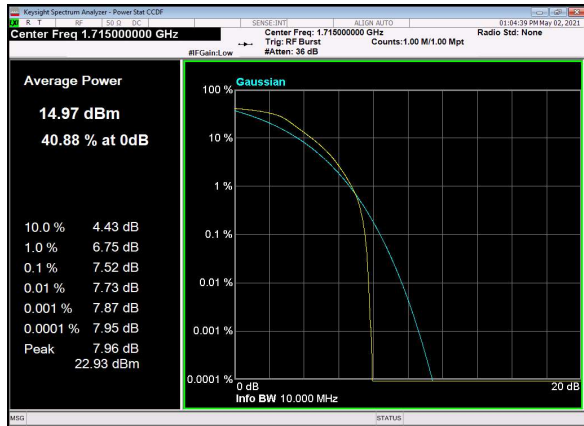
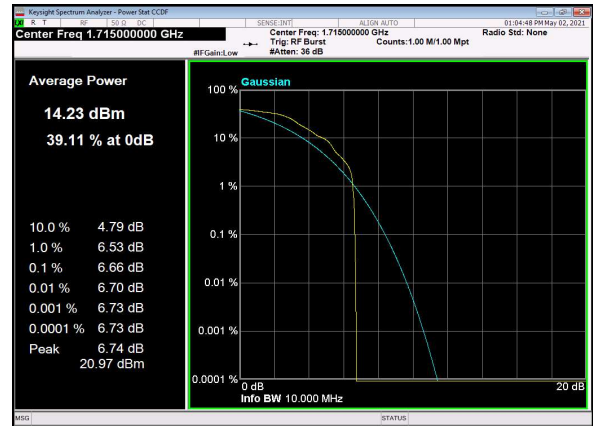
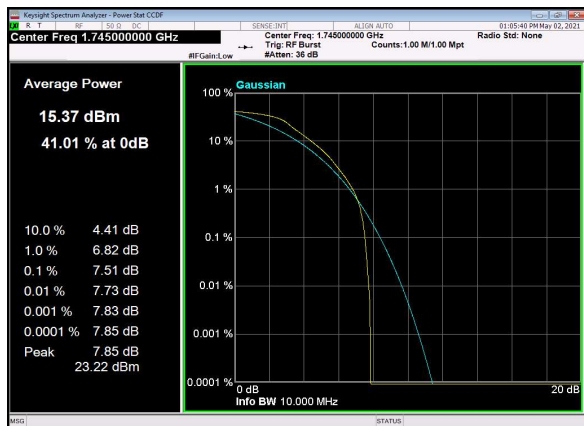
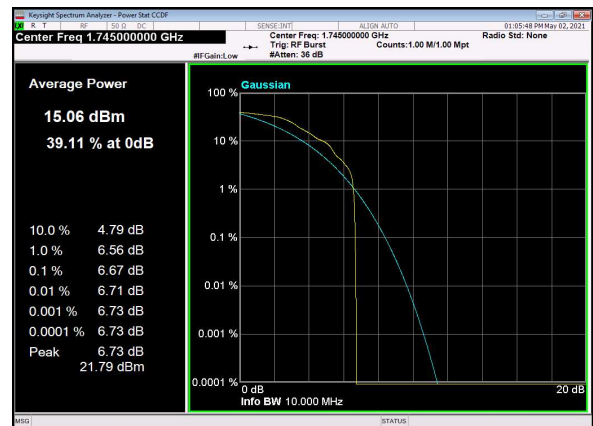
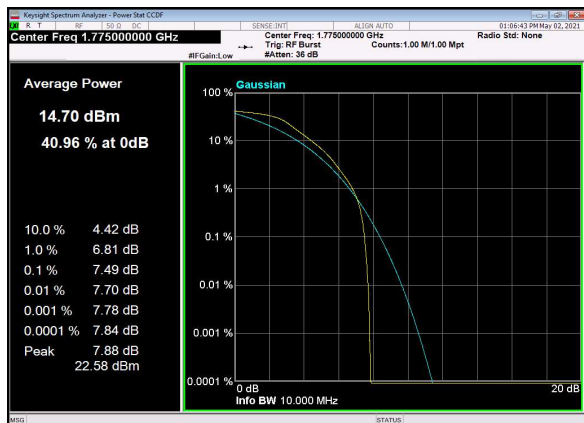
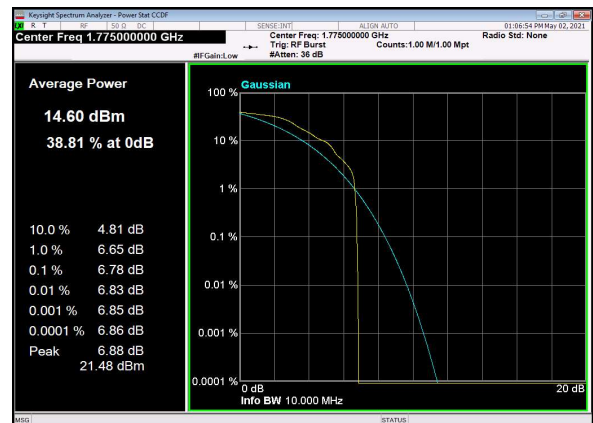
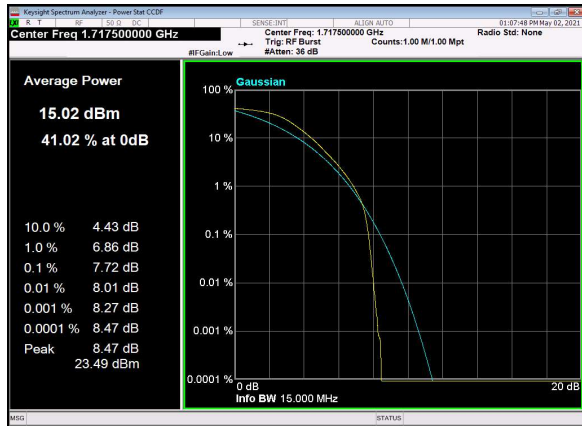
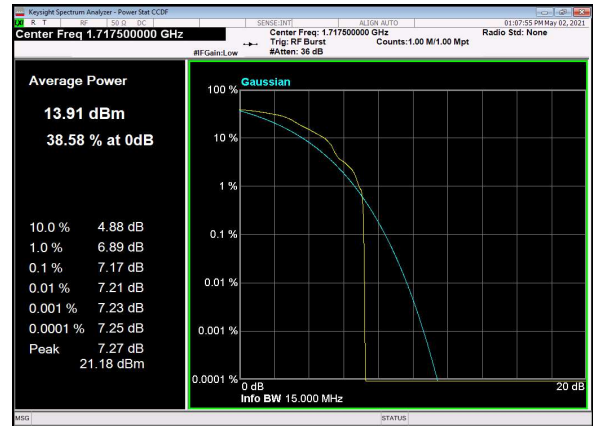
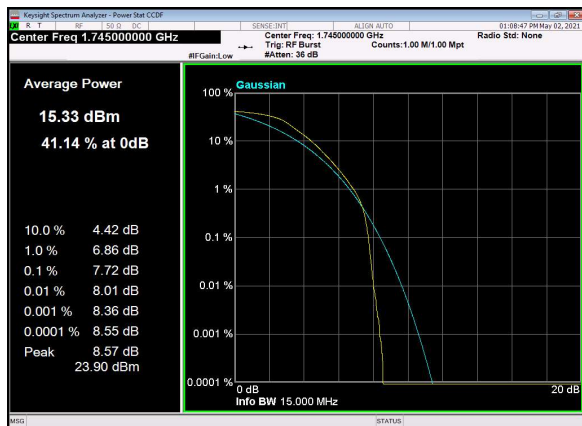
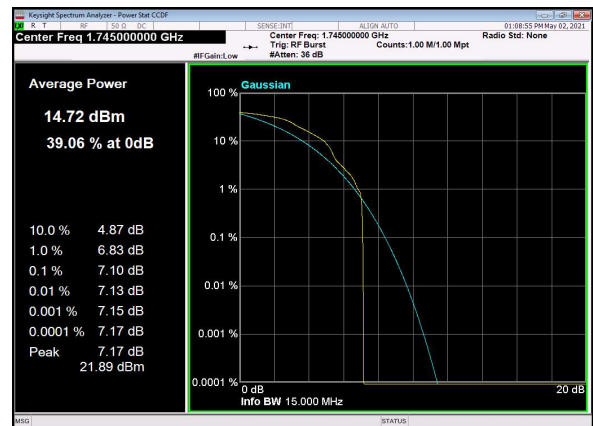
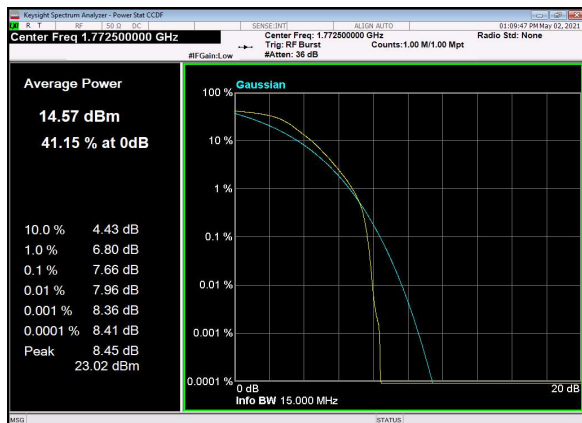
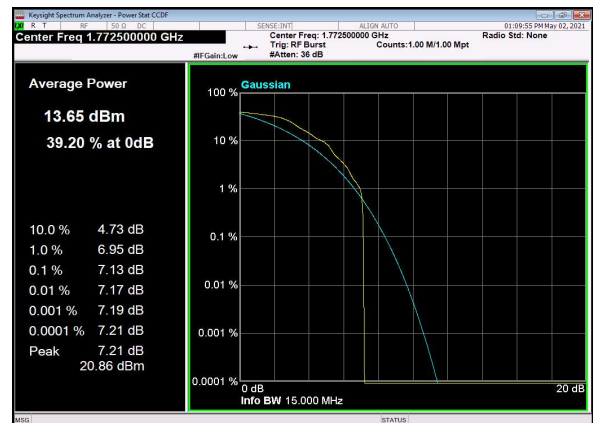
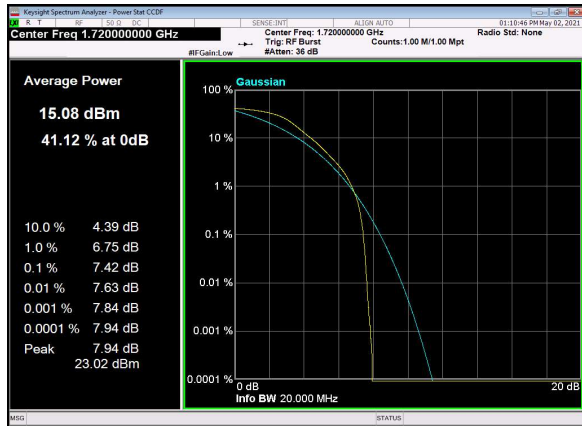
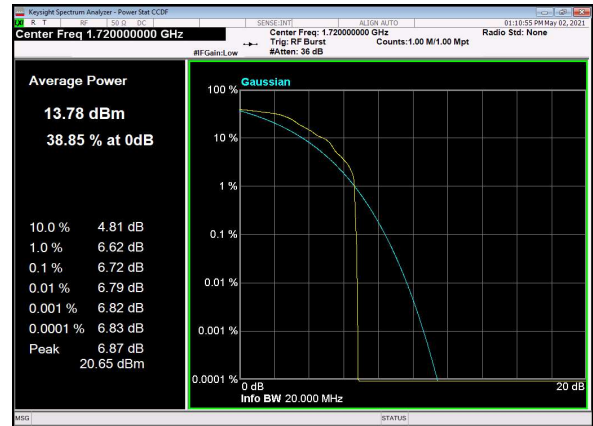
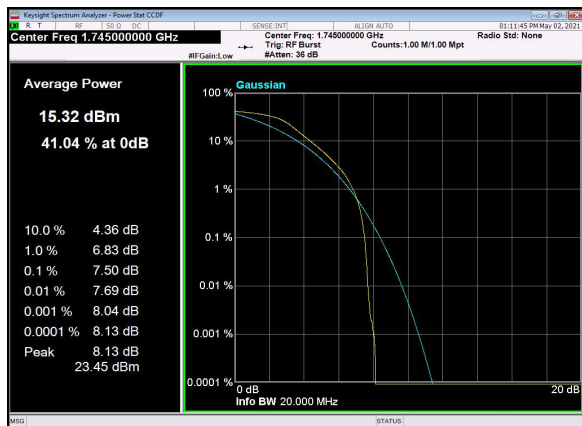
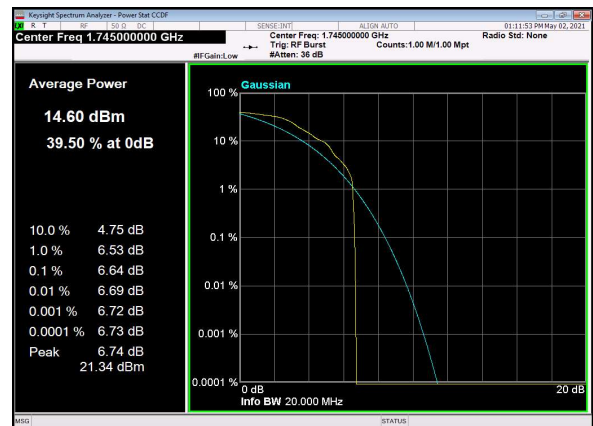
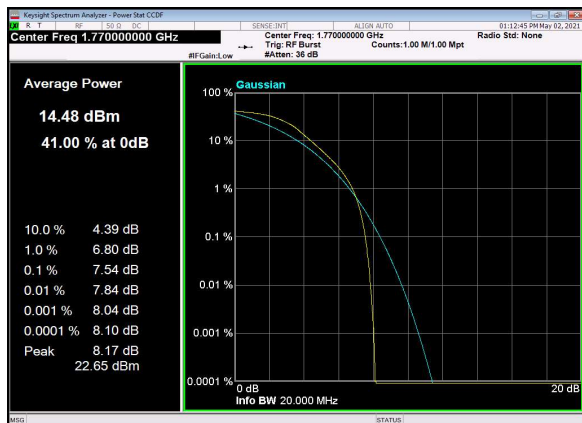
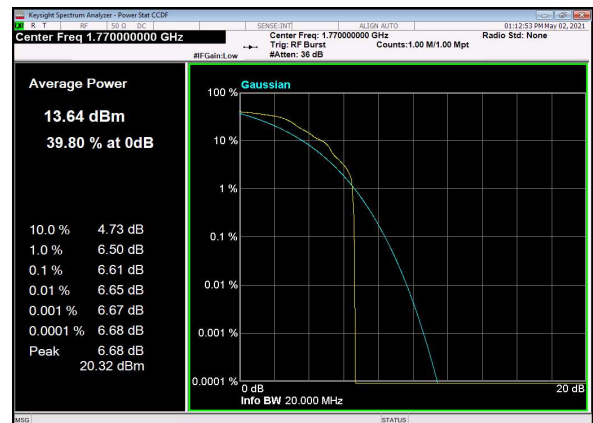
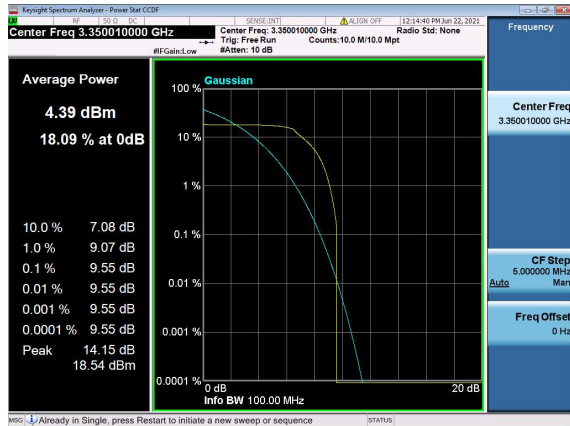
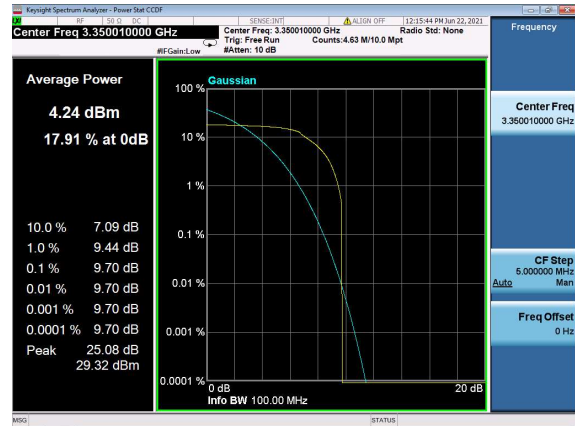
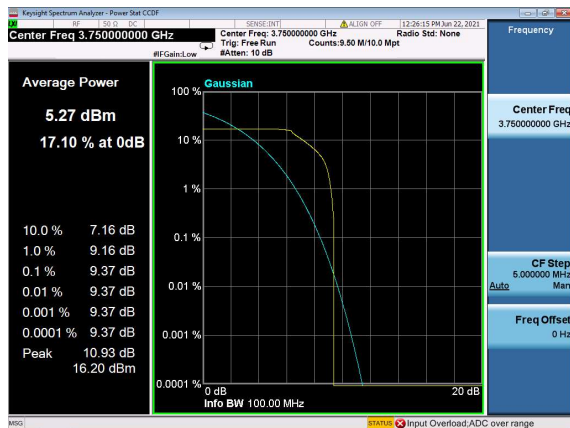
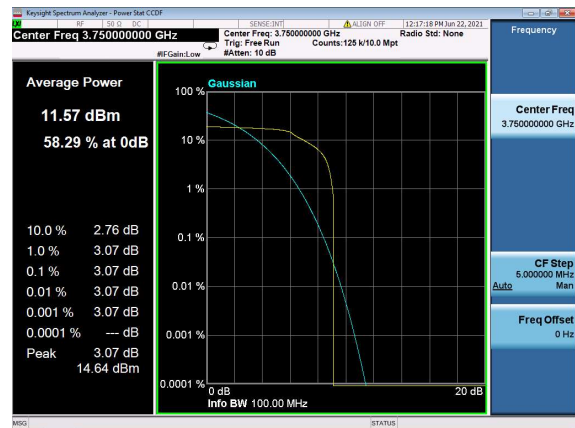
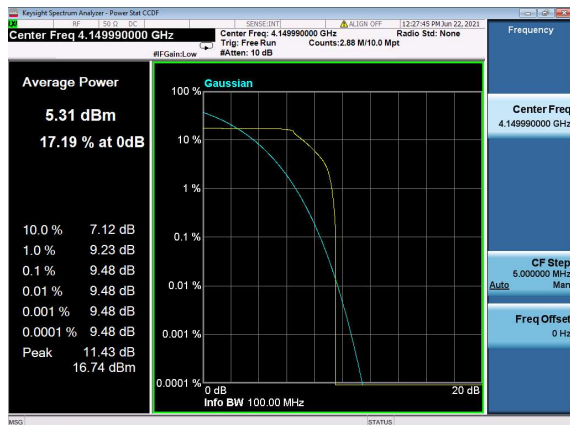
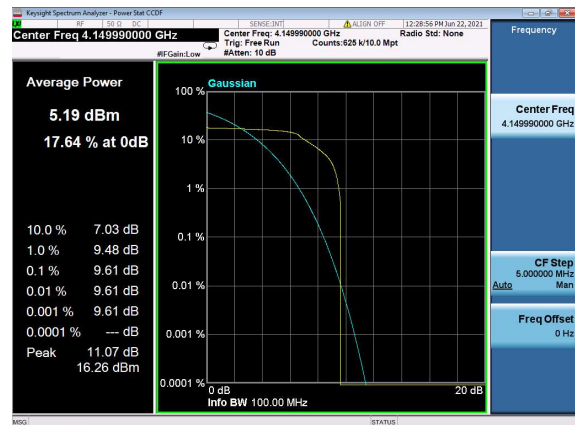


B13\_N66(5M)\_DFT-s-OFDM\_PI\_2-BPSK\_Ou  
ter\_Full\_Low\_CHB13\_N66(5M)\_DFT-s-OFDM\_PI\_2-BPSK\_Edge  
1RB\_Left\_Low\_CHB13\_N66(5M)\_DFT-s-OFDM\_PI\_2-BPSK\_Ou  
ter\_Full\_Mid\_CHB13\_N66(5M)\_DFT-s-OFDM\_PI\_2-BPSK\_Edge  
1RB\_Left\_Mid\_CHB13\_N66(5M)\_DFT-s-OFDM\_PI\_2-BPSK\_Ou  
ter\_Full\_High\_CHB13\_N66(5M)\_DFT-s-OFDM\_PI\_2-BPSK\_Edge  
1RB\_Left\_High\_CH

B13\_N66(10M)\_DFT-s-OFDM\_PI\_2-BPSK\_O  
uter\_Full\_Low\_CHB13\_N66(10M)\_DFT-s-OFDM\_PI\_2-BPSK\_Edge  
\_1RB\_Left\_Low\_CHB13\_N66(10M)\_DFT-s-OFDM\_PI\_2-BPSK\_O  
uter\_Full\_Mid\_CHB13\_N66(10M)\_DFT-s-OFDM\_PI\_2-BPSK\_Edge  
\_1RB\_Left\_Mid\_CHB13\_N66(10M)\_DFT-s-OFDM\_PI\_2-BPSK\_O  
uter\_Full\_High\_CHB13\_N66(10M)\_DFT-s-OFDM\_PI\_2-BPSK\_Edge  
\_1RB\_Left\_High\_CH

B13\_N66(15M)\_DFT-s-OFDM\_PI\_2-BPSK\_O  
uter\_Full\_Low\_CHB13\_N66(15M)\_DFT-s-OFDM\_PI\_2-BPSK\_Edge  
\_1RB\_Left\_Low\_CHB13\_N66(15M)\_DFT-s-OFDM\_PI\_2-BPSK\_O  
uter\_Full\_Mid\_CHB13\_N66(15M)\_DFT-s-OFDM\_PI\_2-BPSK\_Edge  
\_1RB\_Left\_Mid\_CHB13\_N66(15M)\_DFT-s-OFDM\_PI\_2-BPSK\_O  
uter\_Full\_High\_CHB13\_N66(15M)\_DFT-s-OFDM\_PI\_2-BPSK\_Edge  
\_1RB\_Left\_High\_CH

B13\_N66(20M)\_DFT-s-OFDM\_PI\_2-BPSK\_O  
uter\_Full\_Low\_CHB13\_N66(20M)\_DFT-s-OFDM\_PI\_2-BPSK\_Edge  
\_1RB\_Left\_Low\_CHB13\_N66(20M)\_DFT-s-OFDM\_PI\_2-BPSK\_O  
uter\_Full\_Mid\_CHB13\_N66(20M)\_DFT-s-OFDM\_PI\_2-BPSK\_Edge  
\_1RB\_Left\_Mid\_CHB13\_N66(20M)\_DFT-s-OFDM\_PI\_2-BPSK\_O  
uter\_Full\_High\_CHB13\_N66(20M)\_DFT-s-OFDM\_PI\_2-BPSK\_Edge  
\_1RB\_Left\_High\_CH

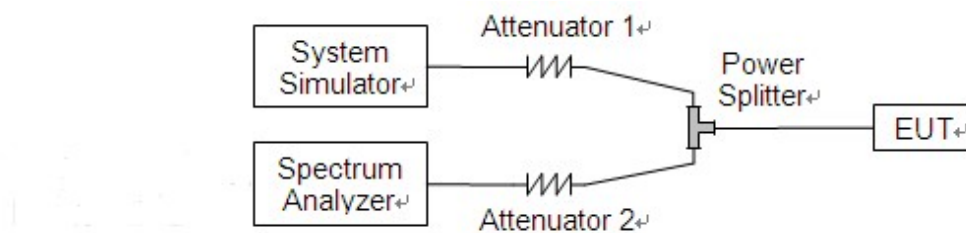
B2\_N77(100M)\_DFT-s-OFDM\_PI\_2-BPSK\_O  
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\_Low\_CHB2\_N77(100M)\_DFT-s-OFDM\_PI\_2-BPSK\_O  
uter\_Full\_Mid\_CHB2\_N77(100M)\_DFT-s-OFDM\_QPSK\_Outer\_Full  
\_Mid\_CHB2\_N77(100M)\_DFT-s-OFDM\_PI\_2-BPSK\_O  
uter\_Full\_High\_CHB2\_N77(100M)\_DFT-s-OFDM\_QPSK\_Outer\_Full  
\_High\_CH

## 2.5. Conducted Spurious Emissions

### 2.5.1. Requirement

According to FCC section 2.1051, section 27.53(h), section 27.53(g), the power of any emission outside of the authorized operating frequency ranges must be attenuated below the transmitting power (P) by a factor of at least  $43 + 10 \cdot \log(P)$  dB. This calculated to be -13dBm.

### 2.5.2. Test Description



The EUT is coupled to the Spectrum Analyzer (SA) and the System Simulator (SS) with Attenuators through the Power Splitter; the RF load attached to the EUT antenna terminal is 50 Ohm; the path loss as the factor is calibrated to correct the reading. The EUT is commanded by the SS to operate at the maximum output power. A call is established between the EUT and the SS.

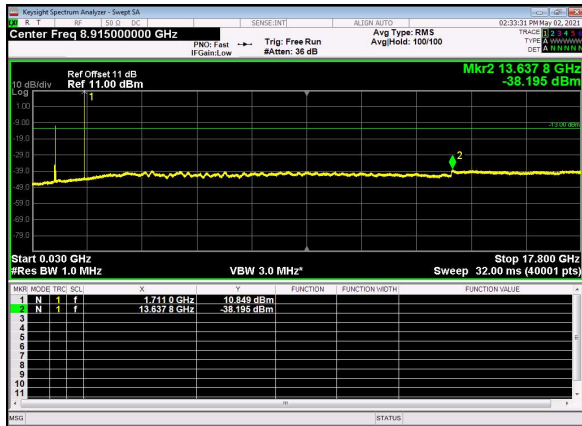
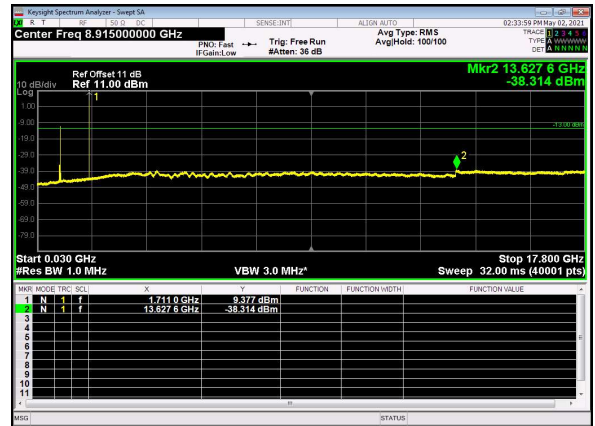
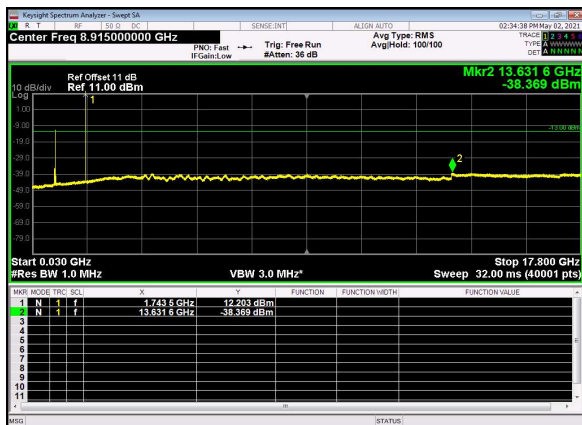
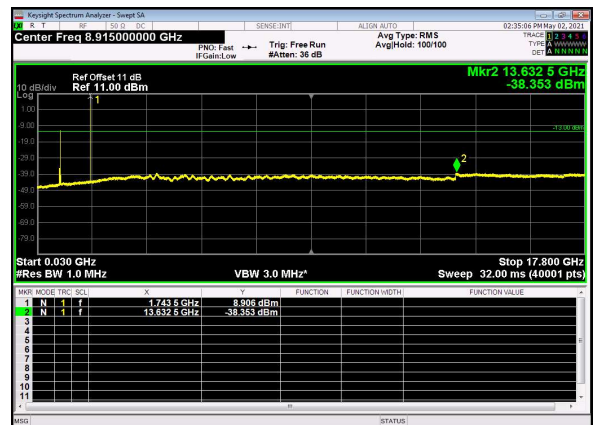
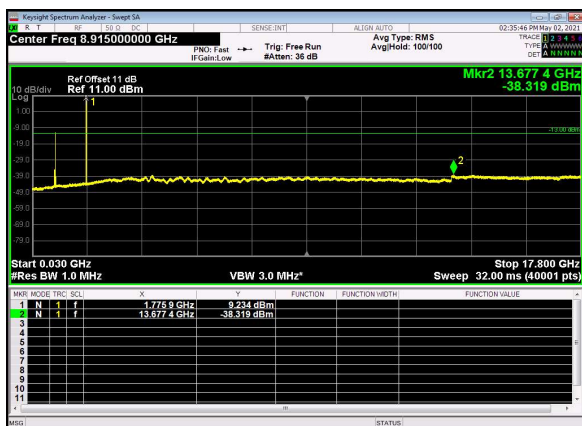


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### **2.5.3. Test procedure**

KDB 971168 D01v03 Section 6.0 and ANSI/TIA-603-E-2016.

### **2.5.4. Test Result**

B13\_N66(5M)\_DFT-s-OFDM\_BPSK\_Edge\_  
1RB\_Left\_Low\_CHB13\_N66(5M)\_DFT-s-OFDM\_QPSK\_Edge\_  
1RB\_Left\_Low\_CHB13\_N66(5M)\_DFT-s-OFDM\_BPSK\_Edge\_  
1RB\_Left\_Mid\_CHB13\_N66(5M)\_DFT-s-OFDM\_QPSK\_Edge\_  
1RB\_Left\_Mid\_CHB13\_N66(5M)\_DFT-s-OFDM\_BPSK\_Edge\_  
1RB\_Left\_High\_CHB13\_N66(5M)\_DFT-s-OFDM\_QPSK\_Edge\_  
1RB\_Left\_High\_CH