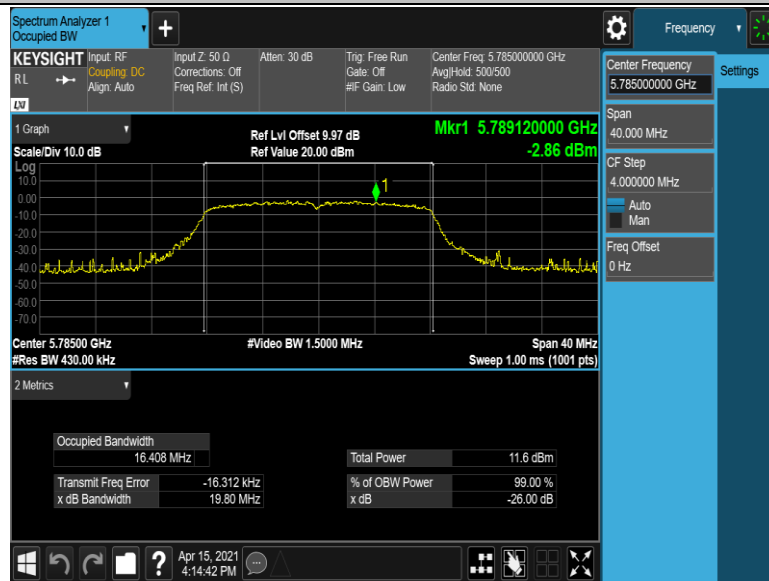
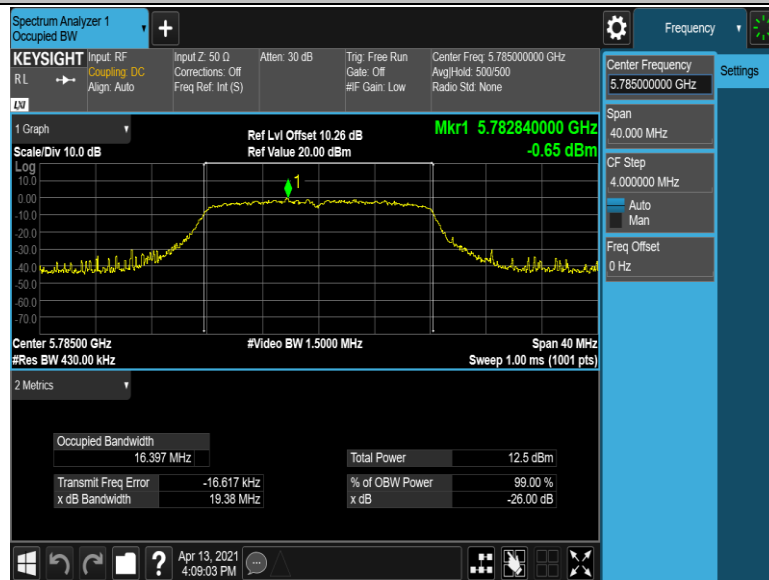


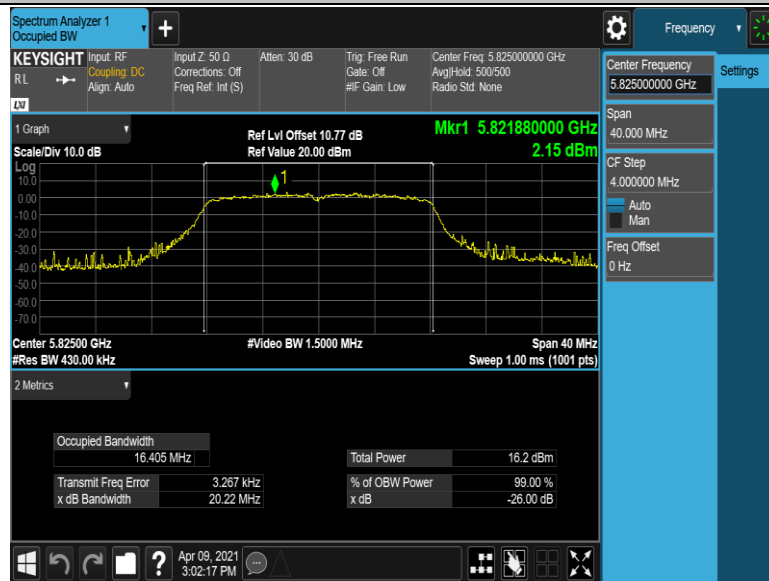
# 11A\_Ant1\_5785



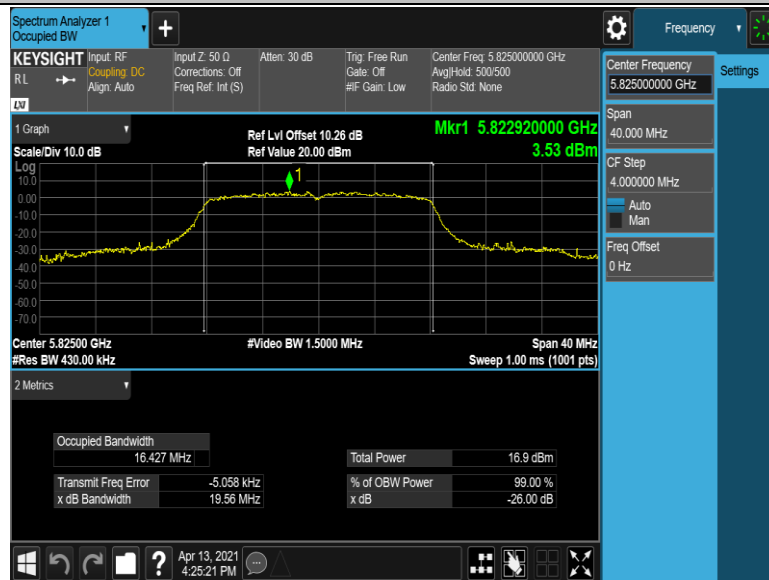
# 11A\_Ant2\_5785



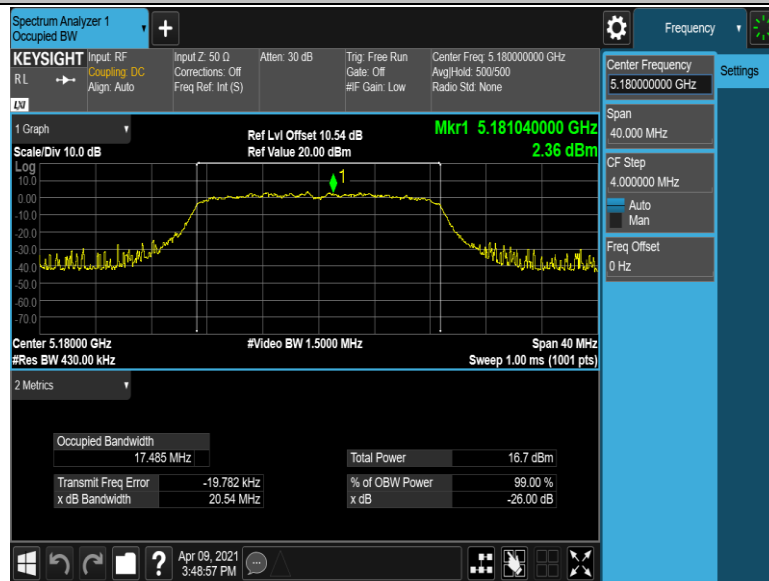
# 11A\_Ant1\_5825



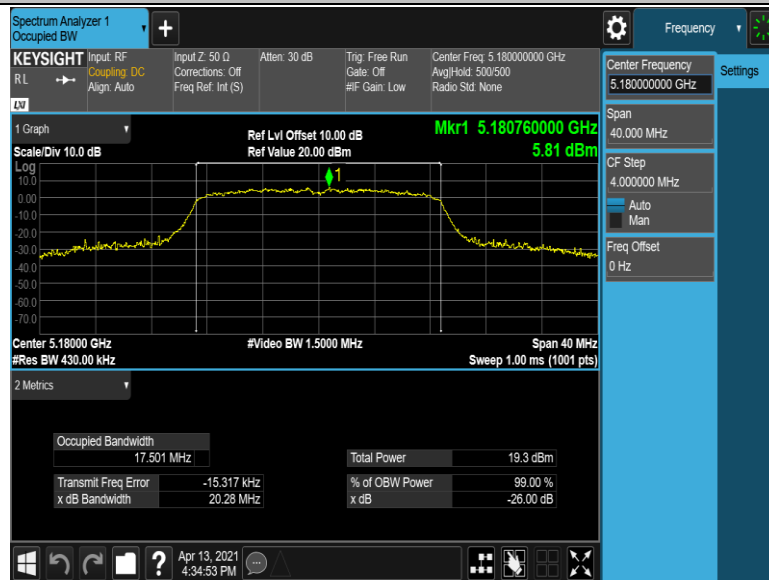
# 11A\_Ant2\_5825



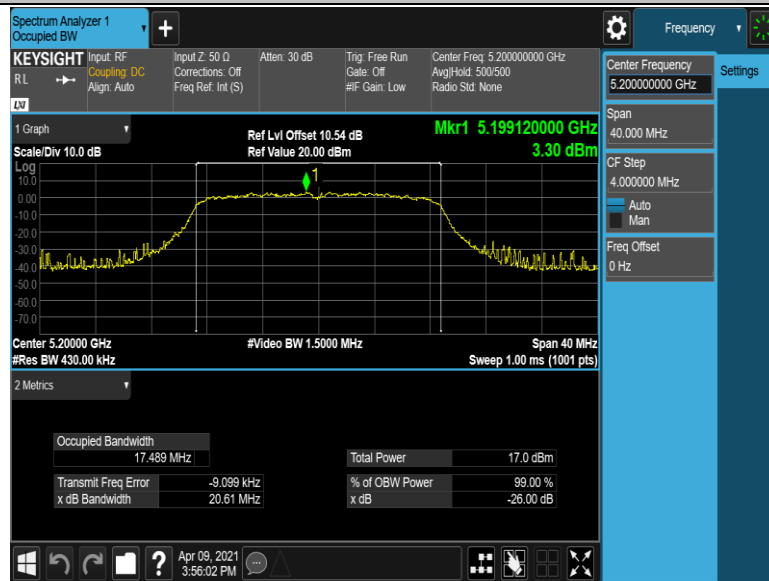
# 11N20SISO\_Ant1\_5180



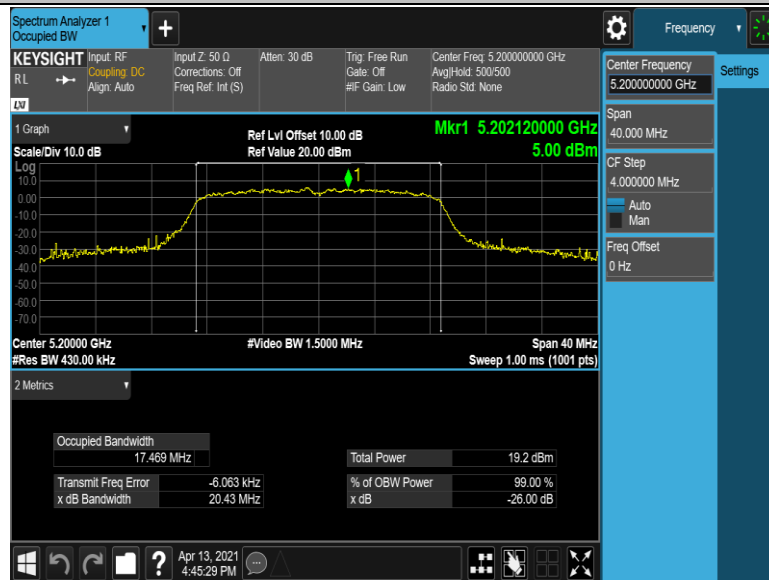
# 11N20SISO\_Ant2\_5180



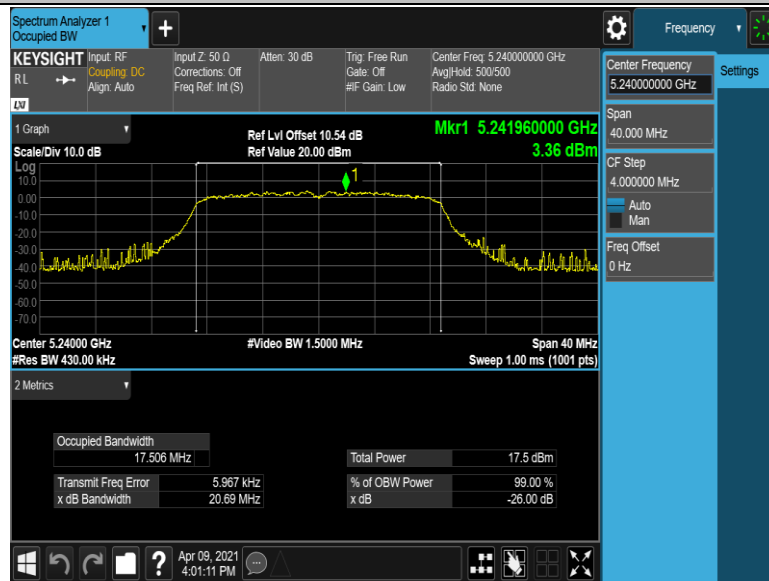
# 11N20SISO\_Ant1\_5200



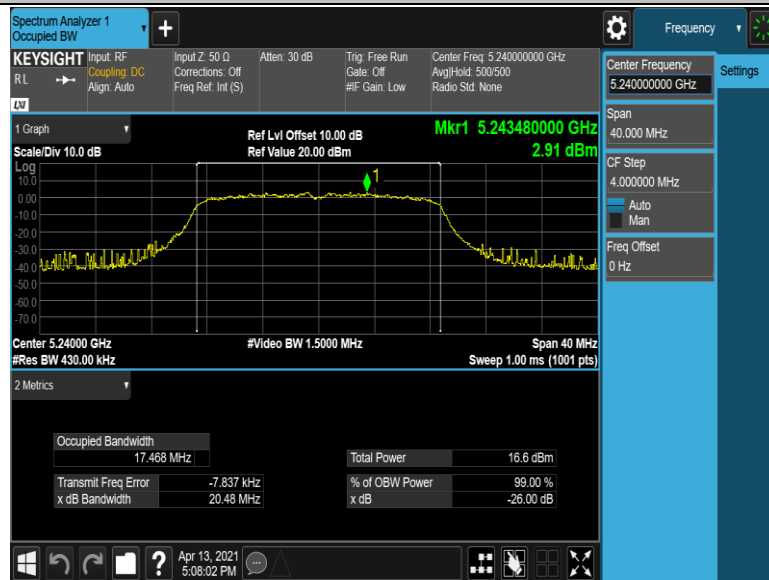
# 11N20SISO\_Ant2\_5200



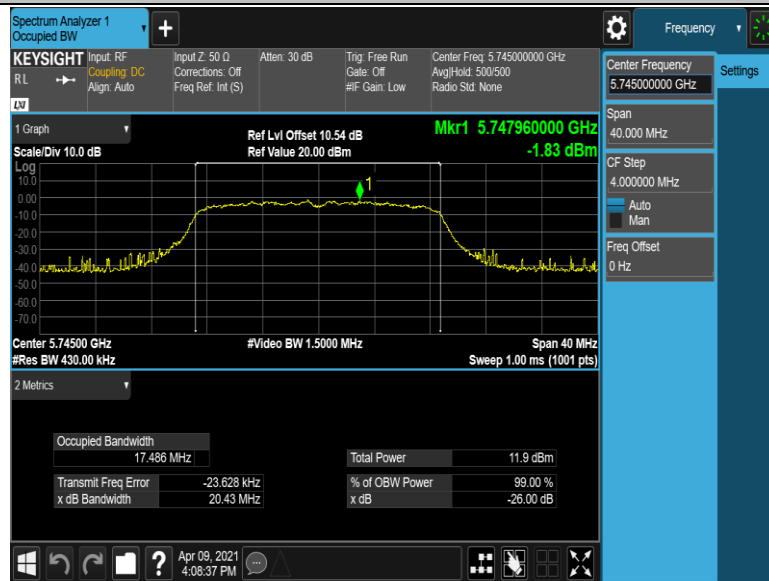
# 11N20SISO\_Ant1\_5240



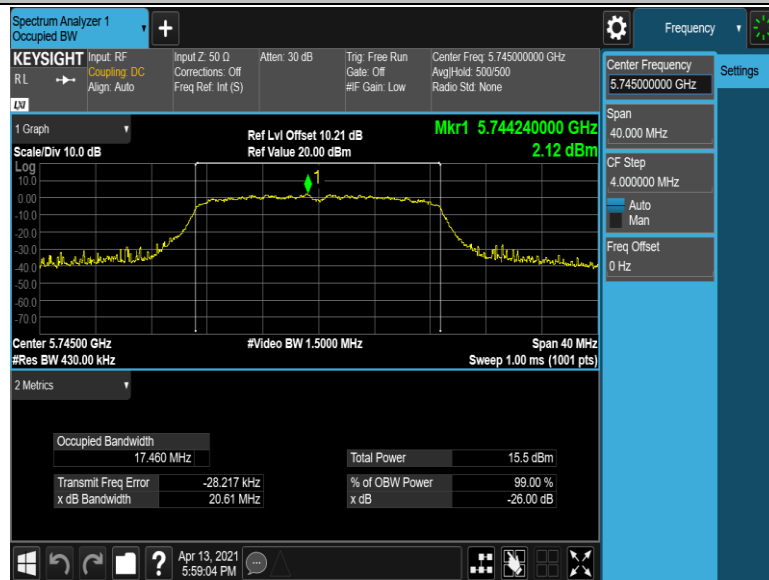
# 11N20SISO\_Ant2\_5240



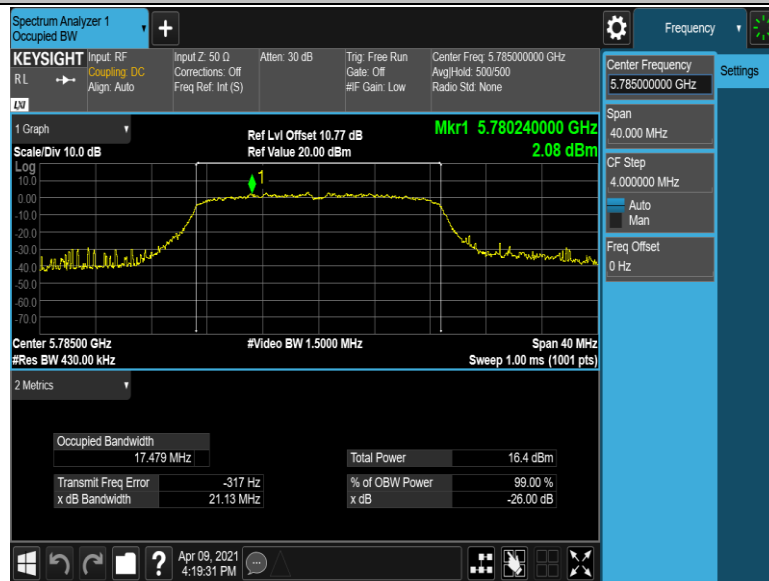
# 11N20SISO\_Ant1\_5745



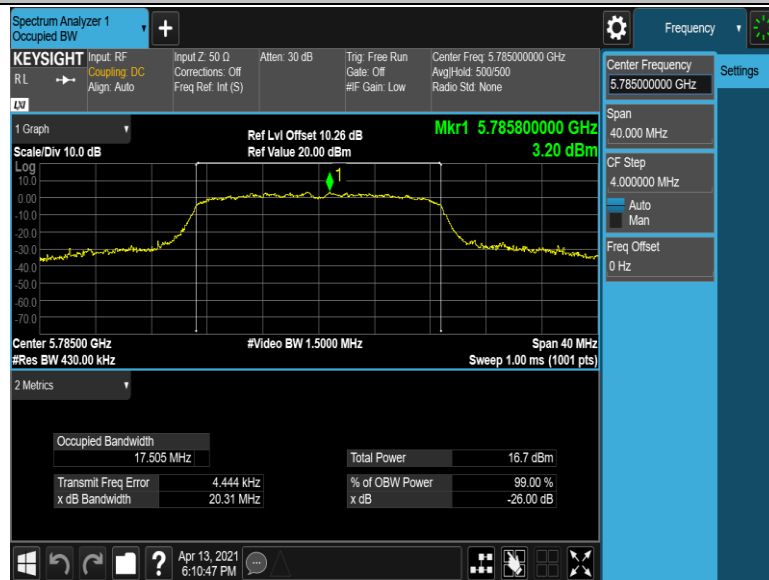
# 11N20SISO\_Ant2\_5745



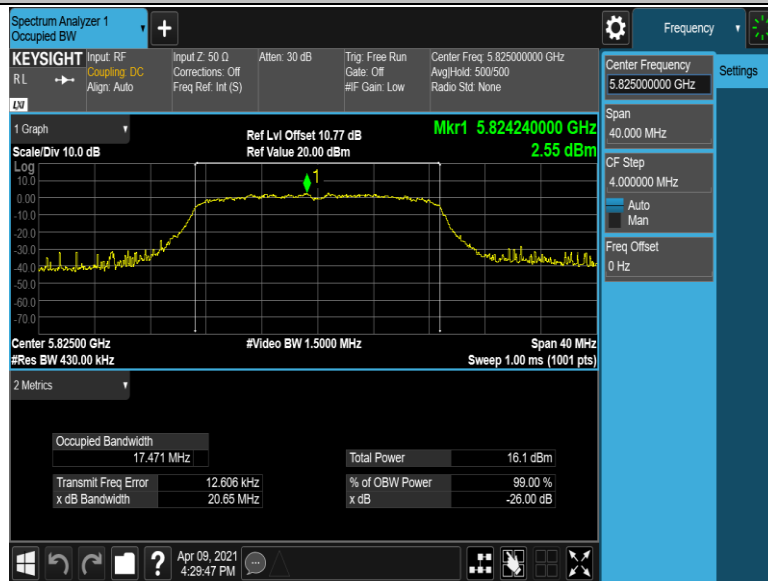
# 11N20SISO\_Ant1\_5785



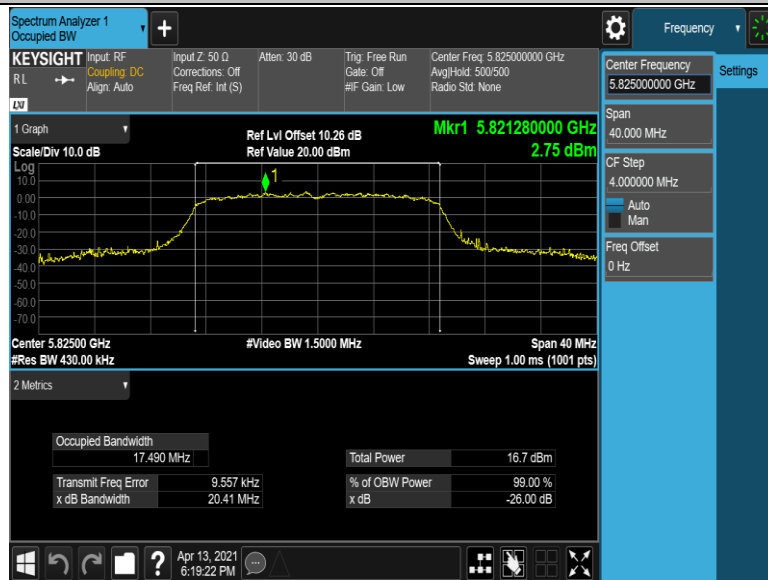
# 11N20SISO\_Ant2\_5785



# 11N20SISO\_Ant1\_5825

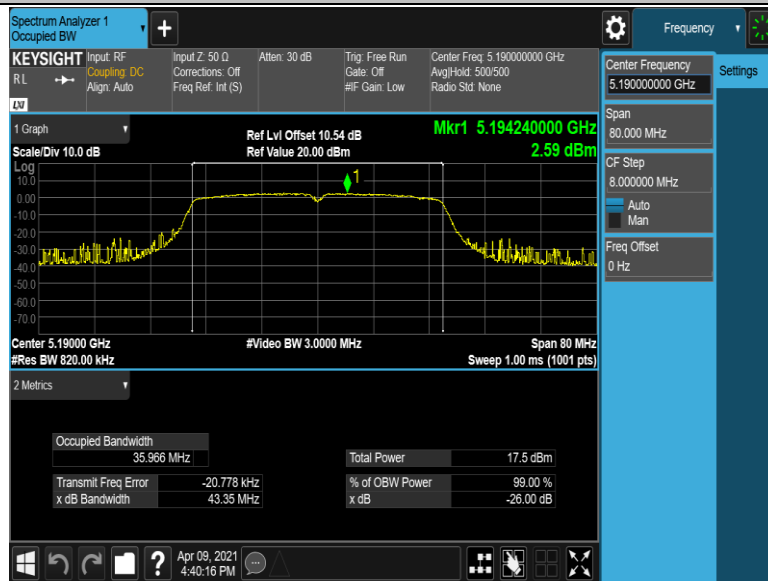


# 11N20SISO\_Ant2\_5825

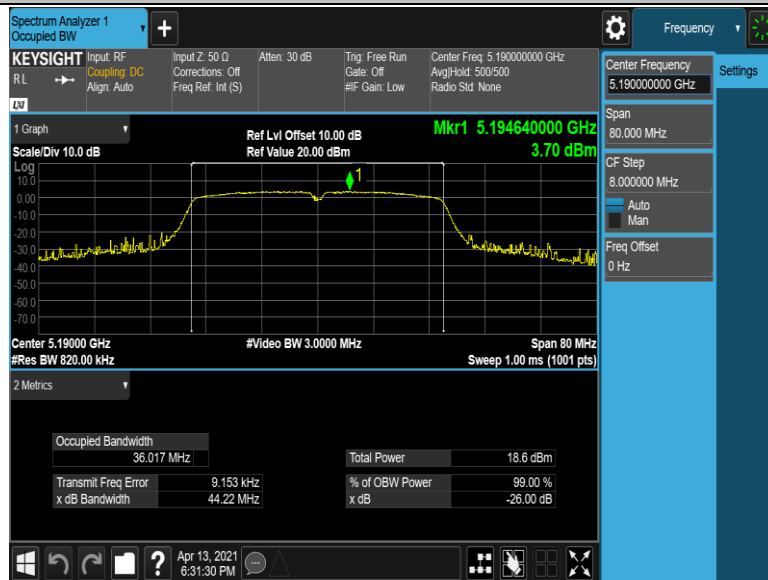




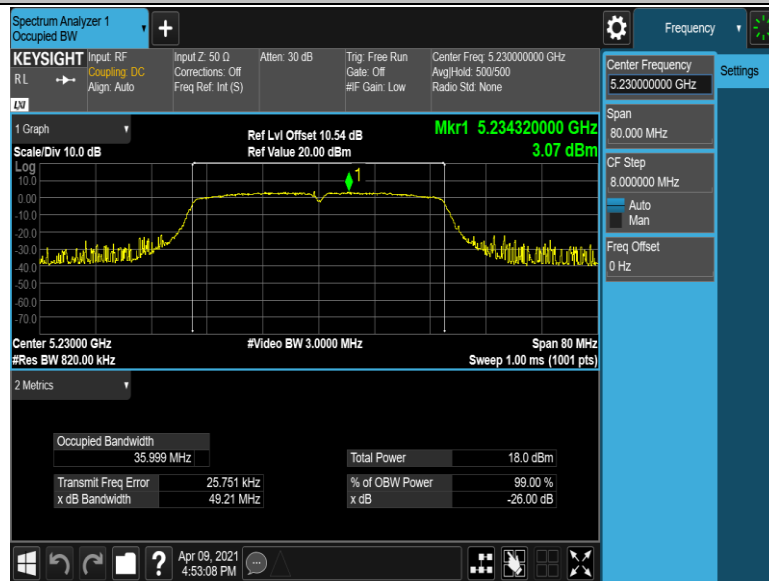
# 11N40SISO\_Ant1\_5190



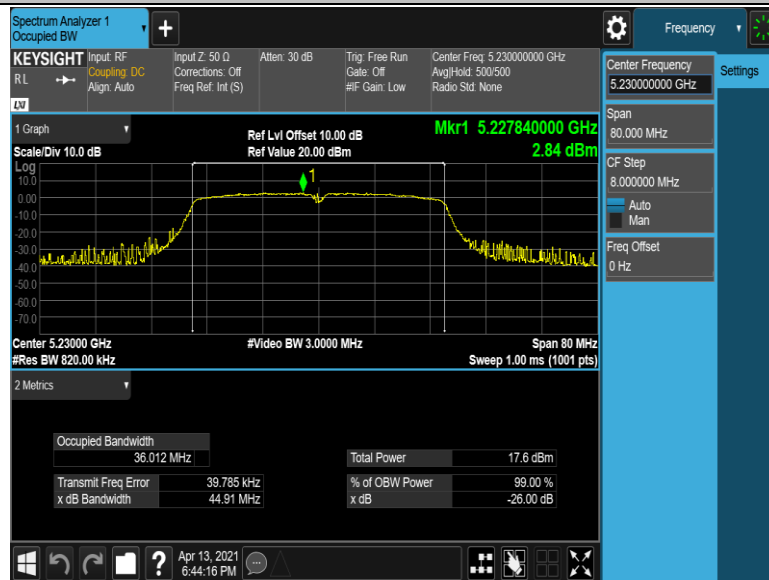
# 11N40SISO\_Ant2\_5190



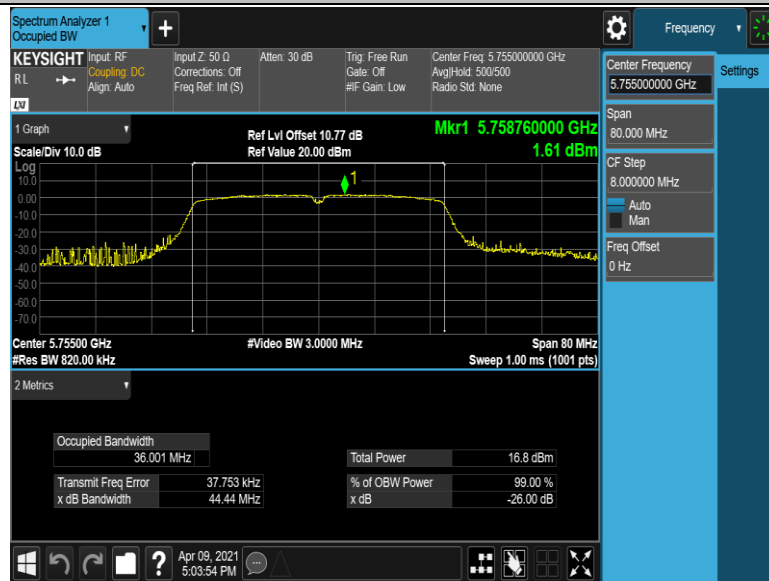
# 11N40SISO\_Ant1\_5230



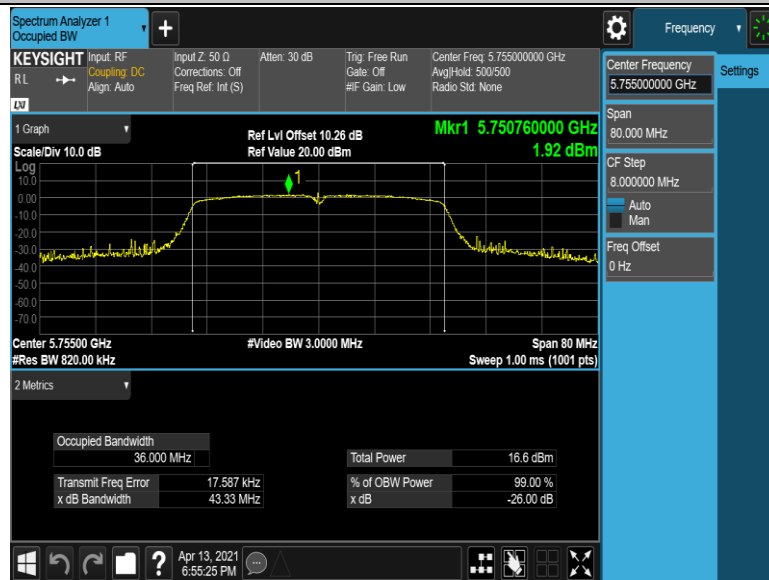
# 11N40SISO\_Ant2\_5230



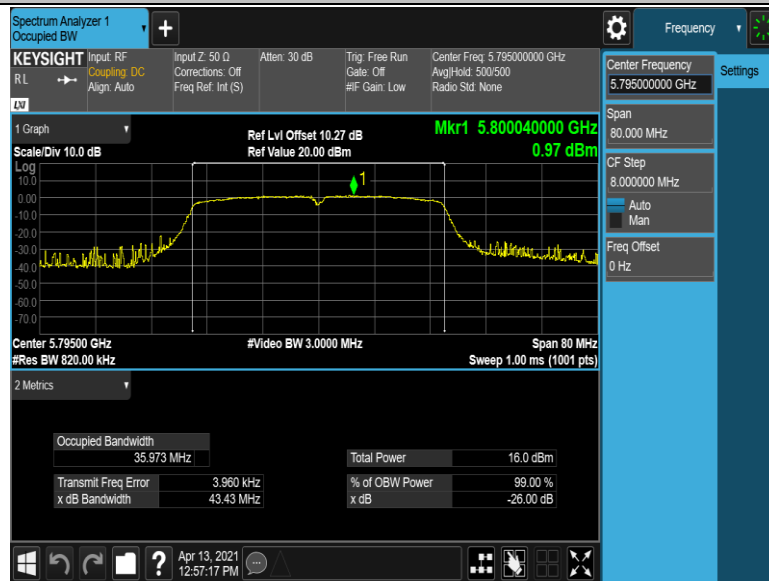
# 11N40SISO\_Ant1\_5755



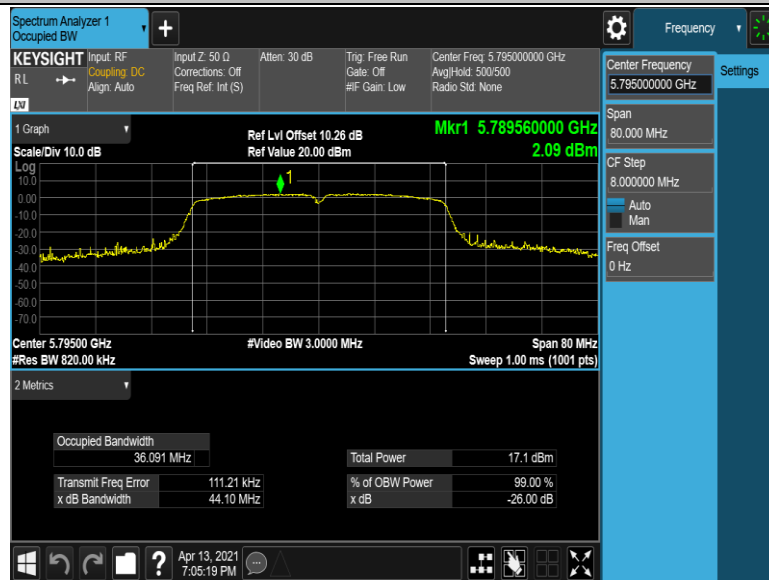
# 11N40SISO\_Ant2\_5755



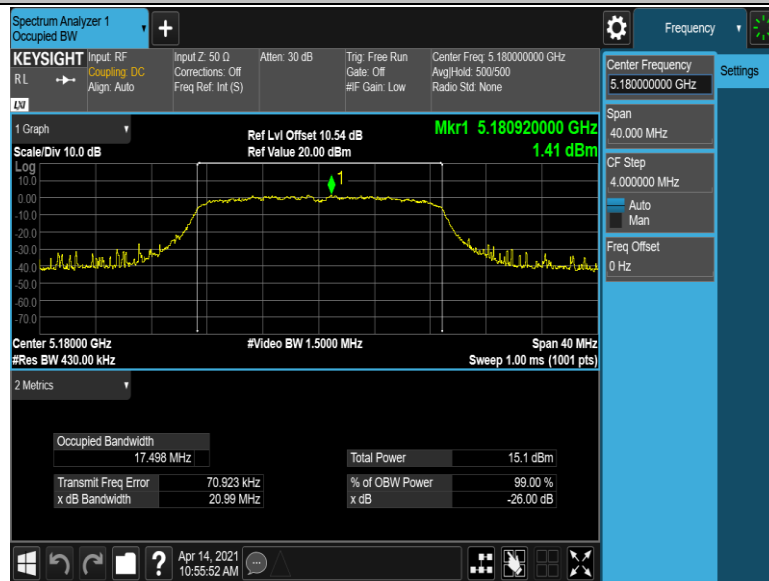
# 11N40SISO\_Ant1\_5795



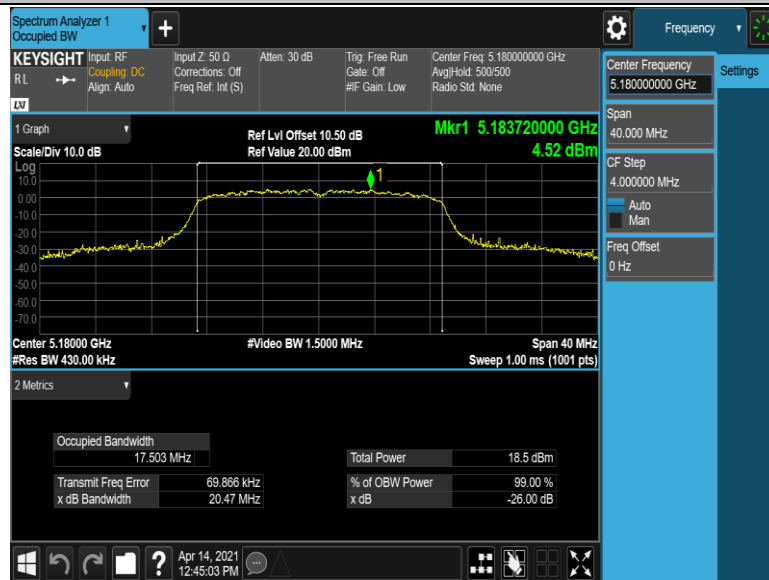
# 11N40SISO\_Ant2\_5795



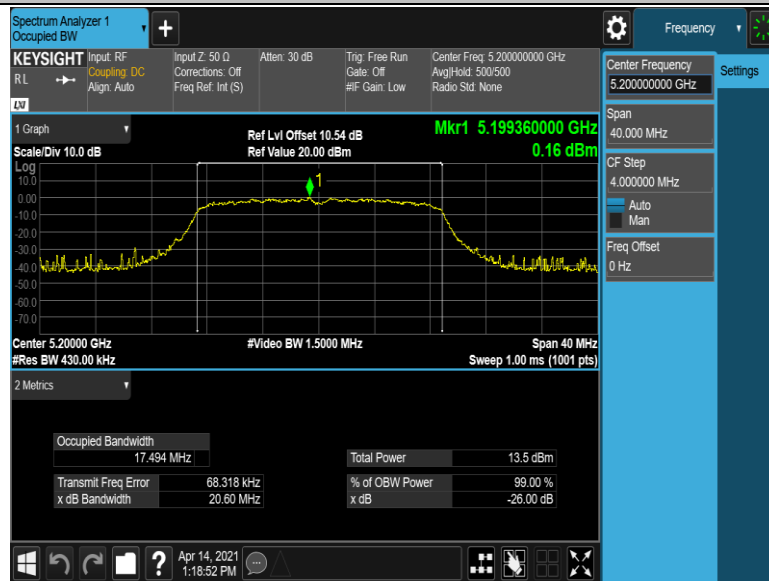
# 11N20MIMO\_Ant1\_5180



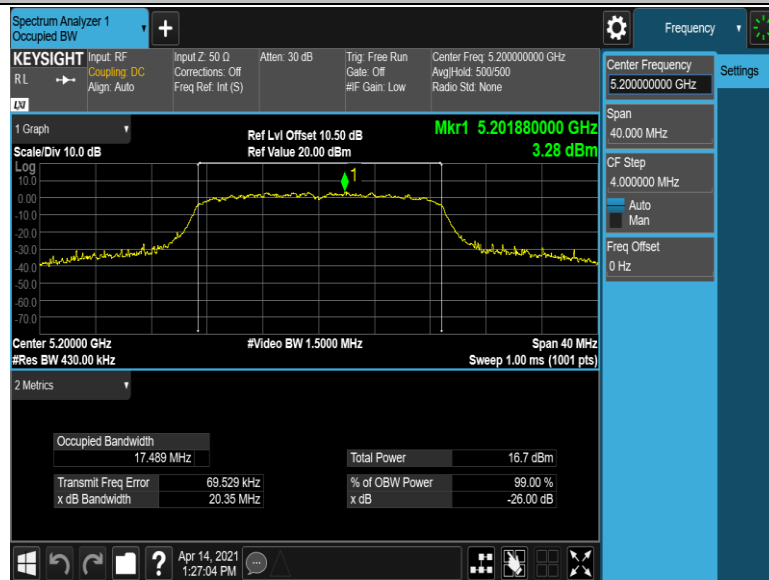
# 11N20MIMO\_Ant2\_5180



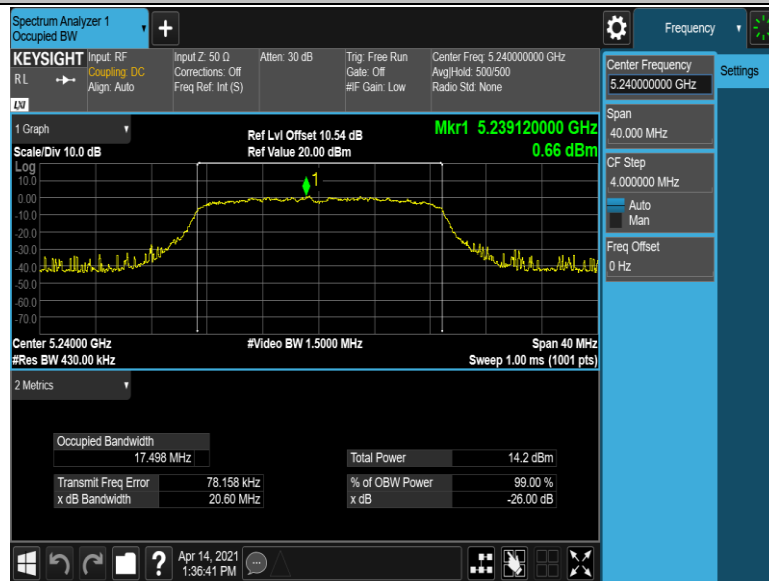
# 11N20MIMO\_Ant1\_5200



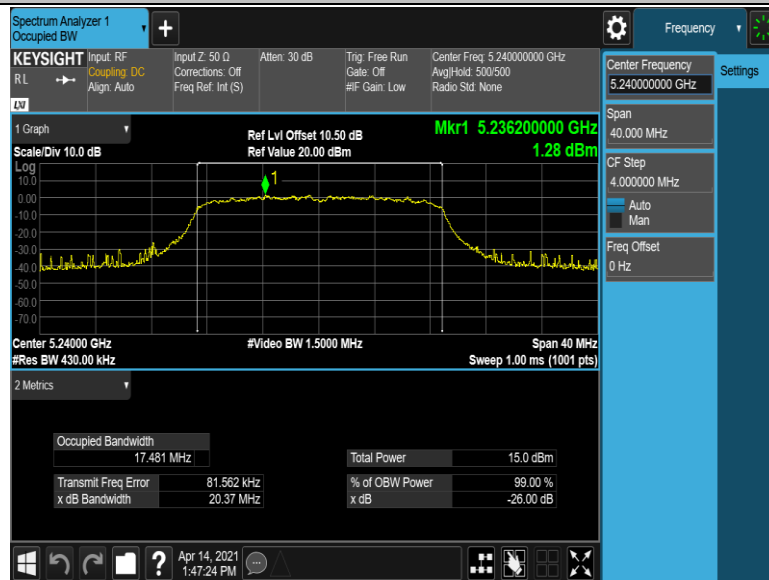
# 11N20MIMO\_Ant2\_5200



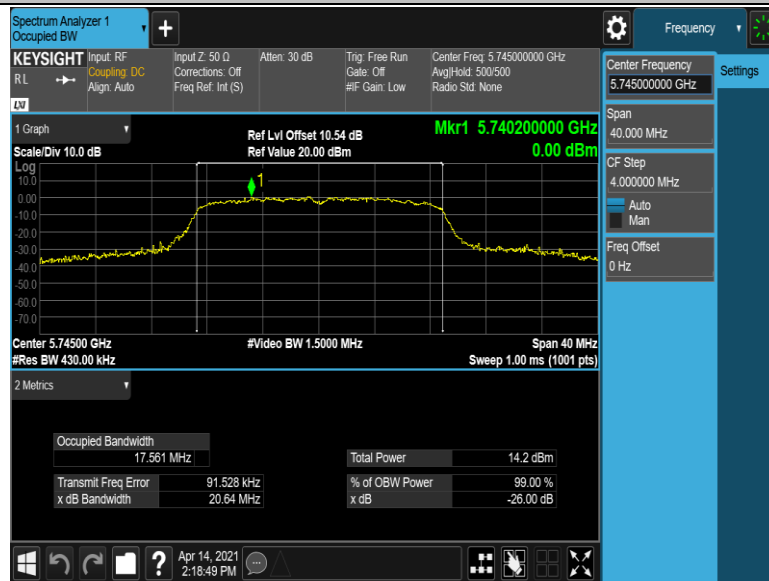
# 11N20MIMO\_Ant1\_5240



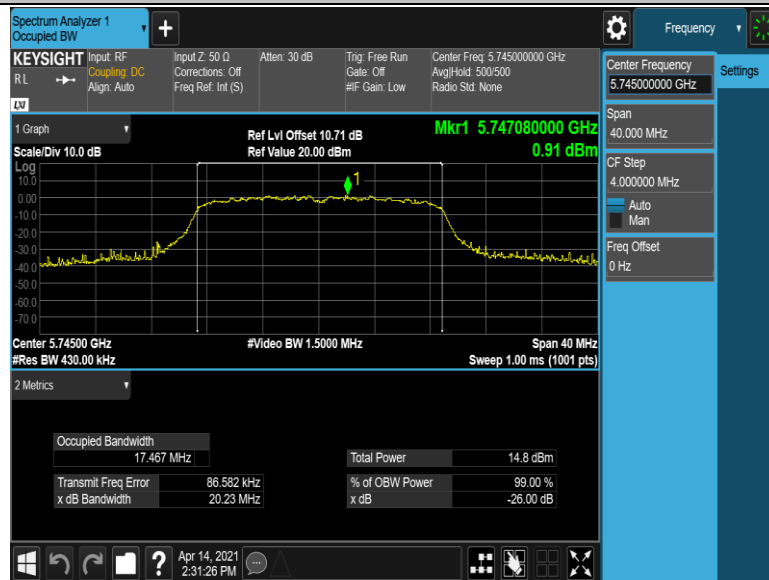
# 11N20MIMO\_Ant2\_5240



# 11N20MIMO\_Ant1\_5745

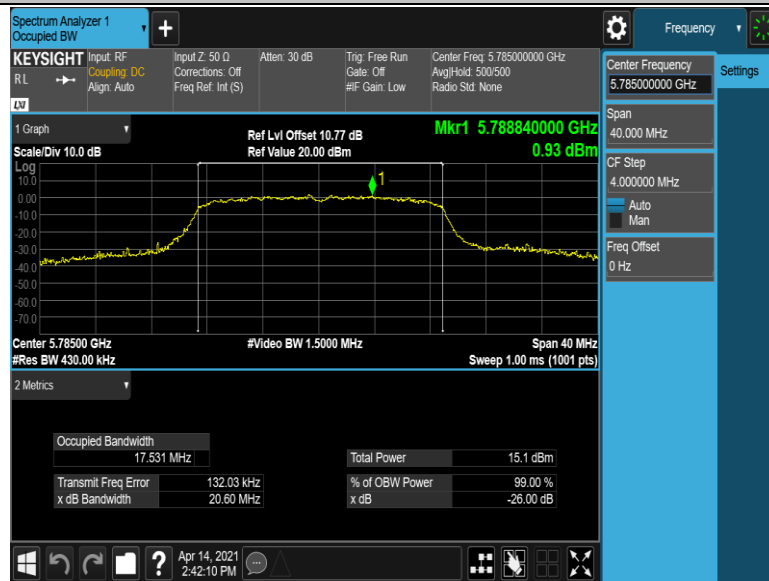


# 11N20MIMO\_Ant2\_5745

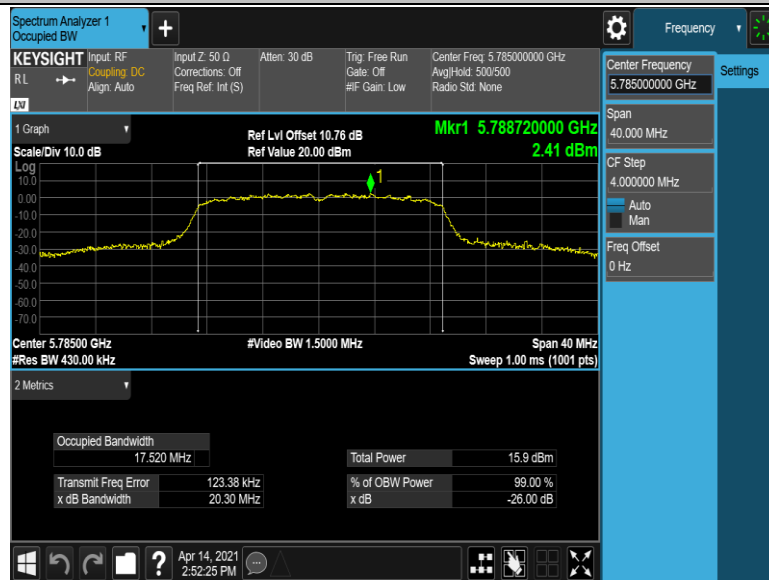




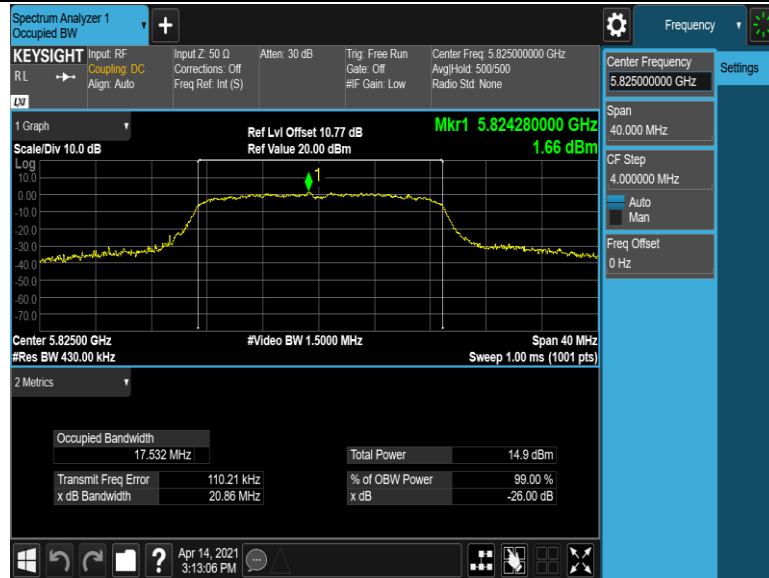
# 11N20MIMO\_Ant1\_5785



# 11N20MIMO\_Ant2\_5785



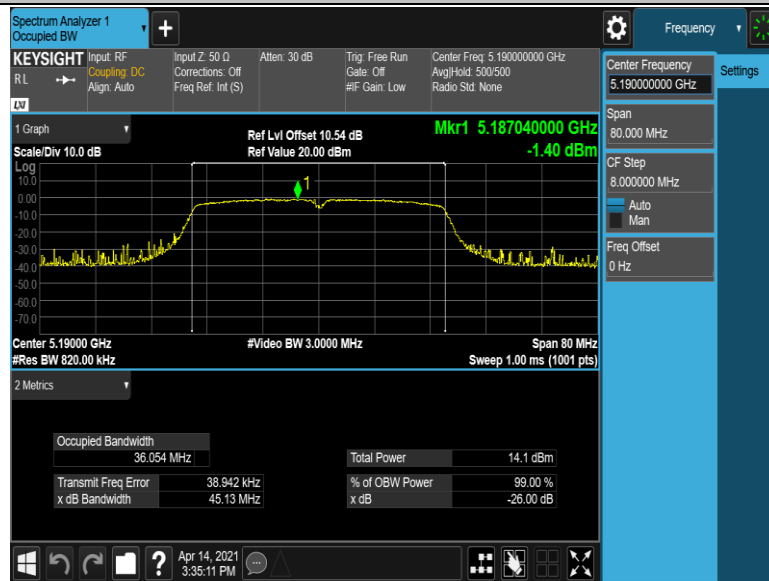
# 11N20MIMO\_Ant1\_5825



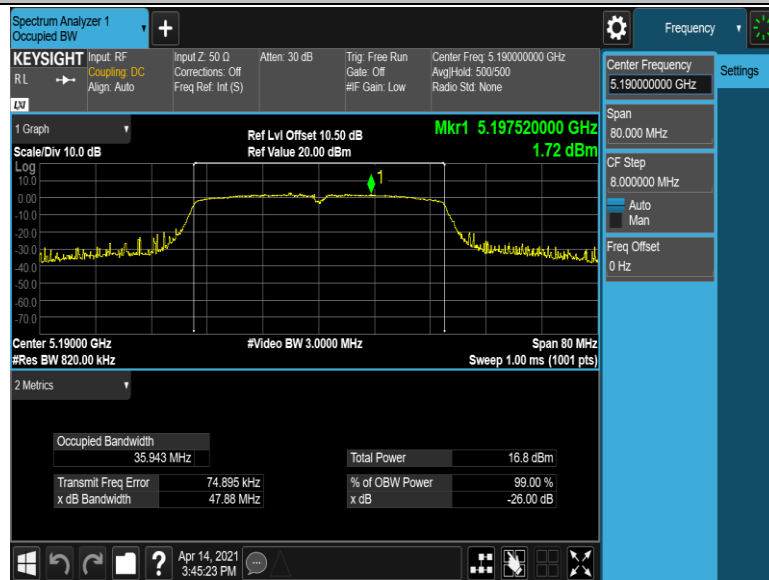
# 11N20MIMO\_Ant2\_5825



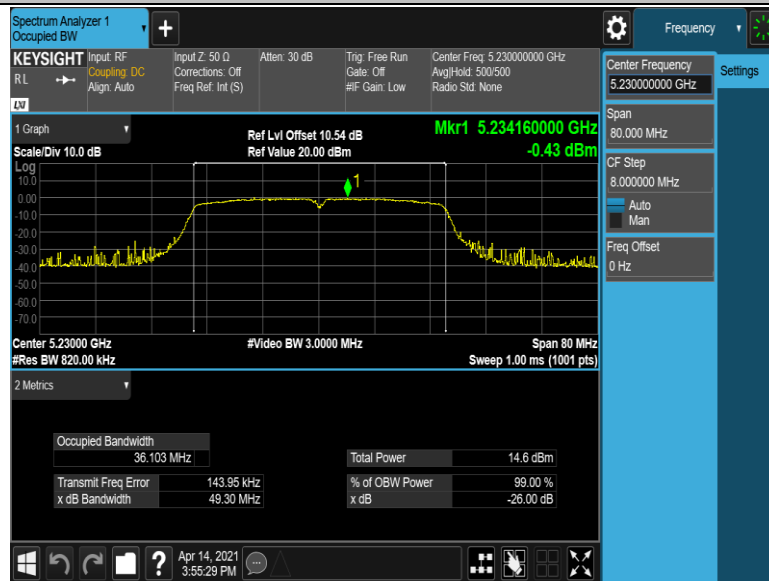
# 11N40MIMO\_Ant1\_5190



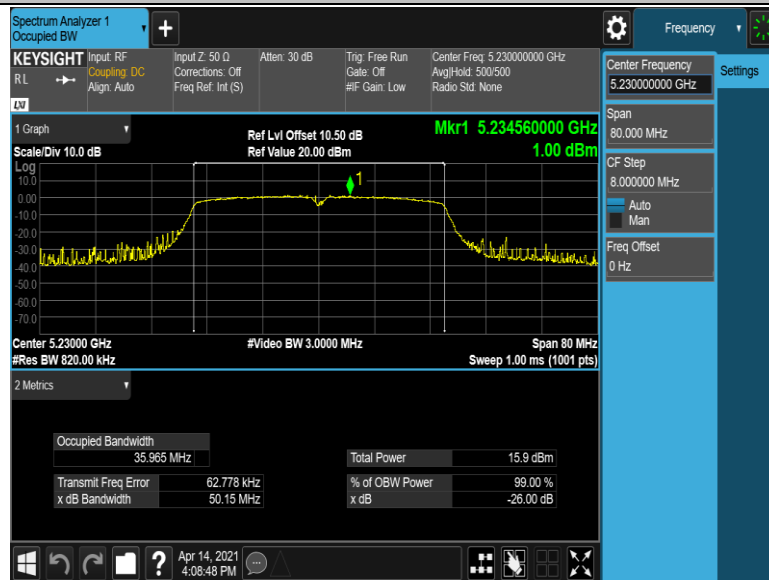
# 11N40MIMO\_Ant2\_5190



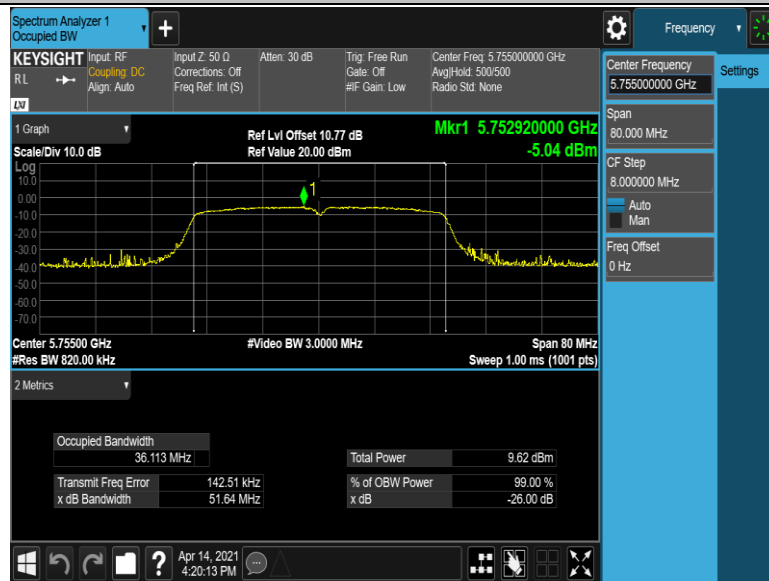
# 11N40MIMO\_Ant1\_5230



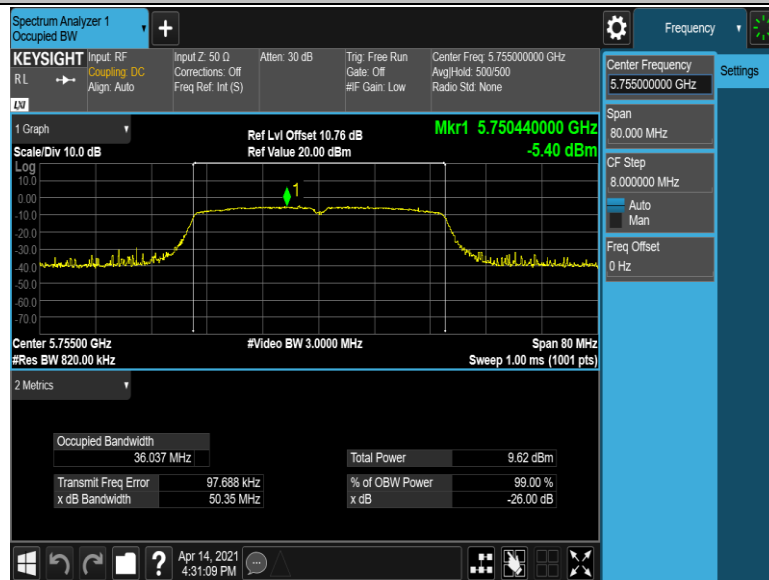
# 11N40MIMO\_Ant2\_5230



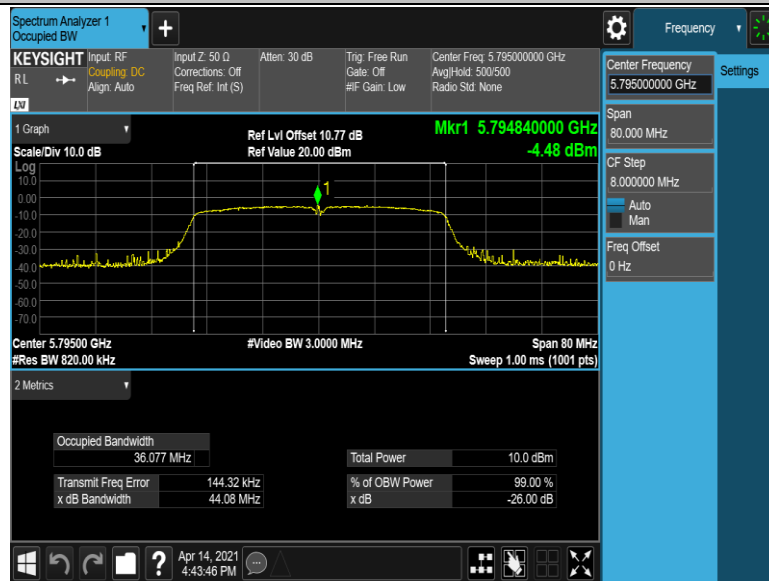
# 11N40MIMO\_Ant1\_5755



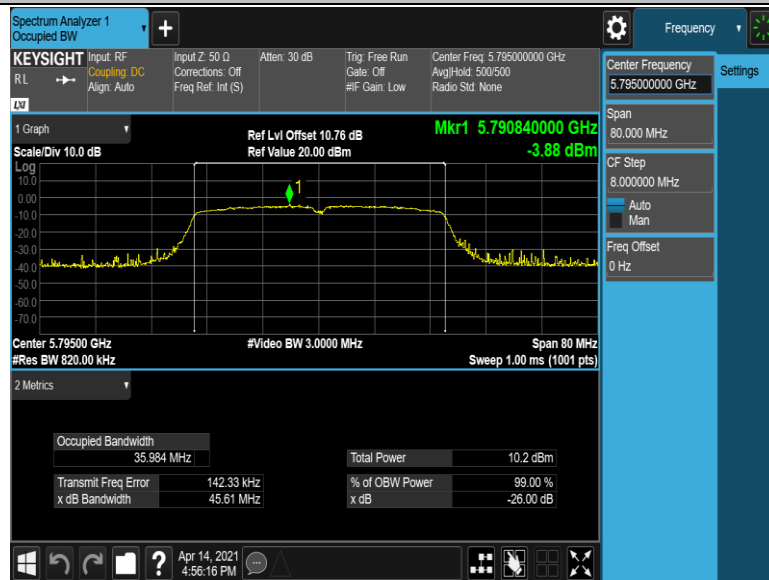
# 11N40MIMO\_Ant2\_5755



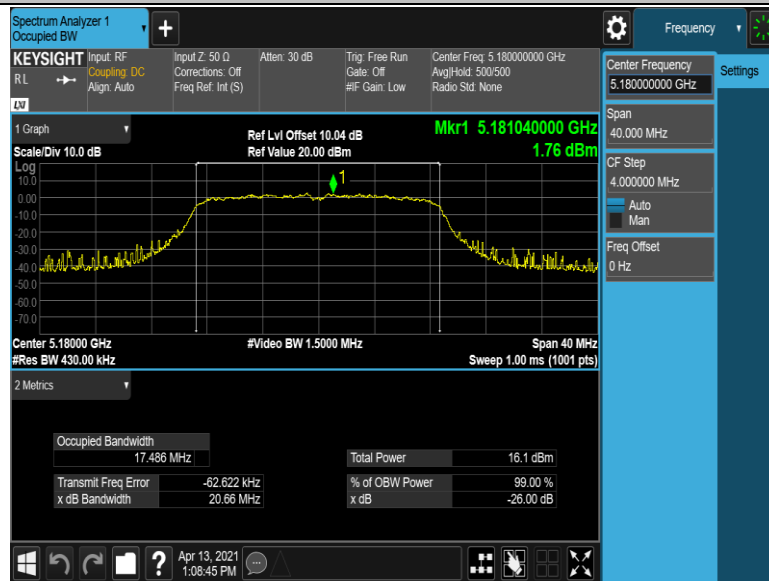
# 11N40MIMO\_Ant1\_5795



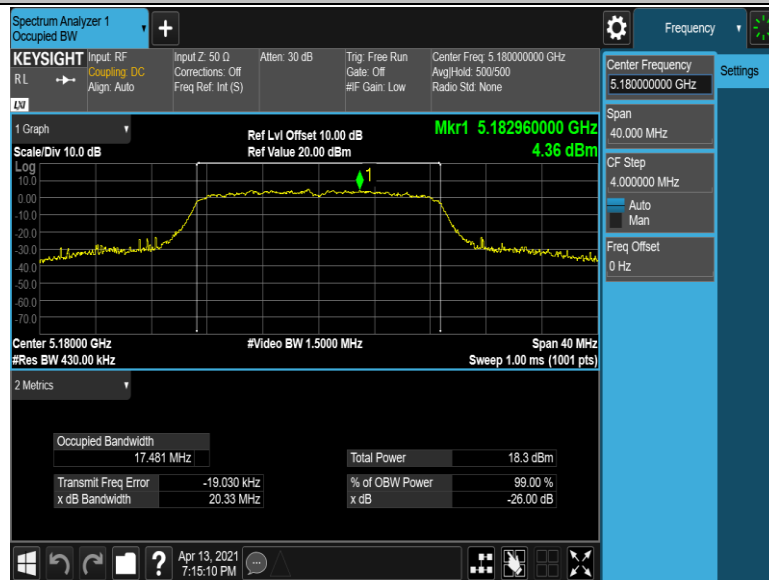
# 11N40MIMO\_Ant2\_5795



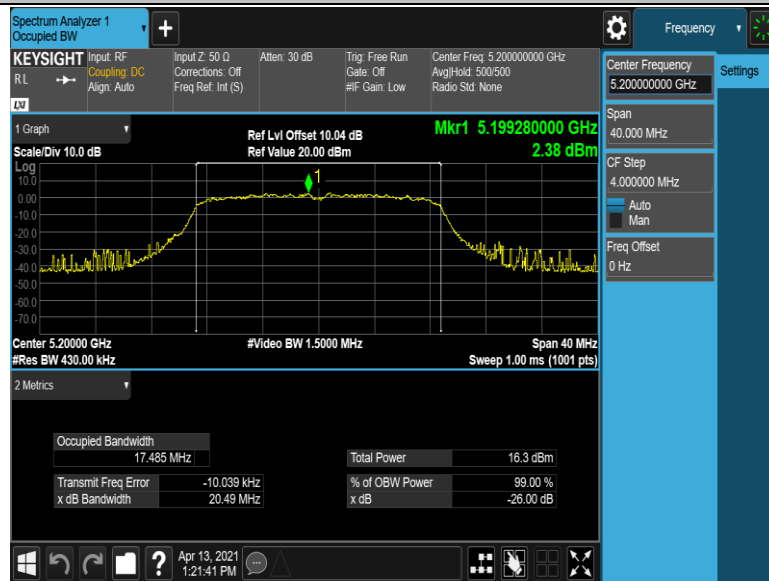
# 11AC20SISO\_Ant1\_5180



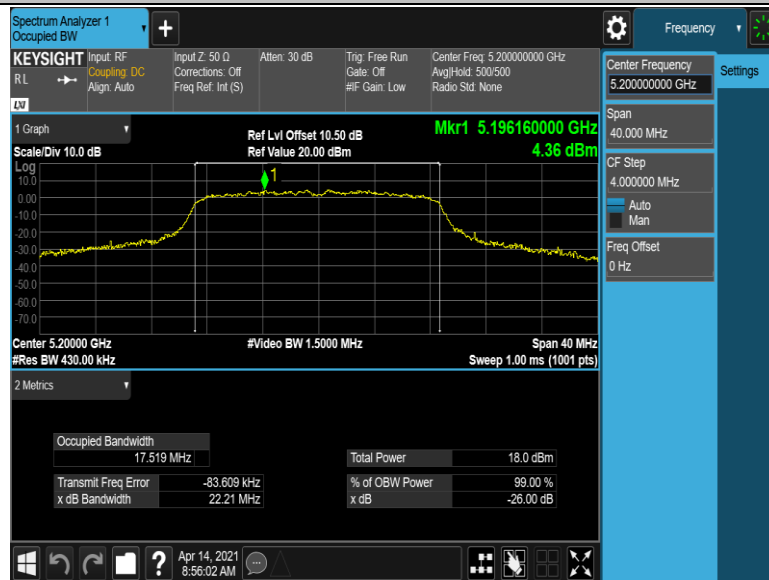
# 11AC20SISO\_Ant2\_5180



# 11AC20SISO\_Ant1\_5200

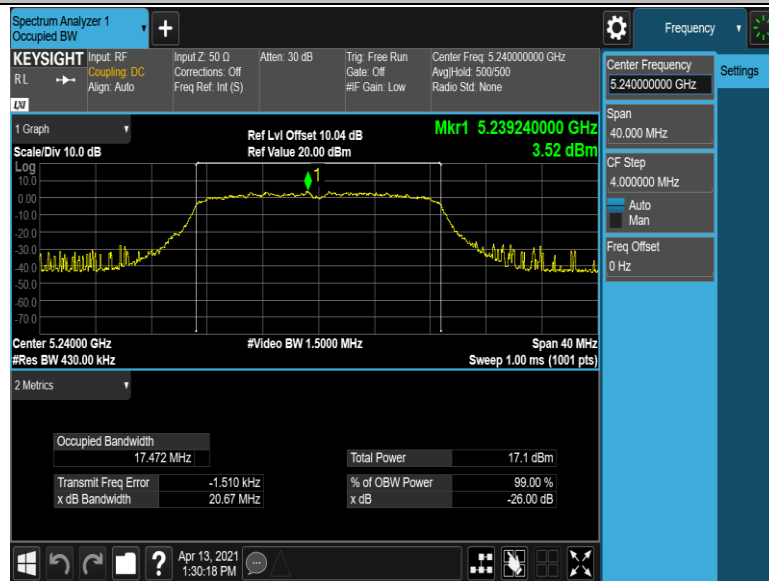


# 11AC20SISO\_Ant2\_5200

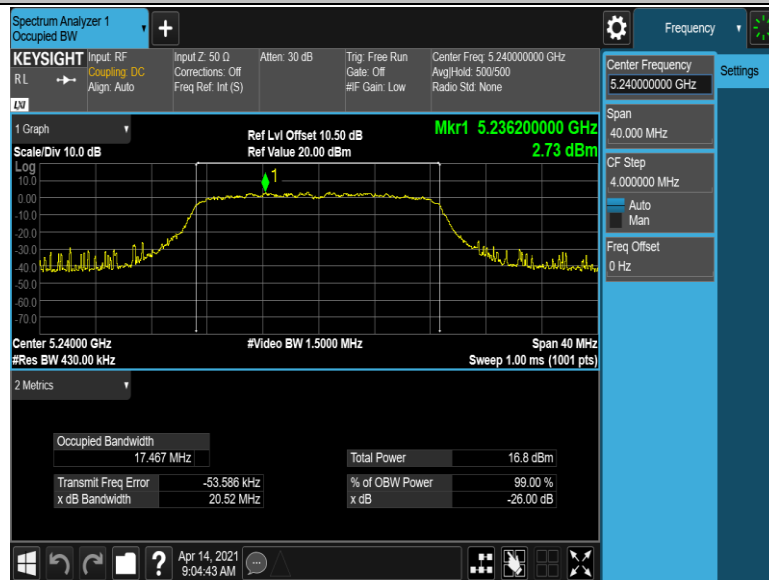




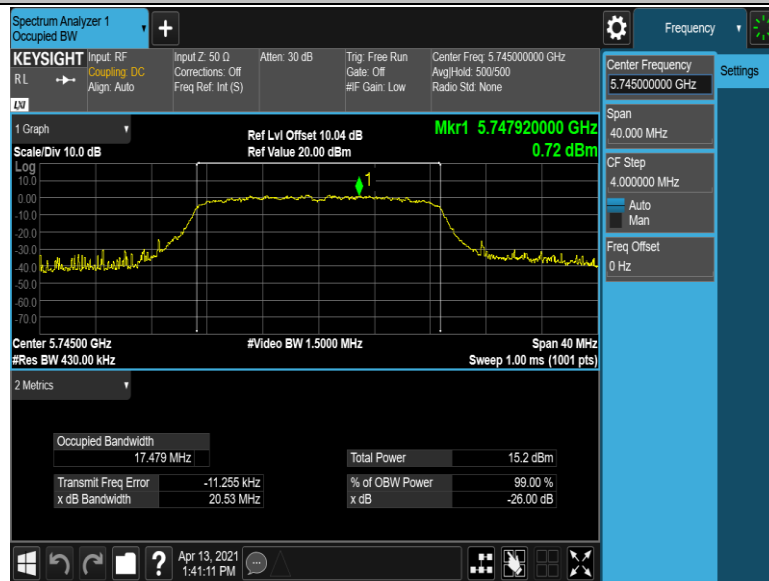
# 11AC20SISO\_Ant1\_5240



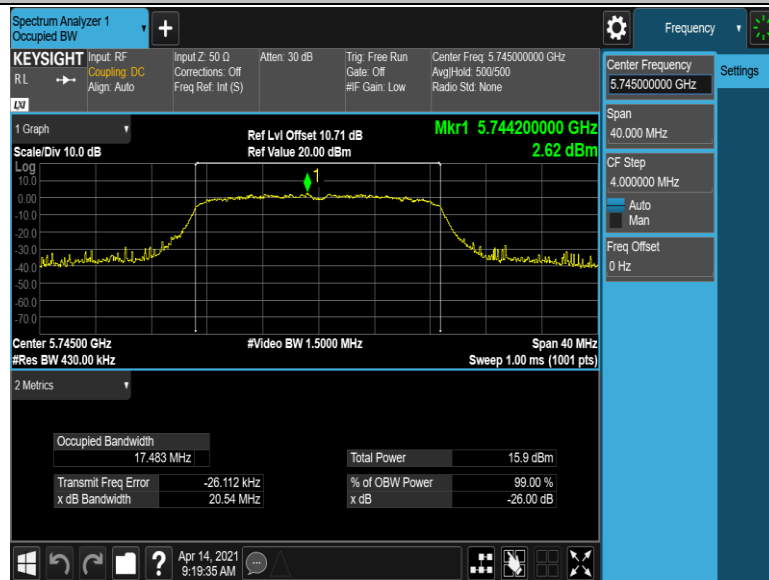
# 11AC20SISO\_Ant2\_5240



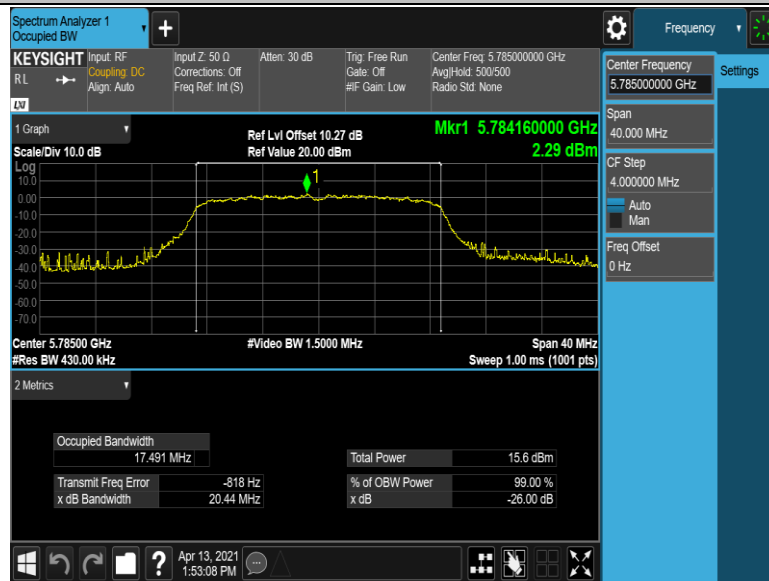
# 11AC20SISO\_Ant1\_5745



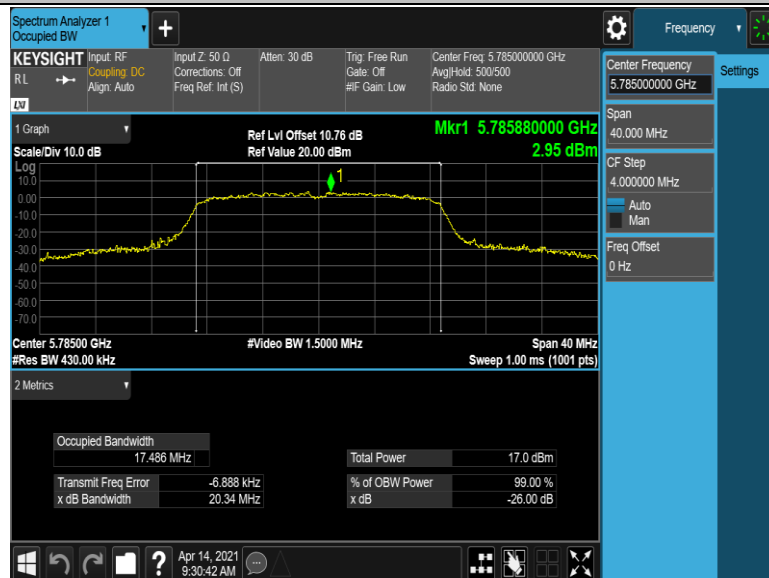
# 11AC20SISO\_Ant2\_5745



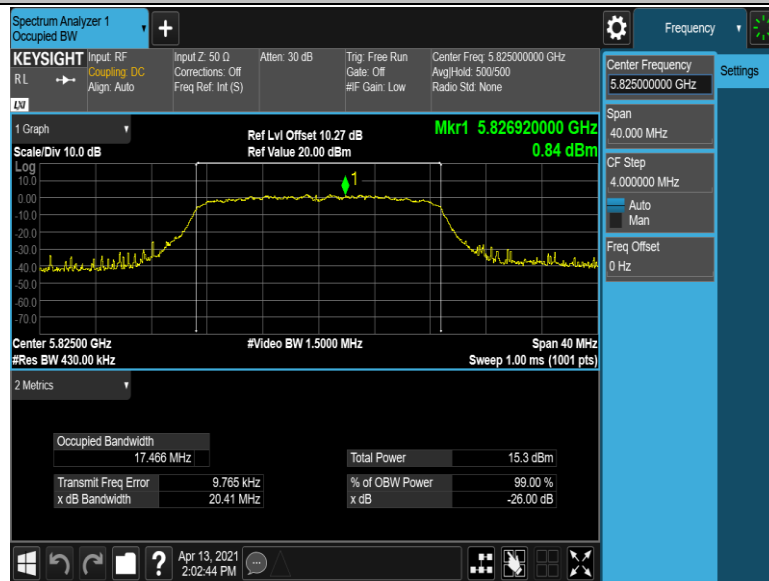
# 11AC20SISO\_Ant1\_5785



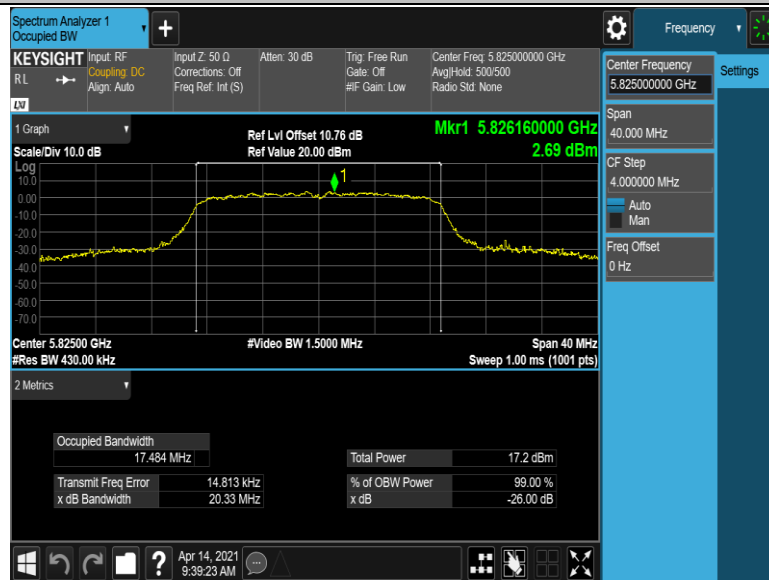
# 11AC20SISO\_Ant2\_5785



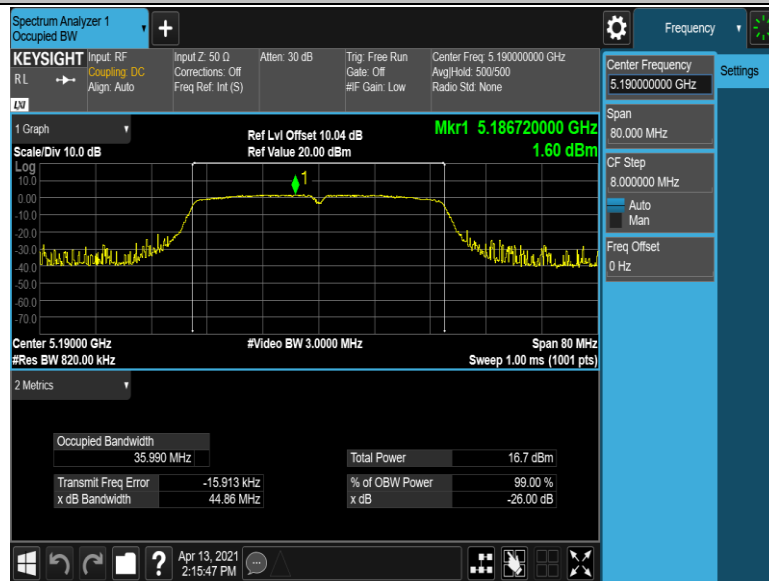
# 11AC20SISO\_Ant1\_5825



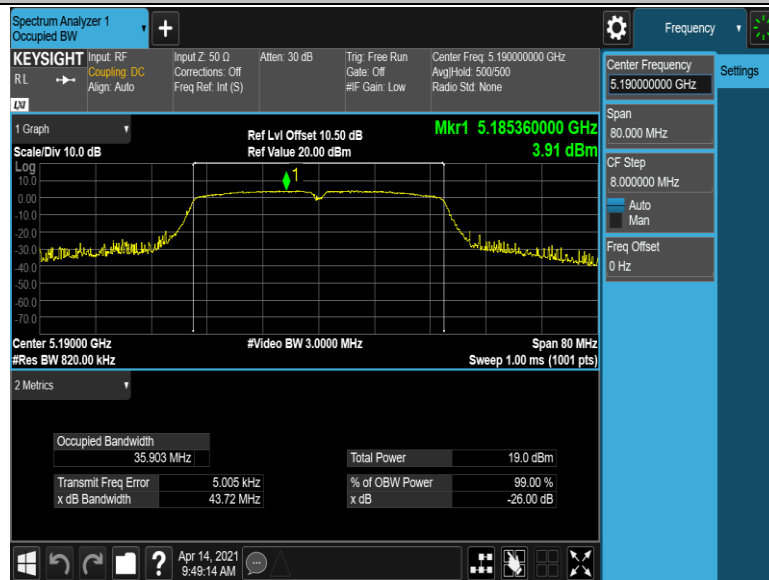
# 11AC20SISO\_Ant2\_5825



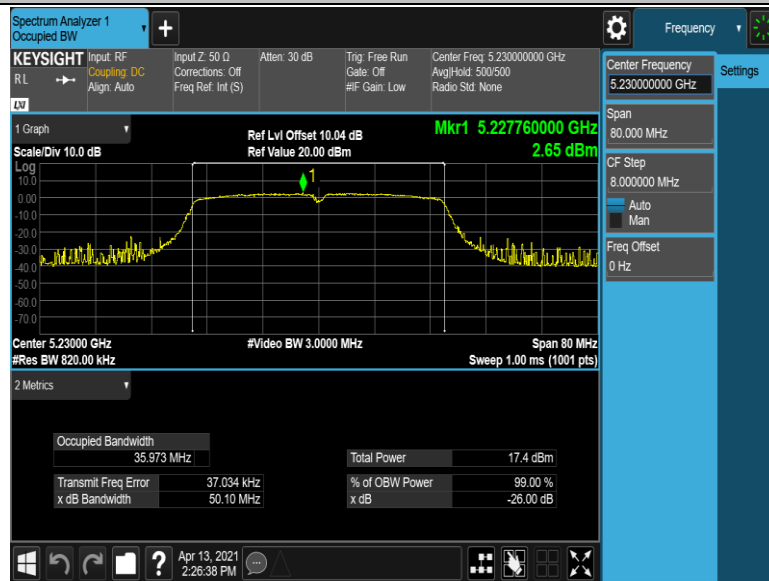
# 11AC40SISO\_Ant1\_5190



# 11AC40SISO\_Ant2\_5190



# 11AC40SISO\_Ant1\_5230



# 11AC40SISO\_Ant2\_5230

