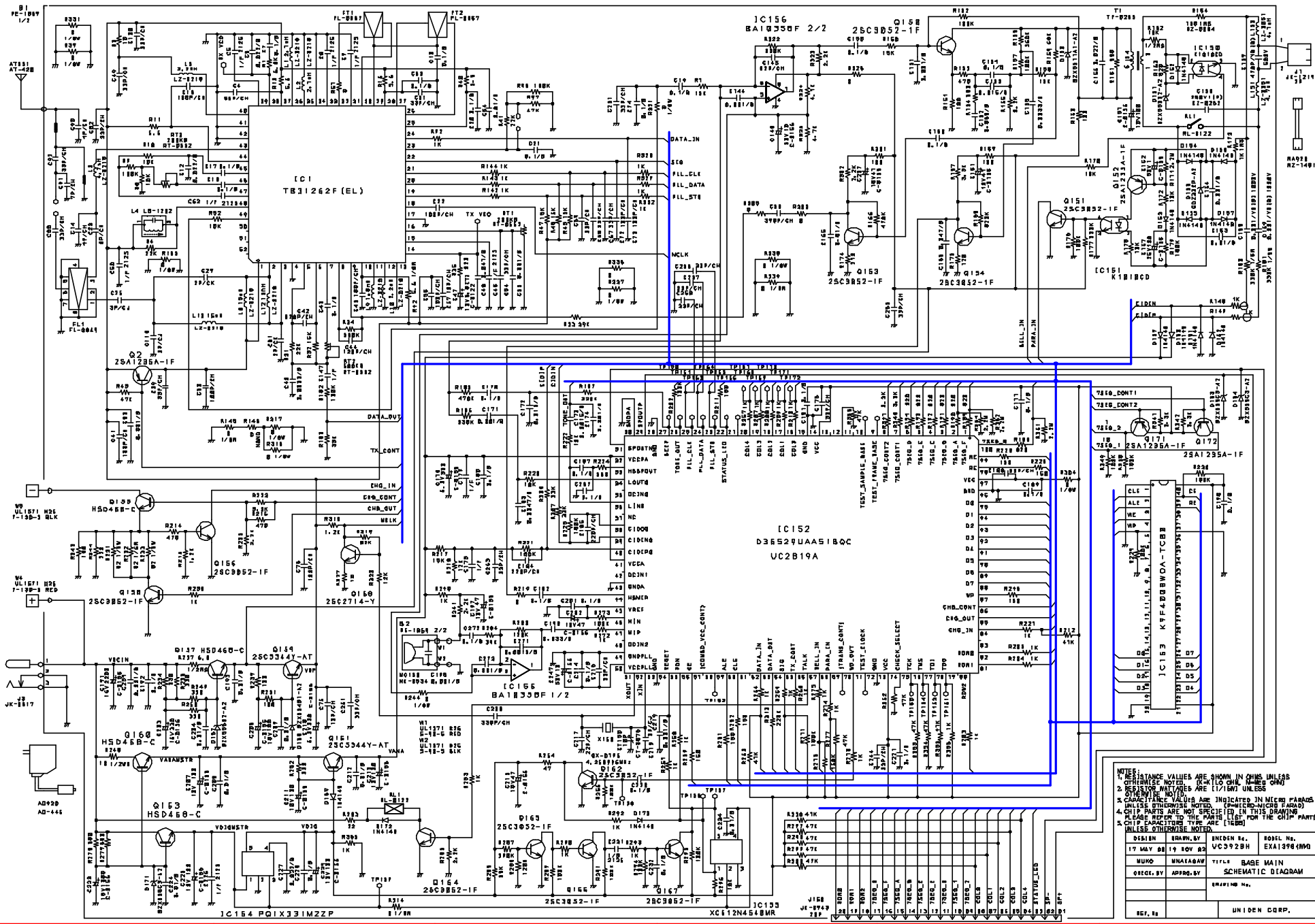




NOTES:

1. RESISTANCE VALUES ARE SHOWN IN OHMS UNLESS OTHERWISE NOTED. (K=1000 OHMS, M=1,000,000 OHMS)
2. RESISTOR TOLERANCES ARE 1% UNLESS OTHERWISE NOTED.
3. CAPACITANCE VALUES ARE INDICATED IN MICRO FARADS UNLESS OTHERWISE NOTED. (N=1000 PICO FARADS, P=100 PICO FARADS)
4. CHIP PARTS ARE NOT SPECIFIED IN THIS DRAWING UNLESS OTHERWISE NOTED. (PARTS LIST FOR THE CHIP PARTS, UNLESS OTHERWISE NOTED.)
5. CHIP CAPACITORS ARE 10% TOLERANCE UNLESS OTHERWISE NOTED.

DESIGN	DATE	UNIDEN	REV.	FILE	NAME
17 MAY 88	19 NOV 88	UC0922B	EXA1398 (M0)		
MUND	UNATAGAN	TITLE	BASE MAIN		
ORIG. BY	APPROV. BY		SCHEMATIC DIAGRAM		
DRAWING NO.					
REV. 00					
UNIDEN CORP.					

A

B

C

D

SP551
SP-424W551
W-004
UL1571 #26
5-60-5 BLK
W552
W-004
UL1571 #26
5-60-5 RED

B501 PE-1870

D502
LTL-16KPE-A

CHARGE/IN USE

COL3

COL2

COL1

COL0

7SEG_2

7SEG_1

7SEG_D

7SEG_E

7SEG_C

7SEG_G

7SEG_A

7SEG_F

7SEG_B

ROW2

ROW1

ROW0

J501
JK-0943
20PSET
RINGVOL
+VOL
-

MENU

ANS

GREET

MEMO

find

PLAY
/STOP

SKIP

REPEAT

DELETE

16 15 14 13 12 11 10 9

1 2 3 4 5 6 7 8

D501
TOD-4201BMRL-D12-C20

DESIGN	DRAWN. BY	UNIDEN No.	MODEL No.
10 OCT 02	19 MQV 02	UC392BH	EXA1398(WM)
MUKO	MNAKAGAW	TITLE BASE KEY PCB	
CHECK. BY	APPRO. BY	SCHEMATIC DIAGRAM	
		DRAWING No.	
REV. No		UNIDEN CORP.	

