## FD-LIDK 欧智通

# 6223C-PUD

Wi-Fi 2.4G 1T1R 11n + BT 4.2

**Module specification** 







## 6223C-PUDModule specification

Office: 6th Floor, Building U6, Junxiang U8, Hangcheng Avenue, Baoan District, Shenzhen city, Guangdong Province, China

Factory: No. 8 litong Road, Liuyang Economic and Technological Development Zone, Changsha, Hunan, China

Telephone: +86-755-2955-8186

Website: www.fn-link.com

客户承认:	公司
	标题
	签名
	日期
	欧智通





## 变更履历

版本	日期	描述	编辑	 核准
1.0	2018/10/18	New version	Lzm	Jacky
1.1	2018/12/18	Modify the telephone number	Lzm	Lxy
1.2	2018/12/25	Modify the office and TEL	Lzm	Lxy
1.3	2021/04/13	Proofread specification details	Lxy	Szs
1.3	2021/05/20	Supplementary Material list	Lxy	Szs
1.4	2021/07/12	Modifying Bluetooth Specifications	Lxy	QJP





#### **CONTENTS**

1 Overview	1
1.1 Introduction	1
1.2 Features	1
1.4 General Specification	. 2
1.5 Recommended Operating Rating	.3
%1.6 EEPROM Information	.3
2 Wi-Fi RF Specification	4
2.1 2.4GHz RF Specification	4
2.2 5GHz RF Specification	.5
3 Bluetooth Specification	.7
3.1 Bluetooth Specification	7
4 Pin Assignments	. 8
4.1 Pin Outline	8
4.2 Pin Definition	9
5 Dimensions	.12
5.1 Module Picture	12
5.2 Physical Outline	.12
5.3 Module Physical Dimensions	.13
5.4 Connector Specification	.13
5.5 Marking Description	14
6 Host Interface Timing Diagram	15
6.1 PCIe Bus during Power On Sequence	15
6.2 PCIe PERST# Timing Sequence	15
6.3 Power Off Sequence	16
7 Reference Design	. 17
8 Ordering Information	18
9 The Key Material List	. 18
10 Recommended Reflow Profile	.19
11 Package Information	20
11.1 Reel	.20
11.2 Carrier Tape Detail	.20
11.3 Packaging Detail	21
12. Moisture sensitivity.	21



#### 1. Overview

#### 1.1 Introduction

6223C-PUD is a highly integrated module that supports 802.11b/g/n 1T1R, PCIe port, USB Bluetooth port and wifi speed up to 150Mbps. Bluetooth supports V2.1+V4.2 systems.

#### 1.2 Features

12 x16 the LGA encapsulation
Anyway IEee802.11b /g/n+BT v2.1/v4.2 system, BLE4.0 dual-mode
Subscribed 2.4g 11n supports a maximum speed of 150Mbps
Anyway Wifi data interface supports PCIe V1.1
Slim Bluetooth data interface supports USB 2.0 and SUPPORTS PCM
Footed supports IEee802.11e/I, WAPI
Anyway WiFi supports P2P apps

BLOCK DIAGRAM:

Confidential



#### 1.2 General Specification

Module type	6223C-PUD
describe	Wi-Fi/Bluetooth module
size	L x W x H: 16 x 12 x 1.65 mm
Wi-fi interface	PCI-e
BT interface	USB / PCM
Working temperature	0°C to 70°C
Storage temperature	-40°C to 80°C
RoHS	All hardware components are fully compliant with EU RoHS directive

## 1.4 Recommended Operating Rating

-	<u> </u>	Min.	Тур.	Мах.	Unit
Working tempe	erature	0	25	70	deg.C
VDD		3.15	3.3	3.45	V
VDDIO		3.15	3.3	3.45	V
		VCC33 = 3.3V(Unit:mA)			
	Wi-Fi on Mode	150.15			
power dissipation	TX	300			
power dissipation	(2.4G HT40)	300			
	RX		1.5	:0	
	(2.4G HT40)	158			

#### **%1.5 EEPROM information**

#### wifi

Reg Domain	Channels 1-11 with active scan
Vendor ID	0x10EC
Device ID	0xD723
BT	
Vendor ID	0x0BDA
Product ID	0xD723



# 2 Wi-Fi Rf specification

## 2.1 2.4GHz Rf specification

features	describe				
\\\\ \\\\\ atandard	802.11b: DSS	S (CCK, DQPSK, DBP	SK)		
WLAN standard	802.11g/n: OF	DM (64QAM, 16QAM,	QPS	K, BPSK)	
	802.11b/g/n(H	T20): 2412MHz to 246	2MHz	Z	
Frequency range	802.11n(HT40	): 2422MHz to 2452M	Hz		
Channel number	802.11b/g/n(H	T20): 11			
Chamilei number	802.11n(HT40	):7			
The output power	11Mbps : 12 d	Bm ± 1.5 dB @ EVM	-9dE	3	
	54Mbps : 12 d	Bm ± 1.5 dB @ EVM	-25d	IB	
	MCS7 : 12 dl	Bm ± 1.5 dB @ EVM	-28d	-28dB	
spectrum	符合 IEEE 标》	隹			
The frequency error	±20 ppm				
The test item	Typical Value			Typical Value	
	- 1Mbps	PER @ -92 dBm		≤-83	
Receive Sensitivity	- 2Mbps	PER @ -90 dBm		≤-80	
(11b,20MHz)@8%PER	- 5.5Mbps	PER @ -87 dBm		≤-79	
	- 11Mbps	PER @ -85 dBm		≤-76	
	- 6Mbps	PER @ -89 dBm		≤-85	
	- 9Mbps	PER @ -88 dBm		≤-84	
Receive Sensitivity	- 12Mbps	PER @ -87 dBm		≤-82	
(11g,20MHz)@10%	- 18Mbps	PER @ -84 dBm		≤-80	
PER	- 24Mbps	PER @ -81 dBm		≤-77	
	- 36Mbps	PER @ -78 dBm		≤-73	
	- 48Mbps	PER @ -73 dBm		≤-69	
	- 54Mbps	PER @ -71 dBm		≤-68	
	- MCS=0	PER @ -89 dBm		≤-85	
	- MCS=1	PER @ -86 dBm		≤-82	
Descive Consisting	- MCS=2	PER @ -84 dBm		≤-80	
Receive Sensitivity	- MCS=3	PER @ -80 dBm		≤-77	
(11n,20MHz)@10%	- MCS=4	PER @ -77 dBm		≤-73	
PER	- MCS=5	PER @ -72 dBm		≤-69	
	- MCS=6	PER @ -71 dBm		≤-68	
	- MCS=7	PER @ -69 dBm		≤-67	



#### 6223C-PUD

	- MCS=0	PER @ -88 dBm	≤-82
	- MCS=1	PER @ -85 dBm	≤-79
Deceive Consitivity	- MCS=2	PER @ -83 dBm	≤-77
Receive Sensitivity (11n,20MHz)@10%	- MCS=3	PER @ -79 dBm	≤-74
PER	- MCS=4	PER @ -76 dBm	≤-70
FER	- MCS=5	PER @ -71 dBm	≤-66
	- MCS=6	PER @ -69 dBm	≤-65
	- MCS=7	PER @ -67 dBm	≤-64
Maximum Input Level	802.11b : -10	) dBm	
	802.11g/n : -	20 dBm	
Antenna Reference	2 dBi peak g	ain	

## 3 Bluetooth Specification

## 3.1 Bluetooth Specification

Feature	Description		
General Specification			
standard	BT v2.1+v4.2/BI	LE4.0.	
Data interface	USB2.0		
Antenna reference	2 dBi peak gair	1	
Frequency range	2402MHz-2480I	MHz	
Channel number	79 channels		
Modulation method	GFSK, π/4-DQF	PSK, 8DPSK	
Rf specifications			
	Minimum	typical	maximum
Output power (Class 1)	2 dBm	5 dBm	8 dBm
Sensitivity@ BER=0.1%		-92 dBm	
for GFSK (1Mbps)		-92 UDIII	
Sensitivity@ BER=0.01%		96 dPm	
for π/4-DQPSK (2Mbps)		-86 dBm	
Sensitivity@ BER=0.01%		-85 dBm	
for 8DPSK (3Mbps)		-oo ubiii	



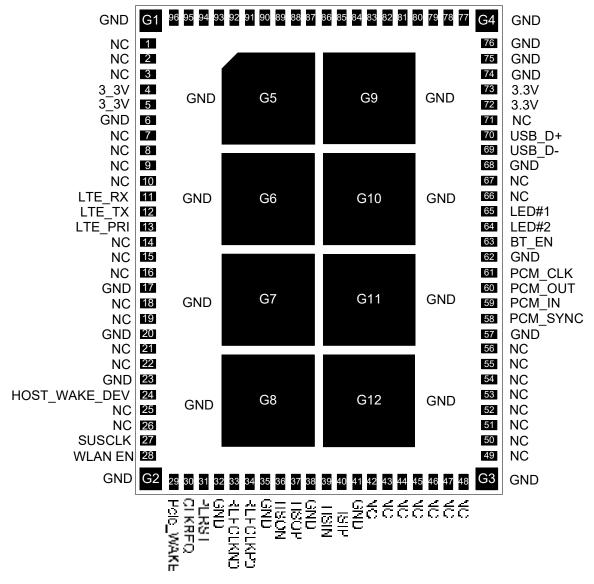


	GFSK (1Mbps):-20dBm
Maximum input level	π/4-DQPSK (2Mbps) :-20dBm
	8DPSK (3Mbps) :-20dBm

#### 4 Pin Assignments

#### 4.1 Pin Outline

#### < TOP VIEW >



#### 4.2 Pin Definition

Pin Name
----------





5         3_3V         P         power input           6         GND         —         grounding           7         NC         —         No connect           8         NC         —         No connect           9         NC         —         No connect           10         NC         —         No connect           11         LTE_RX         I/O         LTE coexist signal         V           12         LTE_TX         I/O         LTE coexistence signal         V           LTE coexistence signal           Share with GPIO12	3.3V 3.3V 'DDIO 'DDIO
3	3.3V 'DDIO 'DDIO
4         3_3V         P         power input           5         3_3V         P         power input           6         GND         —         grounding           7         NC         —         No connect           8         NC         —         No connect           9         NC         —         No connect           10         NC         —         No connect           11         LTE_RX         I/O         LTE coexist signal         V           12         LTE_TX         I/O         LTE coexistence signal         V           Share with GPIO12         V         O: internal NV memory select         V           1: External EEPROM select         No connect         No connect           14         NC         —         No connect           15         NC         —         No connect           16         NC         —         No connect           17         GND         —         grounding           18         NC         —         No connect	3.3V 'DDIO 'DDIO
5         3_3V         P         power input           6         GND         —         grounding           7         NC         —         No connect           8         NC         —         No connect           9         NC         —         No connect           10         NC         —         No connect           11         LTE_RX         I/O         LTE coexist signal         V           12         LTE_TX         I/O         LTE coexistence signal         V           Share with GPIO12         V         O: internal NV memory select         V           13         LTE_PRI         I/O         No connect         V           14         NC         —         No connect           15         NC         —         No connect           16         NC         —         No connect           17         GND         —         grounding           18         NC         —         No connect	3.3V 'DDIO 'DDIO
6         GND         —         grounding           7         NC         —         No connect           8         NC         —         No connect           9         NC         —         No connect           10         NC         —         No connect           11         LTE_RX         I/O         LTE coexist signal         V           12         LTE_TX         I/O         LTE coexistence signal         V           Share with GPIO12         V         O: internal NV memory select         V           1: External EEPROM select         No connect         No connect           15         NC         —         No connect           16         NC         —         No connect           17         GND         —         grounding           18         NC         —         No connect	DDIO DDIO
7	'DDIO
8         NC         —         No connect           9         NC         —         No connect           10         NC         —         No connect           11         LTE_RX         I/O         LTE coexist signal         V           12         LTE_TX         I/O         LTE coexistence signal         V           Share with GPIO12         V         0: internal NV memory select         V           12         LTE_PRI         I/O         No connect         V           NC         —         No connect         No connect         V           14         NC         —         No connect         No connect<	'DDIO
9         NC         —         No connect           10         NC         —         No connect           11         LTE_RX         I/O         LTE coexist signal         V           12         LTE_TX         I/O         LTE coexistence signal         V           Share with GPIO12         V         0: internal NV memory select         V           13         LTE_PRI         I/O         No connect         V           14         NC         —         No connect         No connect           15         NC         —         No connect         No connect           16         NC         —         grounding         No connect           17         GND         —         grounding         No connect           18         NC         —         No connect	'DDIO
10	'DDIO
11         LTE_RX         I/O         LTE coexist signal         V           12         LTE_TX         I/O         LTE coexistence signal         V           LTE_coexistence signal         Share with GPIO12         V           0: internal NV memory select         1: External EEPROM select         V           14         NC         No connect         No connect           15         NC         No connect         No connect           16         NC         No connect         No connect           17         GND         grounding           18         NC         No connect	'DDIO
12	'DDIO
LTE_PRI	
13         LTE_PRI         I/O         Share with GPIO12	יחחוס
13         LTE_PRI         I/O         0: internal NV memory select         V           14         NC         —         No connect           15         NC         —         No connect           16         NC         —         No connect           17         GND         —         grounding           18         NC         —         No connect	יחחי
14         NC         —         No connect           15         NC         —         No connect           16         NC         —         No connect           17         GND         —         grounding           18         NC         —         No connect	
14         NC         —         No connect           15         NC         —         No connect           16         NC         —         No connect           17         GND         —         grounding           18         NC         —         No connect	טוטט
15         NC         —         No connect           16         NC         —         No connect           17         GND         —         grounding           18         NC         —         No connect	
16         NC         —         No connect           17         GND         —         grounding           18         NC         —         No connect	
17         GND         —         grounding           18         NC         —         No connect	
18 NC - No connect	
10 NC - No connect	
19 NC — No connect	
<b>20</b> GND — grounding	
21 NC — No connect	
22 NC — No connect	
23 GND — grounding	
24 HOST_WAK   I/O   Host wake up DEV, GPIO13   V	'DDIO
25 NC — No connect	
26 NC — No connect	
External RTC input(32.768kHz) Share with GPIO15	
27 SUSCLK I 0: USB1.2 or 2.0 1: otherwise	
28 WLAN EN I WLAN on,PCle on (default)	'DDIO





			0: WLAN off, PCIe off				
			Power management event: open drain,active low.				
29	PCIe WAKE	O/D	Used to reactivate the PCle slot's main power rails	VDDIO			
23	I OIG_WAINE		and reference clocks.				
			Share with BT wake up host function via sideband signals.				
30	CLKREQ	I/O/D	PCI-e reference clock request signal.	VDDIO			
	OLIVILLA	1,0,0	Can be shared with BTdevice clock request function	VDDIO			
31	PERST	I	PCI-e reset signal:active low.	VDDIO			
32	GND	_	grounding				
33	REFCLKN0	I	PCI-E CLK Difference -				
34	REFCLKP0	I	PCI-E CLK Difference +				
35	GND		grounding				
36	HSON	0	PCI-E Data Out Difference -				
37	HSOP	0	PCI-E Data Out Difference +				
38	GND		grounding				
39	HSIN	I	PCI-E Data IN Difference -				
40	HSIP	I	PCI-E Data IN Difference +				
41	GND	_	grounding				
42	NC		No connect				
43	NC	_	No connect				
44	NC		No connect				
45	NC		No connect				
46	NC	_	No connect				
47	NC	_	No connect				
48	NC		No connect				
49	NC	_	No connect				
50	NC	_	No connect				
51	NC		No connect				
52	NC	_	No connect				
53	NC	_	No connect				
54	NC		No connect				
55	NC	_	No connect				
56	NC	_	No connect				
57	GND	_	grounding				
58	PCM_SYNC	I/O	PCM sync signal	VDDIO			
59	PCM_IN	I	PCM data input	VDDIO			



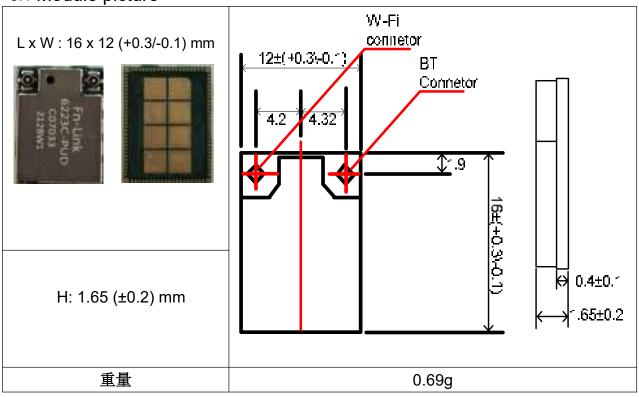
6223C-PUD

60	PCM_OUT	0	PCM Data output	VDDIO	
61	PCM_CLK	I/O	PCM clock	VDDIO	
62	GND	_	grounding		
63	BT_EN	1:BT on, USB on (default)	1:BT on, USB on (default)	VDDIO	
03	DI_LN	ı	0: BT off, USB off	VDDIO	
64	LED#2	0	BT link LED,active low.	VDDIO	
65	LED#1	0	WLAN link LED,active low.	VDDIO	
66	NC	_	No connect		
67	NC	_	No connect		
68	GND	_	grounding		
69	USB_D-	I/O	USB differential -		
70	USB_D+	I/O	USB differential +		
71	NC	_	No connect		
72	3.3V	Р	Main power voltage source input 3.3V	3.3V	
73	3.3V	Р	Main power voltage source input 3.3V	3.3V	
74~76	GND	_	grounding		
G1-G12	GND	_	grounding		

P:POWER I:INPUT O:OUTPUT VDDIO:3.3V

#### 5. Dimensions

#### 5.1 Module picture

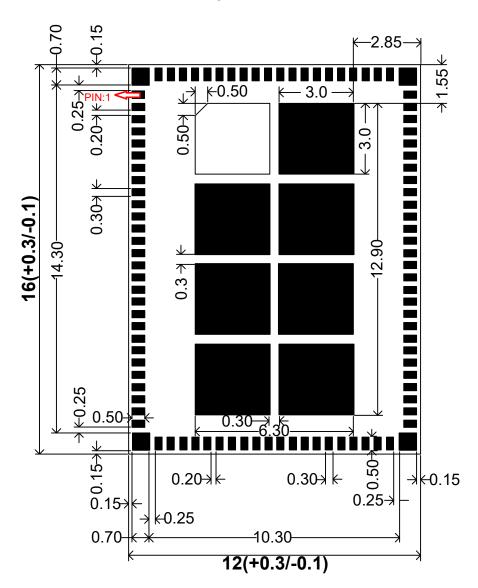




## 5.2 Physical Outline

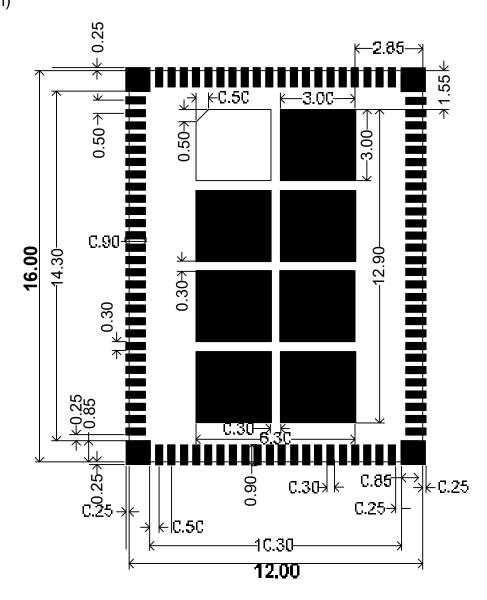
(Unit: mm)

#### < TOP VIEW >



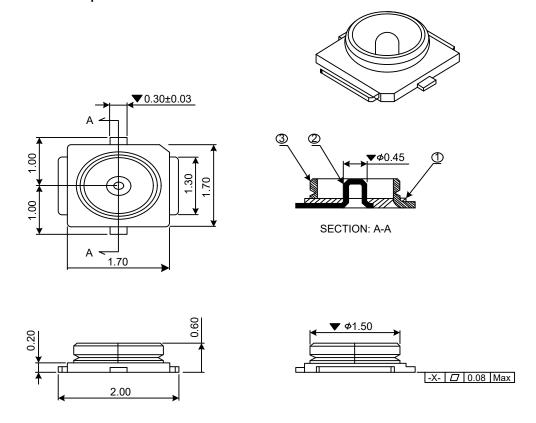


# 5.3 Module Physical Dimensions (Unit: mm)

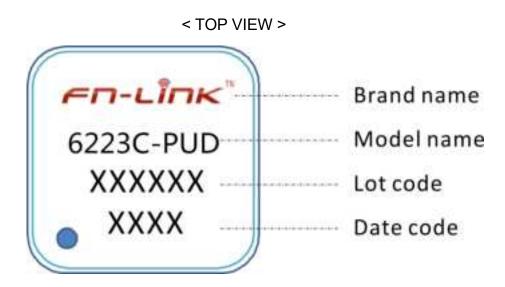




#### 5.4 Connector Specification



#### 5.5 Marking Description



## 6. Host Interface Timing Diagram

## 6.1 Bus during Power On Sequence

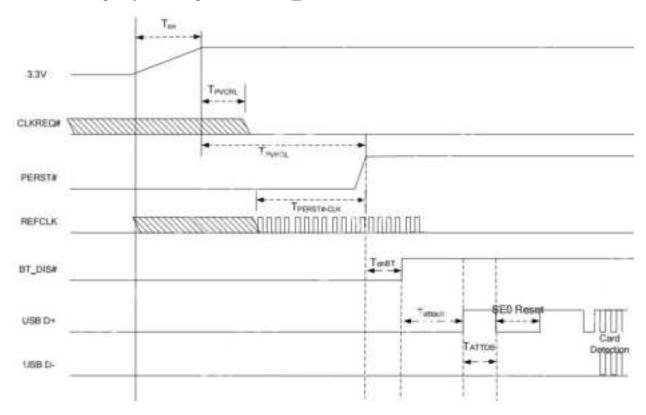


The boot timing diagram is only for cold boot;

If system goes to S3, S4 or reboot without power off 3v3, BT\_DIS is not allowed to be asserted;

BT DIS low duty require at least 5ms;

Do not driving any USB signal when BT\_DIS is low;



T<sub>on</sub>: the main power ramp up duration

T<sub>PRVRL</sub>: power valid to CLKREQ# output active

T<sub>PVPGL</sub>: power valid to PERST# input inactve

T<sub>PERST#-CLK</sub>: reference clock stable before PERST# inactive

T<sub>onBT</sub>: the time interval to turn on BT after PERST# de-asserted

T<sub>attach</sub>: the interval between BT DIS to USB attached state

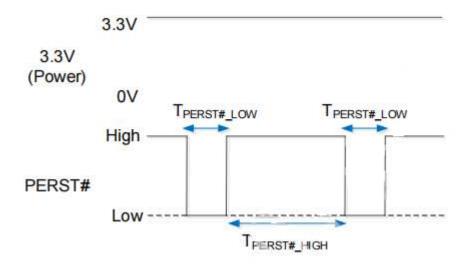
T<sub>ATTDB</sub>: the debounce interval with a minimal duration of 100ms that provide by USB system software

T<sub>SE0 Reset</sub>: USB host send SE0 Reset duration



Symbol	Unit	Min	Typical	Max
Ton	ms	0.2	1.5	5
$T_{PVCRL}$	us	-		100
T <sub>PVPGL</sub>	ms	Implementation specific; recommended 50 ms		-
TPERSTW-CLK	us	100		
$T_{coBT}$	ms	10	50	-
Tatach	ms	0.5	0.7	5
TATION	ms	100	-	-
T <sub>SE0 Reset</sub>	ms	10		20

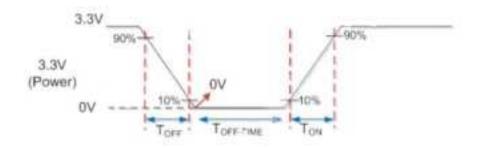
## **6.2 PCIe PERST# Timing Sequence**



	Min	Typical	Max	Unit	Description
TPERSTE_LOW	6	10	X	ms	PERST# low duration
TPERST#_JHGH	400	500	X	ms	PERST# high duration

## **6.3 Power Off Sequence**





Symbol	Unit	Min	Typical	Max	
T <sub>OFF</sub>	ms	5	20	50	
TOFF-TIME	ms	500	-		
Ton	ms	0.5	1:5	5	

# 7. Reference Design





Schematics

Confidential

8. Ordering Information



Part No.	Description
FC6222CDLID W4	RTL8723DE, b/g/n, Wi-Fi+BLE4.2, 1T1R, 12X16mm,
FG6223CPUD-W1	PCIE+USB, PCB V3.0,双天线座

# 9. **The Key Material List**

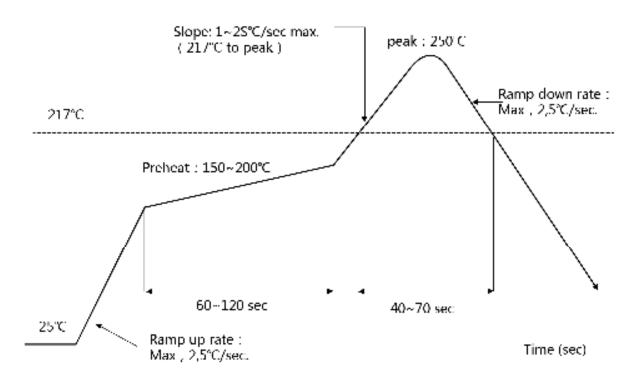
crystal	1612/2016,40MHZ,15pF,10ppm	ECEC,HOSONIC,TKD,JWT
PCB	6223C-PUD V3.0,green, 4L, 12X16X0.4mm	XY-PCB,KX-PCB,SL-PCB,S unlord,Brain-power PCB
Main chip	RTL8723DE-VB-CG QFN56P	Realtek
Shielding cover	6223C-PUD-V3.0 屏 蔽 盖 尺 寸 11.35x15.35x1.25mm 洋白铜,无定位脚,带 绝缘层	信太,精力通
The antenna seat	4 generation antenna base	佳沃,创迪尔



#### 10. Recommended Reflow Profile

Referred to IPC/JEDEC standard.

Peak Temperature : <250°C Number of Times : □2 times

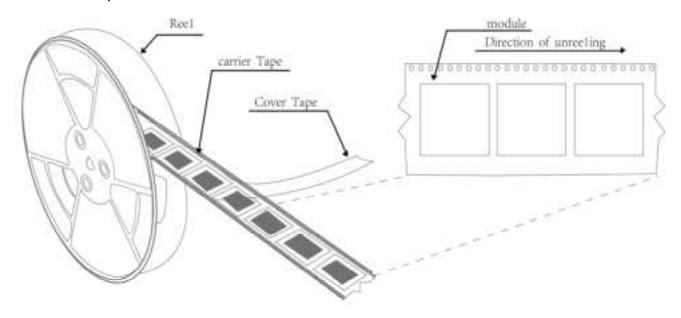




# 11 Package Information

#### 11.1 Reel

A roll of 2000pcs

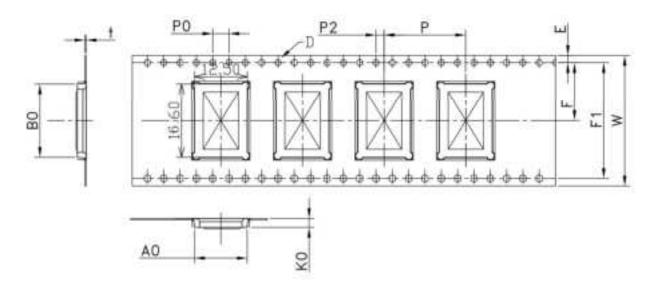


11.2 Carrier Tape Detail

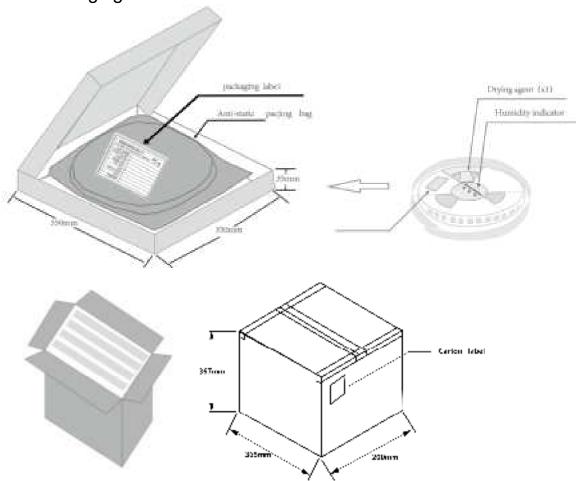


6223C-PUD

ITEM	W	A0	B0	D	E	F	F1	K0	P0	P2	P	T
DIM	32	12, 50	16. 60	1.5	1.75	14. 20	28.4	2, 15	4.0	2.0	20.0	0.30
TOLE	+0.3	±0.18	±0.18	+0.1 -0.0	±0.1	±0.15	±0.10	±0.10	±0.1	$\pm 0.15$	±0.1	±0.05



## 11.3 Packaging Detail



## 12. Moisture sensitivity



The Modules is a Moisture Sensitive Device level 3, in according with standard IPC/JEDEC

J-STD-020, take care

all the relatives requirements for using this kind of components.

Moreover, the customer has to take care of the following conditions:

- a) Calculated shelf life in sealed bag: 12 months at  $<40^{\circ}\,$  C and <90% relative humidity (RH).
- b) Environmental condition during the production: 30° C / 60% RH according to IPC/JEDEC J-STD-033A paragraph 5.
- c) The maximum time between the opening of the sealed bag and the reflow process must

be 168 hours if condition

- b) "IPC/JEDEC J-STD-033A paragraph 5.2" is respected
- d) Baking is required if conditions b) or c) are not respected
- e) Baking is required if the humidity indicator inside the bag indicates 10% RH or more

**FCC Statement** 

FCC standards: FCC CFR Title 47 Part 15 Subpart C Section 15.247

Integral FPC Antenna, Antenna gain 2dBi

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Any Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

Note: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

—Reorient or relocate the receiving antenn	
	2
100110111 of 10100ate the 1000111111111111111111111111111111111	а.

- —Increase the separation between the equipment and receiver.
- —Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- —Consult the dealer or an experienced radio/TV technician for help.

#### FCC Radiation Exposure Statement

The modular can be installed or integrated in mobile or fix devices only. This modular cannot be installed in any portable device. This modular complies with FCC RF radiation exposure limits set forth for an uncontrolled environment. This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter. This modular must be installed and operated with a minimum distance of 20 cm between the radiator and user body.

If the FCC identification number is not visible when the module is installed inside another device, then the outside of the device into which the module is installed must also display a label referring to the enclosed module. This exterior label can use wording such as the following: "Contains Transmitter Module FCC ID: 2AATL-6223C-PUD Or Contains FCC ID: 2AATL-6223C-PUD"

When the module is installed inside another device, the user manual of the host must contain below warning statements;

- 1. This device complies with Part 15 of the FCC Rules. Operation is subject to the following two conditions:
- (1) This device may not cause harmful interference;
- (2) This device must accept any interference received, including interference that may cause undesired operation.

Note: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio

frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications.

However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- —Reorient or relocate the receiving antenna.
- —Increase the separation between the equipment and receiver.
- —Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- —Consult the dealer or an experienced radio/TV technician for help.
- 2. Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

The devices must be installed and used in strict accordance with the manufacturer's instructions as described in the user documentation that comes with the product.

Any company of the host device which install this modular with modular approval should perform the test of radiated & conducted emission and spurious emission, etc. according to FCC part 15C: 15.247 and 15.209 & 15.207,15B Class B requirement, Only if the test result comply with FCC part 15C: 15.247 and 15.209

& 15.207,15B Class B requirement, then the host can be sold legally.