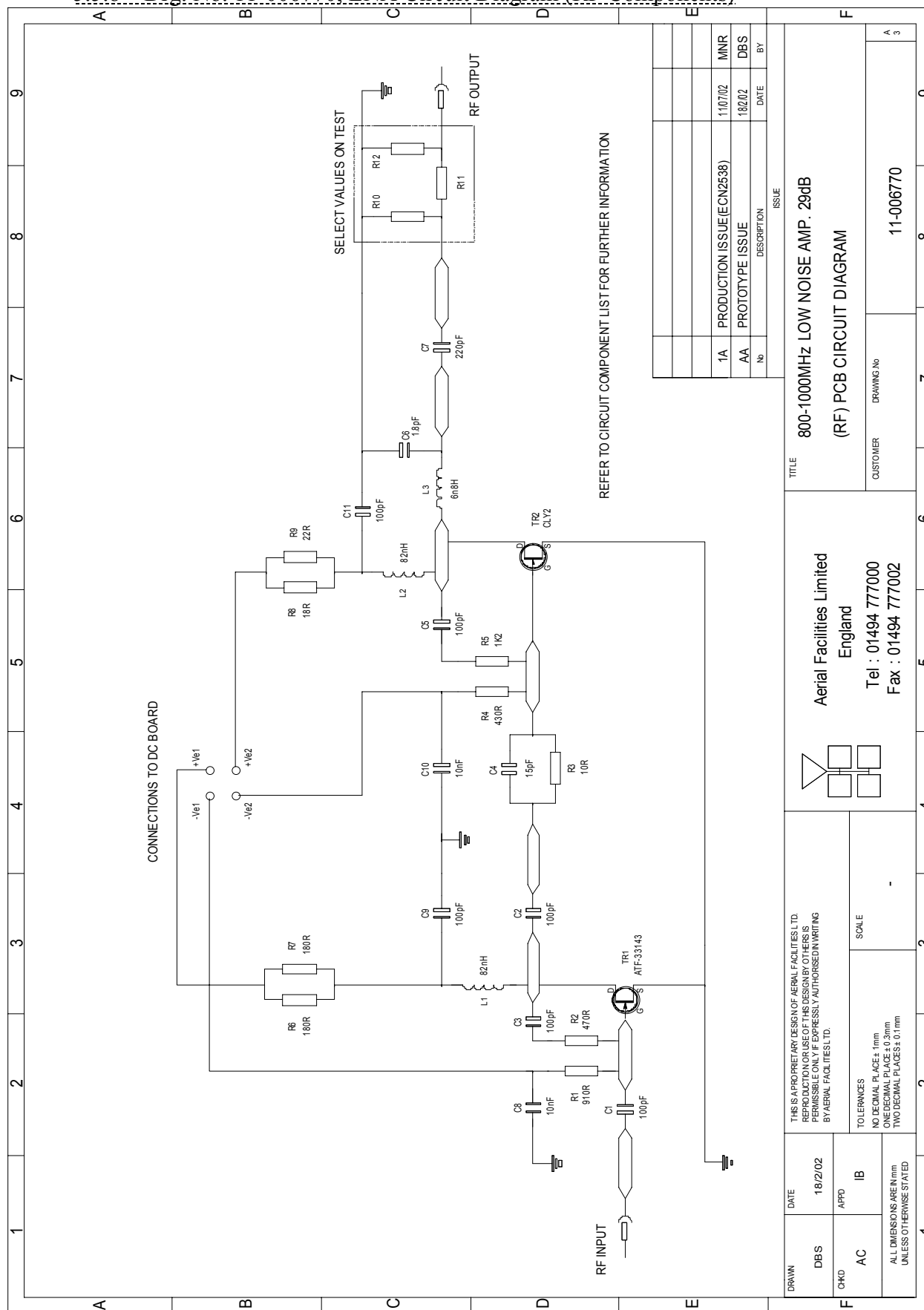
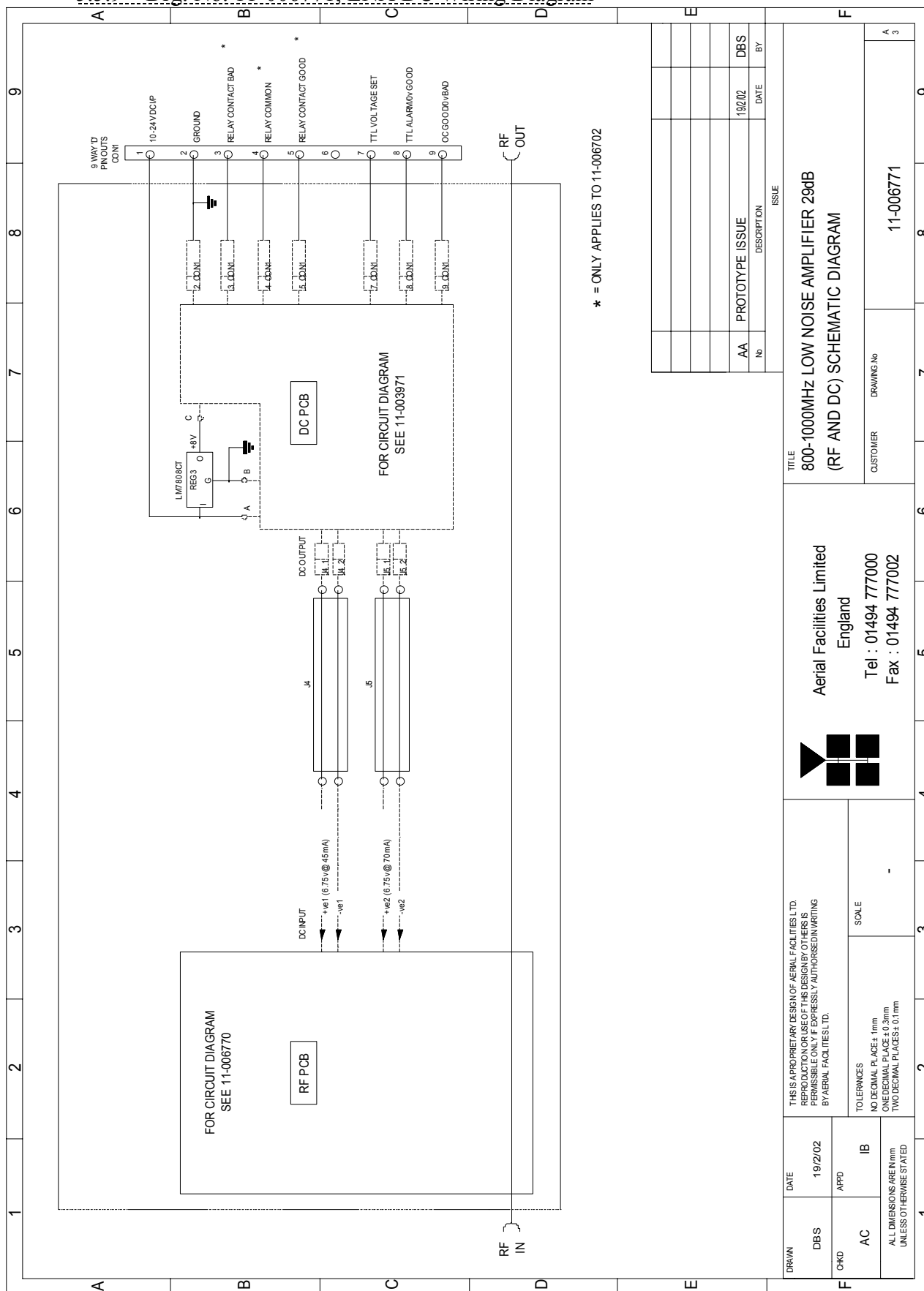


5.5.8 Drg. Nō. 11-006770, LNA Circuit Diagram (RF Components)



5.5.9 Drg. Nō. 11-006771, LNA DC Wiring Diagram



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Technical Literature

800MHz BDA (Pasadena Figuero Tunnel)
Maintenance Handbook

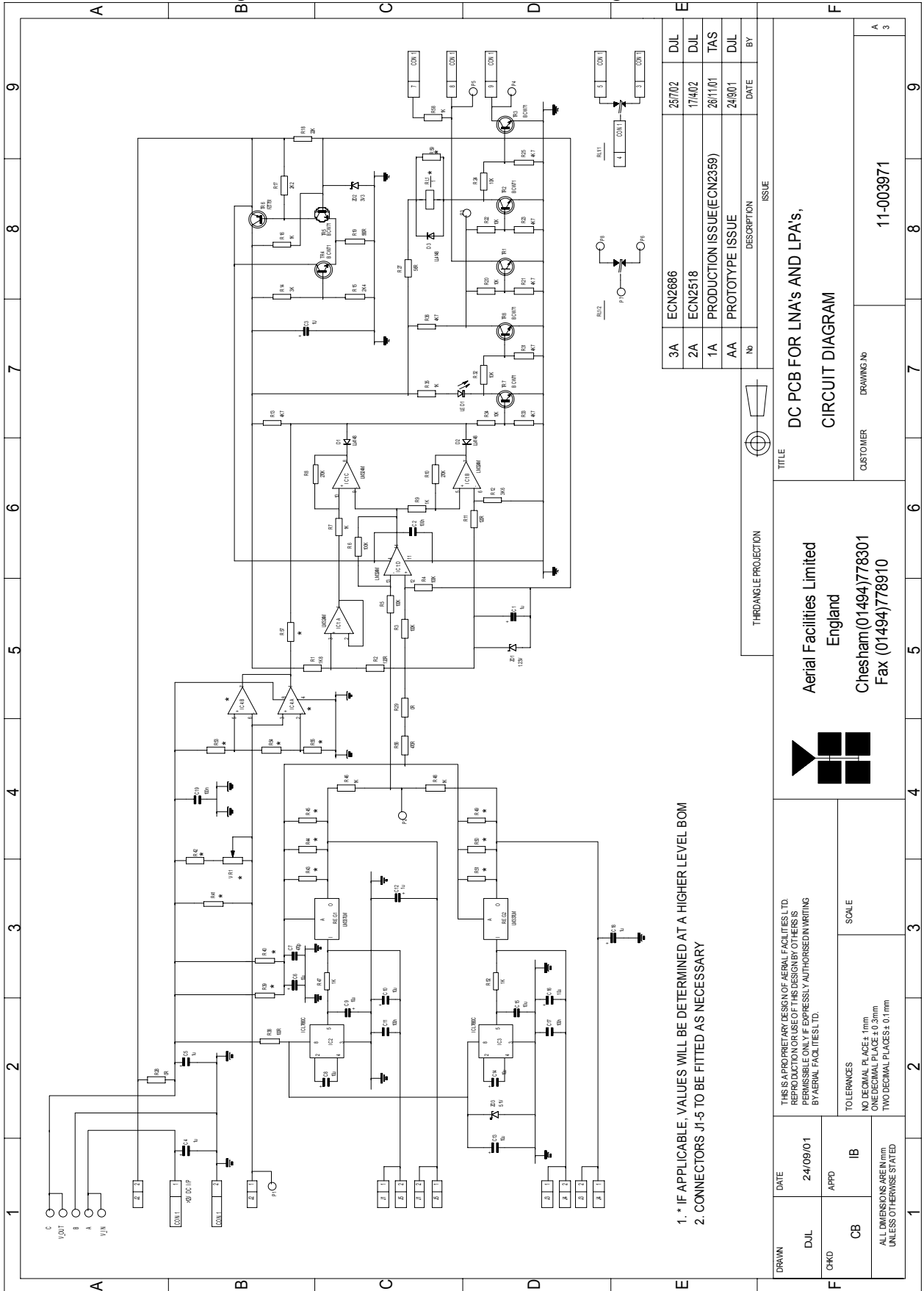
H/book Number:-60-056100HBKM

Issue No:-1

Date:-15/10/2003

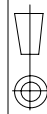
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5.1.5.6 Drg. No. 11-003971, LNA DC Circuit Diagram



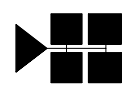
1. * IF APPLICABLE, VALUES WILL BE DETERMINED AT A HIGHER LEVEL BOM
2. CONNECTORS J1-5 TO BE FITTED AS NECESSARY

3A	ECN2686	25702	DJL
2A	ECN2518	17402	DJL
1A	PRODUCTION ISSUE (ECN2359)	261101	TAS
AA	PROTOTYPE ISSUE	24901	DJL
No	DESCRIPTION	DATE	BY



THIRD ANGLE PROJECTION

TITLE	DC PCB FOR LNA's AND LPA's, CIRCUIT DIAGRAM
CUSTOMER	DRAWING No 11-003971



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DRAWN	DATE	24/09/01
DJL	APPRO	IB
CHD	CB	IB

TOLERANCES
NO DECIMAL PLACES ± 1mm
ONE DECIMAL PLACE ± 0.1mm
TWO DECIMAL PLACES ± 0.01mm
UNLESS OTHERWISE STATED