

### SPLML-868 and SP1ML-915

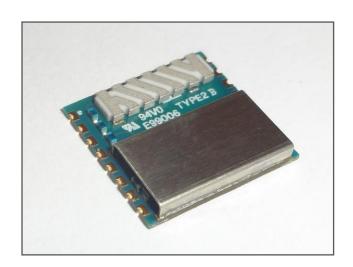
# SPIRIT1 868 and 915 MHz Low Power RF Modules PRELIMINARY DATASHEET

#### **Features**

- Complete RF ready Spirit1 module
- Integrated antenna and crystal
- STM32L microcontroller up to 32 MHz
- Low power consumption
- UART interface with AT command set
- Operates from a single 1.8V to 3.6V supply
- 863 to 870 MHz operation (SP1ML-868)
- 902 to 928 MHz operation (SP1ML-915)
- Output power up to +11dBm
- Data rates up to 500kbps
- Modulation schemes: 2-FSK, GFSK, GMSK, OOK, and ASK
- Compact size: 14mm x 13.4mm x 2.5mmOperating temperature: -40 °C to 85 °C
- FCC and CE regulatory approvals

### **Applications**

- Serial cable replacement
- Home automation
- M2M industrial control
- Service diagnostic
- Data acquisition equipment
- Machine control
- Sensor monitoring
- Security systems
- Mobile health and medical



### **Contents**

1	Des	scription	4
2	Har	dware specification	5
	2.1	Recommended operating conditions	5
	2.2	Absolute maximum ratings	5
	2.3	I/O operating characteristics	5
	2.4	Current consumption	6
	2.5	Pin assignment	7
	2.6	Pin placement	8
	2.7	Hardware block diagram	8
3	Hardware design		
	3.1	Pin usage	9
	3.2	Typical application circuit	10
	3.3	Layout guidelines	11
	3.4	Recommended footprint	12
	3.5	Module reflow installation	13
4	Med	chanical data	14
	4.1	RoHS compliance	14
5	Reg	gulatory compliance	15
	5.1	FCC modular approval	
	5.2	Label instructions	15
6	Ord	lering Information	16

### List of tables

Table 1.	Recommended operating conditions	5
Table 2.	Absolute maximum ratings	5
Table 3.	I/O operating characteristics	5
Table 4.	Current consumption	6
Table 5.	Pin assignment	7
Table 6.	Module pin usage	9
Table 7.	Soldering profile	13
	Ordering information	

# List of figures

Figure 1.	Pin placement	8
Figure 2.	Hardware block diagram	8
	Typical application circuit	
	Layout guidelines	
	Recommended footprint	
Figure 6.	Soldering profile	13
	Mechanical data	

### 1 Description

The SP1ML-868 and SP1ML-915 are low power RF modules based on the SPIRIT1 radio, with integrated voltage regulation, antenna, crystal and microcontroller in a compact surface mount module form-factor. The module allows OEMs to easily add wireless capability to any electronic device without requiring in-depth RF experience, and has the necessary FCC modular approvals and CE compliance to reduce time to market.

The UART host interface allows simple connection to an external microcontroller. Access to module features is provided through an extended AT command set. A simple cable replacement model allows the module to be used similarly to a standard serial interface.

The microcontroller serial wire debug signals are available and allow the standard firmware to be replaced by a user application, enabling use as a wireless application module where no external microprocessor system is required. Select STM32L GPIO and peripherals are available for interfacing to external devices in the user application, and advanced features of the SPIRIT1 radio are also accessible.

## 2 Hardware specification

General conditions ( $V_{IN} = 3 \text{ V}$  and  $T_A = 25 ^{\circ}\text{C}$ ).

#### 2.1 Recommended operating conditions

Table 1. Recommended operating conditions

Symbol	Parameter Parameter		Тур.	Max.	Unit
T <sub>A</sub>	T <sub>A</sub> Operating ambient temperature range		-	85	°C
$V_{DD}$	Operating supply voltage	1.8	3	3.6	V
FREQ RF frequency (SP1ML-868)		863	-	870	MHz
FREQ RF frequency (SP1ML-915)		902	-	928	MHz

### 2.2 Absolute maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter		Тур.	Max.	Unit
T <sub>STG</sub>	Storage temperature range		1	85	°C
$V_{DD}$	Operating supply voltage	-0.3	-	3.9	V
V <sub>IO</sub> I/O pin voltage		-0.3	-	5.5	V

### 2.3 I/O operating characteristics

Table 3. I/O operating characteristics

Symbol	Parameter	Min.	Тур.	Max.	Unit
V <sub>IL</sub> <sup>(1)</sup>	I/O input low level voltage		-	0.3 V <sub>DD</sub>	°C
V <sub>IH</sub> <sup>(1)</sup> I/O input high level voltage		0.7 V <sub>IN</sub>	-	V <sub>DD</sub> +0.3	V

<sup>1.</sup> For more details see the STM32L151RB datasheet, I/O port characteristics.

## 2.4 Current consumption

Table 4. Current consumption

Symbol	Parameter	Test Conditions	Тур.	Unit
		Tx, +11dBm, 915 MHz, SPMS On	21	mA
	Supply ourront	Tx, -7dBm, 915 MHz, SPMS On	7	mA
I <sub>DD</sub>	Supply current	Rx, 915 MHz, SPMS On	11	mA
		Standby	3	μA

### 2.5 Pin assignment

Table 5. Pin assignment

Pin	Name	Туре	Description	STM32L pin <sup>(1)</sup>
1	TXRXLED	0	Active low Tx/Rx LED drive	PA2
2	WKUP	I/O	Wake up module from shutdown	PA0
3	GPIO0	I/O	General purpose input/output 0 <sup>(3)</sup>	PB15
4	GPIO1	I/O	General purpose input/output 1 <sup>(3)</sup>	PB14
5	MODE0	I/O	Protocol mode selection 0 <sup>(2)</sup>	PB13
6	MODE1	I/O	Protocol mode selection 1 <sup>(2)</sup>	PB12
7	VDD	Power	Supply input voltage	-
8	GND	Power	Ground	-
9	SWDIO	I/O	Serial wire I/O	PA13
10	SWCLK	I	Serial wire clock	PA14
11	BOOTMODE	I	Boot mode selection	BOOT0
12	RESET	I	Reset input, active low	NRST
13	TXD	0	UART transmit data	PA9
14	RXD	I	UART receive data	PA10
15	RTS	0	UART request to send	PB6
16	CTS	I	UART clear to send	PB7

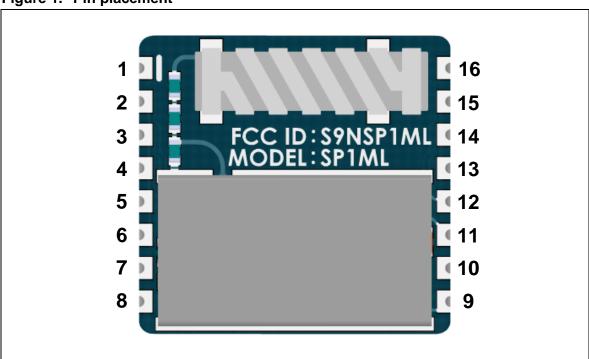
<sup>1.</sup> For more details see the STM32L151RB datasheet, Pin descriptions section. Alternate functions of any given pin are dependent on the user application firmware that is loaded into the module and is beyond the scope of the document.

<sup>2.</sup> Refer to Hardware design section for details of protocol mode selection.

<sup>3.</sup> GPIO function depends on configuration options set using the AT protocol.

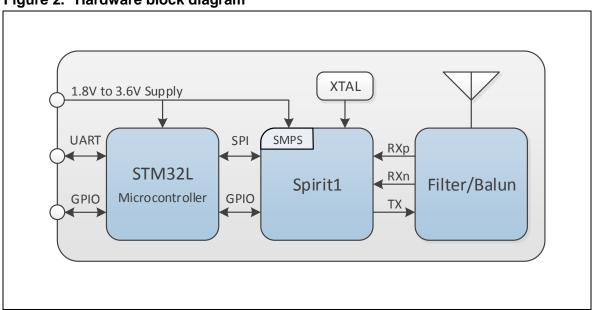
### 2.6 Pin placement

Figure 1. Pin placement



### 2.7 Hardware block diagram

Figure 2. Hardware block diagram



# 3 Hardware design

### 3.1 Pin usage

If used with the default firmware and interfaced to a host MCU, at a minimum the SP1ML module requires power, ground and UART transmit and receive signals to be connected. Other signals are optional and provide additional functionality. These signals are outlined in the following table. Unused signals must not be connected.

Table 6. Module pin usage

Pin	Name	Description	Optional
1	TXRXLED	An active-low open drain output that can drive an external LED for TX/RX activity status indication.	Yes
2	WKUP	Drive this signal low to put the module into a low power shutdown mode. Float or drive the signal high to wake the module.	Yes
3	GPIO0	Can be configured a general purpose input/output.	Yes
4	GPIO1	Can be configured a general purpose input/output.	Yes
5	MODE0	Drive this signal low to force the module into operating mode. Drive this signal high to force the module into command mode. This enables faster switching between modes and obviates the need for escape sequences.	Yes
6	MODE1	Reserved for future use, do not connect.	Yes
7	VDD	Connect to power supply, 1.8V to 3.6V.	No
8	GND	Connect to system ground.	No
9	SWDIO	These signals are the serial wire debug (SWD)	Yes
10	SWCLK	interface to the STM32L microcontroller, supporting the development and loading of custom firmware.	Yes
11	BOOTMODE	Drive this signal high at power up or reset to start the boot loader and allow device firmware update over the UART interface.	Yes
12	RESET	Drive this signal low to hold the module in reset. Drive this signal high to release the module from reset.	Yes
13	TXD	Connect to the host system UART RXD input.	No
14	RXD	Connect to the host system UART TXD output.	No
15	RTS	Connect to the host system UART CTS output if flow control is required.	Yes
16	CTS	Connect to the host system UART RTS input if flow control is required.	Yes

#### 3.2 Typical application circuit

In a typical application the SP1ML module is connected to a host MCU using a standard UART, with transmit and receive signals. This is illustrated in the following diagram.

VDD (1)10uF VDD TXRXLED **HOST** SP1ML TXD **MCU MODULE** RXD RTS RTS CTS CTS 2 VDD **SWCLK** GPIO MODE0 **SWD SWDIO** GPIO WKUP **HEADER** RESET **GPIO** BOOTMODE (5) GPIO RESET GND

Figure 3. Typical application circuit

#### **Optional Functionality**

- The TXRXLED signal is an active-low open drain output that can be used to drive an external LED for status indication when data is sent or received.
- 2. The RTS and CTS signals can be connected to the host UART interface if hardware flow control is required.
- The MODE0 signal provides a hardware method for switching between command and operating modes. The WKUP signal allows the module to be placed into a low power shutdown mode and woken up again.
- 4. The BOOTMODE AND RESET signals should be connected if module firmware will need to be updated over the UART interface.
- 5. To support custom module firmware, the serial wire debug signals need to be accessible for programming and debugging.

#### 3.3 Layout guidelines

The application PCB requires a flooded ground plane. Care must be taken to ensure there are no traces or ground planes under the area surrounding the antenna, with the exception of minimal width traces from pins 1,2,3,4,13,14,15 and 16 of the module if these are used. It is appropriate to place the module toward the edge of the PCB with no traces or ground planes from the antenna to the board edge.

No copper on any layer surrounding antenna, except minimal traces from pins 1,2,3,4,13,14,15 and 16

6 mm

FCC ID: SYNSPIML

MODEL: SPIML

Flooded ground plane required (signal traces allowed)

Application PCB

Figure 4. Layout guidelines

## 3.4 Recommended footprint

15.5 mm

1.5 mm

0.75 mm

1.5 mm

Figure 5. Recommended footprint

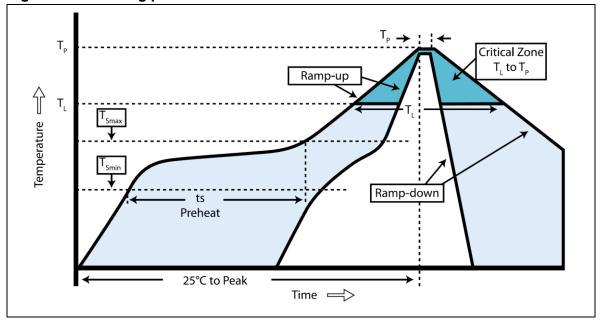
#### 3.5 Module reflow installation

The SP1ML is a surface mount module supplied on a 16-pin, 4-layer PCB. The final assembly recommended reflow profile is indicated below, based on IPC/JEDEC JSTD-020C, July 2004 recommendations.

Table 7. Soldering profile

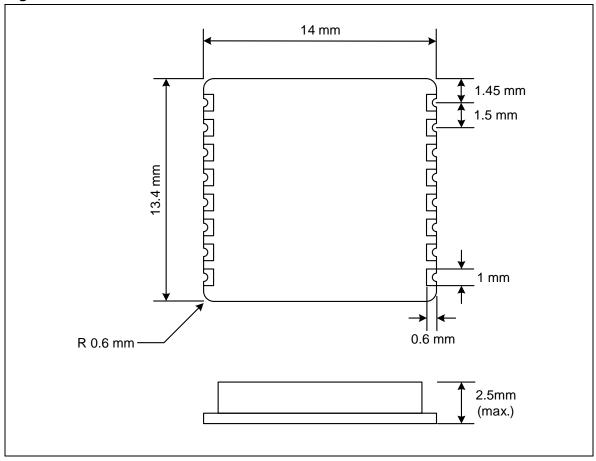
Profile feature	Lead-free assembly
Average ramp-up rate (Tsmax to Tp)	3 °C/sec max
Preheat:  - Temperature min. (Ts min.)  - Temperature max. (Ts max.)  - Time (ts min. to ts max.)(ts)  Time maintained above:  - Temperature TL  - Temperature TL	150 °C 200 °C 60-100 sec 217 °C 60-70 sec
Peak temperature (T <sub>P</sub> )	240 °C
Time within 5 °C of actual peak temperature (T <sub>P</sub> )	10-20 sec
Ramp-down rate	6 °C/sec
Time from 25 °C to peak temperature	8 minutes max.

Figure 6. Soldering profile



### 4 Mechanical data

Figure 7. Mechanical data



### 4.1 RoHS compliance

ST modules are RoHS compliant and comply with ECOPACK® norms.

### 5 Regulatory compliance

This module has been tested and found to comply with the FCC part 15 rules. These limits are designed to provide reasonable protection against harmful interference in approved installations. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications.

However, there is no guarantee that interference may not occur in a particular installation.

This device complies with part 15 of the FCC rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Modifications or changes to this equipment not expressly approved by the part responsible for compliance may render void the user's authority to operate this equipment.

#### 5.1 FCC modular approval

FCC ID: S9NSP1ML

In accordance with FCC part 15, the SP1ML-915 is listed above as a modular transmitter device.

#### 5.2 Label instructions

When integrating the SP1ML-915 into the final product, it must be ensured that the FCC labeling requirements, as specified below, are satisfied. Based on the Public Notice from FCC, the product into which the ST transmitter module is installed must display a label referring to the enclosed module. The label should use wording such as the following:

**Contains Transmitter Module** 

FCC ID: S9NSP1ML

Any similar wording that expresses the same meaning may be used.

# **6** Ordering Information

Table 8. Ordering information

Order code	Description
SP1ML-868	868 MHz Spirit 1 antenna module (Region 1, Europe)
SP1ML-915	915 MHz Spirit 1 antenna module (Region 2, The Americas)

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