

APPENDIX E

DATA SHEETS

RADIATED EMISSIONS

DATA SHEETS

RADIATED EMISSIONS (FCC SECTION 15.249)

COMPANY	Embedded Processor Designs, Inc.	DATE	1/18/05
EUT	InfoCube Pager	DUTY CYCLE	N/A %
MODEL	1	PEAK TO AVG	N/A dB
S/N	N/A	TEST DIST.	3 Meters
TEST ENGINEER	Kyle Fujimoto	LAB	A

Frequency MHz	Peak Reading (dBuV)	Average (A) or Quasi- Peak (QP)	Antenna Polar. (V or H)	Antenna Height (meters)	EUT Azimuth (degrees)	EUT Axis (X,Y,Z)	EUT Tx Channel	Antenna Factor (dB)	Cable Loss (dB)	Amplifier Gain (dB)	Distance Factor (dB)	Mixer Factor (dB)	*Corrected Reading (dBuV/m)	Delta ** (dB)	Spec Limit (dBuV/m)	Comments
902.9090	89.2	QP	V	1.0	180	X	LOW	20.6	6.8	31.0	0.0	0.0	85.6	-8.4	94.0	
902.9090	97.4	97.3 QP	V	1.0	180	Y	LOW	20.6	6.8	31.0	0.0	0.0	93.7	-0.3	94.0	
902.9090	93.0	QP	V	1.0	180	Z	LOW	20.6	6.8	31.0	0.0	0.0	89.4	-4.6	94.0	
902.9090	97.1	97.0 QP	H	1.0	345	X	LOW	20.9	6.3	31.1	0.0	0.0	93.1	-0.9	94.0	
902.9090	94.9	QP	H	1.0	340	Y	LOW	20.9	6.3	31.1	0.0	0.0	91.0	-3.0	94.0	
902.9090	97.5	97.4 QP	H	1.0	340	Z	LOW	20.9	6.3	31.1	0.0	0.0	93.5	-0.5	94.0	
915.0860	88.3	QP	V	1.0	180	X	MID	20.9	6.8	31.1	0.0	0.0	84.9	-9.1	94.0	
915.0860	96.9	96.8 QP	V	1.0	180	Y	MID	20.9	6.8	31.1	0.0	0.0	93.4	-0.6	94.0	
915.0860	89.1	QP	V	1.0	345	Z	MID	20.9	6.8	31.1	0.0	0.0	85.7	-8.3	94.0	
915.0860	97.3	97.2 QP	H	1.0	340	X	MID	20.9	6.8	31.1	0.0	0.0	93.8	-0.2	94.0	
915.0860	94.2	QP	H	1.0	340	Y	MID	20.9	6.8	31.1	0.0	0.0	90.8	-3.2	94.0	
915.0860	93.0	QP	H	1.0	180	Z	MID	20.9	6.8	31.1	0.0	0.0	89.6	-4.4	94.0	
927.2420	85.4	QP	V	1.0	180	X	HIGH	20.6	6.8	31.0	0.0	0.0	81.8	-12.2	94.0	
927.2420	94.9	94.8 QP	V	1.0	345	Y	HIGH	21.1	6.9	31.2	0.0	0.0	91.6	-2.4	94.0	
927.2420	92.3	QP	V	1.0	340	Z	HIGH	21.1	6.9	31.2	0.0	0.0	89.1	-4.9	94.0	
927.2420	94.5	94.4 QP	H	1.0	340	X	HIGH	21.1	6.9	31.2	0.0	0.0	91.2	-2.8	94.0	
927.2420	89.7	QP	H	1.0	180	Y	HIGH	21.1	6.9	31.2	0.0	0.0	86.5	-7.5	94.0	
927.2420	95.3	95.2 QP	H	1.0	180	Z	HIGH	21.1	6.9	31.2	0.0	0.0	92.0	-2.0	94.0	

* CORRECTED READING = METER READING + ANTENNA FACTOR + CABLE LOSS - AMPLIFIER GAIN

** DELTA = SPEC LIMIT - CORRECTED READING

RADIATED EMISSIONS (FCC SECTION 15.249)

COMPANY	Embedded Processor Designs, Inc.	DATE	2/25/2005
EUT	InfoCube Pager	DUTY CYCLE	N/A %
MODEL	1	PEAK TO AVG	N/A dB
S/N	N/A	TEST DIST.	3 Meters
TEST ENGINEER	Kyle Fujimoto	LAB	A

Frequency MHz	Peak Reading (dBuV)	Average (A) or Quasi- Peak (QP)	Antenna Polar. (V or H)	Antenna Height (meters)	EUT Azimuth (degrees)	EUT Axis (X,Y,Z)	EUT Tx Channel	Antenna Factor (dB)	Cable Loss (dB)	Amplifier Gain (dB)	Distance Factor (dB)	Mixer Factor (dB)	*Corrected Reading (dBuV/m)	Delta ** (dB)	Spec Limit (dBuV/m)	Comments
902.9090	86.0	QP	V	1.0	180	X	LOW	20.6	6.1	31.0	0.0	0.0	81.7	-12.3	94.0	Transmitter with Charging
																Board
902.9090	93.4	QP	H	1.0	345	X	LOW	20.6	6.1	31.0	0.0	0.0	89.1	-4.9	94.0	Transmitter with Charging
																Board
915.0860	86.8	QP	V	1.0	180	X	MID	20.9	6.3	31.1	0.0	0.0	82.8	-11.2	94.0	Transmitter with Charging
																Board
915.0860	92.3	QP	H	1.0	340	X	MID	20.9	6.3	31.1	0.0	0.0	88.3	-5.7	94.0	Transmitter with Charging
																Board
927.2420	83.4	QP	V	1.0	180	X	HIGH	21.1	6.4	31.2	0.0	0.0	79.7	-14.3	94.0	Transmitter with Charging
																Board
927.2420	91.5	QP	H	1.0	340	X	HIGH	21.1	6.4	31.2	0.0	0.0	87.8	-6.2	94.0	Transmitter with Charging
																Board

* CORRECTED READING = METER READING + ANTENNA FACTOR + CABLE LOSS - AMPLIFIER GAIN

** DELTA = SPEC LIMIT - CORRECTED READING

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Embedded Processor Designs, Inc.

InfoCube Pager

Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

X-Axis**Low Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1805.83	49.21	V	74	-24.79	Peak	1.23	315	
1805.83	47.06	V	54	-6.94	Avg	1.23	315	
2708.6	42.61	V	74	-31.39	Peak	1.65	315	
2708.6	34.74	V	54	-19.26	Avg	1.65	315	
3611	44.62	V	74	-29.38	Peak	2.12	225	
3611	31.32	V	54	-22.68	Avg	2.12	225	
4515	42.51	V	74	-31.49	Peak	1.91	180	
4515	30.26	V	54	-23.74	Avg	1.91	180	
5417	47.01	V	74	-26.99	Peak	1.91	180	
5417	33.21	V	54	-20.79	Avg	1.91	180	
6319.2	49.14	V	74	-24.86	Peak	1.91	180	
6319.2	36.13	V	54	-17.87	Avg	1.91	180	
7222	50.85	V	74	-23.15	Peak	1.91	225	
7222	38.03	V	54	-15.97	Avg	1.91	225	
8125	52.02	V	74	-21.98	Peak	1.91	225	
8125	38.85	V	54	-15.15	Avg	1.91	225	
9028		V	74	-74	Peak			No Emissions
9028		V	54	-54	Avg			Detected

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Embedded Processor Designs, Inc.

InfoCube Pager

Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

X-Axis**Low Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1805.83	52.24	H	74	-21.76	Peak	1.28	270	
1805.83	50.73	H	54	-3.27	Avg	1.28	270	
2708.6	40.64	H	74	-33.36	Peak	2.23	225	
2708.6	31.14	H	54	-22.86	Avg	2.23	225	
3611	43.33	H	74	-30.67	Peak	2.12	225	
3611	32.68	H	54	-21.32	Avg	2.12	225	
4515	42.22	H	74	-31.78	Peak	1.91	180	
4515	29.05	H	54	-24.95	Avg	1.91	180	
5417	44.62	H	74	-29.38	Peak	1.63	180	
5417	32.59	H	54	-21.41	Avg	1.63	180	
6319.2	47.92	H	74	-26.08	Peak	2.28	180	
6319.2	35.62	H	54	-18.38	Avg	2.28	180	
7222	50.38	H	74	-23.62	Peak	2.28	225	
7222	37.68	H	54	-16.32	Avg	2.28	225	
8125	51.66	H	74	-22.34	Peak	1.91	225	
8125	38.67	H	54	-15.33	Avg	1.91	225	
9028		H	74	-74	Peak			
9028		H	54	-54	Avg			

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Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/17/05
 Lab: B
 Tested By: Kyle Fujimoto

Y-Axis

**Low Channel
 Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1805.83	52.77	V	74	-21.23	Peak	2.38	225	
1805.83	51.89	V	54	-2.11	Avg	2.38	225	
2708.6	46.52	V	74	-27.48	Peak	2	225	
2708.6	43.83	V	54	-10.17	Avg	2	225	
3611	44.09	V	74	-29.91	Peak	3.05	315	
3611	38.82	V	54	-15.18	Avg	3.05	315	
4515	41.17	V	74	-32.83	Peak	1.92	225	
4515	27.42	V	54	-26.58	Avg	1.92	225	
5417	44.32	V	74	-29.68	Peak	1.58	90	
5417	29.63	V	54	-24.37	Avg	1.58	90	
6319.2	43.74	V	74	-30.26	Peak	1.89	270	
6319.2	31.68	V	54	-22.32	Avg	1.89	270	
7222		V	74	-74	Peak			No Emissions
7222		V	54	-54	Avg			Detected
8125		V	74	-74	Peak			No Emissions
8125		V	54	-54	Avg			Detected
9028		V	74	-74	Peak			No Emissions
9028		V	54	-54	Avg			Detected

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Embedded Processor Designs, Inc.

InfoCube Pager

Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

Y-Axis**Low Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1805.83	50	H	74	-24	Peak	2.38	225	
1805.83	48.48	H	54	-5.52	Avg	2.38	225	
2708.6	45.94	H	74	-28.06	Peak	2.44	90	
2708.6	42.1	H	54	-11.9	Avg	2.44	90	
3611	53.93	H	74	-20.07	Peak	2.38	315	
3611	51.67	H	54	-2.33	Avg	2.38	315	
4515	41.71	H	74	-32.29	Peak	1.92	225	
4515	28.73	H	54	-25.27	Avg	1.92	225	
5417	45.42	H	74	-28.58	Peak	1.58	90	
5417	32.44	H	54	-21.56	Avg	1.58	90	
6319.2	46.8	H	74	-27.2	Peak	1.89	270	
6319.2	32.99	H	54	-21.01	Avg	1.89	270	
7222	47.03	H	74	-26.97	Peak	1.52	180	
7222	33.62	H	54	-20.38	Avg	1.52	180	
8125		H	74	-74	Peak			No Emissions
8125		H	54	-54	Avg			Detected
9028		H	74	-74	Peak			No Emissions
9028		H	54	-54	Avg			Detected

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Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/17/05
 Lab: B
 Tested By: Kyle Fujimoto

Z-Axis**Low Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1805.83	47.46	V	74	-26.54	Peak	2.26	270	
1805.83	45.56	V	54	-8.44	Avg	2.26	270	
2708.6	42.53	V	74	-31.47	Peak	1.18	225	
2708.6	36.96	V	54	-17.04	Avg	1.18	225	
3611	50.98	V	74	-23.02	Peak	2	225	
3611	48.64	V	54	-5.36	Avg	2	225	
4515	43.73	V	74	-30.27	Peak	2.55	180	
4515	34.17	V	54	-19.83	Avg	2.55	180	
5417	43.33	V	74	-30.67	Peak	2.24	225	
5417	29.81	V	54	-24.19	Avg	2.24	225	
6319.2	48.49	V	74	-25.51	Peak	2.06	225	
6319.2	40.73	V	54	-13.27	Avg	2.06	225	
7222	45.48	V	74	-28.52	Peak	2.03	225	
7222	33.65	V	54	-20.35	Avg	2.02	225	
8125		V	74	-74	Peak			No Emissions
8125		V	54	-54	Avg			Detected
9028		V	74	-74	Peak			No Emissions
9028		V	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/17/05
 Lab: B
 Tested By: Kyle Fujimoto

Z-Axis**Low Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1805.83	56.54	H	74	-17.46	Peak	2.69	135	
1805.83	48.93	H	54	-5.07	Avg	2.69	135	
2708.6	43.78	H	74	-30.22	Peak	2.67	225	
2708.6	39.82	H	54	-14.18	Avg	2.67	225	
3611	48.21	H	74	-25.79	Peak	2.71	135	
3611	44.06	H	54	-9.94	Avg	2.71	135	
4515	43.21	H	74	-30.79	Peak	2.7	180	
4515	32.13	H	54	-21.87	Avg	2.71	180	
5417	43.64	H	74	-30.36	Peak	2.16	225	
5417	29.93	H	54	-24.07	Avg	2.16	225	
6319.2	46.87	H	74	-27.13	Peak	2.16	180	
6319.2	32.21	H	54	-21.79	Avg	2.16	180	
7222	46.87	H	74	-27.13	Peak	2.16	225	
7222	33.71	H	54	-20.29	Avg	2.16	225	
8125		H	74	-74	Peak			No Emissions
8125		H	54	-54	Avg			Detected
9028		H	74	-74	Peak			No Emissions
9028		H	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.

InfoCube Pager

Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

X-Axis**Middle Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1830	51.49	V	74	-22.51	Peak	1.28	225	
1830	50.09	V	54	-3.91	Avg	1.28	225	
2745	38.17	V	74	-35.83	Peak	3.12	225	
2745	27.54	V	54	-26.46	Avg	3.12	225	
3660	41.79	V	74	-32.21	Peak	2.53	225	
3660	26.6	V	54	-27.4	Avg	2.53	225	
4575	41.07	V	74	-32.93	Peak	2.53	225	
4575	28.25	V	54	-25.75	Avg	2.53	225	
5490	41.73	V	74	-32.27	Peak	2.53	180	
5490	29.62	V	54	-24.38	Avg	2.53	180	
6405	43.81	V	74	-30.19	Peak	2.53	225	
6405	31.75	V	54	-22.25	Avg	2.53	225	
7320	45.58	V	74	-28.42	Peak	2.53	225	No Emissions
7320	33.21	V	54	-20.79	Avg	2.53	225	Detected
8235		V	74	-74	Peak			No Emissions
8235		V	54	-54	Avg			Detected
9150		V	74	-74	Peak			No Emissions
9150		V	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.

InfoCube Pager

Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

X-Axis**Middle Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1830	54.65	H	74	-19.35	Peak	1.24	270	
1830	53.77	H	54	-0.23	Avg	1.24	270	
2745	39.32	H	74	-34.68	Peak	3.31	225	
2745	26.3	H	54	-27.7	Avg	3.31	225	
3660	40.2	H	74	-33.8	Peak	2.37	225	
3660	28.04	H	54	-25.96	Avg	2.37	225	
4575	39.46	H	74	-34.54	Peak	2.37	225	
4575	27.95	H	54	-26.05	Avg	2.37	225	
5490	43.85	H	74	-30.15	Peak	2.37	180	
5490	29.47	H	54	-24.53	Avg	2.37	180	
6405	45.31	H	74	-28.69	Peak	2.37	180	
6405	31.81	H	54	-22.19	Avg	2.37	180	
7320		H	74	-74	Peak			No Emissions
7320		H	54	-54	Avg			Detected
8235		H	74	-74	Peak			No Emissions
8235		H	54	-54	Avg			Detected
9150		H	74	-74	Peak			No Emissions
9150		H	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.

InfoCube Pager

Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

Y-Axis**Middle Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1830	50.18	V	74	-23.82	Peak	1.62	270	
1830	48.5	V	54	-5.5	Avg	1.62	270	
2745	43.78	V	74	-30.22	Peak	1.29	180	
2745	38.14	V	54	-15.86	Avg	1.29	180	
3660	44.66	V	74	-29.34	Peak	2.5	180	
3660	36.02	V	54	-17.98	Avg	2.5	180	
4575	43.06	V	74	-30.94	Peak	2.5	225	
4575	29.48	V	54	-24.52	Avg	2.5	225	
5490	43.22	V	74	-30.78	Peak	2.5	225	
5490	29.69	V	54	-24.31	Avg	2.5	225	
6405	45.43	V	74	-28.57	Peak	2.5	135	
6405	31.82	V	54	-22.18	Avg	2.5	135	
7320	46.08	V	74	-27.92	Peak	2.61	0	
7320	33.31	V	54	-20.69	Avg	2.61	0	
8235		V	74	-74	Peak			No Emissions
8235		V	54	-54	Avg			Detected
9150		V	74	-74	Peak			No Emissions
9150		V	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.

InfoCube Pager

Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

Y-Axis**Middle Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1830	50.18	V	74	-23.82	Peak	1.62	270	
1830	48.5	V	54	-5.5	Avg	1.62	270	
2745	43.78	V	74	-30.22	Peak	1.29	180	
2745	38.14	V	54	-15.86	Avg	1.29	180	
3660	44.66	V	74	-29.34	Peak	2.5	180	
3660	36.02	V	54	-17.98	Avg	2.5	180	
4575	43.06	V	74	-30.94	Peak	2.5	225	
4575	29.48	V	54	-24.52	Avg	2.5	225	
5490	43.22	V	74	-30.78	Peak	2.5	225	
5490	29.69	V	54	-24.31	Avg	2.5	225	
6405	45.43	V	74	-28.57	Peak	2.5	135	
6405	31.82	V	54	-22.18	Avg	2.5	135	
7320	46.08	V	74	-27.92	Peak	2.61	0	
7320	33.31	V	54	-20.69	Avg	2.61	0	
8235		V	74	-74	Peak			No Emissions
8235		V	54	-54	Avg			Detected
9150		V	74	-74	Peak			No Emissions
9150		V	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

Z-Axis**Middle Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1830	51.13	V	74	-22.87	Peak	2.08	270	
1830	49.92	V	54	-4.08	Avg	2.08	270	
2745	45.52	V	74	-28.48	Peak	2.09	180	
2745	42.25	V	54	-11.75	Avg	2.09	180	
3660	52.05	V	74	-21.95	Peak	2.45	135	
3660	49.69	V	54	-4.31	Avg	2.45	135	
4575	40.57	V	74	-33.43	Peak	2.45	225	
4575	28.11	V	54	-25.89	Avg	2.45	225	
5490	42.08	V	74	-31.92	Peak	2.45	225	
5490	29.52	V	54	-24.48	Avg	2.45	225	
6405	46.51	V	74	-27.49	Peak	2.45	135	
6405	31.97	V	54	-22.03	Avg	2.45	135	
7320	45.64	V	74	-28.36	Peak	2.45	135	
7320	33.34	V	54	-20.66	Avg	2.45	135	
8235		V	74	-74	Peak			No Emissions
8235		V	54	-54	Avg			Detected
9150		V	74	-74	Peak			No Emissions
9150		V	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

Z-Axis**Middle Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1830	50.04	H	74	-23.96	Peak	2.28	225	
1830	48.7	H	54	-5.3	Avg	2.28	225	
2745	47.83	H	74	-26.17	Peak	2.37	270	
2745	43.53	H	54	-10.47	Avg	2.37	270	
3660	47.26	H	74	-26.74	Peak	2.45	270	
3660	41.75	H	54	-12.25	Avg	2.45	270	
4575	43.54	H	74	-30.46	Peak	1	270	
4575	32.01	H	54	-21.99	Avg	1	270	
5490	43.18	H	74	-30.82	Peak	2.08	225	
5490	30.9	H	54	-23.1	Avg	2.08	225	
6405	47.85	H	74	-26.15	Peak	1.89	225	
6405	33.47	H	54	-20.53	Avg	1.89	225	
7320		H	74	-74	Peak			No Emissions
7320		H	54	-54	Avg			Detected
8235		H	74	-74	Peak			No Emissions
8235		H	54	-54	Avg			Detected
9150		H	74	-74	Peak			No Emissions
9150		H	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

X-Axis**High Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1854.6	47.64	V	74	-26.36	Peak	3.28	225	
1854.6	45.19	V	54	-8.81	Avg	3.28	225	
2781.7	43.13	V	74	-30.87	Peak	3.48	225	
2781.7	35.92	V	54	-18.08	Avg	3.48	225	
3708.9	44.11	V	74	-29.89	Peak	2.59	225	
3708.9	37.15	V	54	-16.85	Avg	2.59	225	
4636.15	42.07	V	74	-31.93	Peak	1.84	180	
4636.2	28.44	V	54	-25.56	Avg	1.84	180	
5563.3	43.29	V	74	-30.71	Peak	1.69	225	
5563.3	30.71	V	54	-23.29	Avg	1.69	225	
6490.6	44.56	V	74	-29.44	Peak	1.69	225	
6490.6	32.54	V	54	-21.46	Avg	1.69	225	
7417.8		V	74	-74	Peak			
7417.8		V	54	-54	Avg			
8345.1		V	74	-74	Peak			No Emissions
8345.1		V	54	-54	Avg			Detected
9272.2		V	74	-74	Peak			No Emissions
9272.2		V	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

X-Axis**High Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1854.6	52.36	H	74	-21.64	Peak	1.27	90	
1854.6	51.3	H	54	-2.7	Avg	1.27	90	
2781.7	38.7	H	74	-35.3	Peak	3.16	270	
2781.7	25.23	H	54	-28.77	Avg	3.16	270	
3708.9	43.61	H	74	-30.39	Peak	3.52	225	
3708.9	29.14	H	54	-24.86	Avg	3.52	225	
4636.15	42.31	H	74	-31.69	Peak	1.38	45	
4636.2	28.41	H	54	-25.59	Avg	1.38	45	
5563.3	42.84	H	74	-31.16	Peak	2.39	270	
5563.3	30.61	H	54	-23.39	Avg	2.39	270	
6490.6	45.03	H	74	-28.97	Peak	2.4	270	
6490.6	30.52	H	54	-23.48	Avg	2.4	270	
7417.8	47.17	H	74	-26.83	Peak	2.4	270	
7417.8	33.71	H	54	-20.29	Avg	2.4	270	
8345.1		H	74	-74	Peak			No Emissions
8345.1		H	54	-54	Avg			Detected
9272.2		H	74	-74	Peak			No Emissions
9272.2		H	54	-54	Avg			Detected

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Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

Y-Axis**High Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1854.6	52.6	V	74	-21.4	Peak	1.64	225	
1854.6	51.64	V	54	-2.36	Avg	1.64	225	
2781.7	41.24	V	74	-32.76	Peak	2.41	270	
2781.7	30.9	V	54	-23.1	Avg	2.41	270	
3708.9	42.94	V	74	-31.06	Peak	2.02	270	
3708.9	35.24	V	54	-18.76	Avg	2.02	270	
4636.15	43.61	V	74	-30.39	Peak	2.01	180	
4636.2	33.83	V	54	-20.17	Avg	2.01	180	
5563.3	45.15	V	74	-28.85	Peak	2.15	180	
5563.3	34.84	V	54	-19.16	Avg	2.15	180	
6490.6	47.89	V	74	-26.11	Peak	2.77	135	
6490.6	38.96	V	54	-15.04	Avg	2.77	135	
7417.8		V	74	-74	Peak			No Emissions
7417.8		V	54	-54	Avg			Detected
8345.1		V	74	-74	Peak			No Emissions
8345.1		V	54	-54	Avg			Detected
9272.2		V	74	-74	Peak			No Emissions
9272.2		V	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

Y-Axis**High Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1854.6	49.93	H	74	-24.07	Peak	2.33	45	
1854.6	48.32	H	54	-5.68	Avg	2.33	45	
2781.7	45.5	H	74	-28.5	Peak	2.86	225	
2781.7	41.3	H	54	-12.7	Avg	2.86	225	
3708.9	50.99	H	74	-23.01	Peak	2.59	180	
3708.9	48.51	H	54	-5.49	Avg	2.59	180	
4636.15	42.66	H	74	-31.34	Peak	2.59	225	
4636.2	28.94	H	54	-25.06	Avg	2.59	225	
5563.3	45.06	H	74	-28.94	Peak	1.46	225	
5563.3	32.79	H	54	-21.21	Avg	1.46	225	
6490.6	49.32	H	74	-24.68	Peak	1.72	225	
6490.6	41.29	H	54	-12.71	Avg	1.72	225	
7417.8		H	74	-74	Peak			No Emissions
7417.8		H	54	-54	Avg			Detected
8345.1		H	74	-74	Peak			No Emissions
8345.1		H	54	-54	Avg			Detected
9272.2		H	74	-74	Peak			No Emissions
9272.2		H	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.

InfoCube Pager

Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

Z-Axis**High Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1854.6		V	74	-74	Peak			
1854.6		V	54	-54	Avg			
2781.7		V	74	-74	Peak			
2781.7		V	54	-54	Avg			
3708.9		V	74	-74	Peak			
3708.9		V	54	-54	Avg			
4636.15		V	74	-74	Peak			
4636.2		V	54	-54	Avg			
5563.3		V	74	-74	Peak			
5563.3		V	54	-54	Avg			
6490.6		V	74	-74	Peak			
6490.6		V	54	-54	Avg			
7417.8		V	74	-74	Peak			No Emissions
7417.8		V	54	-54	Avg			Detected
8345.1		V	74	-74	Peak			No Emissions
8345.1		V	54	-54	Avg			Detected
9272.2		V	74	-74	Peak			No Emissions
9272.2		V	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/17/05

Lab: B

Tested By: Kyle Fujimoto

Z-Axis**High Channel****Transmit Mode**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1854.6	54.12	H	74	-19.88	Peak	1.78	180	
1854.6	53.16	H	54	-0.84	Avg	1.78	180	
2781.7	43.02	H	74	-30.98	Peak	2.4	315	
2781.7	38.28	H	54	-15.72	Avg	2.4	315	
3708.9	47.18	H	74	-26.82	Peak	2.09	225	
3708.9	42.92	H	54	-11.08	Avg	2.09	225	
4636.15	42.49	H	74	-31.51	Peak	3.36	180	
4636.2	32.17	H	54	-21.83	Avg	3.36	180	
5563.3	44.76	H	74	-29.24	Peak	3.35	180	
5563.3	30.84	H	54	-23.16	Avg	3.35	180	
6490.6	44.84	H	74	-29.16	Peak	3.35	315	
6490.6	32.41	H	54	-21.59	Avg	3.35	315	
7417.8		H	74	-74	Peak			No Emissions
7417.8		H	54	-54	Avg			Detected
8345.1		H	74	-74	Peak			No Emissions
8345.1		H	54	-54	Avg			Detected
9272.2		H	74	-74	Peak			No Emissions
9272.2		H	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.

InfoCube Pager

Model: 1

Date: 02/25/05

Lab: B

Tested By: Kyle Fujimoto

Low Channel**Transmit Mode with Charging Board**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1805.83	48.99	V	74	-25.01	Peak	2.31	315	
1805.83	47.87	V	54	-6.13	Avg	2.31	315	
2708.6	46.76	V	74	-27.24	Peak	2.45	225	
2708.6	43.31	V	54	-10.69	Avg	2.45	225	
3611	47.32	V	74	-26.68	Peak	2.52	225	
3611	41.41	V	54	-12.59	Avg	2.52	225	
4515	41.79	V	74	-32.21	Peak	2.52	270	
4515	27.59	V	54	-26.41	Avg	2.52	270	
5417	42.21	V	74	-31.79	Peak	2.74	180	
5417	29.45	V	54	-24.55	Avg	2.74	180	
6319.2	44.73	V	74	-29.27	Peak	1.82	270	
6319.2	32.08	V	54	-21.92	Avg	1.82	270	
7222		V	74	-74	Peak			No Emissions
7222		V	54	-54	Avg			Detected
8125		V	74	-74	Peak			No Emissions
8125		V	54	-54	Avg			Detected
9028		V	74	-74	Peak			No Emissions
9028		V	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 02/25/05

Lab: B

Tested By: Kyle Fujimoto

Low Channel**Transmit Mode with Charging Board**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1805.83	52.9	H	74	-21.1	Peak	1.97	180	
1805.83	52.02	H	54	-1.98	Avg	1.97	180	
2708.6	49.41	H	74	-24.59	Peak	2.47	225	
2708.6	46.59	H	54	-7.41	Avg	2.47	225	
3611	45.38	H	74	-28.62	Peak	2.43	135	
3611	41.97	H	54	-12.03	Avg	2.43	135	
4515	40.74	H	74	-33.26	Peak	1.72	180	
4515	28.54	H	54	-25.46	Avg	1.72	180	
5417	43.67	H	74	-30.33	Peak	1.71	225	
5417	31.21	H	54	-22.79	Avg	1.71	225	
6319.2	44.36	H	74	-29.64	Peak	1.81	270	
6319.2	31.42	H	54	-22.58	Avg	1.81	270	
7222	45.95	H	74	-28.05	Peak	1.81	135	
7222	33.56	H	54	-20.44	Avg	1.81	135	
8125		H	74	-74	Peak			No Emissions
8125		H	54	-54	Avg			Detected
9028		H	74	-74	Peak			No Emissions
9028		H	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.

InfoCube Pager

Model: 1

Date: 02/25/05

Lab: B

Tested By: Kyle Fujimoto

Middle Channel**Transmit Mode with Charging Board**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1830	50.41	V	74	-23.59	Peak	1.68	270	
1830	48.59	V	54	-5.41	Avg	1.68	270	
2745	44.97	V	74	-29.03	Peak	1.99	90	
2745	40.61	V	54	-13.39	Avg	1.99	90	
3660	44.11	V	74	-29.89	Peak	2.03	270	
3660	40.14	V	54	-13.86	Avg	2.03	270	
4575	40.93	V	74	-33.07	Peak	1.91	315	
4575	29.27	V	54	-24.73	Avg	1.91	315	
5490	42.94	V	74	-31.06	Peak	1.9	45	
5490	29	V	54	-25	Avg	1.9	45	
6405	44.07	V	74	-29.93	Peak	2.42	270	
6405	34.07	V	54	-19.93	Avg	2.42	270	
7320		V	74	-74	Peak			No Emissions
7320		V	54	-54	Avg			Detected
8235		V	74	-74	Peak			No Emissions
8235		V	54	-54	Avg			Detected
9150		V	74	-74	Peak			No Emissions
9150		V	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 02/25/05
 Lab: B
 Tested By: Kyle Fujimoto

Middle Channel
Transmit Mode with Charging Board

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1830	54.14	H	74	-19.86	Peak	1	180	
1830	53.43	H	54	-0.57	Avg	1	180	
2745	44.4	H	74	-29.6	Peak	1.41	135	
2745	40.94	H	54	-13.06	Avg	1.41	135	
3660	46.19	H	74	-27.81	Peak	1.16	135	
3660	42.6	H	54	-11.4	Avg	1.16	135	
4575	37.73	H	74	-36.27	Peak	1.81	225	
4575	25.46	H	54	-28.54	Avg	1.81	225	
5490	41.62	H	74	-32.38	Peak	2.18	135	
5490	28.61	H	54	-25.39	Avg	2.18	135	
6405	41.18	H	74	-32.82	Peak	2.18	315	
6405	28.32	H	54	-25.68	Avg	2.18	315	
7320		H	74	-74	Peak			No Emissions
7320		H	54	-54	Avg			Detected
8235		H	74	-74	Peak			No Emissions
8235		H	54	-54	Avg			Detected
9150		H	74	-74	Peak			No Emissions
9150		H	54	-54	Avg			Detected

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Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 02/25/05

Lab: B

Tested By: Kyle Fujimoto

High Channel**Transmit Mode with Charging Board**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1854.6	50.09	V	74	-23.91	Peak	2.24	315	
1854.6	49.09	V	54	-4.91	Avg	2.24	315	
2781.7	44.21	V	74	-29.79	Peak	2.76	225	
2781.7	40.25	V	54	-13.75	Avg	2.76	225	
3708.9	43.66	V	74	-30.34	Peak	2.78	45	
3708.9	38.84	V	54	-15.16	Avg	2.78	45	
4636.15	39.29	V	74	-34.71	Peak	2.78	315	
4636.2	26.76	V	54	-27.24	Avg	2.78	315	
5563.3	41.74	V	74	-32.26	Peak	1.69	225	
5563.3	29.33	V	54	-24.67	Avg	1.69	225	
6490.6	43.57	V	74	-30.43	Peak	2.31	270	
6490.6	33.05	V	54	-20.95	Avg	2.31	270	
7417.8		V	74	-74	Peak			No Emissions
7417.8		V	54	-54	Avg			Detected
8345.1		V	74	-74	Peak			No Emissions
8345.1		V	54	-54	Avg			Detected
9272.2		V	74	-74	Peak			No Emissions
9272.2		V	54	-54	Avg			Detected

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Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 02/25/05
 Lab: B
 Tested By: Kyle Fujimoto

High Channel
Transmit Mode with Charging Board

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
1854.6	48.26	H	74	-25.74	Peak	3.36	270	
1854.6	46.94	H	54	-7.06	Avg	3.36	270	
2781.7	45.57	H	74	-28.43	Peak	2.44	315	
2781.7	42.27	H	54	-11.73	Avg	2.44	315	
3708.9	46.08	H	74	-27.92	Peak	2.15	225	
3708.9	43.08	H	54	-10.92	Avg	2.15	225	
4636.15	40.25	H	74	-33.75	Peak	2.47	180	
4636.2	26.28	H	54	-27.72	Avg	2.47	180	
5563.3	41.53	H	74	-32.47	Peak	1.81	225	
5563.3	29.43	H	54	-24.57	Avg	1.81	225	
6490.6	41.81	H	74	-32.19	Peak	1.97	225	
6490.6	29.21	H	54	-24.79	Avg	1.97	225	
7417.8	42.32	H	74	-31.68	Peak	1.97	270	
7417.8	29.46	H	54	-24.54	Avg	1.97	270	
8345.1		H	74	-74	Peak			No Emissions
8345.1		H	54	-54	Avg			Detected
9272.2		H	74	-74	Peak			No Emissions
9272.2		H	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Receive Mode**Low Channel****Y-Axis Worst Case**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
903.077	44.55	V	66	-21.45	Peak	1.23	180	
903.077	44.29	V	46	-1.71	QP	1.23	180	
1805.83	41.31	V	74	-32.69	Peak	2.18	225	
1805.83	36.68	V	54	-17.32	Avg	2.18	225	
2708.6	36.49	V	74	-37.51	Peak	2.57	180	
2708.6	24.31	V	54	-29.69	Avg	2.57	180	
3611	42.21	V	74	-31.79	Peak	1.71	180	
3611	32.47	V	54	-21.53	Avg	1.71	180	
4515	40.36	V	74	-33.64	Peak	1.71	180	
4515	27.94	V	54	-26.06	Avg	1.71	180	
5417		V	74	-74	Peak			No Emissions
5417		V	54	-54	Avg			Detected
6319.2		V	74	-74	Peak			No Emissions
6319.2		V	54	-54	Avg			Detected
7222		V	74	-74	Peak			No Emissions
7222		V	54	-54	Avg			Detected
8125		V	74	-74	Peak			No Emissions
8125		V	54	-54	Avg			Detected
9028		V	74	-74	Peak			No Emissions
9028		V	54	-54	Peak			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Receive Mode**Low Channel****X-Axis Worst Case**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
903.077	45.95	H	66	-20.05	Peak	1.35	135	
903.077	45.06	H	46	-0.94	QP	1.35	135	
1805.83	40.84	H	74	-33.16	Peak	2.52	270	
1805.83	35.95	H	54	-18.05	Avg	2.52	270	
2708.6	38.17	H	74	-35.83	Peak	2.52	180	
2708.6	24.32	H	54	-29.68	Avg	2.52	180	
3611	38.99	H	74	-35.01	Peak	2.52	180	
3611	26.96	H	54	-27.04	Avg	2.52	180	
4515	41.12	H	74	-32.88	Peak	2.53	90	
4515	27.92	H	54	-26.08	Avg	2.53	90	
5417		H	74	-74	Peak			No Emissions
5417		H	54	-54	Avg			Detected
6319.2	42.55	H	74	-31.45	Peak	1.97	180	
6319.2	30.12	H	54	-23.88	Avg	1.97	180	
7222		H	74	-74	Peak			No Emissions
7222		H	54	-54	Avg			Detected
8125		H	74	-74	Peak			No Emissions
8125		H	54	-54	Avg			Detected
9028		H	74	-74	Peak			No Emissions
9028		H	54	-54	Peak			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Receive Mode
Middle Channel
Y-Axis Worst Case

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
915.25	46.46	V	66	-19.54	Peak	1.23	180	
915.25	45.46	V	46	-0.54	QP	1.23	180	
1830	43.03	V	74	-30.97	Peak	2.86	225	
1830	40.04	V	54	-13.96	Avg	2.86	225	
2745	36.87	V	74	-37.13	Peak	2.86	315	
2745	24.11	V	54	-29.89	Avg	2.86	315	
3660	42.58	V	74	-31.42	Peak	2.2	180	
3660	27	V	54	-27	Avg	2.2	180	
4575	40.93	V	74	-33.07	Peak	1.86	270	
4575	28.29	V	54	-25.71	Avg	1.86	270	
5490	47.13	V	74	-26.87	Peak	1.86	180	
5490	39.89	V	54	-14.11	Avg	1.86	180	
6405		V	74	-74	Peak			No Emissions
6405		V	54	-54	Avg			Detected
7320		V	74	-74	Peak			No Emissions
7320		V	54	-54	Avg			Detected
8235		V	74	-74	Peak			No Emissions
8235		V	54	-54	Avg			Detected
9150		V	74	-74	Peak			No Emissions
9150		V	54	-54	Peak			Detected

FCC 15.249

Embedded Processor Designs, Inc.

InfoCube Pager

Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Receive Mode**Middle Channel****X-Axis Worst Case**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
915.269	46.26	H	66	-19.74	Peak	1.2	180	
915.269	45.49	H	46	-0.51	QP	1.2	180	
1830	43.17	H	74	-30.83	Peak	1.17	225	
1830	40.23	H	54	-13.77	Avg	1.17	225	
2745	36.79	H	74	-37.21	Peak	2.52	315	
2745	23.93	H	54	-30.07	Avg	2.52	315	
3660	40.67	H	74	-33.33	Peak	2.15	180	
3660	26.66	H	54	-27.34	Avg	2.15	180	
4575	40.66	H	74	-33.34	Peak	2.15	225	
4575	28.04	H	54	-25.96	Avg	2.15	225	
5490		H	74	-74	Peak			No Emissions
5490		H	54	-54	Avg			Detected
6405		H	74	-74	Peak			No Emissions
6405		H	54	-54	Avg			Detected
7320		H	74	-74	Peak			No Emissions
7320		H	54	-54	Avg			Detected
8235		H	74	-74	Peak			No Emissions
8235		H	54	-54	Avg			Detected
9150		H	74	-74	Peak			No Emissions
9150		H	54	-54	Peak			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Receive Mode
High Channel
Y-Axis Worst Case

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
927.418	45.46	V	66	-20.54	Peak	2.01	135	
927.418	44.55	V	46	-1.45	QP	2.01	135	
1854.6	44.85	V	74	-29.15	Peak	2.86	225	
1854.6	41.47	V	54	-12.53	Avg	2.86	225	
2781.7	37.15	V	74	-36.85	Peak	2.86	225	
2781.7	24.75	V	54	-29.25	Avg	2.86	225	
3708.9	44.4	V	74	-29.6	Peak	1.39	225	
3708.9	37.5	V	54	-16.5	Avg	1.39	180	
4636.15	41.34	V	74	-32.66	Peak	1.39	225	
4636.2	28.29	V	54	-25.71	Avg	1.39	225	
5563.3	46.13	V	74	-27.87	Peak	1.61	180	
5563.3	37.96	V	54	-16.04	Avg	1.61	180	
6490.6		V	74	-74	Peak			No Emissions
6490.6		V	54	-54	Avg			Detected
7417.8		V	74	-74	Peak			No Emissions
7417.8		V	54	-54	Avg			Detected
8345.1		V	74	-74	Peak			No Emissions
8345.1		V	54	-54	Avg			Detected
9272.2		V	74	-74	Peak			No Emissions
9272.2		V	54	-54	Peak			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 01/18/05

Lab: B

Tested By: Kyle Fujimoto

Receive Mode
High Channel
X-Axis Worst Case

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
927.423	41.76	H	66	-24.24	Peak	1.78	180	
927.423	40.86	H	46	-5.14	QP	1.78	180	
1854.6	41.62	H	74	-32.38	Peak	1.93	225	
1854.6	37.6	H	54	-16.4	Avg	1.93	225	
2781.7	38.23	H	74	-35.77	Peak	1.95	225	
2781.7	24.63	H	54	-29.37	Avg	1.95	225	
3708.9	41.37	H	74	-32.63	Peak	1.93	225	
3708.9	27.5	H	54	-26.5	Avg	1.93	180	
4636.15		H	74	-74	Peak			No Emissions
4636.2		H	54	-54	Avg			Detected
5563.3		H	74	-74	Peak			No Emissions
5563.3		H	54	-54	Avg			Detected
6490.6		H	74	-74	Peak			No Emissions
6490.6		H	54	-54	Avg			Detected
7417.8		H	74	-74	Peak			No Emissions
7417.8		H	54	-54	Avg			Detected
8345.1		H	74	-74	Peak			No Emissions
8345.1		H	54	-54	Avg			Detected
9272.2		H	74	-74	Peak			No Emissions
9272.2		H	54	-54	Avg			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 02/25/05
 Lab: B
 Tested By: Kyle Fujimoto

Low Channel
Receive Mode with Charging Board

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
903.077	38.48	V	66	-27.52	Peak	1.23	180	
903.077	38.22	V	46	-7.78	QP	1.23	180	
1805.83	40.95	V	74	-33.05	Peak	1.95	180	
1805.83	38	V	54	-16	Avg	1.95	180	
2708.6	37.48	V	74	-36.52	Peak	1.44	270	
2708.6	24.12	V	54	-29.88	Avg	1.44	270	
3611	39.58	V	74	-34.42	Peak	2.03	90	
3611	32.51	V	54	-21.49	Avg	2.03	90	
4515	38.81	V	74	-35.19	Peak	1.61	225	
4515	26.62	V	54	-27.38	Avg	1.61	225	
5417		V	74	-74	Peak			No Emissions
5417		V	54	-54	Avg			Detected
6319.2		V	74	-74	Peak			No Emissions
6319.2		V	54	-54	Avg			Detected
7222		V	74	-74	Peak			No Emissions
7222		V	54	-54	Avg			Detected
8125		V	74	-74	Peak			No Emissions
8125		V	54	-54	Avg			Detected
9028		V	74	-74	Peak			No Emissions
9028		V	54	-54	Peak			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 02/25/05

Lab: B

Tested By: Kyle Fujimoto

Low Channel**Receive Mode with Charging Board**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
903.077	43.98	H	66	-22.02	Peak	1.35	135	
903.077	42.9	H	46	-3.1	QP	1.35	135	
1805.83	39.49	H	74	-34.51	Peak	2.31	225	
1805.83	35	H	54	-19	Avg	2.31	225	
2708.6	35.67	H	74	-38.33	Peak	1.69	225	
2708.6	24.16	H	54	-29.84	Avg	1.69	225	
3611	40.65	H	74	-33.35	Peak	2.52	180	
3611	29.97	H	54	-24.03	Avg	2.52	180	
4515	41.12	H	74	-32.88	Peak	2.53	90	
4515	27.92	H	54	-26.08	Avg	2.53	90	
5417		H	74	-74	Peak			No Emissions
5417		H	54	-54	Avg			Detected
6319.2	42.55	H	74	-31.45	Peak	1.97	180	
6319.2	30.12	H	54	-23.88	Avg	1.97	180	
7222		H	74	-74	Peak			No Emissions
7222		H	54	-54	Avg			Detected
8125		H	74	-74	Peak			No Emissions
8125		H	54	-54	Avg			Detected
9028		H	74	-74	Peak			No Emissions
9028		H	54	-54	Peak			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 02/25/05

Lab: B

Tested By: Kyle Fujimoto

Middle Channel**Receive Mode with Charging Board**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
915.25	37.18	V	66	-28.82	Peak	1.23	180	
915.25	36.54	V	46	-9.46	QP	1.23	180	
1830	41.37	V	74	-32.63	Peak	2.55	180	
1830	37.58	V	54	-16.42	Avg	2.55	180	
2745	36.77	V	74	-37.23	Peak	1.61	180	
2745	24.05	V	54	-29.95	Avg	1.61	180	
3660	38.33	V	74	-35.67	Peak	2.07	180	
3660	25.11	V	54	-28.89	Avg	2.07	180	
4575	38.9	V	74	-35.1	Peak	2.04	180	
4575	26.54	V	54	-27.46	Avg	2.04	180	
5490		V	74	-74	Peak			No Emissions
5490		V	54	-54	Avg			Detected
6405		V	74	-74	Peak			No Emissions
6405		V	54	-54	Avg			Detected
7320		V	74	-74	Peak			No Emissions
7320		V	54	-54	Avg			Detected
8235		V	74	-74	Peak			No Emissions
8235		V	54	-54	Avg			Detected
9150		V	74	-74	Peak			No Emissions
9150		V	54	-54	Peak			Detected

FCC 15.249

Embedded Processor Designs, Inc.
 InfoCube Pager
 Model: 1

Date: 02/25/05

Lab: B

Tested By: Kyle Fujimoto

Middle Channel**Receive Mode with Charging Board**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
915.269	41.68	H	66	-24.32	Peak	1.2	180	
915.269	41.23	H	46	-4.77	QP	1.2	180	
1830	41.68	H	74	-32.32	Peak	2.6	180	
1830	38.55	H	54	-15.45	Avg	2.6	180	
2745	37.79	H	74	-36.21	Peak	1.58	225	
2745	23.93	H	54	-30.07	Avg	1.58	225	
3660	40.67	H	74	-33.33	Peak	2.15	180	
3660	26.66	H	54	-27.34	Avg	2.15	180	
4575	40.66	H	74	-33.34	Peak	2.15	225	
4575	28.04	H	54	-25.96	Avg	2.15	225	
5490		H	74	-74	Peak			No Emissions
5490		H	54	-54	Avg			Detected
6405		H	74	-74	Peak			No Emissions
6405		H	54	-54	Avg			Detected
7320		H	74	-74	Peak			No Emissions
7320		H	54	-54	Avg			Detected
8235		H	74	-74	Peak			No Emissions
8235		H	54	-54	Avg			Detected
9150		H	74	-74	Peak			No Emissions
9150		H	54	-54	Peak			Detected

FCC 15.249

Embedded Processor Designs, Inc.
InfoCube Pager
Model: 1

Date: 02/25/05

Lab: B

Tested By: Kyle Fujimoto

High Channel**Receive Mode with Charging Board**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
927.418	35.58	V	66	-30.42	Peak	2.01	135	
927.418	35.14	V	46	-10.86	QP	2.01	135	
1854.6	42.54	V	74	-31.46	Peak	2.58	180	
1854.6	39.8	V	54	-14.2	Avg	2.58	180	
2781.7	38.32	V	74	-35.68	Peak	2.58	180	
2781.7	24.49	V	54	-29.51	Avg	2.58	180	
3708.9	41.46	V	74	-32.54	Peak	2.03	270	
3708.9	35.75	V	54	-18.25	Avg	2.03	270	
4636.15	39.06	V	74	-34.94	Peak	2.52	135	
4636.2	27.07	V	54	-26.93	Avg	2.52	135	
5563.3		V	74	-74	Peak			No Emissions
5563.3		V	54	-54	Avg			Detected
6490.6		V	74	-74	Peak			No Emissions
6490.6		V	54	-54	Avg			Detected
7417.8		V	74	-74	Peak			No Emissions
7417.8		V	54	-54	Avg			Detected
8345.1		V	74	-74	Peak			No Emissions
8345.1		V	54	-54	Avg			Detected
9272.2		V	74	-74	Peak			No Emissions
9272.2		V	54	-54	Peak			Detected

FCC 15.249

Embedded Processor Designs, Inc.

InfoCube Pager

Model: 1

Date: 02/25/05

Lab: B

Tested By: Kyle Fujimoto

High Channel**Receive Mode with Charging Board**

Freq. (MHz)	Level (dBuV)	Pol (v/h)	Limit	Margin	Peak / QP / Avg	Ant. Height (m)	Table Angle (deg)	Comments
927.423	42.48	H	66	-23.52	Peak	1.78	180	
927.423	42.06	H	46	-3.94	QP	1.78	180	
1854.6	42.71	H	74	-31.29	Peak	1.54	135	
1854.6	39.52	H	54	-14.48	Avg	1.54	135	
2781.7	38.21	H	74	-35.79	Peak	1.65	270	
2781.7	24.61	H	54	-29.39	Avg	1.65	270	
3708.9	41.09	H	74	-32.91	Peak	1.96	225	
3708.9	26.35	H	54	-27.65	Avg	1.96	225	
4636.15		H	74	-74	Peak			No Emissions
4636.2		H	54	-54	Avg			Detected
5563.3		H	74	-74	Peak			No Emissions
5563.3		H	54	-54	Avg			Detected
6490.6		H	74	-74	Peak			No Emissions
6490.6		H	54	-54	Avg			Detected
7417.8		H	74	-74	Peak			No Emissions
7417.8		H	54	-54	Avg			Detected
8345.1		H	74	-74	Peak			No Emissions
8345.1		H	54	-54	Avg			Detected
9272.2		H	74	-74	Peak			No Emissions
9272.2		H	54	-54	Avg			Detected

Embedded Processor Designs, Inc.
InfoCube Pager
Model: 1

Date: 01/18/05
Lab: B
Tested By: Kyle Fujimoto

Digital Portion From 1 GHz to 9.3 GHz

[illegible]

Embedded Processor Designs, Inc.
InfoCube Pager
Model: 1

Date: 02/25/05
Lab: B
Tested By: Kyle Fujimoto

Digital Portion From 1 GHz to 9.3 GHz with Charging Board

[illegible]

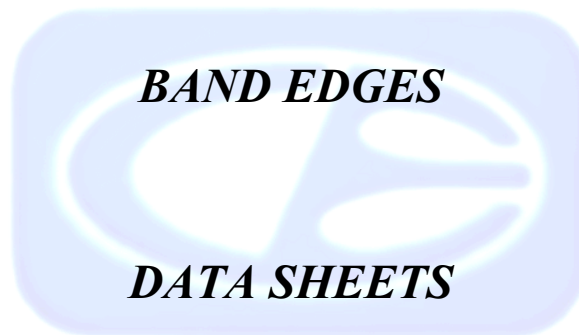
Test Location : Compatible Electronics
Customer : EP Designs, Inc.
Manufacturer : EP Designs, Inc.
Eut name : InfoCube Pager
Model : 1
Serial # : N/A
Specification : FCC B
Distance correction factor ($20 * \log(\text{test}/\text{spec})$) : 0.00
Test Mode : Radiated Emissions - 10 kHz to 1000 MHz
Vertical and Horizontal Polarization
Worst Case Axis - X-Axis
Tested By: Kyle Fujimoto

Page : 1/1
Date : 1/19/2005
Time : 10:42:46
Lab : A
Test Distance : 3.0 Meters

Pol	Freq MHz	Rdng dBuV	Cable loss dB	Ant factor dB	Amp gain dB	Cor'd rdg = R dBuV	Limit = L dBuV/m	Delta R-L dB
1V	30.000	49.50	1.80	11.00	32.40	29.90	40.00	-10.10
2V	34.860	46.30	1.80	10.81	32.40	26.51	40.00	-13.49
3V	40.440	47.70	1.81	11.00	32.40	28.11	40.00	-11.89
4V	132.716	45.90	2.76	12.88	32.33	29.21	43.50	-14.29
5H	154.805	35.20	3.06	12.44	32.24	18.46	43.50	-25.04
6H	200.204	38.20	3.00	16.39	32.40	25.19	43.50	-18.31
7V	204.804	36.00	3.02	16.24	32.42	22.84	43.50	-20.66
8V	256.019	40.10	3.35	15.75	32.25	26.95	46.00	-19.05
9V	264.056	41.60	3.41	16.85	32.19	29.68	46.00	-16.32
10V	501.216	32.20	5.21	17.42	31.99	22.83	46.00	-23.17
11H	501.340	45.40	5.21	17.42	31.99	36.03	46.00	-9.97
12H	568.540	39.20	5.40	18.46	31.76	31.30	46.00	-14.70
13H	638.854	40.50	5.79	19.18	31.70	33.78	46.00	-12.22
14H	702.854	38.70	6.19	19.67	31.71	32.84	46.00	-13.16

Test Location	: Compatible Electronics	Page	: 1/1
Customer	: EP Designs, Inc.	Date	: 2/25/2005
Manufacturer	: EP Designs, Inc.	Time	: 13:46:32
Eut name	: InfoCube Pager	Lab	: A
Model	: 1	Test Distance	: 3.0 Meters
Serial #	: N/A		
Specification	: FCC B		
Distance correction factor (20 * log(test/spec))			: 0.00
Test Mode	: Radiated Emissions - 10 kHz to 1000 MHz Horizontal and Vertical Polarizations Transmit Mode (worst case) with Charging Board Tested By: Kyle Fujimoto		

Pol	Freq MHz	Rdng dBuV	Cable loss dB	Ant factor dB	Amp gain dB	Cor'd rdg = R dBuV	Limit = L dBuV/m	Delta R-L dB
1V	30.290	39.30	1.80	9.84	32.40	18.54	40.00	-21.46
2V	36.710	46.70	1.80	11.04	32.40	27.14	40.00	-12.86
3V	39.222	47.10	1.80	11.94	32.40	28.44	40.00	-11.56
4V	56.956	48.00	2.07	8.74	32.47	26.34	40.00	-13.66
5H	57.963	36.60	2.08	8.49	32.48	14.69	40.00	-25.31
6V	60.661	44.60	2.12	8.03	32.49	22.26	40.00	-17.74
7H	63.903	39.40	2.22	8.16	32.46	17.33	40.00	-22.67
8V	73.205	49.90	2.40	7.83	32.37	27.76	40.00	-12.24
9V	127.057	47.20	2.64	12.98	32.38	30.45	43.50	-13.05
10H	150.145	38.50	3.10	11.92	32.20	21.32	43.50	-22.18
11V	200.097	37.50	3.00	16.40	32.40	24.50	43.50	-19.00
12V	309.640	34.00	3.64	13.06	32.28	18.43	46.00	-27.57
13V	377.406	33.20	4.08	14.10	32.20	19.18	46.00	-26.82
14V	503.669	30.40	5.22	17.46	31.98	21.09	46.00	-24.91
15V	936.780	33.80	6.47	21.34	31.30	30.31	46.00	-15.69



RADIATED EMISSIONS (FCC SECTION 15.249)

COMPANY	Embedded Processor Designs, Inc.	DATE	1/18/05
EUT	InfoCube Pager	DUTY CYCLE	N/A %
MODEL	1	PEAK TO AVG	N/A dB
S/N	N/A	TEST DIST.	3 Meters
TEST ENGINEER	Kyle Fujimoto	LAB	A

Frequency MHz	Peak Reading (dBuV)	Average (A) or Quasi- Peak (QP)	Antenna Polar. (V or H)	Antenna Height (meters)	EUT Azimuth (degrees)	EUT Axis (X,Y,Z)	EUT Tx Channel	Antenna Factor (dB)	Cable Loss (dB)	Amplifier Gain (dB)	Distance Factor (dB)	Mixer Factor (dB)	*Corrected Reading (dBuV/m)	Delta ** (dB)	Spec Limit (dBuV/m)	Comments
901.8620	48.7	47.0 QP	H	1.0	180	Z	LOW	20.5	6.8	31.0	0.0	0.0	43.3	-2.7	46.0	Band Edge - Low Channel
																Horizontal Polarization
																Z-Axis (Worst Case)
928.4320	46.3	QP	H	1.0	340	X	HIGH	21.2	6.9	31.2	0.0	0.0	43.1	-2.9	46.0	Band Edge - High Channel
																Horizontal Polarization
																X-Axis (Worst Case)

* CORRECTED READING = METER READING + ANTENNA FACTOR + CABLE LOSS - AMPLIFIER GAIN

** DELTA = SPEC LIMIT - CORRECTED READING

RADIATED EMISSIONS (FCC SECTION 15.249)

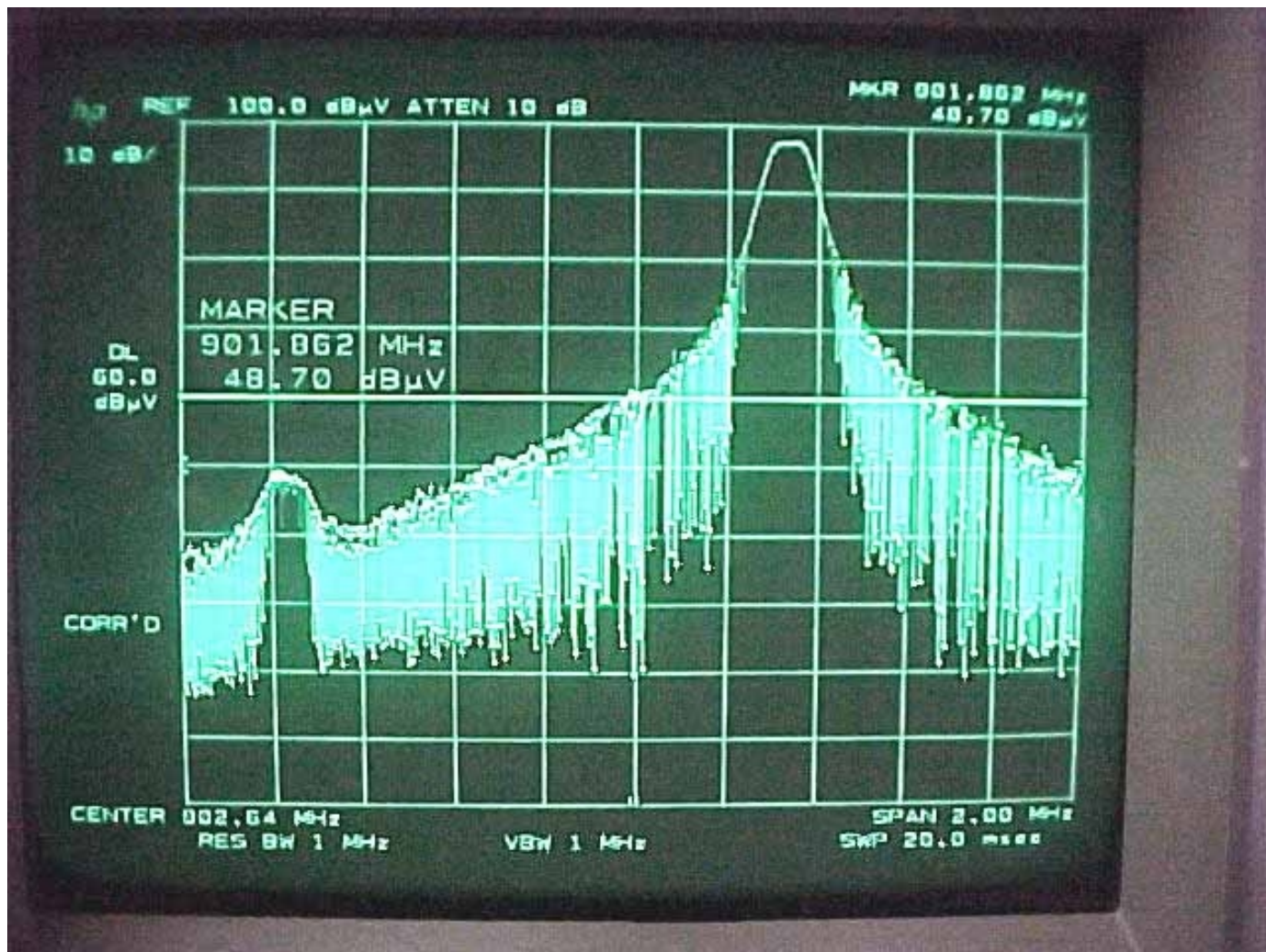
COMPANY	Embedded Processor Designs, Inc.	DATE	2/25/2005
EUT	InfoCube Pager	DUTY CYCLE	N/A %
MODEL	1	PEAK TO AVG	N/A dB
S/N	N/A	TEST DIST.	3 Meters
TEST ENGINEER	Kyle Fujimoto	LAB	A

Frequency MHz	Peak Reading (dBuV)	Average (A) or Quasi- Peak (QP)	Antenna Polar. (V or H)	Antenna Height (meters)	EUT Azimuth (degrees)	EUT Axis (X,Y,Z)	EUT Tx Channel	Antenna Factor (dB)	Cable Loss (dB)	Amplifier Gain (dB)	Distance Factor (dB)	Mixer Factor (dB)	*Corrected Reading (dBuV/m)	Delta ** (dB)	Spec Limit (dBuV/m)	Comments
901.8620	45.3	QP	H	1.0	180	X	LOW	20.5	6.1	31.0	0.0	0.0	40.9	-5.1	46.0	Band Edge - Low Channel
																Horizontal Polarization
																Tx With Charging Board
928.4320	43.2	QP	H	1.0	340	X	HIGH	21.2	6.4	31.2	0.0	0.0	39.5	-6.5	46.0	Band Edge - High Channel
																Horizontal Polarization
																Tx With Charging Board

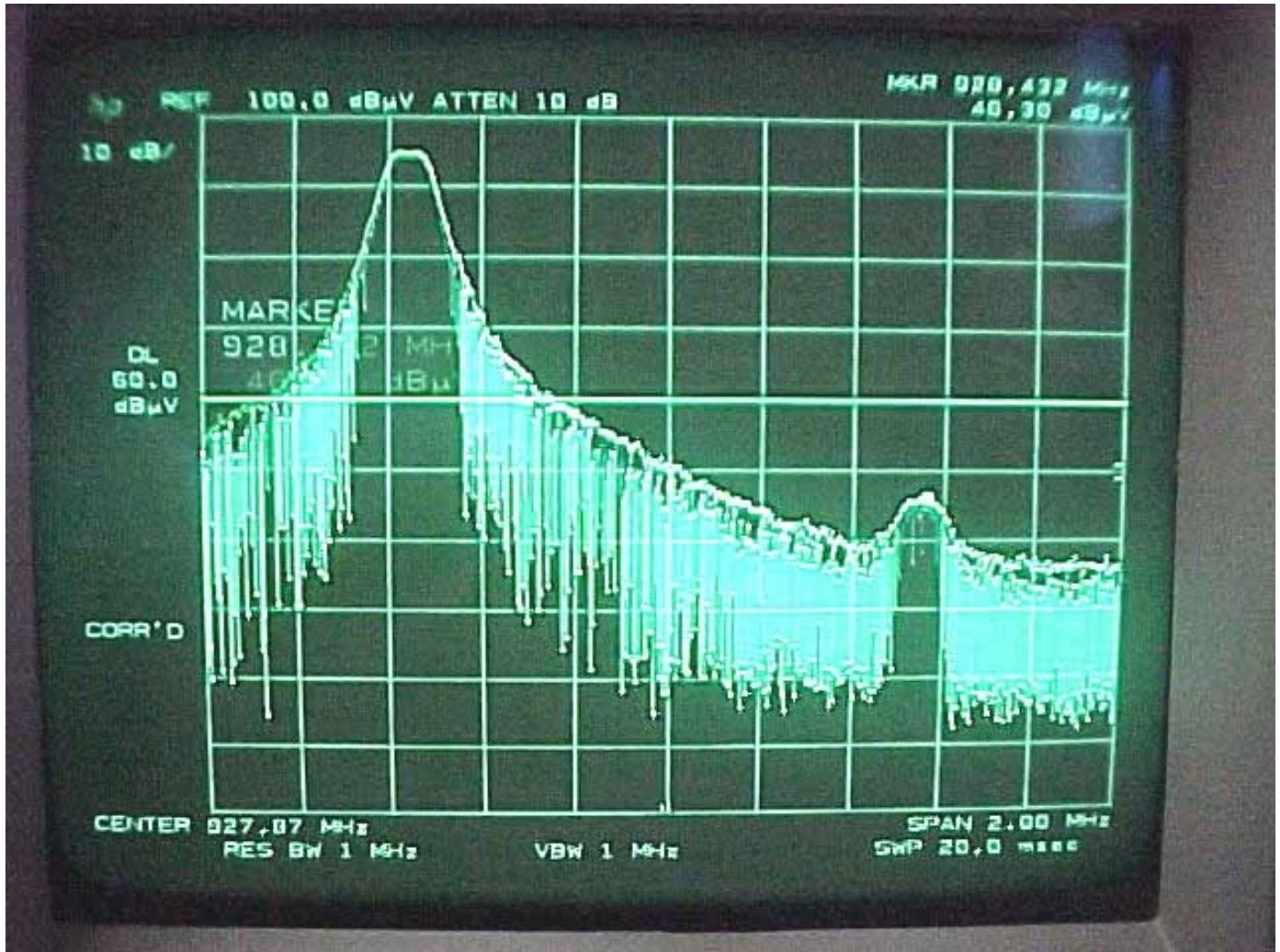
* CORRECTED READING = METER READING + ANTENNA FACTOR + CABLE LOSS - AMPLIFIER GAIN

** DELTA = SPEC LIMIT - CORRECTED READING

PAGE 2 of PAGE 10



**PHOTOGRAPH SHOWING THE BAND EDGE
FOR THE LOW CHANNEL**

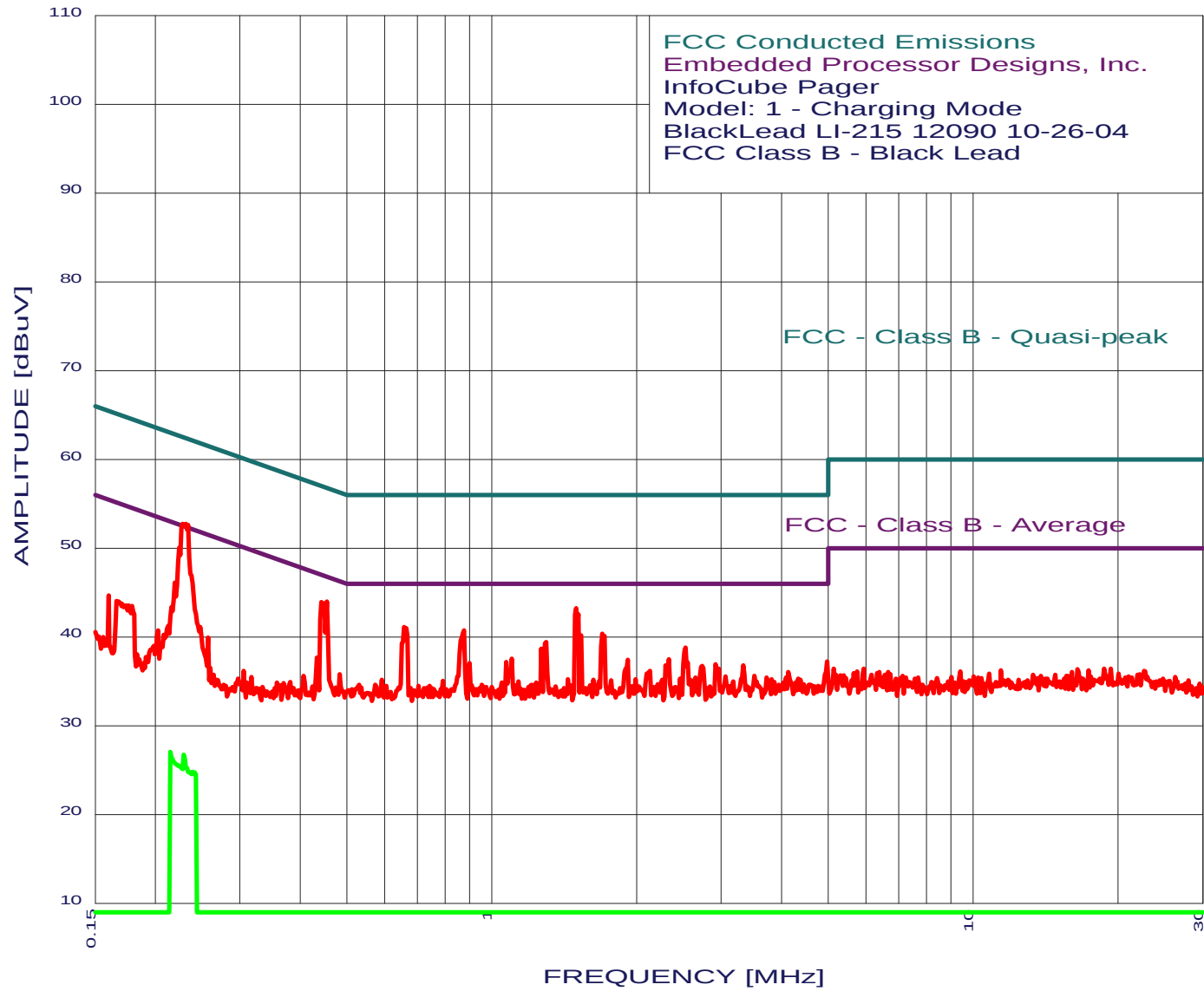


**PHOTOGRAPH SHOWING THE BAND EDGE
FOR THE HIGH CHANNEL**

CONDUCTED EMISSIONS
DATA SHEETS

EMISSION LEVEL [dBuV] PEAK
Graph for Peak & Average

1/19/2005 10:13:38



Embedded Processor Designs, Inc.
InfoCube Pager
Model: 1 - Charging Mode
FCC Class B - Black Lead
TEST ENGINEER : Kyle Fujimoto

45 highest peaks above -50.00 dB of FCC - Class B - Average limit line

Peak criteria : 1.00 dB, Curve : Peak

Peak#	Freq(MHz)	Amp(dBuV)	Limit(dB)	Delta(dB)
1	0.228	52.72	52.52	0.20*
2	1.496	43.22	46.00	-2.78
3	0.454	43.98	46.80	-2.82
4	0.442	43.88	47.02	-3.13
5	1.512	42.52	46.00	-3.48
6	0.658	41.09	46.00	-4.91
7	0.876	40.71	46.00	-5.29
8	1.699	40.36	46.00	-5.64
9	1.536	40.13	46.00	-5.87
10	1.297	39.38	46.00	-6.62
11	0.220	46.12	52.83	-6.70
12	2.527	38.77	46.00	-7.23
13	1.269	38.67	46.00	-7.33
14	1.100	37.54	46.00	-8.46
15	2.334	37.45	46.00	-8.55
16	1.918	37.40	46.00	-8.60
17	4.980	37.22	46.00	-8.78
18	1.072	37.14	46.00	-8.86
19	0.899	37.01	46.00	-8.99
20	2.916	36.91	46.00	-9.09
21	3.328	36.75	46.00	-9.25
22	2.736	36.69	46.00	-9.31
23	0.433	37.69	47.19	-9.51
24	2.134	36.13	46.00	-9.87
25	4.137	36.03	46.00	-9.97
26	3.511	35.57	46.00	-10.43
27	3.059	35.53	46.00	-10.47
28	0.484	35.78	46.27	-10.50
29	4.092	35.43	46.00	-10.57
30	3.722	35.39	46.00	-10.61
31	2.651	35.39	46.00	-10.61
32	2.462	35.27	46.00	-10.73
33	4.339	35.25	46.00	-10.75
34	1.992	35.22	46.00	-10.78
35	1.441	35.21	46.00	-10.79
36	3.820	35.20	46.00	-10.80
37	0.160	44.66	55.47	-10.81
38	0.592	35.18	46.00	-10.82
39	4.384	35.16	46.00	-10.84
40	2.262	35.15	46.00	-10.85
41	1.629	35.15	46.00	-10.85
42	1.772	35.08	46.00	-10.92
43	0.792	35.00	46.00	-11.00
44	1.136	34.95	46.00	-11.05
45	3.175	34.94	46.00	-11.06

* Please See the Average Readings On the Next Page and on the Plot

Embedded Processor Designs, Inc.
InfoCube Pager
Model: 1 - Charging Mode
FCC Class B - Black Lead
TEST ENGINEER : Kyle Fujimoto

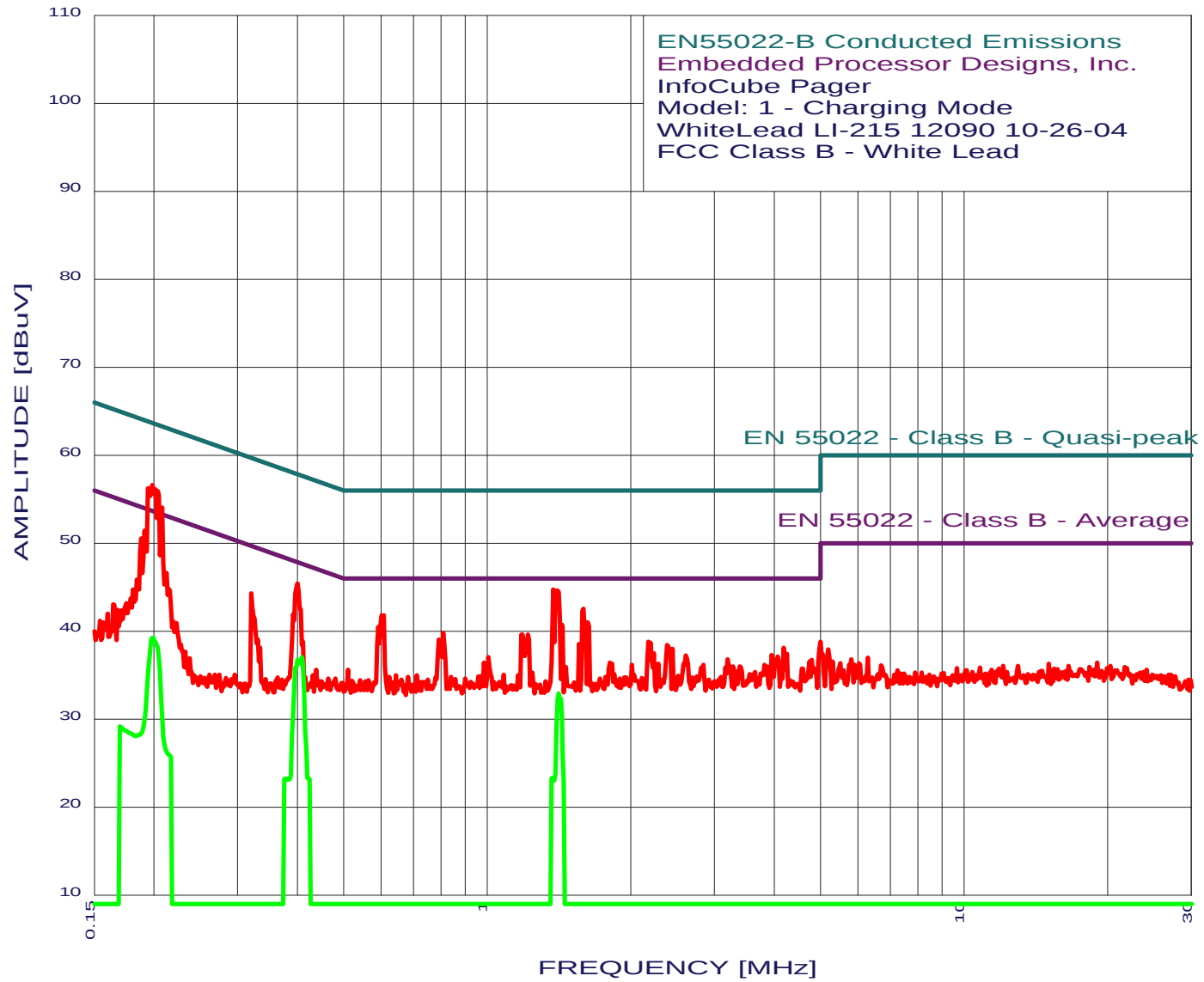
4 highest peaks above -50.00 dB of FCC - Class B - Average limit line

Peak criteria : 0.00 dB, Curve : Average

Peak#	Freq(MHz)	Amp(dBuV)	Limit(dB)	Delta(dB)
1	0.229	26.73	52.48	-25.75
2	0.215	27.04	53.00	-25.97
3	0.242	24.75	52.04	-27.29
4	0.239	24.75	52.12	-27.37

EMISSION LEVEL [dBuV] PEAK
Graph for Peak & Average

1/19/2005 10:24:40



Embedded Processor Designs, Inc.
InfoCube Pager
Model: 1 - Charging Mode
FCC Class B - White Lead
TEST ENGINEER : Kyle Fujimoto

45 highest peaks above -50.00 dB of EN 55022 - Class B - Average limit line

Peak criteria : 1.00 dB, Curve : Peak

Peak#	Freq(MHz)	Amp(dBuV)	Limit(dB)	Delta(dB)
1	0.199	56.62	53.67	2.95*
2	0.204	55.91	53.44	2.47*
3	0.208	54.01	53.27	0.74*
4	1.374	44.71	46.00	-1.29*
5	1.404	44.61	46.00	-1.39*
6	0.400	45.39	47.86	-2.46*
7	0.192	51.42	53.97	-2.55*
8	1.594	42.55	46.00	-3.45
9	0.188	50.52	54.10	-3.58*
10	0.608	41.78	46.00	-4.22
11	1.611	41.06	46.00	-4.94
12	1.629	40.96	46.00	-5.04
13	1.434	40.72	46.00	-5.28
14	0.320	44.31	49.71	-5.40
15	0.592	40.48	46.00	-5.52
16	0.809	39.81	46.00	-6.19
17	1.217	39.57	46.00	-6.43
18	1.184	39.57	46.00	-6.43
19	0.213	46.61	53.09	-6.49*
20	0.792	39.00	46.00	-7.00
21	2.179	38.76	46.00	-7.24
22	2.201	38.66	46.00	-7.34
23	1.560	38.55	46.00	-7.45
24	0.801	38.40	46.00	-7.60
25	2.384	38.38	46.00	-7.62
26	4.182	38.08	46.00	-7.92
27	2.449	37.99	46.00	-8.01
28	0.184	45.83	54.28	-8.46*
29	2.238	37.47	46.00	-8.53
30	4.249	37.38	46.00	-8.62
31	3.945	37.26	46.00	-8.74
32	2.610	37.21	46.00	-8.79
33	4.071	37.07	46.00	-8.93
34	1.006	37.03	46.00	-8.97
35	3.192	36.77	46.00	-9.23
36	4.799	36.70	46.00	-9.30
37	4.748	36.50	46.00	-9.50
38	0.984	36.43	46.00	-9.57
39	1.810	36.40	46.00	-9.60
40	0.181	44.73	54.46	-9.73*
41	2.286	36.27	46.00	-9.73
42	3.820	36.14	46.00	-9.86
43	2.013	36.14	46.00	-9.86
44	2.840	36.13	46.00	-9.87
45	3.383	35.99	46.00	-10.01

* Please See the Average Readings on the Next Page and on the Plot

Embedded Processor Designs, Inc.
InfoCube Pager
Model: 1 - Charging Mode
FCC Class B - White Lead
TEST ENGINEER : Kyle Fujimoto

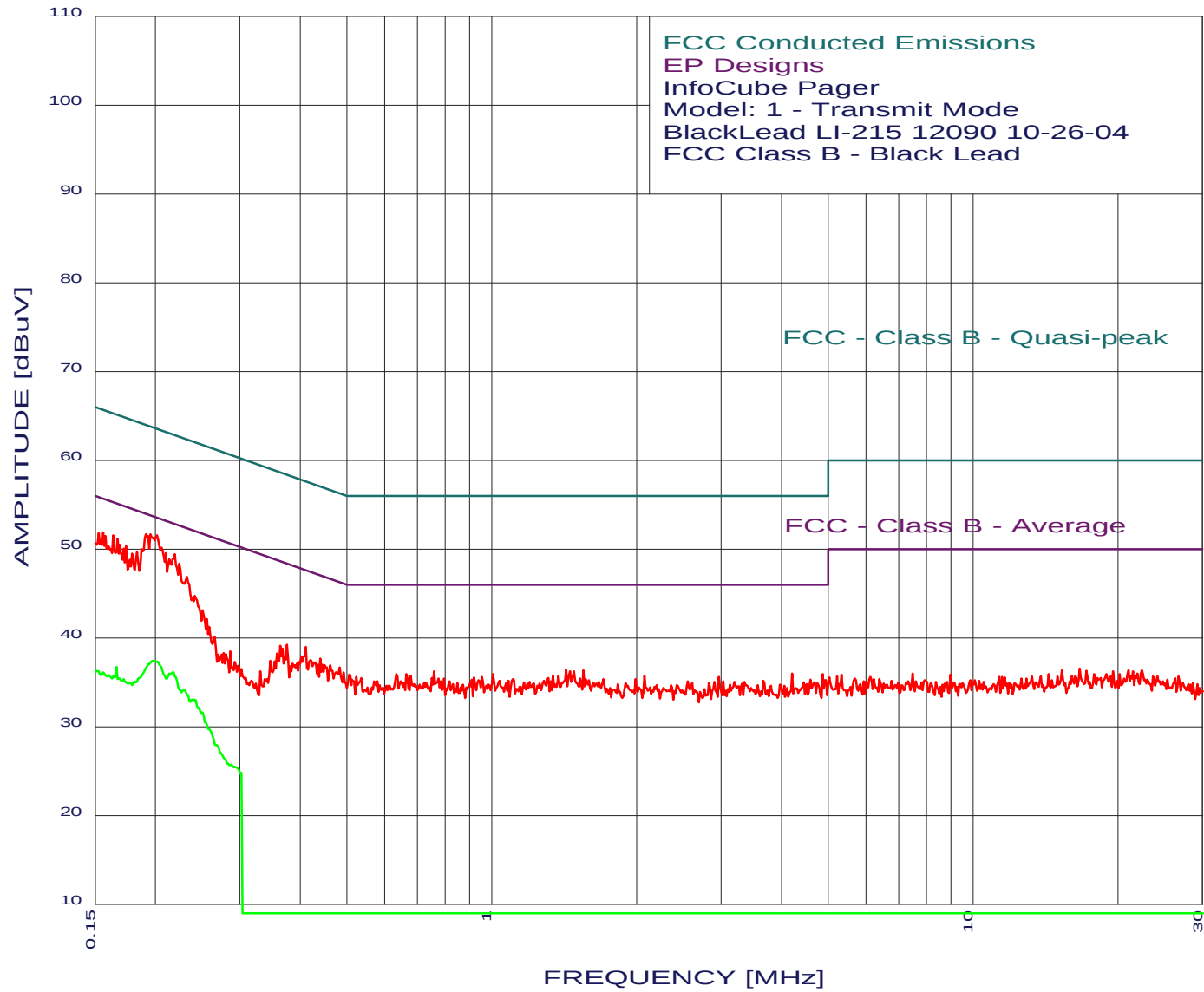
9 highest peaks above -50.00 dB of EN 55022 - Class B - Average limit line

Peak criteria : 0.00 dB, Curve : Average

Peak#	Freq(MHz)	Amp(dBuV)	Limit(dB)	Delta(dB)
1	0.409	37.01	47.68	-10.67
2	0.400	36.74	47.86	-11.11
3	1.412	32.89	46.00	-13.11
4	1.426	32.27	46.00	-13.73
5	0.199	39.23	53.67	-14.44
6	1.367	23.27	46.00	-22.73
7	0.383	23.16	48.21	-25.05
8	0.377	23.24	48.34	-25.10
9	0.170	29.17	54.98	-25.82

EMISSION LEVEL [dBuV] PEAK
Graph for Peak & Average

2/25/2005 15:04:31



2/25/2005 15:04:31

EP Designs
 InfoCube Pager
 Model: 1 - Transmit Mode
 FCC Class B - Black Lead
 TEST ENGINEER : Kyle Fujimoto

 44 highest peaks above -50.00 dB of FCC - Class B - Average limit line

Peak criteria : 1.00 dB, Curve : Peak

Peak#	Freq(MHz)	Amp(dBuV)	Limit(dB)	Delta(dB)
1	0.196	51.64	53.80	-2.16*
2	0.192	51.64	53.97	-2.33*
3	0.220	49.42	52.83	-3.40*
4	0.163	51.56	55.29	-3.74*
5	0.156	51.86	55.69	-3.83*
6	0.167	51.15	55.11	-3.96*
7	0.152	51.76	55.86	-4.10*
8	0.157	51.46	55.60	-4.14*
9	0.224	48.32	52.65	-4.33*
10	0.174	50.35	54.77	-4.41*
11	0.183	49.65	54.33	-4.68*
12	0.172	50.05	54.86	-4.80*
13	0.179	49.45	54.54	-5.09*
14	0.251	43.10	51.73	-8.63*
15	0.411	38.99	47.63	-8.64
16	0.375	39.21	48.38	-9.18
17	0.440	37.69	47.06	-9.38
18	0.256	42.10	51.55	-9.45*
19	0.363	39.11	48.65	-9.53
20	1.426	36.41	46.00	-9.59
21	0.484	36.68	46.27	-9.60
22	1.552	36.33	46.00	-9.67
23	1.480	36.22	46.00	-9.78
24	0.461	36.88	46.67	-9.79
25	1.456	36.21	46.00	-9.79
26	0.759	36.20	46.00	-9.80
27	0.419	37.49	47.46	-9.97
28	4.204	35.94	46.00	-10.06
29	0.492	36.07	46.14	-10.07
30	1.006	35.92	46.00	-10.08
31	4.877	35.91	46.00	-10.09
32	0.665	35.89	46.00	-10.11
33	0.634	35.88	46.00	-10.12
34	0.505	35.87	46.00	-10.13
35	2.077	35.73	46.00	-10.27
36	1.374	35.70	46.00	-10.30
37	0.679	35.69	46.00	-10.31
38	1.338	35.69	46.00	-10.31
39	2.963	35.62	46.00	-10.38
40	0.953	35.62	46.00	-10.38
41	0.524	35.57	46.00	-10.43
42	0.919	35.51	46.00	-10.49
43	1.311	35.48	46.00	-10.52
44	0.796	35.40	46.00	-10.60

 * Please See the Average Readings on the Next Page and on the Plot

2/25/2005 15:04:31

EP Designs
InfoCube Pager
Model: 1 - Transmit Mode
FCC Class B - Black Lead
TEST ENGINEER : Kyle Fujimoto

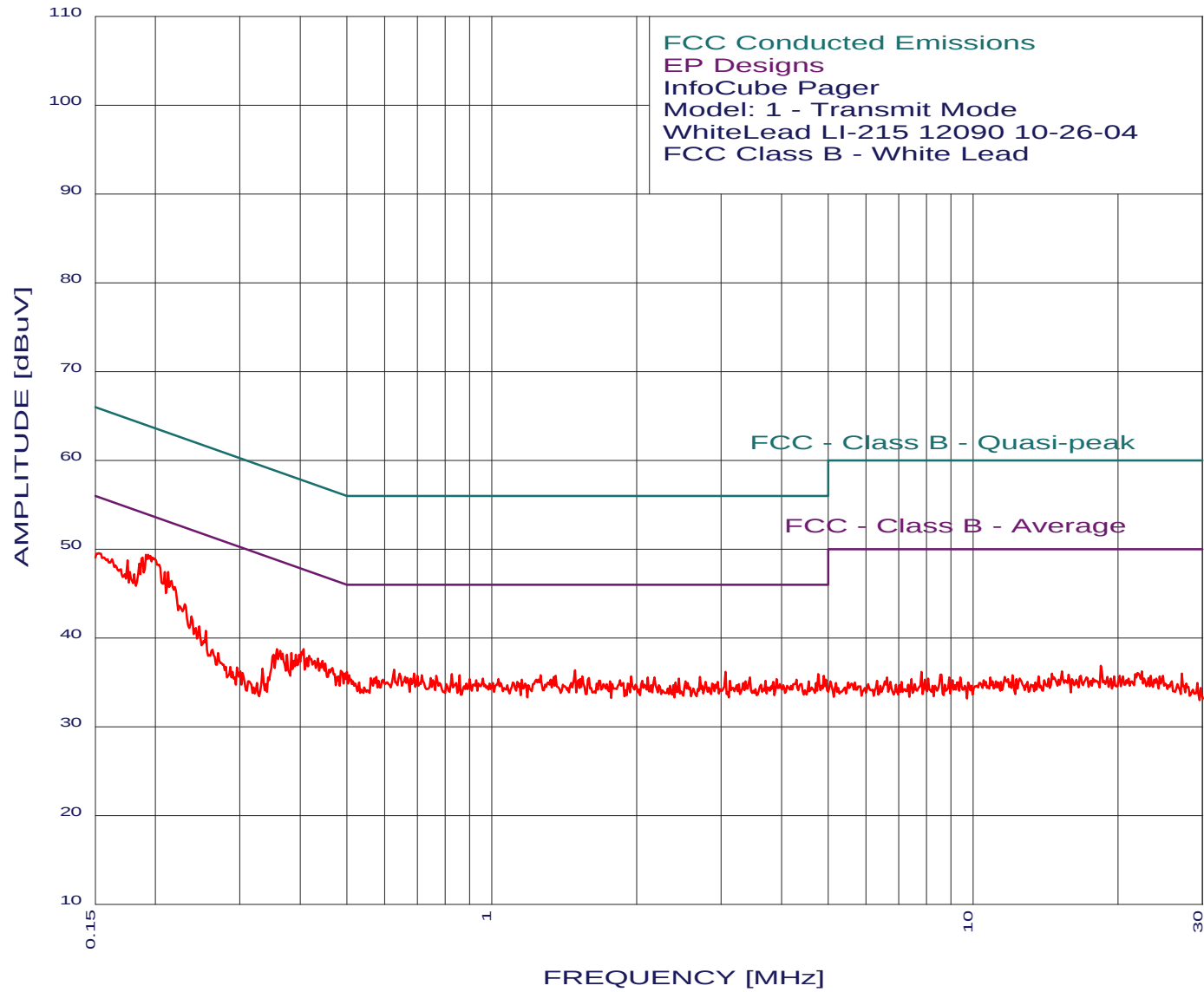
13 highest peaks above -50.00 dB of FCC - Class B - Average limit line

Peak criteria : 0.10 dB, Curve : Average

Peak#	Freq(MHz)	Amp(dBuV)	Limit(dB)	Delta(dB)
1	0.198	37.43	53.71	-16.28
2	0.217	36.12	52.91	-16.80
3	0.230	34.13	52.43	-18.31
4	0.166	36.72	55.16	-18.43
5	0.238	33.04	52.17	-19.13
6	0.181	35.12	54.46	-19.34
7	0.172	35.33	54.86	-19.52
8	0.178	34.98	54.59	-19.61
9	0.156	36.08	55.69	-19.61
10	0.163	35.66	55.29	-19.64
11	0.160	35.81	55.47	-19.66
12	0.151	36.27	55.95	-19.68
13	0.302	24.77	50.19	-25.42

EMISSION LEVEL [dBuV] PEAK
Graph for Peak

2/25/2005 15:09:06



2/25/2005 15:09:06

EP Designs
 InfoCube Pager
 Model: 1 - Transmit Mode
 FCC Class B - White Lead
 TEST ENGINEER : Kyle Fujimoto

 45 highest peaks above -50.00 dB of FCC - Class B - Average limit line

Peak criteria : 1.00 dB, Curve : Peak

Peak#	Freq(MHz)	Amp(dBuV)	Limit(dB)	Delta(dB)
1	0.192	49.32	53.97	-4.65
2	0.188	49.02	54.10	-5.08
3	0.213	47.41	53.09	-5.69
4	0.210	47.41	53.23	-5.82
5	0.185	48.33	54.24	-5.91
6	0.175	48.73	54.72	-5.99
7	0.177	47.43	54.63	-7.20
8	0.406	38.69	47.72	-9.03
9	0.435	37.69	47.15	-9.46
10	0.417	37.99	47.50	-9.51
11	0.445	37.38	46.98	-9.59
12	0.627	36.39	46.00	-9.61
13	1.488	36.33	46.00	-9.67
14	0.238	42.38	52.17	-9.79
15	0.500	36.17	46.01	-9.83
16	3.059	36.16	46.00	-9.84
17	0.383	38.30	48.21	-9.91
18	0.391	38.10	48.03	-9.94
19	0.486	36.27	46.23	-9.95
20	0.690	35.99	46.00	-10.01
21	0.644	35.99	46.00	-10.01
22	0.358	38.70	48.78	-10.08
23	0.826	35.91	46.00	-10.09
24	4.774	35.90	46.00	-10.10
25	0.858	35.81	46.00	-10.19
26	0.716	35.80	46.00	-10.20
27	1.262	35.78	46.00	-10.22
28	0.375	38.10	48.38	-10.28
29	4.902	35.71	46.00	-10.29
30	1.359	35.70	46.00	-10.30
31	0.767	35.70	46.00	-10.30
32	0.701	35.69	46.00	-10.31
33	1.544	35.64	46.00	-10.36
34	1.950	35.63	46.00	-10.37
35	2.707	35.62	46.00	-10.38
36	0.662	35.59	46.00	-10.41
37	0.577	35.58	46.00	-10.42
38	2.179	35.56	46.00	-10.44
39	2.111	35.55	46.00	-10.45
40	3.438	35.50	46.00	-10.50
41	0.747	35.50	46.00	-10.50
42	1.594	35.45	46.00	-10.55
43	0.958	35.42	46.00	-10.58
44	0.247	41.27	51.86	-10.59
45	2.449	35.39	46.00	-10.61
